

**STD-WORDS****FEATURES**

- ~ STD Bus Compatible
- ~ VOTRAX™ SC-01 Speech Synthesizer Device
- ~ Phoneme Driven - Unlimited Vocabulary
- ~ Easy To Use
- ~ Text-To-Speech Capabilities
- ~ Parallel Bus I/O for Fast Data Transfers.
- ~ Occupies Single I/O Port Address
- ~ On-board Audio Amplifier (800 miliWatt)
- ~ Direct Connect to 8 Ohm Speaker
- ~ 1 Year Warranty

**Figure 1. STD-WORDS****DESCRIPTION**

The COLEX STD-WORDS board is a cost effective means of adding electronic speech to any STD Bus microcomputer system. The STD-WORDS is based on the Votrax™ SC-01 Phoneme Speech Synthesizer, is easy to use, and is capable of synthesizing continuous speech of unlimited vocabulary from extremely low data rate 6-bit codes.

**BLOCK DIAGRAM**

Figure 3. STD-WORDS Control Header and Connector Locations

## THEORY OF OPERATION

There have been a number of schemes devised to allow human sounding speech to be generated by computer systems. These schemes range from analog formant filtering of waveforms and noise to wholesale digitization of the analog voice signal and then later digital to analog regeneration of the original signal.

The quality of the resultant speech ranged from passable with the former system (using moderate data rates) to outstanding for the latter system (using extremely high data rates). But in neither case is the ratio of speech quality to memory requirements satisfactory.

### Signal Digitization

Speech digitization is a brute force type of procedure that produces the highest quality speech but uses up the most amount of memory. The analog signal is sampled at a rate that is roughly twice that of highest frequency of interest expected in the analog sample. At each sampling point, the instantaneous voltage of the signal is digitized and stored in consecutive memory locations as a byte of data. Each word in the vocabulary to be spoken by the machine is input, digitized, and assigned a block of memory.

To regenerate the original signal, each word to be spoken is selected and the corresponding block of memory addressed. Then consecutive memory locations within that block are read at the same rate as the signal was originally sampled and the contents applied to a digital to analog converter and low-pass filter. This procedure is repeated for each word to be spoken. The resulting output is a faithful recreation of the original signal.

Human speech (especially female speech) contains significant harmonic energy up to and somewhat beyond 8 kHz. Therefore, to provide good quality reproduction and to prevent excessive aliasing (beat notes between the sampling frequency and the higher frequencies of the signal to be sampled that fall within the pass band of the output low-pass filter and therefore cannot be easily removed from the output signal), the sampling rate should be around 16 kHz.

The memory requirements for the storage of the digitized speech works out to be about 16 kbytes per second of speech. To reconstruct the speech, a data rate of 16 kHz. is required. Various methods have been developed to reduce the memory requirements and data rates required in this sort of system. These have included reducing the bandwidth of the input signal and/or the sampling rate, reducing the conversion resolution, and special data compaction routines but have not produced satisfactory results for low cost systems.

## Formant Synthesis

In formant synthesis systems, electronic filter networks are designed to model the human vocal tract. These filters have variable characteristics and are implemented by either analog, or more recently, digital methods and closely model the qualities of the tongue, teeth, resonant cavities, and other components of the vocal tract.

Excitation signals consisting of periodic and random waveforms resembling vocal cord pitch and the sounds of air turbulence are applied to the vocal tract model filter network. The filter network removes unwanted portions of the excitation signal spectra as the components of the human vocal tract remove portions of the signal from the vocal cords. An algorithm in the computer generates complex control signals for the filter network as well as the filter excitation signals in real time for each word spoken. The characteristics of both the excitation signals as well as the filter network are varied together in such a way as to produce artificial speech.

In ordinary formant synthesis systems, the excitation signal characteristics as well as control parameters (parametric data) for the filter network are analyzed for each word in the vocabulary and stored in memory. When the word is to be spoken, these data are read from memory and applied to the signal generators and filter network. And thus the word is spoken by the system. This realizes a significant savings in both data rates and memory requirements over the voice digitization systems since only filter and excitation signal parameters must be stored for each word in the vocabulary.

## Phoneme Synthesis

The next level of data rate and storage reduction is achieved when formant synthesis data for entire words are no longer stored. This requires that the words of speech be broken down into a number of basic sounds that can be strung together in various combinations to form the words of English speech. These basic sounds are called phonemes.

It has been found that English speech can be broken down into 62 phonemes (along with two different no-sound intervals) that can be strung together in various combinations to adequately reconstruct most English words. Now, instead of storing the formant synthesis data for each word in the vocabulary to be spoken, only 64 phonemes need be stored for a vocabulary of any size. A phoneme list for each word to be spoken is either stored in memory or obtained from an outside source at the time of speech.

To create speech, the computer reads the formant synthesis data from memory for each phoneme required to simulate the word to be spoken. This data is applied to the excitation signal generators and the filter network to create a string of phonemes approximating the desired word.

## The SC-01 Phoneme Synthesizer Chip

The next reduction in data rate and memory requirements for speech synthesis systems (and the one used by the COLEX STD-WORDS board) removes the complex filter network parameter and excitation signal characteristic control duties from the host processor and gives them to a hardware device designed specifically for the job. This eliminates about 99% of the processor overhead required for synthetic speech. All that is now required from the host is a list of the phonemes required for the word to be spoken. The hardware does the rest.

In the case of the COLEX STD-WORDS board, the phoneme synthesis hardware is in the form of a VOTRAX SC-01 LSI CMOS phoneme synthesizer chip and a few other items such as data latches, logic blocks, audio amplifiers, and the like. The SC-01 can produce 62 different phonemes and two no sound intervals, each of which are accessed by a 6-bit binary code.

Synthetic speech using the SC-01 requires an average data rate of approximately 70 bits (less than 9 bytes) per second. The normalized duration of each phoneme ranges from about 47 milliseconds to 250 milliseconds. The pitch of the phoneme is increased and duration of the phoneme is decreased as the clock rate is increased (standard timing and pitch result from a 720 kHz. clock rate).

The SC-01 device can be divided into two functional parts (figure 4). The first part is called the phoneme controller and translates the 6-bit digital word applied to its input lines into the required phoneme. Internal algorithms and look-up tables generate the complex array of temporal vs spectral parameters that control the second part of the device (the filter network model and excitation signal generators) for the selected phoneme.

Figure 4. The SC-01 Phoneme Synthesizer Chip

The second part of the device contains a pair of signal sources. The first is a variable frequency oscillator for simulating the action of the vocal cords. The second is a pink noise generator that simulates the sound of rushing air. These signal sources are controlled by the phoneme controller to match the requirements of the desired phoneme.

The output of these two sources are summed together and become the excitation signals that are applied to the filter network. This network is composed of four bandpass filters that simulate the voicing components of the human vocal tract (tongue, sinus cavities, teeth, etc.). The transfer function for each of the four filters is instantaneously adjusted by the phoneme controller (parametric data) in real time to produce the required phoneme.

The combination of controlling the excitation signal sources (inflection) and parametric data driving the filter network (voicing) produce life-like phonemes that can be combined to provide good quality speech.

#### The SC-01 Phoneme Synthesizer Chip Operation

A six bit data word corresponding to the desired phoneme is applied to the six data lines of the SC-01. The data word is latched into the register and applied to the phoneme controller by bringing the STROBE line down. This also RESETS the ACKNOWLEDGE/REQUEST flip-flop causing that line to go low.

Once data is applied to the phoneme controller, it determines which phoneme is desired, generates a spectral matrix for that phoneme, and operates the excitation signals and filter network accordingly. The output sound is amplified and appears at the OUTPUT pin of the chip.

Each phoneme is internally timed and has a duration of from 47 to 250 milliseconds. When generation of the phoneme is complete, the REQUEST/ACKNOWLEDGE flip-flop is SET causing the REQUEST/ACKNOWLEDGE line to go high signifying that the synthesizer is ready for the next phoneme data word. A dynamic articulation controller provides a smooth transition from one phoneme to the next producing a higher quality speech.

An on-chip clock oscillator provides a clock signal to the various parts of the SC-01 device. The clock rate is adjusted by changing the external R C time constant. A nominal clock rate of 720 kHz. provides the standard phoneme pitch and durations. Increasing the clock rate raises the pitch of the output sound and reduces the duration of the phonemes. Accurate phonemes are still generated due to the fact that all parts of the synthesizer track each other closely.

This is made possible by the fact that the filter network is of the switched-capacitor type. The bandpass of such filters is dependent upon the clock rate applied to them. Therefore, as the clock rate increases, the filter excitation signal frequency increases, and the bandpass of the filter network increases as well.

Two pitch control lines allow gross adjustment of the excitation signal frequency without disturbing any parameters of the filter network. This allows more than one voice to be emulated.

## STD-WORDS OPERATION

Latch U5 receives the 8-bit data word from the bus, latches it, and applies the lower six bits (phoneme selection word) directly to the control word inputs of the SC-01 phoneme synthesizer chip in response to the WRITE ENA\* signal generated elsewhere on the board. The SC-01 is a CMOS device and is operated off the +12 vdc power supply line. The six data byte input lines, however, are TTL level capable and only and require the output of latch U5 to be pulled up by resistor network UR1.

The MSB and next to MSB data bits (I1 and I2) are inflection bits. They are inverted and level-shifted to 12 volts by transistors Q1 and Q2 before being applied to the SC-01 device.

During the time that the SC-01 is "talking", before the phoneme in progress times out, it outputs a high on the A/R line. This signal is inverted and driven out on the bus by four elements of U8. This low on the MSB indicates to the bus that the SC-01 is busy. The processor tests the MSB of the port where the data word was sent out by activating IORQ\* and RD\*. When the MSB goes high, the STD-WORDS card is ready for another data byte.

There are no timing problems with the output data byte; watch MSB; write another data byte procedure because the The SC-01 continues to produce the last phoneme until told to be silent, stop, or to speak the next one.

A pair of 4-bit magnitude comparators check for coincidences between the port address select header and the address on the bus. If a coincidence is found, the on-board ADDRESS DECODE signal goes true. This signal is applied to the read/write decode logic (U4 and three elements of U8). Bus RD\*, IORQ\*, and WR\* signals are inverted and applied to decoder gates U4.

A coincidence of IORQ\*, RD\*, and a valid port address (as detected by U6 and U7) creates an on-board READ ENA\* signal which drives the MSB of the port with the A/R signal from the SC-01 chip. This is used when the bus processor tests the MSB of the port to see if the SC-01 is finished with the last phoneme and is ready for the next one.

To write a data byte to the SC-01, the bus processor addresses the STD-WORDS card and activates the IORQ\* and WR\* lines. These signals are decoded and a WRITE ENA signal applied to the data latch (U5) and one-shot U3. The first section of U3 inserts a delay in the SC-01 STB strobe line allowing the data byte to be latched (U5), applied to the data lines of the SC-01, and the SC-01 data inputs to settle before the chip is strobed.

The second half of U3 generates the appropriate pulse width for the SC-01 strobe signal. The I/O write pulse coming from the processor card is very narrow -- on the order of a microsecond or so, and is much too brief for the CMOS SC-01.

The clock signal for the SC-01 is generated on the chip and the frequency is controlled by C7 and the resistor network attached to pins 15 and 16 of the SC-01. The clock rate is user adjustable through a small range making the overall pitch higher or lower by VR1 which is the trim pot closest to the card ejector.

Speech output is obtained from pins 20, 21, and 22. This signal is applied to the input of audio power amplifier U1 through volume control trim pot VR2. The amplified speech is then available at J-2, a miniature phone jack.

## OPTION SELECTION

Jumper selectable options, port addresses, and so on are selected by installing a shorting strap across a pair of control header pins. The STD-WORDS card uses a special three-row control header. The center row is connected to the control circuitry and the outside rows are connected to the power supply rails.

A selection is made by strapping the center pin to either of the outside pins as required by the desired port address. The pins are situated directly across the header from each other allowing the option to be selected by wire-wrapping or inserting a Berg type strap.

## PORT ADDRESSING

	<u>ADDRESS</u>	<u>SELECTION</u>	<u>HEADER</u>	<u>J-3</u>
A7	1	`	`(2)	` 3
A6	4	`	`(5)	` 6
A5	7	`	`(8)	` 9
A4	10	`	`(11)	` 12
A3	13	`	`(14)	` 15
A2	16	`	`(17)	` 18
A1	19	`	`(20)	` 21
A0	22	`	`(23)	` 24
		Vcc		Vss

## ADDRESS STRAP SELECTION CHART

X	A3	A2	A1	A0	—	LSB
	A7	A6	A5	A4	—	MSB
0	0	0	0	0		
1	0	0	0	1		
2	0	0	1	0		
3	0	0	1	1		
4	0	1	0	0		
5	0	1	0	1		
6	0	1	1	0		
7	0	1	1	1		
8	1	0	0	0		
9	1	0	0	1		
A	1	0	1	0		
B	1	0	1	1		
C	1	1	0	0		
D	1	1	0	1		
E	1	1	1	0		
F	1	1	1	1		

## NOTES

0 indicates a strap connected from the center pin to the Vss (ground) pin on header J3.

1 indicates a strap connected from the center pin to the Vcc (+ 5 VDC) pin on header J3.

Figure 5. STD-WORDS Port Address Charts



## IOEXP

IOEXP (I/O EXPand) is used in some STD Bus systems to double the number of I/O ports available. There are 256 strappable I/O ports in a basic system. When IOEXP is pulled high, another 256 ports sharing the same logical addresses become available. This line is usually tied to either + 5 vdc or to ground, but may be toggled either way during system operation according to the requirements of the boards on the bus.

The IOEXP line is, essentially, an additional address line and I/O boards are most likely to use this it. When a port is addressed, an I/O function on a board that is strapped for that port address is selected. If IOEXP is used, then two I/O functions may be strapped for the same address with the IOEXP line determining which is to be activated.

In most cases, IOEXP is ignored. To configure the STD-WORDS card for operation excluding IOEXP, strap pins one and two of J-4 together. The board is shipped with this option strapped.

To configure the STD-WORDS board for use in systems where IOEXP is used, strap pins one and three on J-4 together. The board then becomes active only when its port is addressed AND IOEXP is high.



Figure 6. IOEXP HEADER

## USING THE STD-WORDS

Using the STD-WORDS card is very simple and quite straight forward. The procedure is to write a data byte to the STD-WORDS port and toggle the IORQ\* and WR\* bus lines. The card latches the data byte and speaks the phoneme selected. When the phoneme is complete (but the phoneme is still sounding), the MSB of the port goes high. The next data byte is then applied to the card, and so on, and so on.

While the data byte is eight bits wide, only six are required for phoneme selection. The other two (MSB and next to MSB) are inflection bits specifying one of four subtle pitch variations. This helps to alleviate the "robot monotone" quality of most phoneme synthesized speech and make the output more life-like. The inflection bits work on the phoneme specified by the other six bits and do things like produce the upward movement in pitch in the last few sounds of a question.

The SC-01 chip, once started, produces sound (the phoneme selected) until instructed to be quiet for a specific time with a PAUSE phoneme or to shut-up entirely with a STOP command, or to produce the next sound phoneme.

The following flowchart, Z-80 machine language code, and data DATA describe how a processor can send a string of phonemes to the STD-WORDS card to cause it to speak the word "HELLO". Similar sequences can be used to send a single sound, a group of sounds (word), or a group of words to the card and can be implemented with high-level languages and look-up tables for accurate text-to-speech operation.

<u>Z80 CODE</u>	<u>START</u>	<u>DATA</u>
LD HL,data	Set A Pointer To 1st Sound	<u>PHONEME</u> H, 1B EH1 02 UH3 23
READ: IN A,(port)	Get STD-WORDS Data From Port	L 18 UH3 23 O1 35 U1 37
BIT 7,A	IS BIT	PA1 3E
JP NZ,READ	7 SET?	STOP 3F
LD A,(HL)		
OUT(port),A	Output Sound To STD-WORDS Card	
INC HL	Point TO Next Sound	
LD A, 3FH	Is	
CP (HL)	It A	
JP NZ,READ	STOP?	
	STOP	

NOTE In this program, it is important to silence the chip with a PAUSE before issuing a STOP command.

PHONEME SOUND CHART

Phoneme Code	Phoneme Symbol	Example Word	Phoneme Code	Phoneme Symbol	Example Word
00	EH3	<u>j</u> acket	20	A	<u>d</u> ay
01	EH2	<u>e</u> nlist	21	AY	<u>d</u> ay
02	EH1	<u>h</u> eavy	22	Y1	<u>y</u> ard
03	PA0	no sound	23	UH3	<u>m</u> ission
04	DT	<u>b</u> utter	24	AH	<u>m</u> op
05	A2	<u>m</u> ade	25	P	<u>p</u> ast
06	A1	<u>m</u> ade	26	O	<u>c</u> old
07	ZH	<u>a</u> zure	27	I	<u>p</u> in
08	AH2	<u>h</u> onest	28	U	<u>m</u> ove
09	13	<u>i</u> nhibit	29	Y	<u>a</u> ny
0A	12	<u>i</u> nhibit	2A	T	<u>t</u> ap
0B	11	<u>i</u> nhibit	2B	R	<u>r</u> ed
0C	M	<u>m</u> at	2C	E	<u>m</u> eat
0D	N	<u>s</u> un	2D	W	<u>w</u> in
0E	B	<u>b</u> ag	2E	AE	<u>d</u> ad
0F	V	<u>v</u> an	2F	AE1	<u>a</u> fter
10	CH*	<u>ch</u> ip	30	AW2	<u>s</u> alty
11	SH	<u>sh</u> op	31	UH2	<u>a</u> bout
12	Z	<u>z</u> oo	32	UH1	<u>u</u> ncle
13	AW1	<u>l</u> awful	33	UH	<u>c</u> up
14	NG	<u>th</u> ing	34	O2	<u>f</u> or
15	AH1	<u>f</u> ather	35	O1	<u>a</u> board
16	OO1	<u>l</u> ooking	36	IU	<u>y</u> ou
17	OO	<u>b</u> ook	37	U1	<u>y</u> ou
18	L	<u>l</u> and	38	THV	<u>t</u> he
19	K	<u>t</u> rick	39	TH	<u>t</u> hin
1A	J*	<u>j</u> udge	3A	ER	<u>b</u> ird
1B	H	<u>h</u> ello	3B	EH	<u>g</u> et
1C	G	<u>g</u> et	3C	E1	<u>b</u> e
1D	F	<u>f</u> ast	3D	AW	<u>c</u> all
1E	D	<u>p</u> aid	3E	PA1	no sound
1F	S	<u>p</u> ass	3F	STOP	no sound

\* T must precede CH to produce CH sound;  
D must precede J to produce J sound

# STD-280 BUS CONNECTOR

Bus Connector: 56 pin dual edge connector, 0.125 inch contact centers

<u>SIGNAL NAME</u>	<u>PIN NUMBERS</u>		<u>SIGNAL NAME</u>
+ 5 VDC	2	1	+ 5 VDC
GROUND	4	3	GROUND
N/C	6	5	N/C
D7	8	7	D3
D6	10	9	D2
D5	12	11	D1
D4	14	13	D0
N/C	16	15	A7
N/C	18	17	A6
N/C	20	19	A5
N/C	22	21	A4
N/C	24	23	A3
N/C	26	25	A2
N/C	28	27	A1
N/C	30	29	A0
RD*	32	31	WR*
N/C	34	33	IORQ*
N/C	36	35	IOEXP
N/C	38	37	N/C
N/C	40	39	N/C
N/C	42	41	N/C
N/C	44	43	N/C
N/C	46	45	N/C
N/C	48	47	N/C
N/C	50	49	N/C
PCI	52	51	PCO
N/C	54	53	N/C
N/C	56	55	+ 12 Volts

## SPECIFICATIONS

### ELECTRICAL

System Bus: STD Bus  
Audio Output: 0.8 Watt into 8 Ohm load  
I/O Address: Single I/O Port, Jumper Selectable  
System Interrupt Units: 0 SIU  
Bus Signal loading: Inputs: one 74LS load maximum  
Outputs: -3 mA min @ 2.4 volts  
24 mA min @ 0.5 volts  
Operating Temperature: 0° to 60° C.

Power Requirements @ 25° C:

<u>PARAMETER</u>	<u>CONDITION</u>	<u>MIN</u>	<u>TYP</u>	<u>MAX</u>	<u>UNITS</u>
V <sub>cc</sub>	-	4.75	5.00	5.25	volts
I <sub>cc</sub>	5vdc	--	100	125	mA
V <sub>cc</sub>	-	11.50	12.00	12.50	volts
I <sub>cc</sub>	12vdc	--	100	75	mA

**MECHANICAL**

Card Dimensions:

<u>FORM FACTOR</u>	<u>H</u>	<u>W</u>	<u>L</u>	<u>UNITS</u>
STD Bus	0.60	4.50	6.50	inches

PC Board Thickness: 0.062 inch

**CONNECTORS**

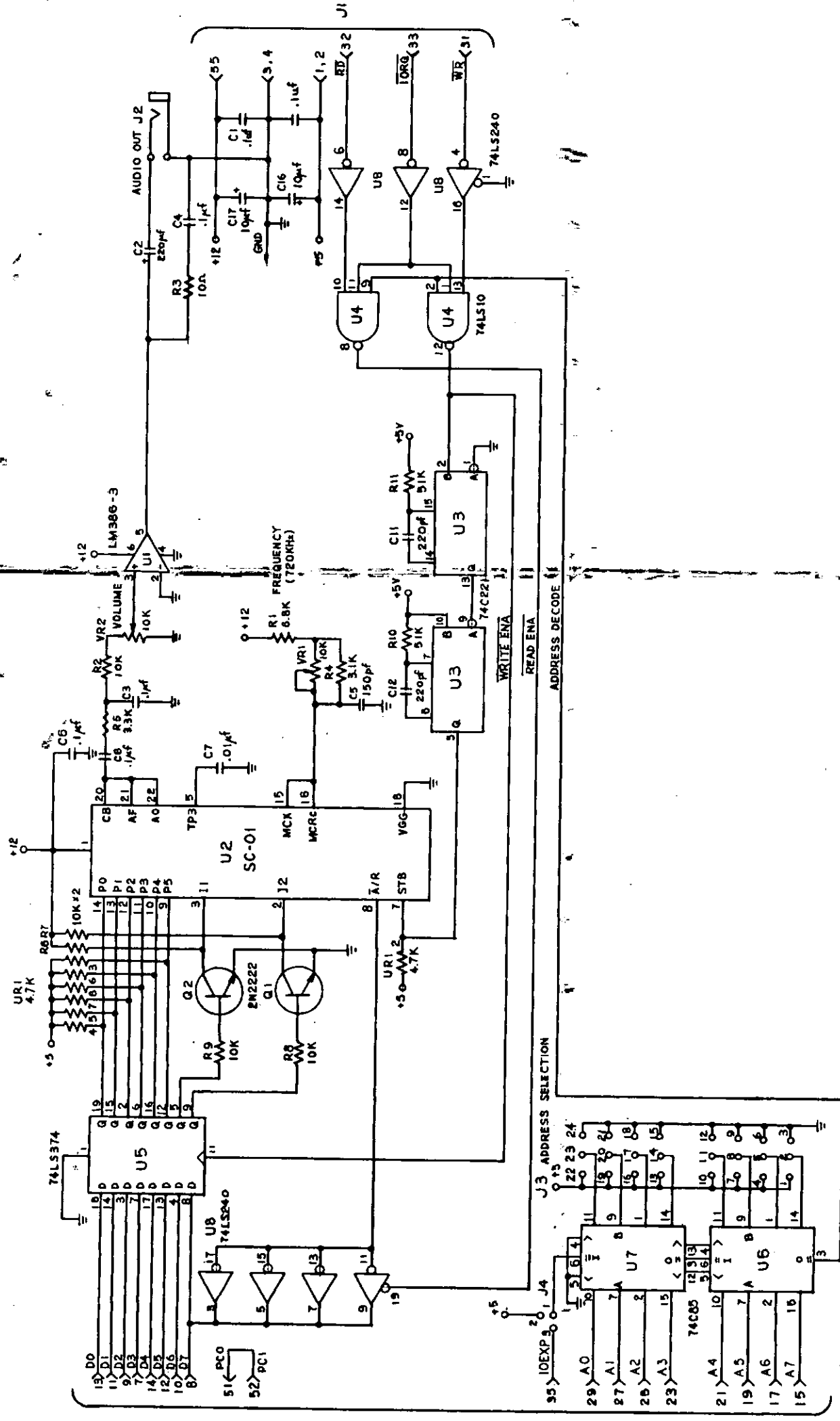
STD Bus 56 pin dual readout; 0.125 inch centers

Audio Output Standard miniature phone jack

**ORDERING INFORMATION**

<u>ITEM</u>	<u>DESCRIPTION</u>
STD-WORDS	STD Bus Phoneme Speech Synthesizer Board
STM-WORDS	Technical Manual for STD-WORDS board

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<b>COLEX</b> ELECTRONIC CO., LTD.	
TITLE STD - WORDS	
DRAWN 08/25/83	DRAWING NO. 07-02-001 REVA
CHECKED	PROJECT/EXT ASSY. NO.
APPROVED	DATE SCALE SHEET 1 OF 1

## COLEX ELECTRONIC CO., LTD. PARTS LIST FOR STD-WORDS SHEET

RUN DATE: 02DEC83

PAGE: 1-

PART CODE	FACTOR	UNIT	DESCRIPTION	LOCATIONS
01-02-000	1	PC	PCB STD-WORDS	
01-02-105	1	PC	IC 74LS10 TRIPLE 3-INPUT NAND	U4
01-02-130	1	PC	IC 74LS221 DUAL ONE SHOT	U3
01-02-131	1	PC	IC 74LS240 OCTAL INVERTER	U8
01-02-138	1	PC	IC 74LS374 OCTAL LATCH	U5
01-02-221	1	PC	IC CMOS SC-01 VOTRAX SPEECH	U2
01-02-227	2	PC	IC TTL 74C85	U6,7
01-02-228	1	PC	IC LIN LM386-3	U1
01-02-303	2	PC	CAP 10 UF AL EL AXIAL 0.6" SPC	C16-17
01-02-305	10	PC	CAP 0.1 UF MYLAR 0.2 IN.	C1,3,4,6,8-10,13-15
01-02-312	1	PC	CAP 220 UF 15V AXIAL 0.8 IN. S	C2
01-02-313	1	PC	CAP 150 PF MICA 100V 0.256" SP	C5
01-02-314	2	PC	CAP 220 PF MICA 100V 0.256" SP	C11-12
01-02-315	1	PC	CAP 0.01 UF 0.2" SPC	C7
01-02-324	2	PC	TRANSISTOR 2N2222	Q1-2
01-02-408	1	PC	RES 3K OHM 1/4 WATT 5%	R4
01-02-409	1	PC	RES 3.3K OHM 1/4 WATT 5%	R5
01-02-411	2	PC	RES 5.1K OHM 1/4 WATT 5%	R10,11
01-02-413	1	PC	RES 6.8K OHM 1/4 WATT 5%	R1
01-02-414	5	PC	RES 10K OHM 1/4 WATT 5%	R2,6,7,8,9
01-02-423	1	PC	RES 1K OHM 8 PIN SIP BOSSED	VR1
01-02-435	2	PC	RES 10K OHM ONE TURN LIN POT T	VR1,2
01-02-436	1	PC	RES 100 OHM 1/4 WATT 5%	R3
01-02-501	1	PC	CARD EJECTOR	
01-02-508	1	PC	SOCKET 22 PIN ST LP 0.6" WIDE	X2
01-02-516	1	PC	CONN MIN AUDIO JACK	J2
01-02-602	1	PC	HEADER 0.1" 1 BY 2 ST.	J4
01-02-609	1	PC	HEADER 0.1" 2 BY 8 ST.	J3
01-02-628	1	PC	HEADER 0.1" 1 BY 8 ST.	J3
01-02-629	1	PC	HEADER 0.1" 1 BY 1 ST.	J4

SIZE: A4 CODE: PL NUMBER: 07-02-010 REV: D

# GENERAL DESCRIPTION OF THE SC-01 CHIP

The SC-01 Speech Synthesizer is a completely self-contained solid state device. This single chip phonetically synthesizes continuous speech, of unlimited vocabulary, from low data rate inputs.

Speech is synthesized by combining phonemes (the building blocks of speech) in the appropriate sequence. The SC-01 Speech Synthesizer contains 64 different phonemes which are accessed by a 6-bit code. It is the proper sequential combination of these phoneme codes that creates continuous speech.

## PHONEME DESCRIPTION

Table 1 lists the 64 phonemes produced by the SC-01. Each sound is represented by its VOTRAX' phoneme code and is accompanied by its phoneme symbol and an example. The underlined segments of the example word demonstrate the phoneme use, i.e., sound to be pronounced.

Table 2 provides the phoneme sequences used to produce vowels in the group called diphthongs, (2 vowel sounds in sequence, identified as a single sound, e.g., the long "i" vowel).

A - BEGIN

Phonetic Programs	Phoneme	Phoneme
A	A1, AV, Y	UH1, P, R, AH1, K, PAO, S, EH3, M, I3, T
A-2	UH2, UH3	UH1, P, R, AH1, K, PAO, S, EH3, M, A2, Y, T
able	A1, Y, B, UH3, L	A1, Y, P, R, UH2, L
about	UH1, B, O2, O2, R, T	AH1, R, K, UH2, T, EH3, EH2, K, T
above	UH1, B, UH1, UH3, V	(see "R" program)
accept	EH1, K, PAO, S, EH1, EH3, P, T	EH1, EH3, R, Y, UH1
access	AE1, EH3, K, PAO, S, EH1, EH3, S	UH1, R, AH1, EH3, Y, V
account	UH1, K, AH1, UH3, W, N, T	EH1, EH3, R, O1, U1
acid	AE1, EH3, S, I1, D	AH1, R, T, EH3, K, UH3, L
act	AE1, EH3, K, T	AE1, EH3, Z
active	AE1, EH3, K, T, I1, V	AE1, EH3, S, K, Y
actual	AE1, EH3, K, T, CH, U1, UH3, L	UH1, S, EH1, EH3, M, B.
add	AE1, EH3, D	UH1, S, AH1, EH3, Y, N
address	AE1, EH3, D, R, EH1, EH3, S	UH1, S, I1, I3, S, T
ade	(use "ud" program)	UH1, S, O1, SH, Y, A1, Y, T
adjust	UH1, O, J, UH1, UH3, S, T	UH1, S, O1, SH, Y, I2, T
adjuvant	UH1, O, J, A1, AV, S, EH3, N, T	UH1, S, I1, U1, M
advance	AE1, EH3, D, V, AE1, EH3, N, T, S	AE1, EH3, T
advise	AE1, EH3, D, V, AH1, EH3, Y, Z	(see "right" program)
affect	UH1, F, EH1, EH3, K, T	UH1, T, AE1, EH3, T, CH
after	AE1, EH3, F, T, ER	UH1, T, EH1, EH3, M, P, T
again	UH1, G, A2, EH1, N	UH1, T, EH1, EH3, N, D
age	A1, AV, Y, D, J	AW, D, Y, O1, U1
agent	A1, Y, D, J, EH3, N, T	UH1, M, EH1, EH3, D
ahead	UH1, M, EH1, EH3, D	A1, AV, Y, D
aid	AH2, EH2, R	UH1, L, AH1, R, M
air	UH1, L, ER, R, T	UH1, L, ER, R, T
alarm	UH1, L, AH1, R, M	AW, L
alert	UH1, L, ER, R, T	AE1, UH3, L, UH2, K, A1, Y, T
all	AW, L	UH1, L, AH1, UH3, U1
allocate	AE1, UH3, L, UH2, K, A1, Y, T	AE1, AW2, L, F, UH1
allow	UH1, L, AH1, UH3, U1	AW, L, R, EH1, EH3, D, Y
alpha	AE1, AW2, L, F, UH1	AW, L, S, O1, U1
already	AW, L, R, EH1, EH3, D, Y	AE1, UH3, L, T, I2, T, I1, U1, U1, D
also	AW, L, S, O1, U1	UH1, L, U1, U1, M, I3, N, UH1, M
altitude	AE1, UH3, L, T, I2, T, I1, U1, U1, D	AE1, EH3, M
aluminum	UH1, L, U1, U1, M, I3, N, UH1, M	UH1, M, EH1, R, I3, K, UH2
am	AE1, EH3, M	UH3
america	UH1, M, EH1, R, I3, K, UH2	UH1, M, AH1, UH3, W, N, T
amount	UH3	AE1, EH3, M, P
amp	UH1, M, AH1, UH3, W, N, T	AE1, EH3, M, P, L, I3, F, AH1
an	AE1, EH3, M, P	EH3, AV
and	AE1, EH3, M, P, L, I3, F, AH1	AE1, EH3, N
angle	EH3, AV	AE1, EH3, N, D
another	AE1, EH3, N	AE1, EH3, NG, G, UH3, L
answer	UH1, N, UH1, UH3, THV, ER	UH1, N, UH1, UH3, THV, ER
any	AE1, EH3, N, S, ER	AE1, EH3, N, S, ER
apostrophe	EH2, EH2, N, Y	UH1, P, AH1, UH3, S, T, R
approach	UH3, F, Y	UH3, F, Y
approve	UH1, P, R, O1, U1, T, CH	UH1, P, R, O1, U1, T, CH
	UH1, P, R, I1, U1, U1, V	UH1, P, R, I1, U1, U1, V
approximate	UH1, P, R, AH1, K, PAO, S, EH3, M, I3, T	UH1, P, R, AH1, K, PAO, S, EH3, M, I3, T
approximate-2	UH1, P, R, AH1, K, PAO, S, EH3, M, A2, Y, T	UH1, P, R, AH1, K, PAO, S, EH3, M, A2, Y, T
apt	A1, Y, P, R, UH2, L	A1, Y, P, R, UH2, L
architect	AH1, R, K, UH2, T, EH3, EH2, K, T	AH1, R, K, UH2, T, EH3, EH2, K, T
are	(see "R" program)	(see "R" program)
area	EH1, EH3, R, Y, UH1	EH1, EH3, R, Y, UH1
arrow	UH1, R, AH1, EH3, Y, V	UH1, R, AH1, EH3, Y, V
arrow	EH1, EH3, R, O1, U1	EH1, EH3, R, O1, U1
arrows	AH1, R, T, EH3, K, UH3, L	AH1, R, T, EH3, K, UH3, L
ASCII	AE1, EH3, Z	AE1, EH3, Z
ask	AE1, EH3, S, K, Y	AE1, EH3, S, K, Y
ask	UH1, S, EH1, EH3, M, B.	UH1, S, EH1, EH3, M, B.
assemble	UH1, S, AH1, EH3, Y, N	UH1, S, AH1, EH3, Y, N
asset	UH1, S, I1, I3, S, T	UH1, S, I1, I3, S, T
assist	UH1, S, O1, SH, Y, A1, Y, T	UH1, S, O1, SH, Y, A1, Y, T
associate	UH1, S, O1, SH, Y, I2, T	UH1, S, O1, SH, Y, I2, T
associate-2	UH1, S, I1, U1, M	UH1, S, I1, U1, M
assume	AE1, EH3, T	AE1, EH3, T
at	(see "right" program)	(see "right" program)
attach	UH1, T, AE1, EH3, T, CH	UH1, T, AE1, EH3, T, CH
attempt	UH1, T, EH1, EH3, M, P, T	UH1, T, EH1, EH3, M, P, T
attend	UH1, T, EH1, EH3, N, D	UH1, T, EH1, EH3, N, D
audio	AW, D, Y, O1, U1	AW, D, Y, O1, U1
adjust	AW2, AW2, G, EH2, S, T	AW2, AW2, G, EH2, S, T
authorize	AW2, AW2, TH, ER, AH1, Y, Z	AW2, AW2, TH, ER, AH1, Y, Z
automatic	AW2, AW2, OT, UH3, M, AE1, EH3, OT, I3, K	AW2, AW2, OT, UH3, M, AE1, EH3, OT, I3, K
available	UH1, V, A1, Y, L, UH3, B, UH3, L	UH1, V, A1, Y, L, UH3, B, UH3, L
average	AE1, EH3, V, R, I1, D, J	AE1, EH3, V, R, I1, D, J
avoid	UH1, V, O1, UH3, I3, AV, D	UH1, V, O1, UH3, I3, AV, D
back	B, E1, Y	B, E1, Y
back	B, AE1, AE1, K	B, AE1, AE1, K
bad	B, AE1, AE1, D	B, AE1, AE1, D
bad	B, AE1, AE1, D, J	B, AE1, AE1, D, J
badge	B, AE1, AE1, G	B, AE1, AE1, G
bag	B, AE1, AH2, L, I3, N, DT, S	B, AE1, AH2, L, I3, N, DT, S
balance	B, AW2, AH1, L	B, AW2, AH1, L
ball	B, AE1, EH3, N, D	B, AE1, EH3, N, D
band	B, AE1, I3, NG, K	B, AE1, I3, NG, K
band	B, AH1, UH3, R	B, AH1, UH3, R
bank	B, A1, AV, Y, S	B, A1, AV, Y, S
bar	B, A1, Y, S, I2, K	B, A1, Y, S, I2, K
base	B, AE1, EH3, T	B, AE1, EH3, T
base	B, AE1, EH3, T, CH	B, AE1, EH3, T, CH
batch	B, AE1, AE1, EH3, TH	B, AE1, AE1, EH3, TH
batch	B, AE1, EH3, T, ER, Y	B, AE1, EH3, T, ER, Y
battery	(use "B" program)	(use "B" program)
be	B, EH1, EH3, O	B, EH1, EH3, O
bed	B, EH1, EH3, N	B, EH1, EH3, N
been	B, E1, Y, P	B, E1, Y, P
been	B, Y, F, O2, O2, R	B, Y, F, O2, O2, R
before	B, Y, G, I1, I3, N	B, Y, G, I1, I3, N
begin		



BELL COIN

bell	B, EH1, UH3, L	C	S, E1, Y
believe	B, Y, L, UH3, O2, U1	candle	K, A1, Y, B, UH3, L
belong	B, EH1, EH3, N, D	calendar	K, O01, UH3, L, I3, N, D, ER
best	B, EH1, EH3, S, T	calibrate	K, AE1, UH3, L, UH3, B, R, A1, Y, T
beta	B, A2, A2, AY, T, UH2	call	K, AW2, AW1, L
better	B, EH1, EH3, T, ER	came	K, A1, AY, Y, M
between	B, Y, T, W, E1, Y, N	can	K, AE1, EH3, N
bhd	B, I1, I3, D	cancel	K, A1, Y, P, UH3, B, UH3, L
bld	B, I1, I3, G	capable	K, A1, Y, P, AE1, EH3, S, EH3, T, ER
blt	B, I1, I3, L	capacitor	K, UH2, P, AE1, EH3, S, I3, T, ER
bltton	B, I1, I3, L, Y, UH3, N	capacity	K, UH2, P, AE1, EH3, S, I3, DT, Y
bnt	B, AH1, Y, N, EH3, EH3, ER, Y	car	K, AH1, UH3, R
bnry	B, ER, R, TH, D, A1, I3, Y	card	K, AH1, R, D
brd	B, I1, I3, T	care	K, EH3, EH3, ER
bte	B, UH3, AW2, Y, T	carpenter	K, AH1, R, P, I3, N, D, ER
black	B, L, AE1, EH3, K	carrage	K, EH2, EH3, R, I1, D, J
black	B, L, AE1, EH3, NG, K	carry	K, EH2, EH3, R, Y
blew	(use "blue" program)	carton	K, AH1, R, T, I3, N
blnd	B, L, AH1, EH3, Y, N, D	case	K, A1, AY, Y, S
block	B, L, AH1, UH3, K	casette	K, AE1, EH3, SH
blown	B, L, O1, U1, N	casette-2	K, UH1, S, EH1, EH3, T
blue	B, L, U1, U1, U1	category	K, A2, AY, S, EH1, EH2, T
blur	B, L, ER, R	catalog	K, AE1, EH3, DT, UH3, G, O1, R, Y
board	B, O1, O2, R, D	caution	AW2, AW2, G
board	B, O2, O2, L, T	caution	K, AW2, AW1, SH, UH3, N
board	B, AH1, UH3, N, D	cent	S, EH1, EH3, N, T, ER
book	B, O01, O01, K	cent	S, EH1, EH3, N, T, I1, I3
booked	(use "board" program)	centigrade	S, EH1, N, T, I3, G, R, A1, Y, D
boss	B, AW1, AW2, S	change	T, CH, A1, AY, Y, N, D, J
bottom	B, AH1, UH3, THV, ER	change	K, EH1, R, EH1, K, T, ER
bottom	B, AH1, UH3, T, UH1, M	character	T, CH, AH1, R, D, J
bought	B, AW1, AW2, T	charlie	T, CH, AH1, R, L, Y
box	B, AH1, UH3, K, PAO, S	chart	T, CH, AH1, R, T
brace	B, R, A1, Y, S	check	T, CH, AH1, EH3, K
brain	B, R, A1, Y, N	cheer	T, CH, AY, I2, R
brake	B, R, A1, Y, K	chico	T, CH, I1, I3, P
branch	B, R, AE1, EH3, N, T, CH	choice	T, CH, O1, UH3, I3, AV, S
bravo	B, R, AH1, UH3, V, O1, U1	circle	S, ER, R, K, UH3, L
break	(use "brake" program)	circuit	S, I1, T, Y
break	B, R, I1, I3, D, J	claim	K, L, A1, AY, Y, M
bridge	B, R, AV, Y, F	claim	K, L, AE1, EH3, S
brld	B, R, UH3, AH2, Y, T	class	K, L, AY, AV, N
bright	B, R, I1, I3, NG	clean	K, L, AY, I3, R
bring	B, R, O1, U1, K	clear	K, L, ER, K
broke	B, R, AW, T	clerk	K, L, I1, I3, P
brought	B, R, AH1, UH3, U1, N	clock	K, L, AH1, UH3, K
brown	B, UH1, UH2, B, UH3, L	close	K, L, UH3, O2, U1, 2
bubble	B, UH1, UH2, G	close-2	K, L, UH3, O2, U1, S
bug	B, UH1, UH2, G	cloud	K, L, AH1, UH3, W, D
bugget	B, I2, I2, L, D	course	K, O1, O2, R, S
buld	B, UH1, UH2, S	code	K, O01, O2, U1, D
bullet	B, I3, I3, Z, N, EH2, S	code	K, O1, UH3, I3, AV, N
business	B, I3, I2, Z, Y	code	K, O1, UH3, I3, AV, N
buuy	B, UH1, UH2, T	code	K, O1, UH3, I3, AV, N
but	B, UH1, UH3, T, EH3, N	code	K, O1, UH3, I3, AV, N
buton	B, AH1, EH3, I3, Y	code	K, O1, UH3, I3, AV, N
buy	B, AH1, EH3, I3, Y	code	K, O1, UH3, I3, AV, N
by	B, AH1, EH3, I3, Y	code	K, O1, UH3, I3, AV, N
bze	(use "bze" program)	code	K, O1, UH3, I3, AV, N
bye	(use "bze" program)	code	K, O1, UH3, I3, AV, N

COLLAR DEMAND

collar	K, AH1, UH3, L, ER	course	K, O1, O2, R, S
collect	K, UH1, L, EH1, K, T	court	K, O1, UH3, R, T
color	K, O01, O2, U1, L, I2, N	cover	K, UH1, EH3, V, ER
color	K, UH2, UH2, L, ER	crane	K, R, A1, AY, Y, N
column	K, AH1, UH3, L, UH3, M	crash	K, R, AE1, EH3, SH
combine	K, UH2, M, B, AH1, EH3, Y, N	create	K, R, E1, Y, S
comma	K, AH1, UH3, M, UH1	create	K, R, Y, A1, Y, T
command	K, UH2, M, AE, EH3, N, D	creation	K, R, Y, A1, Y, SH, UH3, N
commerce	K, AH1, UH3, M, ER, S	credit	K, R, EH1, EH3, O, I1, T
commercial	K, UH1, UH3, M, ER, SH, UH3, L	crew	K, R, U1, U1, U1
communicate	K, UH2, M, Y1, U1, N, I3, K	critical	K, R, I1, T, I3, K, UH3, L
com	A1, Y, T	cross	K, R, AW, S
comany	K, UH1, UH3, M, P, EH3, N, Y	crowd	K, R, AH1, UH3, U1, D
compare	K, UH1, UH3, M, P, EH3, EH3, ER	cry	K, R, AH1, EH3, I3, Y
comple	K, UH1, UH3, M, P, AH1, EH3, I3, UH3, L	cud	(use "O" program)
complete	K, UH1, UH3, M, P, L, AY, Y, T	cur	K, UH1, UH2, P
comply	K, UH1, UH3, M, P, L, AH1, EH3, Y	curious	K, Y, ER, Y, UH1, S
component	K, UH2, M, P, O2, O1, N, EH2, N, T	current	K, ER, R, EH3, N, T
computer	K, UH1, M, P, Y1, U1, T, ER	currency	K, ER, R, I2, N, DT, S, Y
concal	K, UH1, N, S, E1, AY, L	curse	K, ER, R, S
condense	K, UH1, N, O, EH1, EH3, N, S	curve	K, ER, R, V
condition	K, UH1, N, O, I1, I3, SH, UH3, N	customer	K, UH1, UH2, S, T, UH1, M, ER
confirm	K, UH1, N, F, ER, R, M	cut	K, UH1, UH2, T
conituse	K, UH1, N, F, Y1, U1, U1, Z	cycle	S, UH3, AH2, Y, K, UH3, L
confusion	K, UH1, N, F, Y1, U1, U1, U1, ZH, UH3, N	D	
congrat	K, UH1, N, G, R, AE1, D, J	D	O, E1, Y
ulations	UH3, L, A1, AV, SH, UH3, N, Z	daily	O, A1, AV, Y, L, Y
connect	K, UH1, N, EH1, EH3, K, T	damage	O, AE1, EH3, M, I1, D, J
console	K, AH1, UH3, N, S, O1, U1, L	dangr	O, A1, AV, Y, N, D, J, ER
console-2	K, UH1, N, S, O1, O2, L	dash	O, AH1, R, K
consult	K, UH1, N, S, UH1, UH2, L, T	dash	O, AE1, EH3, SH
consume	K, UH1, N, S, U1, U1, M	data	O, A1, Y, DT, UH1
constin	K, UH3, UH3, N, T, A1, AV, Y, N	date	O, A1, AV, Y, T
continue	K, UH1, N, T, I1, I3, N, Y1, U1, U1	day	O, A1, I3, Y
contract	K, AH1, UH3, N, T, R, AE1, EH3, K, T	dead	O, EH1, EH3, F
contract	K, AH1, UH3, N, T, R, AE1, EH3, S, T	dealer	O, E1, AV, L, ER
contrast	K, UH1, N, T, R, O1, O2, L	dear	O, AV, I3, R
control	K, UH2, N, V, E1, N, AV, EH3, N, T	debit	O, EH1, EH3, B, I2, T
convenient	K, AH1, UH3, P, ER	decide	O, EH1, EH3, T
copy	K, AH1, UH3, P, Y	decide	O, Y, S, AH1, EH3, Y, D
correct	K, O2, O2, R, EH1, EH3, K, T	decidnd	O, Y, S, M, UH3, L
correspond	K, O1, R, I3, S, P, AH1, AH2, N, D	decision	O, Y, S, I1, ZH, UH3, N
cosine	K, O1, U1, S, AH1, Y, N	decline	O, Y, K, L, AH1, EH3, Y, N
cost	K, AW2, AW1, S, T	decrease	O, Y, K, R, E1, Y, S
could	K, U1, U1, O01, D	deduct	O, Y, O, UH1, UH2, K, T
count	K, AH1, UH3, W, N, T	deer	(use "dear" program)
country	K, UH1, N, T, R, Y	defeat	O, Y, F, E1, AV, T
couple	K, UH3, UH1, P, UH3, L	defend	O, Y, F, EH1, EH3, N, D
courage	K, ER, R, I3, D, J	defensive	O, Y, F, EH1, EH3, N, S, I1, Y
		defer	O, E1, F, ER, R
		deficit	O, E1, Y, P
		degree	O, Y, G, R, E1, Y
		delay	O, I1, L, EH3, A1, Y
		delete	O, E1, L, E1, Y, T
		deliver	O, Y, L, I1, V, ER
		deltis	O, EH2, EH3, L, T, UH1
		demand	O, Y, M, AE1, EH3, N, D

EXAMINE - GAP

examine	EH1, EH3, G, PAO, Z, AE1, EH3, M, 11, N	length	F, 11, N, 11, SM
exceed	EH1, EH3, K, PAO, S, E1, Y, D	fire	F, AH1, EH3, AV, R
excess	EH1, EH3, K, PAO, S, EH1, EH3, P, T	first	F, ER, R, S, T
exchange	EH1, EH3, K, PAO, S, T, CH, A1, AV, Y, N, D, J	five	F, AH1, EH3, Y, V
execute	EH1, EH3, K, PAO, S, UH3, K, Y1, IU, U1, T	fix	F, 11, 13, K, PAO, S
exempt	EH1, EH3, G, PAO, Z, EH1, EH3, M, P, T	fixture	F, 11, 13, K, PAO, S, T, CH, ER
exit	EH1, EH3, G, PAO, Z, 11, 13, T	flight	F, L, AE1, EH3, T
expect	EH1, EH3, K, PAO, S, P, EH1, EH3, K, T	float	F, L, UH3, AH2, Y, T
expedite	EH1, EH3, K, PAO, S, P, EH1, EH3, D, UH3, AH2, Y, T	flip	F, L, 11, 13, P
expand	EH1, EH3, K, PAO, S, P, EH1, EH3, N, D	floor	F, L, O1, O2, R
experiment	EH1, K, PAO, S, P, EH1, R, UH3, M, EH3, N, T	floor	F, L, AH1, UH3, P
exponent	EH1, K, PAO, S, P, O2, O2, N, EH1, EH3, N, T	flow	F, L, O1, U1
express	EH1, EH3, K, PAO, S, P, R, EH1, S	fold	F, L, AH1, EH3, Y
extension	EH1, EH3, K, PAO, S, T, EH1, EH3, N, SH, UH3, N	follow	F, O2, O2, L, L, D
face	EH1, EH2, F	food	F, AH1, AW2, L, O1, U1
facility	F, A1, AV, Y, S	foot	F, O01, O01, T
fact	F, UH2, S, 11, L, 13, T, Y	for	(use "four" program)
fahrenheit	F, AE1, EH3, K, T	force	(use "four" program)
fail	F, AH1, R, 12, N, H, UH3, AH2, Y, T	for	F, O2, O2, R, S
fall	F, A1, AV, 13, UH3, L	fore	F, O2, O2, R, M, EH2, N
false	F, AW, L, S	foreman	F, O2, O2, R, G, EH1, EH3, T
familiar	F, UH1, M, 11, L, Y1, ER	forgot	F, O2, O2, R, G, 11, 13, Y
far	F, AH1, UH3, R	forgot	F, O2, O2, R, M, AE1, EH3, T
farad	F, EH3, EH3, ER, AE1, EH3, D	format	F, O2, O2, R, W, ER, D
fast	F, AE1, EH3, S, T	format	F, AH1, UH3, W, N, D
fault	F, AW, L, T	four	F, O1, O2, R, TH
feature	(use "feat" program)	fox	F, AH1, UH3, K, PAO, S, T, R, AH1, UH3, T
february	F, E1, AV, T, CH, ER	frame	F, R, A1, AV, Y, M
federal	F, EH1, B, Y1, IU, W, EH1, R, Y	fray	F, R, AW, D
fee	F, EH1, EH3, D, R, UH3, L	free	F, R, E1, Y
feed	F, E1, Y, D	frequency	F, R, EH1, EH3, N, T, CH
feet	F, E1, Y, T	frequent	F, R, E1, K, W, EH3, N, DT, S, Y
femur	F, AV, Y, M, A1, AV, UH3, L	Friday	F, R, E1, K, W, EH3, N, DT, S, Y
field	F, E1, AV, UH3, L, D	fuel	F, R, UH3, AH2, Y, T
fifteen	F, 11, 13, F, T, E1, Y, N	fuel	F, R, UH3, UH3, M
film	F, 11, 13, F, T, Y	hull	F, R, UH3, UH1, N, T
final	F, AH1, EH3, 13, UH3, L	hull	F, R, IU, U1, T
finance	F, AH1, Y, N, UH3, L	hull	F, Y1, IU, U1, UH3, L
find	F, AH1, EH3, Y, N, D	hull	F, O01, L
finger	F, 11, 13, NG, G, ER	function	F, UH1, UH2, N, K, SH, UH3, N

DEMONSTRATE - EXACT

demonstrate	D, EH1, M, UH3, N, S, T, R, A1, Y, T	during	D, ER, R, 11, NG
demy	D, Y, N, AH1, EH3, Y	duty	D, IU, U1, U1, T, Y
destroy	D, E, T, ER, O1, UH3, 13, AV	dwell	D, W, EH1, EH3, L
detail	D, Y, T, ER, M, 11, N	E	E1, Y
determine	D, Y, V, UH3, AH2, Y, S	each	E1, AV, T, CH
device	(use "do" program)	ear	E1, 12, R
dew	D, AH1, AV, 13, G, N, AH1, UH3, S, T, 13, K	earn	ER, R, L, Y
diagnostic	D, AH1, AV, 13, G, N, AH1, UH3, S, T, 13, K	earn	ER, R, N
diel	D, AH1, EH3, 13, UH3, L	easy	E1, AV, S, T
dictionary	D, 11, 13, K, SH, UH3, N, EH3, EH3, ER, Y	echo	E1, AV, Z, Y
did	D, 11, 13, D	edge	EH1, EH3, D, J, T
die	D, AH1, EH3, Y	edit	EH1, EH3, D, 12, T
diet	D, AH1, EH3, AV, 12, T	educate	EH1, D, J, U1, K, A1, Y, T
differ	D, 11, 13, F, ER	effect	UH1, F, EH1, EH3, K, T
difference	D, 11, F, R, EH3, N, DT, S	efficient	E1, F, 11, SH, EH3, N, T
different	D, 11, F, R, EH3, N, T	effort	A2, EH3, F, ER, T
digital	D, 11, D, J, 11, T	eight	A2, A2, Y, T
dine	D, 11, D, J, 13, T, UH3, L	eight	A2, A2, Y, DT, DT, TH
direct	D, AH1, EH3, Y, M	eighty	A2, A2, Y, T, Y
directory	D, AH1, EH3, AV, O1, U1, D	either	E1, Y, THV, ER
dirty	D, ER, EH1, EH3, K, T, ER, Y	electric	EH3, L, SH1, K, T, R, 12, K
disagree	D, ER, R, T	electrician	EH3, L, EH1, K, PAO, T, R, 11, SH, UH3, N
disappear	D, 11, S, UH1, G, R, E1, Y	electronic	EH3, L, EH1, K, T, R, AH1, N, 12, K
disconnect	D, 11, S, UH1, P, AV, 13, R	elevator	EH1, L, UH3, V, A2, AV, D, ER
discuss	D, 11, S, K, UH1, N, EH1, EH3, K, T	eleven	EH1, L, EH1, EH3, V, 12, N
dark	D, 11, 13, S, K, UH1, UH2, S	enclave	EH1, L, UH3, D, J, EH3, B, UH3, L
dashy	D, 11, 13, S, K	eliminate	EH1, L, 11, M, 11, N, A1, Y, T
distance	D, 11, 13, S, P, L, A1, 13, Y	else	EH1, EH3, L, S
divide	D, 11, S, T, EH3, N, T, S	emit	Y, M, 11, 13, T
dividend	D, 11, V, AH1, EH3, Y, D	employ	EH1, EH3, M, P, L, O1, UH3, 13, AV
division	D, 11, V, 11, D, EH1, EH3, N, D	empty	EH1, EH3, M, P, T, Y
do	D, 11, V, 11, ZH, UH3, N	enable	EH1, N, A1, Y, B, UH3, L
doct	D, IU, U1, U1	enclose	EH1, EH3, N, K, L, O1, U1, Z
doctor	D, AH1, UH3, K, T, ER	engine	EH1, EH3, N, D, J, 11, N
document	D, AH1, K, Y1, UH3, M, EH3, N, T	engineer	EH1, N, D, J, 12, N, AV, 11, R
doze	D, UH2, UH1, Z	english	EH1, EH3, N, D, O2, O2, R, S
dollar	D, AH1, UH3, L, ER	enter	11, NG, G, L, 12, SH
done	D, UH1, UH3, N	entry	EH1, EH3, N, T, ER
door	D, O1, O2, R	equal	EH1, EH3, N, T, R, Y
double	D, UH3, UH1, B, UH3, L	equal	Y, K, W, UH3, L
doubt	D, UH3, AH2, U1, T	equip	E1, K, W, L, P, M, EH3, N, T
down	D, AH1, UH3, U1, N	error	E1, R, A1, Y, S
draft	D, R, AE1, EH3, F, T	escape	EH3, EH3, EH3, R, ER
draw	D, R, AV	escape	EH1, EH3, S, K, A1, AV, Y, P
drill	D, R, 11, 13, L	establish	EH1, EH3, S, K, R, O1, U1
drive	D, R, 11, 13, NG, K	estimate	UH1, S, T, AE1, EH3, B, L, 12, SM
drop	D, R, AH1, EH3, Y, V	exact	EH1, EH3, S, T, A1, AV, Y, T
drum	D, R, AH1, UH2, M		EH1, S, T, EH3, M, 13, T
dry	D, R, AH1, EH3, 13, Y		EH1, EH3, G, PAO, Z, AE1,
due	(use "do" program)		EH3, K, T
dund	D, UH1, UH2, M, P		
duration	D, ER, R, A1, Y, SM, UH3, N		

GARAGE - INVALID

garage	G, UH1, R, AH1, UH3, ZH
gas	G, AE1, EH3, S
gate	G, A1, AV, Y, T
gauze	G, A1, AV, Y, D, J
general	D, J, EH1, EH3, N, ER, UH3, L
generate	D, J, EH1, N, ER, A1, Y, T
gentleman	D, J, EH1, EH3, N, T, L, M, I2, N
german	D, J, ER, R, M, EH2, N
get	G, EH1, EH3, T
girl	G, ER, R, L
give	G, I1, I3, Y
glass	G, L, AE1, EH3, S
glitch	G, L, I1, I3, T, CH
globe	G, L, O1, U1, B
go	G, OOI1, O1, U1
good	G, AW2, AW2, UH3, L, F
goose	G, OOI1, OOI1, D
government	G, UH1, UH3, V, ER, N
grade	G, R, A1, AV, Y, D
grand	G, R, AE1, EH3, M
grand	G, R, AE1, EH3, N, D
gratich	G, R, AE1, EH3, F
grate	(use "great" program)
great	G, R, A1, Y, T
green	G, R, E1, Y, N
grey	G, R, E1, Y, T
gry	G, R, A1, AV, Y
ground	G, R, AH1, EH3, Y, N, D
grocery	G, R, O1, U1, S, ER, Y
ground	G, R, AH1, UH3, W, N, D
group	G, R, U1, U1, P
grow	G, R, O1, U1
guard	G, AH1, R, D
guesstimate	G, EH1, R, I3, N, T, E1, Y
guess	G, EH1, EH3, S
H	A1, AV, Y, T, CH
had	H, AE1, EH3, D
half	H, AE1, EH3, F
hair	H, AV, L, T
hammer	H, AE1, EH3, M, ER
hand	H, AE1, EH3, N, D
handic	H, AE1, EH3, N, D, UH3, L
handic	H, AE1, I3, NG
hang	H, AE1, EH3, P, Y
hardy	H, AH1, R, D
hard	H, AE1, EH3, Z
has	H, AE1, EH3, V
have	H, E1, Y
head	H, EH1, EH3, D
hear	H, AV, I3, R
heart	H, AH1, UH3, R, T
heat	H, E1, AV, T
heavy	H, EH1, V, Y
height	H, UH3, AH2, Y, T
held	H, EH1, UH3, L, D
held	H, EH1, UH3, L, UH3, O1, U1
held	H, EH1, EH3, L, P
held	H, EH1, EH3, N, R, Y
here	H, ER
here	(use "near" program)
here	H, R, R, T, S
here	H, EH1, EH3, K, PAO, S
high	H, AH1, EH3, Y
his	H, I1, I3, Z
hold	H, O2, O2, L, L, D
hole	H, O1, U1, L
home	H, O1, U1, M
hook	H, OOI1, OOI1, K
host	H, O1, U1, S, T
hot	H, AH1, UH3, T
hotel	H, O1, U1, T, EH2, EH2, L
hour	AH1, UH3, W, ER
house	H, UH3, AH2, U1, S
how	H, AH1, O2, U1
human	H, Y1, IJ, U1, U1, M, EH2, N
hundred	H, UH1, UH2, N, D, R, I3, D
hungry	H, UH1, UH2, NG, G, R, Y
idle	AH1, EH3, I3, Y
idol	AH1, Y, D, UH3, L
I	(use "ide" program)
immediate	I1, I3, M, E1, D, Y, EH3, T
important	I1, I3, M, P, O2, O2, R, T, EH3, T
improper	I1, I3, M, P, R, AH1, UH3, P, ER
improve	I1, I3, M, P, R, IJ, U1, U1, V
in	I1, I3, N
inch	I1, I3, N, T, CH
include	I1, I3, N, K, L, IJ, U1, U1, D
income	I1, I3, N, K, UH1, UH3, M
index	I1, N, D, E1, P, EH2, EH3, N, D, EH3, N, T
index	I1, I3, N, D, EH1, EH3, K, PAO, S
indicate	I2, I3, N, D, Y, UH2
industrial	I1, I3, N, D, I3, K, A1, Y, T
inform	I1, I3, N, D, UH1, UH2, S, T, R, AV, UH3, L
inform	I1, I3, N, F, O2, O2, R, M
initial	I1, I3, N, I1, SH, UH3, L
in	(use "in" program)
input	I1, I3, N, P, OOI1, OOI1, T
inquire	I1, I3, N, K, W, AH1, EH3, AV, R
insert	I1, I3, N, S, R, R, T
inspect	I1, I3, N, S, P, EH1, EH3, K, T
install	I1, I3, N, S, T, AV, L
instead	I1, I3, N, S, T, EH1, EH3, D
instrument	I1, I3, N, S, T, R, UH1, UH2, K, T
instrument	I1, I3, N, S, T, R, UH1, M, EH1, EH3, N, T
insufficient	I1, N, S, UH2, F, I1, SH, EH3, N, T
insurance	I1, I3, N, SH, ER, R, EH3, N, T, S
interest	I1, N, T, R, EH1, S, T
interface	I1, I3, N, T, ER, F, A1, AV, Y, S
interpret	I1, I3, N, T, ER, P, R, EH3, T
interrupt	I1, N, T, ER, UH3, UH1, P, T
introduce	I1, I3, N, T, R, IJ, U1, U1, D
invalid	I1, I3, N, V, AE1, AW2, L, I1, D

INVENT - METAL

invent	I1, I3, N, V, EH1, EH3, N, T
inventory	I1, N, V, EH1, N, T, O1, R, Y
invest	I1, I3, N, V, EH1, EH3, S, T
invoice	I1, I3, N, V, O1, UH3, I3, AV, S
irregular	I1, R, EH1, G, Y1, UH3, L, ER
is	I1, I3, Z
item	I1, I3, T
J	AH2, UH3, Y, D, UH3, M
jack	D, J, EH3, A1, AV, Y
january	D, J, AE1, EH3, K
jar	D, J, AE1, EH3, N, Y1, U1, UH3, ER, Y
job	D, J, AH1, UH3, B
join	D, J, O1, UH3, I3, AV, N
join	D, J, O2, O2, L, T
join	D, J, O1, UH3, I3, AV
joy	D, J, UH1, UH2, D, J
judge	D, J, UH1, UH2, D, J
judel	D, J, IJ, U1, L, Y, EH2, EH3, T
jump	D, J, UH1, L, AH1, EH3, Y
jump	D, J, UH1, UH2, M, P
jump	D, J, IJ, U1, U1, N
line	O, J, IJ, U1, U1, N
K	EH3, A1, AV, Y
keep	K, E1, Y, P
key	K, E1, Y
keyboard	K, AV, Y, B, O1, O2, R, D
kid	K, I1, I3, L
kid	K, E1, AV, L, UH3, O2, U1
know	(use "new" program)
know	(use "not" program)
know	(use "no" program)
knowledge	N, AH1, UH3, L, I3, D, J
L	EH1, EH3, UH3, L
lab	L, AE1, EH3, B
labor	L, A1, Y, B, ER
language	L, AE1, EH3, NG, G, W, I1, D, J
large	L, AE1, EH3, P, S
large	L, AH1, R, D, J
last	L, AE1, EH3, S, T
late	L, A1, AV, Y, T
law	L, AV
law	L, E1, Y, D
lead	L, EH1, EH3, D
lead	L, EH1, EH3, F, T
left	L, EH1, EH3, G
leg	L, E1, G, UH3, L
legal	L, E1, G, UH3, D
length	L, EH1, EH3, N, D
length	L, EH1, EH3, NG, TH
less	L, EH1, EH3, S
less	L, EH1, EH3, T
letter	L, EH1, EH3, T, ER
level	L, EH1, EH3, V, UH3, L
life	L, UH3, AH2, Y, F
light	L, UH3, AH2, Y, T
like	L, UH3, AH2, Y, K
limit	L, AV, Y, M, UH1
limit	L, I1, M, I1, T
line	L, AH1, EH3, Y, N
linear	L, I2, I3, N, AV, Y, ER
link	L, I1, I3, NG, K
lip	L, I1, I3, P
liquid	L, I1, K, W, I1, D
list	L, I1, I3, S, T
listen	L, I1, I3, S, I2, N
listen	L, I1, I3, T, UH3, L
listen	L, UH3, O1, U1, D
listen	L, UH3, O1, U1, N
listen	L, O2, O2, K, UH3, L
listen	L, AH1, UH3, K
listen	L, AW, G
listen	L, AW, NG
listen	L, OOI1, OOI1, K
listen	L, AW, S
listen	L, AW, S, T
listen	L, AH1, UH3, T
listen	L, O1, U1
M	EH1, EH2, M
machine	M, UH2, SH, E1, Y, N
mail	M, A1, Y, N, T, EH2, N, EH3, N, (use "male" program)
mail	DT, S
make	M, A1, AV, Y, K
make	M, A2, A2, AV, UH3, L
make	M, AE1, EH3, N
make	M, AE1, EH3, N, I1, D, J
make	M, AE1, EH3, N, Y1, U1, UH3, L
make	M, AE1, EH3, N, Y1, U1, F
make	AE1, EH3, K, T, CH, ER
make	M, EH2, EH2, N, Y
make	M, AE1, EH3, P
make	M, AH1, R, T, CH
make	M, AH1, UH3, R, D, J, I2, N
make	M, AH1, R, K
make	M, AH1, R, K, EH3, T
make	M, AE1, EH3, T, CH
make	M, UH1, T, CH, IJ, ER
make	M, AE1, EH3, K, PAO, S, EH3, M
make	M, UH2, M
make	M, A1, I3, Y
make	M, E1, Y
make	M, EH3, EH1, ZH, ER
make	M, E1, AV, T
make	M, UH1, K, AE1, EH3, N, I3, K, UH3, L
make	M, E1, AV, D, Y, UH1
make	M, E1, O, AV, UH1, M
make	(use "meal" program)
make	M, EH1, EH3, G, UH2, UH3
make	M, EH1, EH3, M, B, ER
make	M, EH1, EH3, M, ER, Y
make	M, EH1, EH3, N
make	M, ER, T, CH, EH3, N, D, AH1, EH3, Y, Z
make	M, ER, R, D, J
make	M, EH1, EH3, S, I2, O, J
make	V, EH1, EH3, T, UH3, L



QUANTITY - SEPARATE-2

quantity	K. W. AH1, N. T. 13, T. Y	responsible	R. 12, S. P. AH1, UH3, N. OT
quart	K. W. 01, R. T	restrict	R. EH1, EH3, S. T
quarter	K. W. 01, R. T, ER	result	R. ET, S. T, R. 11, 13, K. T
quodoc	K. W. 11, B, EH1, EH3, K	resume	R. ET, Z. UH1, UH2, L. T
question	K. W. EH1, EH3, S. T, CH, UH3, N	retail	R. AV, ET, T, EH3, U1, 13, UH3, L
quick	K. W. 11, 13, K	return	R. ET, T, A1, AV, Y, N
quiet	K. W. AH1, EH3, AV, 12, T	return	R. ET, T, ER, R, N
quit	K. W. 11, 13, T	revision	R. ET, V, 11, ZH, UH3, N
quiz	K. W. 11, 13, Z	revolve	R. ET, V, AH1, UH3, L, V
quota	K. W. 01, 02, T, UH1	ribbon	R. 12, 13, B, UH3, 3
quote	K. W. 01, U1, T	right	R. UH3, AH2, Y, T
R	AH1, UH2, ER	romeo	R. 01, U1, M, Y, 01, U1
rak	R. A1, AV, 13, UH3, L	room	R. U1, U1, M
rain	R. A1, AV, Y, N	round	R. AH1, UH3, W, N, D
raise	R. A1, AV, Y, Z	route	R. UH2, AH2, U1, T
range	R. A1, AV, Y, N, D, J	row	R. 01, U1
radio	R. A1, Y, D, Y, 01, U1	run	R. UH1, UH3, N
rate	R. A1, AV, Y, T	rush	R. UH1, UH2, SH
ratio	R. A1, Y, SM, Y, 01, U1	S	EH1, EH2, S
reach	R. ET, Y, T, CH	safe	S. A1, AV, Y, F
read	R. ET, Y, D	said	(use "sale" program)
ready	R. EH1, EH3, D, Y	salary	S. AE1, AH2, L, UH3, R, Y
real	R. ET, AV, L	sale	S. A1, A2, AV, UH3, L
reason	R. ET, Y, Z, UH1, N	same	S. A1, AV, Y, M
rebate	R. ET, B, A1, Y, T	saturday	S. AE1, EH3, T, ER, D, A1, Y
recall	R. ET, K, AW2, AW1, L	save	S. A1, AV, Y, V
receive	R. ET, S, ET, Y, V	save	S. A1, 13, Y
record	R. ET, K, 02, 02, R, D	scan	S. K, AE1, EH3, N
record-2	R. EH1, EH3, K, ER, D	scan	(use "cent" program)
red	R. EH1, EH3, D	schedule	S. K, EH1, EH3, D, J, U1, U1, L
red	(use "real" program)	school	S. K, U1, U1, L
refer	R. ET, F, UH1, UH2, N, D	science	S. AH1, 13, Y, EH3, N, DT, S
refuse	R. ET, F, Y1, U1, U1, U1, Z	score	S. K, 02, 02, R
register	R. EH1, D, J, 11, S, T, ER	score	S. K, R, AE1, EH3, P
regular	R. EH1, G, Y1, U1, L, ER	scrib	S. K, R, U1, U1, U1
rent	(use "rain" program)	scrib	(use "C" program)
reject	R. ET, D, J, EH1, EH3, K, T	scrib	S. ET, AV, T
relay	R. ET, L, A1, 13, Y	second	S. EH1, EH3, K, UH1, N, D
release	R. ET, L, ET, AV, S	secret	S. ET, K, R, 13, T
remain	R. ET, M, A1, AV, Y, N	section	S. EH1, EH3, K, SM, UH3, N
remove	R. ET, M, U1, U1, V	security	S. EH1, EH3, K, Y, ER, 11, T
repair	R. ET, P, EH2, SH2, R	see	(use "C" program)
repair	R. ET, P, ET, AV, T	select	S. ET, Y, Z
replace	R. ET, P, L, A1, AV, Y, S	sell	S. UH1, L, EH1, EH2, K, T
report	R. ET, P, 02, 02, R, T	semi	S. EH1, EH3, L
represent	R. EH1, P, R, 12, Z, EH1, EH3, N, T	semicon	S. EH1, M, AH1, Y, K, 001
request	R. ET, K, W, EH1, EH3, S, T	send	O1, L, 12, N
require	R. ET, K, W, AH1, EH3, AV, R	sent	S. EH1, EH3, N, D
requestion	R. EH1, K, W, 12, Z, 11, SH, UH3, N	sentence	(use "cent" program)
rescue	R. EH1, EH3, S, K, Y1, U1, U1	separate	S. EH1, N, T, 12, N, OT, S
reschedule	R. ET, Z, EH1, EH3, M, B	separate-2	S. EH1, EH3, P, UH1, R, A1, AV, T
reset	UH3, L		S. EH1, EH3, P, R, 12, T
resistor	R. ET, S, EH1, EH3, T		
respect	R. ET, S, P, EH1, EH3, K, T		
respond	R. ET, S, P, AH1, UH3, N, D		

SEPTEMBER - SYSTEM

september	S. EH1, EH3, P, T, EH1, EH3, M, B, ER	spend	S. P, EH1, EH3, N, D
sequence	S. EH1, K, W, EH1, EH3, N, S	spoil	S. P, L, 11, 13, T
serial	S. 11, R, Y, UH3, L	spoon	S. P, U1, U1, N
series	S. 11, R, Y, Z	sprung	S. P, R, 11, 13, NG
services	S. ER, V, 11, S	square	S. K, W, EH1, R
set	S. EH1, EH3, T	stack	S. T, AE1, EH3, K
seven	S. EH1, EH3, Y, 12, N	star	(use "stare" program)
seventh	S. EH1, EH3, Y, 12, N, DT, TM	stand	S. T, AE1, EH3, N, D
seventy	S. EH1, V, 12, N, D, Y	standard	S. T, AE1, EH3, N, D, ER, D
several	S. EH1, V, ER, UH3, L	star	S. T, AH1, UH3, R
share	(use "saw" program)	stare	S. T, EH3, EH3, ER
share	SM, EH3, EH3, ER	start	S. T, AH1, R, T
sharp	SM, AH1, R, P	station	S. T, A1, AV, Y, T
shut	SM, 11, 13, F, T	status	S. T, A1, Y, SM, UH3, N
shut	SM, 11, 13, P	steel	S. T, AE1, EH3, T, 12, S
shut	SM, AH1, UH3, P	steal	(use "steel" program)
shut	SM, 02, 02, R, T	step	S. T, ET, Y, L
shut	SM, 10, 10, 10, D	stock	S. T, ET, Y, L
shut	SM, UH1, UH2, N, T	stock	S. T, 11, 13, K
shut	SM, UH1, UH2, N, T	stock	S. T, AH1, UH3, K
side	S. AH1, EH3, Y, D	stop	S. T, AH1, UH3, P
side	S. ET, 13, EH1, R, UH1	store	S. T, 02, 02, R
side	S. 11, 13, G, N, UH3, L	strat	(use "straight" program)
side	S. 11, 13, L, V, ER	straight	S. T, R, A1, AV, Y, T
side	S. 11, 13, NG, G, UH3, L	street	S. T, R, ET, Y, T
side	S. 11, 13, K, PAO, S	stress	S. T, R, EH1, EH3, S
side	S. 11, 13, K, PAO, S, TH	string	S. T, R, 11, 13, NG
side	S. 11, 13, K, PAO, T, Y	structure	S. T, R, UH1, K, T, CH, ER
side	S. AH1, EH3, Y, Z	style	S. T, AH1, EH3, AV, UH3, L
side	S. K, 11, 13, N	subject	S. UH1, UH2, B, D, J, EH1, EH3, K, T
side	S. K, AH1, EH3, 13, Y	substitute	S. UH1, UH2, B, S, T, 13, T, U1, U1, T
side	S. L, AE1, EH3, NG	subtract	S. UH1, UH2, B, T, R, AE1, EH3, K, T
side	S. L, A1, AV, Y, V	sufficient	S. UH1, F, 11, SH, EH3, N, T
side	S. L, 11, 13, P	suggest	S. UH1, UH2, G, D, J, EH1, EH3, U1, T
side	S. L, 01, U1		EH3, U1, T
side	S. M, AH1, EH3, L		S. W, AV, Y, T
side	S. M, AH1, EH3, 13, UH3, L		S. UH1, UH2, M, ER, Y
side	S. M, 01, U1, X		S. UH1, UH2, M, ER
side	S. N, 001, 02, U1		S. UH1, UH2, N
side	S. 001, 02, U1		S. UH1, UH2, N, D, A1, 13, Y
side	S. AW, F, T		S. U1, U1, P, ER
side	S. 02, 02, L, L, D		S. UH2, P, L, AH1, Y
side	S. AH1, UH3, L, 11, D		S. ER, F, 12, S
side	(use "sun" program)		S. ER, R, D, J
side	(use "sun" program)		S. ER, R, D, J
side	S. AW, R, Y		S. ER, R, D, J
side	S. 02, 02, R, T		S. ER, R, D, J
side	S. AH1, UH3, W, N, D		S. ER, P, L, UH1, S
side	S. 01, 02, R, S		S. UH1, S, P, EH1, EH3, N, D
side	S. AH1, UH3, U1, TH		S. W, ET, Y, P
side	S. P, A1, AV, Y, S		(use "suite" program)
side	S. P, AH1, R, K		S. W, 11, 13, T, CH
side	S. P, ET, AV, K		S. 11, N, T, AE1, EH3, K, PAO, S
side	S. P, EH1, EH3, SM, UH3, L		S. 11, S, T, UH3, M
side	S. P, ET, Y, D		
side	S. P, ET, Y, T, CH		
side	S. P, EH1, EH3, L		

TABLE: WEIGH

T	T, E1, AV, Y	travel	T, R, AE1, EH3, V, UH3, L
table	T, A1, Y, B, UH3, L	triangle	T, R, AE1, EH3, V, UH3, L
tail	(use "base" program)	trouble	T, R, AE1, EH3, V, UH3, L
tail	T, A1, Y, UH3, L	truck	T, R, UH1, UH2, K
tail	T, AV, K	true	T, R, UH1, UH2, K
target	T, AE1, EH3, N, D, J, EH3, N, T	trust	T, R, UH1, UH2, S, T
target	T, AH1, UH3, R, G, I2, T	try	T, R, AH1, EH3, I3, Y
tea	(use "T" program)	Tuesday	T, R, UH1, UH2, S, T
tea	T, E1, Y, M	turn	T, R, UH1, UH2, S, T
technical	T, EH1, EH3, K, N, I3, K, UH3, L	twelve	T, ER, R, N
tee	(use "T" program)	twenty	T, W, EH1, EH3, UH3, L, Y
temperature	T, EH1, EH3, M, P, ER, UH1, T	two	T, W, EH1, EH3, N, T, Y
ten	CH, ER	type	T, UH3, AH2, Y, P
terminal	T, EH1, EH3, N	U	Y1, IU, U1, U1
test	T, ER, M, EH3, N, UH2, L	ultra	UH3, UH2, L, T, R, UH1
than	TH, V, EH1, EH3, N	under	UH2, UH2, N, D, ER
than	TH, V, UH1, UH3	uniform	Y1, IU, U1, N, I3, F, O1, R, M
then	(use "than" program)	unit	UH1, UH2, P
theory	TH, AV, I2, R, Y	urgent	R, R, D, J, I3, N, T
thing	TH, I1, I3, N	use	UH1, UH2, S
think	TH, I1, I3, NG, K	use-2	Y1, IU, U1, U1, S
third	TH, ER, R, D	V	V, E1, AV, Y
thirteen	TH, ER, T, T, E1, Y, N	vacant	V, A1, Y, K, EH3, N, T
thirty	TH, ER, R, D, Y	valid	V, AE1, UH3, L, I1, D
thousand	TH, AH1, UH3, U1, Z, EH3, N, D	vary	(use "very" program)
three	TH, R, E1, Y	value	V, AE1, EH3, L, Y1, IU, U1
through	(use "through" program)	vendor	V, EH1, EH3, N, T
through	TH, R, IU, U1	very	V, EH1, R, I3, F, AH1, EH3, Y
Thursday	TH, ER, R, Z, O, A1, I3, Y	via	V, E1, AV, UH2, UH3
ticket	T, I1, I3, K, EH3, T	violate	V, I1, I3, K, T, ER
tie	T, I1, I3, L	voice	V, O1, UH3, I3, AV, S
time	T, AH1, EH3, Y, M	void	V, O2, O2, L, T
time	T, UH3, AH2, Y, T, UH3, L	volume	V, AH1, UH3, L, Y1, IU, U1, M
to	(use "two" program)	W	D, UH1, B, UH3, L, Y1, IU, U1
today	T, U1, O, A1, I3, Y	wage	W, A1, AV, Y, D, J
today	T, O1, EH3, I3, L, I3, T	wait	W, A1, AV, Y, T
today	T, O2, O2, O01, L	want	W, AH1, UH3, N, T
today	T, U1, M, AH1, R, O1, U1	was	W, UH1, UH3, Z
tomorrow	T, UH1, UH2, N, N	wash	W, AH1, SH
ton	T, O1, U1, N	water	W, AH1, UH3, T, ER
tone	(use "two" program)	wait	W, AH1, UH3, T
tool	T, U1, U1, L	was	W, A1, AV, Y, Y
tool	T, O1, U1, T, UH3, L	we	(use "weigh" program)
touch	T, UH1, UH3, T, CH	we	W, E1, Y
touch	T, AH1, W, UH3, L	weak	(use "weak" program)
trace	T, R, A1, AV, Y, S	week	W, EH2, EH2, P, UH1, N
trade	T, R, A1, AV, Y, D	weaken	(use "where" program)
trade	T, R, A1, AV, Y, N	wear	W, EH1, N, Z, D, A1, I3, Y
trans	T, R, AE1, EH3, N, S, AE1	Wednesday	W, E1, Y, X
transact	EH3, K, T	week	W, A2, A2, Y
transfer	T, R, AE1, EH3, N, S, F, ER	weigh	
transfer	T, R, AE1, N, Z, I1, S, T, ER		
transfer	T, R, AE1, EH3, N, Z, M, I1, I3, T		
transport	O2, R, T		
transport	T, R, AE1, N, S, P, ER, T, A1		
transport	AV, SH, UH3, N		

WEIGHT: ZULU

weight	(use "wait" program)	weight	(use "wait" program)
went	W, EH1, EH3, N, T	went	W, EH1, EH3, N, T
west	W, EH1, EH3, S, T	west	W, EH1, EH3, S, T
wet	W, UH3, UH1, T	what	W, UH3, UH1, T
wheel	W, E1, Y, L	wheel	W, E1, Y, L
when	W, EH1, EH3, N	when	W, EH1, EH3, N
where	W, EH3, A2, EH3, R	where	W, EH3, A2, EH3, R
which	W, I1, I3, T, CH	which	W, I1, I3, T, CH
while	W, AH1, EH3, I1, UH3, L	while	W, AH1, EH3, I1, UH3, L
whiskey	W, I1, I3, S, K, AV, Y	whiskey	W, I1, I3, S, K, AV, Y
white	W, UH3, AH2, Y, T	white	W, UH3, AH2, Y, T
who	H, IU, U1, U1	who	H, IU, U1, U1
whole	(use "hole" program)	whole	(use "hole" program)
why	(use "Y" program)	why	(use "Y" program)
will	W, I1, I3, L	will	W, I1, I3, L
window	W, I1, N, D, O1, U1	window	W, I1, N, D, O1, U1
writer	W, I1, I3, N, T, ER	writer	W, I1, I3, N, T, ER
wire	W, AH1, EH3, AV, R	wire	W, AH1, EH3, AV, R
with	W, I1, I3, TH	with	W, I1, I3, TH
without	W, I1, I3, TH, D, R, AV	without	W, I1, I3, TH, D, R, AV
without	W, I1, I3, TH, UH2, AH2, U1, T	without	W, I1, I3, TH, UH2, AH2, U1, T
word	(use "one" program)	word	(use "one" program)
work	W, ER, R, D	work	W, ER, R, D
work	W, ER, R, K	work	W, ER, R, K
write	(use "right" program)	write	(use "right" program)
wrong	R, AV, NG	wrong	R, AV, NG
X	EH1, EH2, K, PAO, S	X	EH1, EH2, K, PAO, S
z-ray	EH1, EH2, K, PAO, S, R, A1, I3, Y	z-ray	EH1, EH2, K, PAO, S, R, A1, I3, Y
Y	W, AH1, EH3, I3, Y	Y	W, AH1, EH3, I3, Y
yankee	Y1, AE1, EH3, NG, K, E1, Y	yankee	Y1, AE1, EH3, NG, K, E1, Y
yard	Y1, AH1, R, D	yard	Y1, AH1, R, D
year	Y1, AV, I3, R	year	Y1, AV, I3, R
yellow	Y1, EH1, EH3, L, O1, U1	yellow	Y1, EH1, EH3, L, O1, U1
yes	Y1, EH3, EH1, S, T, ER, D, A1, I3, Y	yes	Y1, EH3, EH1, S, T, ER, D, A1, I3, Y
yesterday	Y1, EH1, EH3, T	yesterday	Y1, EH1, EH3, T
yet	(use "U" program)	yet	(use "U" program)
you	Y, O2, O2, R	you	Y, O2, O2, R
your	(use "your" program)	your	(use "your" program)
you're		you're	
Z	Z, E1, Y	Z	Z, E1, Y
zad	Z, AE1, EH3, P	zad	Z, AE1, EH3, P
zero	Z, AV, I1, R, O1, U1	zero	Z, AV, I1, R, O1, U1
zone	Z, O1, U1, N	zone	Z, O1, U1, N
Zulu	Z, IU, U1, L, IU, U1	Zulu	Z, IU, U1, L, IU, U1

PREFIXES

Prefixed	K, UH1, N
can	O, I1, S
con	EH1, N
en	I1, N
n	N, AH1, UH3, N
non	P, R, E1
pre	R, E1
re	UH1, N
un	
Suffixes	
d	D
ed	I2, D
er	ER
as	I2, Z
lu	F, UH3, L
ng	I2, NG
less	L, EH2, S
ly	Y
ment	M, EH3, N, T
ness	N, EH3, S
s	S
tion (...ed)	SH, UH3, N
tion (...ion)	T, E1, Y, N
lean	W, ER, D
ward	Y
z (...ss)	Z