

PRINT

SIGNAL NAME

PRINT WHERE USED

DAPA

BR 00 H	DAPB(2), DAPE(2), IRCF, IRCH
BR 01 H	DAPB(2), DAPE(2), IRCE, IRCH
BR 02 H	DAPB(2), DAPE(2), IRCF, IRCH
BR 03 H	DAPB(2), DAPE(2), IRCF, IRCH
BR 04 H	DAPB(2), DAPE(2)
BR 05 H	DAPB(2), DAPE(2)
BR 06 H	DAPC(2), DAPE(2)
BR 07 H	DAPC(2), DAPD, DAPE(2)
BR 08 H	DAPC(2)
BR 09 H	DAPC(2)
BR 10 H	DAPC(2)
BR 11 H	DAPC(2)
BR 12 H	DAPD(2)
BR 13 H	DAPD(2)
BR 14 H	DAPD(2)
BR 14 L	RACK
BR 15 H	DAPD(2)

DAPB

AMX 00 H	DAPF, GRAA, IRCE(2)
AMX 01 H	DAPF
AMX 02 H	DAPF
AMX 03 H	DAPF
AMX 04 H	DAPF
AMX 05 H	DAPF
AMX S0 L	DAPB(3), DAPC(3), DAPD(2), SCCA
AMX S1 L	DAPB(3), DAPC(3), DAPD(2), SCCA
BAMX 00 H	TMCC(3), ADME, SCCA, SSRH
BAMX 01 H	ADME, SAPH, SCCA
BAMX 02 H	ADME, SAPH, SCCA
BAMX 03 H	ADME, SAPH, SCCA
BAMX 04 H	ADME, SAPH, SAPK, SCCA, SSRH
BAMX 05 H	ADME, SAPH, SCCA, SSRH
BAMX S0 L	DAPB(3), DAPC(3), DAPD(2)
BAMX S1 L	DAPB(3), DAPC(3), DAPD(2)
BMX 00 H	DAPF
BMX 01 H	DAPF
BMX 02 H	DAPF
BMX 03 H	DAPF
BMX 04 H	DAPF
BMX 05 H	DAPF
BMX S0 LOB L	DAPB(3), DAPC
BMX S1 LOB L	DAPB(3), DAPC

PRINT

SIGNAL NAME

PRINT WHERE USED

DAPC

AMX 06 H
AMX 07 H
AMX 08 H
AMX 09 H
AMX 10 H
AMX 11 H
BAMX 06 H
BAMX 07 H
BAMX 08 H
BAMX 09 H
BAMX 10 H
BAMX 11 H
BMX 06 H
BMX 07 H
BMX 08 H
BMX 09 H
BMX 10 H
BMX 11 H

DAPF
DAPF, DAPJ, IRCE(2)
DAPH
DAPH
DAPH
DAPH
TMCD, SAPH, SCCA, SSRH
IRCE, TMCD, SAPH, SCCA, SSRH
PDRC, SAPH, SCCA, SSRH
PDRC, SAPH, SCCA, SSRH
PDRC, SAPH, SCCA, SSRH
PDRC, SAPH, SCCA, SSRH
DAPF, SAPH
DAPF, SAPH
DAPH, SAPH
DAPH, SAPH
DAPH, SAPH
DAPH, SAPH

DAPD

AMX 12 H
AMX 13 H
AMX 14 H
AMX 15 H
BAMX 12 H
BAMX 13 H
BAMX 14 H
BAMX 15 H
BMX 12 H
BMX 13 H
BMX 14 H
BMX 15 H
BMX S0 HIB L
BMX S1 HIB L
EX MEM FLAG H
K0EX H
KOMX 00 BL
KOMX 00 H
KOMX 01 B L
KOMX 01 H
KOMX 02 B L
KOMX 02 H
KOMX 03 B L
KOMX 03 H

DAPH
DAPH
DAPH
DAPH, DAPJ, IRCE
PDRC, SAPH, SCCA
DAPD, PDRC, SAPH, SAPK(2), SCCA
DAPD, PDRC, SAPH, SAPK(2), SCCA
DAPD, PDRC, SAPH, SAPK(2), SCCA
DAPH, SSRH
DAPH
DAPH
DAPH, IRCE
DAPC(2), DAPD(2)
DAPC(2), DAPD(2)

DAPC(4), DAPD(4)
SSRF(3)
DAPB, DAPD
SSRF(3)
DAPB, DAPD
SSRF(2)
DAPB, DAPD
SSRF(3)
DAPB, DAPD

OSI502

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
DAPE	K1EX H	DAPC(3), DAPD(4)
	K1MX 01 H	DAPB
	K1MX 02 H	DAPB
	K1MX 03 H	DAPB
	K1MX 04 H	DAPB
	K1MX 05 H	DAPB
	K1MX 06 H	DAPC
	K1MX 07 H	DAPC
	SV 02 H	DAPE
	SV 03 H	DAPE
	SV 04 H	DAPE
	SV 05 H	DAPE
	SV 06 H	DAPE
	SV 07 H	DAPE
	TV 01 H	DAPE
	TV 02 H	DAPE
	TV 03 H	DAPE
	TV 04 H	DAPE
	TV 05 * 07 H	DAPE(2)
	TV 06 H	DAPE
DAPF	A=B(7:0) H	IRCF
	A=B(15:0) L	IRCF
	ALU 00 H	DAPF(2), DAPH, GRAH
	ALU 01 H	DAPF(3), DAPH
	ALU 02 H	DAPF(3), DAPH
	ALU 03 H	DAPF(3), DAPH
	ALU 04 H	DAPF(3), DAPH
	ALU 05 H	DAPF(3), DAPH
	ALU 06 H	DAPF(2), DAPH(2)
	ALU 07 H	DAPF, DAPH, DAPJ(3)
	COU0 03 L	DAPF
	COU0 07 L	DAPH, DAPJ
	LM H	DAPF(2)
	LS0 H	DAPF(2)
	LS1 H	DAPF(2)
	LS2 H	DAPF(2)
	LS3 H	DAPF(2)
	PCB 00 H	DAPB(2), DAPF
	PCB 01 H	DAPB(2), DAPF
	PCB 02 H	DAPB(2), DAPF
	PCB 03 H	DAPB(2), DAPF
	PCB 04 H	DAPB(2), DAPF
	PCB 05 H	DAPB(2), DAPF
	SHFR 00 H	GRAB, GRAD(3), PDRA, PDRF
	SHFR 01 H	GRAB, GRAD(3), PDRA, PDRF
	SHFR 02 H	GRAB, GRAD(3), PDRA, PDRF
	SHFR 03 H	GRAB, GRAD(3), PDRA, PDRF
	SHFR 04 H	GRAB, GRAE(3), PDRA, PDRF
	SHFR 05 H	GRAB, GRAE(3), PDRA, PDRF
	SHRF 06 H	GRAB, GRAE(3), PDRA, PDRF
	SHFR S0 B H	DAPJ(5)
	SHFR S0 H	DAPF(4), DAPH(4)
	SHFR S1 B H	DAPJ(6)
	SHFR S1 H	DAPF(4), DAPH(4)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
DAPH	ALU 08 H	DAPF, DAPH(2)	
	ALU 09 H	DAPE, DAPH(3)	
	ALU 10 H	DAPF, DAPH(3)	
	ALU 11 H	DAPF, DAPH(3)	
	ALU 12 H	DAPF, DAPH(3)	
	ALU 13 H	DAPF, DAPH(3)	
	ALU 14 H	DAPF, DAPH(3)	
	ALU 15 H	DAPH(2), DAPJ(5)	
	BUS A=B(15:8) H	DAPF, DAPJ	
	COUT 11 L	DAPH	
	COUT 15 H	DAPJ	
	COUT 15 L	DAPH, DAPJ	
	HM H	DAPH(2)	
	HSO H	DAPH(2)	
	HS1 H	DAPH(2)	
	HS2 H	DAPH(2)	
	HS3 H	DAPH(2)	
	PCB 06 H	DAPC(2), DAPF	
	PCB 07 H	DAPC(2), DAPH, DAPJ	
	PCB 08 H	DAPC(2), DAPH	
	PCB 09 H	DAPC(2), DAPH	
	PCB 10 H	DAPC(2), DAPH	
	PCB 11 H	DAPC(2), DAPH	
	PCB 12 H	DAPD(2), DAPH	
	PCB 13 H	DAPD(2), DAPH	
	PCB 14 H	DAPD(2), DAPH	
	PCB 15 H	DAPD(2), DAPJ	
	SHFR 08 H	GRAB, GRAF(3), PDRA, PDRF	
	SHFR 09 H	GRAB, GRAF(3), PDRA, PDRF	
	SHFR 10 H	GRAB, GRAF(3), PDRA, PDRF	
	SHFR 11 H	GRAB, GRAF(3), PDRA, PDRF	
	SHFR 12 H	GRAB, GRAF(3), PDRA, PDRF	
	SHFR 13 H	GRAB, GRAH(3), PDRA, PDRF	
	SHFR 14 H	GRAB, GRAH(3), PDRA, PDRF	
	DAPJ	A=B(15:8) + BYTE H	IRCF
		ALU CN 1	IRCF(2)
ALU SIGN H		IRCE(2)	
AMX SIGN H		IRCE(2)	
CLKPCA H		DAPF, DAPH(2)	
CLKPCB H		DAPF, DAPH(2)	
LEFT DATA H		GRAB, GRAD	
PCB + RTSHF 07 H		DAPJ(2)	
PCB + RTSHF 15 H		DAPJ(2)	
SHFR 07 H		GRAB, GRAE(3), PDRA	
SHFR 15 H		GRAB	
SHFRA 07 H		IRCE(2), IRCF(2), PDRF	
SHFRA 15 H		IRCE(2), IRCF(2), PDRA, PDRF	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
GRAA	ALU INSDEP L	GRAA(2), GRAJ(2)	
	ALUC H	DAPF	
	ALUM L	DAPF	
	ALUS 0 L	DAPF, DAPH	
	ALUS 1 L	DAPF, DAPH	
	ALUS 2 L	DAPF, DAPH	
	ALUS 3 L	DAPF, DAPH	
	AMXO * ASH L	IRCF	
	CDEP L	GRAA	
	CIN L	GRAA(4)	
	MODE H	GRAA	
	OB SWAP H	GRAA(2), GRAB	
	OB SWAP L	GRAA(2), IRCE	
	ROM SO H	GRAA(3)	
	ROM SO L	GRAA	
	ROM S1 H	GRAA	
	ROM S2 H	GRAA	
	ROM S3 H	GRAA	
	ROMC H	GRAA(4)	
	ROMM H	GRAA(4)	
	ROMSHFRSO H	GRAA(2)	
	ROMSHFRS1 H	GRAA(2)	
	SGNEX MOV B L	DAPD	
	SHFR SO L	DAPF(2)	
	SHFR S1 L	DAPF(2)	
	SWAP L	GRAA(2)	
	UALUO L	GRAA, IRCC	
	WORD + OB SWAP H	IRCF(2)	
	WORD + OB SWAP L	GRAB, IRCE(3), IRCF(4)	
	GRAB	DMO(MFP + MTP) COND L	GRAB, GRAC
		DST SET 1 L	GRAC(2)
		LEFT SAVE (0) H	GRAJ
		LEFT SAVE (1) H	GRAJ(4)
OBD (0) H		GRAA, RACK	
OBD (1) H		IRCB, RACL	
SRC SET 1 L		GRAC(2)	
Z DATA 2 L		IRCF	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
GRAC	ACT * IRO3 L	GRAC(2), IRCC	
	DMO(MFP + MTP) COND H	GRAC(2)	
	DRMX SEL L	GRAD, GRAE, GRAF, GRAH	
	GATE T6 H	GRAC(2), IRCH(2)	
	GD REG SET 1 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GD6L	TMCC	
	GDA 0 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GDA 1 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GDA 2 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GRA 0 L	SSRF	
	GRA 1 L	SSRF	
	GRA 2 L	SSRF	
	GRA 3 L	TMCC, SSRF	
	GRAD EN L	GRAC(4)	
	GRWE HIB L	GRAC, GRAF(2), GRAH	
	GRWE LOB L	GRAC, GRAD(2), GRAE(2)	
	GS REG SET 1 H	GRAD, GRAE, GRAF, GRAH	
	GSA 0 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GSA 1 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	GSA 2 H	GRAC, GRAD, GRAE, GRAF, GRAH	
	PAD 00 L	GRAC(2)	
	PLUS 1 L	GRAC(2)	
	SRMX SEL L	GRAD, GRAE, GRAF, GRAH	
	T6 L	GRAC(2)	
	GRAD	CLKDR H	GRAD(2), GRAE, GRAF, GRAH
		CLRDR L	GRAD, GRAE, GRAF, GRAH
		CLKSR H	GRAD, GRAE(2), GRAF, GRAH
DR 00 H		DAPB(2), GRAJ, IRCF, RACK(2)	
DR 01 H		DAPB(2), GRAJ	
DR 02 H		DAPB(2), GRAJ	
DR 03 H		DAPB(2), GRAE, GRAJ	
DRMX 00 H		GRAB, GRAD	
GS 00 L		GRAD	
GS 01 L		GRAD, GRAE	
GS 02 L		GRAD, GRAE	
GS 03 L		GRAD, GRAE	
SR 00 H		DAPB(3), GRAE(2), IRCC	
SR 01 H		DAPB(3)	
SR 02 H		DAPB(3)	
SR 03 H		DAPB(3)	
SR 04 H		DAPB(3)	
SR 05 H		DAPB(3)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
GRAE	DR 04 H	DAPB(2), GRAD, GRAJ	
	DR 05 H	DAPB(2), GRAJ	
	DR 06 H	DAPC(2)	
	DR 07 H	DAPC(2), GRAF	
	GS 04 L	GRAE(2)	
	GS 05 L	GRAE(2)	
	GS 06 L	GRAE(2)	
	GS 07 L	GRAE(2)	
	SR EQ ONE L	RACK	
	SR LEQ ZERO H	GRAJ	
	SRMX 04 H	GRAD	
	SRMX 05 H	GRAD	
	SRMX 06 H	GRAF	
	SRMX 07 H	GRAF	
	GRAF	DR 08 H	DAPC(2), GRAE
		DR 09 H	DAPC(2)
DR 10 H		DAPC(2)	
DR 11 H		DAPC(2), GRAH	
GS 08 E		GRAE, GRAF	
GS 09 L		GRAE, GRAF	
GS 10 L		GRAE, GRAF	
GS 11 L		GRAE, GRAF	
SR 06 H		DAPC(3)	
SR 07 H		DAPC(3), DAPD	
SR 08 H		DAPC(3)	
SR 09 H		DAPC(3)	
SR 10 H	DAPC(3)		
SR 11 H	DAPC(3)		
GRAH	DR 12 H	DAPD(2), GRAF	
	DR 13 H	DAPD(2)	
	DR 14 H	DAPD(2)	
	DR 15 H	DAPD(2), IRCF	
	DR 15 L	GRAA, GRAH, IRCF	
	GS 12 L	GRAE, GRAH	
	GS 13 L	GRAE, GRAH	
	GS 14 L	GRAE, GRAH	
	GS 15 L	GRAE, GRAH	
	SR 12 H	DAPD(3)	
	SR 13 H	DAPD(3)	
	SR 14 H	DAPD(3)	
	SR 15 H	DAPD(3), GRAH, GRAJ(2), RACK	
	SR 15 L	GRAE(3), GRAJ(3)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
GRAJ	DIV QUIT L	RACK	
	DIV SUB L	RACK	
	LDSC L	GRAJ(2)	
	SC=0L	RACK(2)	
	SC 00 H		
	SC 01 H		
	SC 02 H		
	SC 03 H		
	SC 04 H	GRAJ	
	SC 05 L	GRAJ(2), RACK	
	SHFR DATA H	DAPJ	
	TP(3:5)H	GRAD, GRAJ(2)	
	IRCA	IR 00(0) H	GRAB(5), IRCD(2)
		IR 00(1) H	IRCC, IRCD, IRCH(2)
		IR 01(0) H	GRAB, IRCA, IRCD
IR 01(1) H		GRAB, GRAC(2), IRCC, IRCD	
IR 01B(1) H		IRCD(2), IRCH(2)	
IR 02(0) H		GRAB, IRCD	
IR 02(1) H		GRAB, GRAC(2), IRCC, IRCD	
IR 02B(1) H		IRCD(2), IRCH(2)	
IR 03(0) H		IRCC	
IR 03(1) H		GRAC, IRCC, IRCH(2)	
IR 04(0) H		IRCH	
IR 04(1) H		IRCC(2), IRCH	
IR 05(0) H		IRCC,	
IR 05(1) H		IRCC	
IR 06(0) H		GRAB, GRAC	
IR 06(1) H		IRCB, IRCH	
IR 07(0) H		GRAB, SSRB	
IR 07(1) H		GRAB, GRAC(3), IRCB, IRCC, IRCH	
IR 08(0) H		GRAB, IRCA, IRCD	
IR 08(1) H		GRAB, GRAC(3), IRCB, IRCD, IRCH	
IR 08B(1) H		IRCC, IRCD	
IR 09(0) H		IRCB	
IR 09(1) H		IRCB, IRCC, IRCH(2)	
IR 10(0) H			
IR 10(1) H		IRCB(2), IRCC, IRCH	
IR 11(0) H		IRCB, IRCH	
IR 11(1) H		IRCB, IRCC, IRCH	
IR 12(0) H		IRCB, IRCD, IRCH(2)	
IR 12(1) H		IRCB, IRCH(2)	
IR 13(0) H		IRCB, IRCH	
IR 13(1) H		IRCB, IRCD, IRCH(3)	
IR 14(0) H		IRCB, IRCH	
IR 14(1) H	IRCB, IRCD, IRCH(3)		
IR 15(0) H	IRCA, IRCB, IRCC(2), IRCD		
IR 15(1) H	IRCD(6), IRCH, SSRB		
IR 15B(1) H	IRCB(4), IRCC, IRCD		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
IRCB	BO RAB 00 L	RACL
	BO RAB 01 L	RACL
	BO RAB 02 L	RACL
	BO RAB 03 L	RACL(2)
	BO RAB 04 L	RACL(2)
	B1 RAB 00 L	RACL
	F CLASS H	IRCB(2), IRCC(2), IRCD(2), IRCE
	FJ CLASS L	IRCB(2), RACK, RACL
	I CLASS L	GRAC(2), GRAJ
	IR(8:6) 0 L	IRCB, IRCD
	IR(8:6) 0+2 H	IRCB(2)
	IR(8:6) 1 L	IRCB
	IR(8:6) 2 L	IRCB
	IR(8:6) 3 L	IRCB
	IR(8:6) 4 L	IRCB
	IR(8:6) 6 L	IRCC(2)
	IR(14:9) 00 L	IRCB(2)
	IR(14:9) 04 L	IRCB(3), IRCD
	IR(14:9) 05 L	IRCB, IRCC, IRCD
	IR(14:9) 06 L	IRCC(3), IRCD
	IR(14:12) 0 H	IRCB, IRCD(2)
	IR(14:12) 0 L	
	IR(14:12) 1 L	IRCB, IRCC(2)
	IR(14:12) 7 H	IRCB, IRCC
	IR(14:12) 7 L	IRCB(2)
	JMP + JSR H	IRCB(2)
	K CLASS H	IRCB(2)
	MOVB H	GRAA, IRCB
	MOVB L	IRCB, IRCD
	(MUL:ASH)+MFP L	IRCB
	NEG.B H	IRCB(2)
	NEG.B L	GRAA, IRCB
	OBD(ASRB+RORB) L	GRAA, IRCB(2)
	P CLASS H	GRAJ, IRCB, TMCE
	PART P CLASS L	IRCB, IRCC
	SRCF 7 L	DAPJ, GRAC, IRCC, SSRB
	SWAB H	IRCB

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
IRCE	CC ← BR H	IRCE, IRCF(3)	
	EN HIB H	GRAB	
	EN LOB H	GRAB	
	EN WORD H	GRAB	
	LOAD FCC L	IRCE(2), IRCF(3)	
	LOB CARRY H	IRCE(2), IRCF	
	PS LOAD L	IRCE(2), IRCF(3)	
	V DATA L	IRCH	
	VEN 1 L	IRCE	
	VEN 2 L	IRCE(2)	
	WOB CARRY H	IRCE(2), IRCF	
	IRCF	BR--1 SAVE(0) H	IRCF
		BR--1 SAVE(1) H	IRCH
		C DATA L	IRCE(2), IRCF(3)
CC INSDEP H		IRCF(4)	
CC INSDEPA H		IRCE, IRCF(3)	
CC NON AFF L		IRCE, IRCF	
CC FP LOAD L		IRCF(3), IRCH	
CCL OOL		IRCF(4), IRCH	
CCL 02 L		IRCE, IRCF	
CCLD 4 L		IRCE(5), IRCF	
CCLD 5 L		IRCF(3)	
CCLD 6 L		IRCE, IRCF	
CCLD 67 L		IRCF(3)	
CCLD 7 L		IRCF	
CEN 1 H		IRCF(2)	
CEN 2 H		GRAB	
CHECK Z H		IRCE, IRCF(2)	
MUL + DIV NZVEN H		IRCH	
N DATA L		IRCE(3), IRCF(4)	
NEN 1 H		IRCE, IRCF	
SET V H		IRCF	
X L		IRCF	
Z DATA 1 L		IRCF(2)	
ZINV H		IRCH	
Z1(0) H			
Z1(1) H			
Z2(0) H		GRAJ, IRCH	
Z2(1) H		RACK	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
IRCH	BIN L	IRCC, IRCH(4)
	C (0) H	GRAA, IRCH
	C (1) H	GRAA, GRAJ, IRCF, RACF
	C B (1) H	DAPJ, PDRD, PDRE
	CCCLK H	IRCF(3)
	CLK INH L	IRCH(2)
	CMOD 0 H	IRCF(3)
	CMOD 1 H	IRCF(3)
	DIV + ASHC H	GRAA
	ENC H	IRCF(3)
	ENV H	IRCE(2)
	ENZN H	IRCF(2)
	MAT C H	MAINTENANCE BOARD
	MAT N H	MAINTENANCE BOARD
	MAT V H	MAINTENANCE BOARD
	MAT Z H	MAINTENANCE BOARD
	MODZN H	IRCE(3), IRCF(4)
	N (0) H	GRAJ, IRCH, RACF
	N (1) H	GRAA, GRAJ, IRCF, RACF(2), RACK
	N B (1) H	PDRD, PDRE
	R (ICLASS) H	DAPJ(2), IRCB
	R (K CLASS) H	IRCB
	R (MFP + MTP) H	IRCC(2)
	R (MUL:ASHC + MFP) H	IRCB
	R (PART P CLASS) H	IRCB
	R (ROR.B + MUL) H	IRCC
	SET CC H	IRCH(4)
	SUB L	IRCH
	SUBROMA 0 H	GRAA, IRCH(2)
	SUBROMA 1 H	GRAA, IRCH(2)
	SUBROMA 2 H	GRAA, IRCH(2)
	SUBROMA 3 H	GRAA, IRCH(2)
	SUBROMA 4 H	GRAA, IRCH(2)
	SWAB L	GRAA(3), IRCE(3), IRCF(2)
	SXT H	GRAA
	V (0) H	RACF
	V (1) H	IRCE, PDRD, PDRE, RACF(2)
	VMOD 0 H	IRCE(2)
	VMOD 1 H	IRCE(3)
	Z (0) H	
	Z (1) H	GRAB, IRCF(2), IRCH, PDRD, PDRE, RACF(2)
	Z CLR L	IRCF(2)
	Z SET L	IRCF(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
KNLA	DISP D00 H	KNLA	
	DISP D01 H	KNLA	
	DISP D02 H	KNLA	
	DISP D03 H	KNLA	
	DISP D04 H	KNLA	
	DISP D05 H	KNLA	
	DISP D06 H	KNLA	
	DISP D07 H	KNLA	
	DISP D08 H	KNLA	
	DISP D09 H	KNLA	
	DISP D10 H	KNLA	
	DISP D11 H	KNLA	
	DISP D12 H	KNLA	
	DISP D13 H	KNLA	
	DISP D14 H	KNLA	
	DISP D15 H	KNLA	
	DISP PAR HI H	KNLA	
	DISP PAR LO H	KNLA	
	PNL LOCK H	KNLB, SCCJ	
	PNL LOCK L	KNLA, KNLC	
	SWITCHED GROUND	KNLC	
	KNLB	DISP ADRS 04 H	KNLB
		DISP ADRS 05 H	KNLB
DISP ADRS 06 H		KNLB	
DISP ADRS 07 H		KNLB	
DISP ADRS 08 H		KNLB	
DISP ADRS 09 H		KNLB	
DISP ADRS 10 H		KNLB	
DISP ADRS 11 H		KNLB	
DISP ADRS 12 H		KNLB	
DISP ADRS 13 H		KNLB	
DISP ADRS 14 H		KNLB	
DISP ADRS 15 H		KNLB	
DISP ADRS 16 H		KNLB	
DISP ADRS 17 H		KNLB	
DISP ADRS 18 H		KNLB	
DISP ADRS 19 H		KNLB	
DISP ADRS 20 H		KNLB	
DISP ADRS 21 H		KNLB	
IND ADRS ERR H		KNLB	
IND DATA H		KNLD	
IND MASTER H		KNLB	
IND PAR ERR H		KNLB	
IND PAUSE H		KNLB	
IND RUN H		KNLB	
MMRO MODE 0 H		KNLD(3)	
MMRO MODE 1 H		KNLD(2)	
VA 00 H		KNLB	
VA 01 H	KNLB		
VA 02 H	KNLB		
VA 03 H	KNLB		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
KNLC	CONT SW H	SCCJ
	DEP SW H	SCCJ
	EXAM SW H	SCCJ
	HALT SW H	SCCJ
	LOAD ADRS SW H	SCCJ
	SNG BUS CYC SW 4	SCCJ
	START SW H	SCCJ
	SWR <21:00> H	SCCJ
KNLD	BUS REGISTER L	KNLD
	CONSOLE PHY L	KNLD(2)
	DATA PATHS L	KNLD(2)
	DISP ADRS SELO H	KNLB, SCCJ
	DISP ADRS SEL1 H	KNLB, SCCJ
	DISP ADRS SEL2 H	KNLB, SCCJ
	DISP DATA SELO H	KNLA
	DISP DATA SEL1 H	KNLA
	DISPLAY REGISTER L	KNLD(2)
	IND 16 BIT MAPPING H	KNLD
	IND 18 BIT MAPPING H	KNLD
	IND 22 BIT MAPPING H	KNLD
	KERNEL D.V.L	KNLD(2)
	KERNEL H	KNLD
	KERNEL I.V.L	KNLD(2)
	LAMP TEST L	KNLA(18), KNLB(27), KNLD(19)
	PROGRAM PHY L	KNLD(2)
	SUPER D.V.L	KNLD(2)
	SUPER H	KNLD
	SUPER I.V.L	KNLD(2)
	USER D.V.L	KNLD(2)
	USER H	KNLD
	USER I.V.L	KNLD(2)
UADRS FPP/CPU L	KNLD(3)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
PDRA	BRMX 00 H	DAPA, IRCA, PDRB, RACJ
	BRMX 01 H	DAPA, IRCA, PDRB, RACJ
	BRMX 02 H	DAPA, IRCA, PDRB, RACJ
	BRMX 03 H	DAPA, IRCA, PDRB, RACJ
	BRMX 04 H	DAPA, IRCA, PDRB, RACJ
	BRMX 05 H	DAPA, IRCA, PDRB, RACJ
	BRMX 06 H	DAPA, IRCA, PDRB, RACJ
	BRMX 07 H	DAPA, IRCA, PDRB, RACJ
	BRMX 08 H	DAPA, IRCA, PDRB, RACJ
	BRMX 09 H	DAPA, IRCA, PDRB, RACH, RACJ
	BRMX 10 H	DAPA, IRCA, PDRB, RACH, RACJ
	BRMX 11 H	DAPA, IRCA, PDRB, RACJ
	BRMX 12 H	DAPA, IRCA, PDRB, RACJ
	BRMX 13 H	DAPA, IRCA, PDRB, RACJ
	BRMX 14 H	DAPA(2), IRCA, PDRB, RACJ
	BRMX 15 H	DAPA, IRCA, PDRB, RACJ
	BRMXSO H	PDRA(8)
	BRMXS1 H	PDRA(8)
	INTD 00 H	PDRA
	INTD 01 H	PDRA
	INTD 02 H	PDRA
	INTD 03 H	PDRA
	INTD 04 H	PDRA
	INTD 05 H	PDRA
	INTD 06 H	PDRA
	INTD 07 H	PDRA
	INTD 08 H	PDRA
	INTD 09 H	PDRA
	INTD 10 H	PDRA
	INTD 11 H	PDRA
	INTD 12 H	PDRA
	INTD 13 H	PDRA
INTD 14 H	PDRA	
INTD 15 H	PDRA	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
PDRB	BR 00A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 00B L	IRCH, CDPE, SAPK, SCCL, SSRE
	BR 01A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 01B L	IRCH, CDPE, SAPK, SCCL
	BR 02A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 02B L	IRCH, CDPE, SAPK, SCCL
	BR 03A H	PDRB(2), PDRC, PDRE, PDRF
	BR 03B L	IRCH, CDPE, SAPK, SCCL
	BR 04A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 04B L	CDPE, SAPK, SCCL
	BR 05A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 05B L	CPDE, SAPK, SCCL
	BR 06A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 06B L	CDPE, SAPK
	BR 07A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 07B L	CDPE, SAPK
	BR 08A H	PDRB(2), PDRC, PDRE, PDRF
	BR 08B L	CDPE, SAPK, SSRE
	BR 09A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 09B L	CDPE, SAPK, SSRD
	BR 10A H	PDRB(2), PDRC, PDRD, PDRE, PDRF
	BR 10B L	CDPE, SAPK
	BR 11A H	PDRB(2), PDRC, PDRD(3), PDRE, PDRF
	BR 11B L	CDPE, SAPK
	BR 12A H	PDRB(2), PDRC, PDRD(3), PDRE, PDRF
	BR 12B L	CDPE, SAPD, SSRD
	BR 13A H	PDRB(2), PDRC, PDRD(3), PDRE, PDRF
	BR 13B L	CDPE, SAPD, SSRC
	BR 14A H	PDRB(2), PDRC, PDRD(3), PDRE, PDRF
	BR 14B L	CDPE, SAPD, SSRC
	BR 15A H	PDRB(2), PDRC, PDRD(3), PDRE, PDRF
	BR 15B L	CDPE, SAPD, SSRC
	HI PAR H	PDRH
	LO PAR H	PDRH
	LR 00 H	PDRF
	LR 01 H	PDRF
	LR 02 H	PDRF
	LR 03 H	PDRF
	LR 04 H	PDRF
	LR 05 H	PDRF
	LR 06 H	PDRF
	LR 07 H	PDRF
	LR 08 H	PDRF
	LR 09 H	PDRF
	LR 10 H	PDRF
	LR 11 H	PDRF
	LR 12 H	PDRF
	LR 13 H	PDRF
	LR 14 H	PDRF
	LR 15 H	PDRF

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
PDRC	PB CMP H	TIGB(2)
	PB 00 (1) H	PDRC, PDRE
	PB 01 (1) H	PDRC, PDRE
	PB (07:02) H	PDRC, PDRE
	RED ZONE H	TMCD
	SL 00 (1) H	PDRC, PDRE
	SL 01 (1) H	PDRC, PDRE
	SL (07:02) H	PDRC, PDRE
	STACK LIMIT H	TMCD(2)
	PDRD	BUS INTD (07:00) L
BUS INTD (15:11) L		PDRA
IBS 00 B L		PDRD(4)
KERNEL DATI B H		PDRD(2)
LOAD PS H		PDRD(4)
PIA 00 H		PDRE(2)
PIA 01 H		PDRE(2)
PIA 02 H		PDRE(2)
PIR 09 (1) H		PDRE, TMCB
PIR 10 (1) H		PDRE, TMCB
PIR 11 (1) H		PDRE, TMCB
PIR 12 (1) H		PDRE, TMCA
PIR 13 (1) H		PDRE, TMCA
PIR 14 (1) H		PDRE, TMCA
PIR 15 (1) H		PDRE, TMCA
PRIORITY=0 H		TMCB
PS CLK H		PDRD(5)
PS 04 (1) H		PDRD, PDRE, TMCB
PS 05 (0) H		PDRD
PS 05 (1) H		PDRD, PDRE, TMCB(3)
PS 06 (0) H		PDRD
PS 06 (1) H		PDRD, PDRE, TMCB(5)
PS 07 (0) H		PDRD
PS 07 (1) H		PDRD, PDRE, TMCB(5)
PS 11 (0) H		GRAB, GRAC (PS11(1)L) (2)
PS 11 (1) H		PDRD, PDRE
PS 12 (0) H		
PS 12 (1) H		GRAC, PDRD, PDRE, SSRB(3)
PS 13 (0) H		GRAC, SSRB(3)
PS 13 (1) H		PDRD, PDRE
PS 14 (0) H		GRAC(2), TMCC, TMCD, TMCE, UBCC, SSRB(2)
PS 14 (1) H		GRAB, PDRD(3), PDRE
PS 15 (0) H		GRAC, SSRB(3)
PS 15 (1) H		GRAC(3), PDRD(2), PDRE
READ PS H	PDRD(13)	
PDRE	BUS D <15:00> L	PDRJ, UNIBUS
	DMX S0 L	PDRE (8)
	DMX S1 L	PDRE (8)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
PDRF	DISP D <15:00> H DISP SOL DISP S1L	PDRH PDRF(8) PDRF(8)
PDRH	CACHE PERF L DISP DATA SELO H DISP DATA SEL1 H IND HI PAR H IND LO PAR H	UBCB(2), CCBJ(3) PDRF PDRF PDRH PDRH
PDRJ	D <15:00> H	PDRA
RACA	CLK RAR H CLKA RAR H CLR RBR L RARB 02 H RARB 03 H RARB 04 H RARB 05 H RARB 06 H RARB 07 H UBRK H UBRX H UCCL 00 H UCCL 01 H UCCL 02 H UDRK 00 H UDRK 01 H UDRX 00 H UDRX 01 H UIRK H UIRKA H UPCA H UPCB 00 H UPCB 01 H USHF 00 H USHF 01 H USRK H USRX 00 H USRX 01 H ZAP L	RACA(2), RACC RACC, RACD(3) RACA(3), RACB(3), RACC(2) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) RACA(4), RACB(4) TIGA PDRA(2) IRCF(6) IRCF(4), IRCH IRCF(5) GRAB(2), GRAD, GRAE, GRAF, GRAH GRAB(2), GRAD, GRAE, GRAF, GRAH GRAC, GRAD GRAC(2), GRAD IRCA, SSRH RACA, RACJ(2) DAPJ DAPJ(3) DAPJ(3) GRAA GRAA GRAD GRAC GRAC(2) RACA(2), RACC(2), RACD(3)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
RACB	CLK RBR (34:28) H	RACC(2)
	ROM 32 H	RACC
	ROM 33 H	RACC
	ROM 34 H	RACC
	ROM 40 B L	TIGA(2)
	ROM 40 H	RACB(2)
	ROM 46 H	RACA
	ROM 47 H	RACA
	UBAX 00 H	DAPB, TMCC
	UBAX 01 H	DAPB
	UBSD 00 H	TIGA, TMCE(2)
	UBSD 01 H	TMCC, TMCD, TMCE(2), UBCB
	UIBS 00 H	IRCE, PDRD(2), TMC(2)
	UIBS 01 H	IRCE, PDRD(5), TMC
	UPAD 00 H	GRAC(3)
	UPAD 01 H	GRAC(5)
	UPAD 02 H	GRAC(3)
	UPWE 00 H	DAPJ, GRAB, GRAC(2)
	UPWE 01 H	GRAC(2)
RACC	RARA 04 H	RACC(4), RACD(4)
	RARA 05 H	RACC(4), RACD(4)
	RARA 06 H	RACC(4), RACD(4)
	RARA 07 H	RACC(4), RACD(4)
	RARB 00 H	RACA(4), RACB(4)
	RARB 01 H	RACA(4), RACB(4)
	UALU 0 H	GRAA(5), IRCH
	UALU 1 H	GRAA(6), IRCH
	UALU 2 H	GRAA(6), IRCH
	UAMX 00 H	DAPB
	UAMX 01 H	DAPB
	UBCT 00 H	TMCC(2), TMCE, TMC(2), UBCB
	UBCT 01 H	TMCC(2), TMCE, UBCB(2), UBCC, UBCF
	UBCT 02 H	TMCC(2), TMCE, UBCB, UBCC, UBCF
	UBMX 00 H	DAPB, DAPD(3)
	UBMX 01 H	DAPB, DAPD(3)
	UBSC 00 H	TMCC, TMCE(2)
	UBSC 01 H	TMCC(2), TMCE(2)
	UBSC 02 H	TMCC(2), TMCE(2)
	UKMX 00 H	DPAD(2), DAPE(4)
	UKMX 01 H	DAPD(2), DAPE(4)
	UMSC 00 H	RACH, TMCE(2)
	UMSC 00 L	TMCE(2)
	UMSC 01 H	RACH, TMCE(3)
	UMSC 02 H	RACH, TMCE(3)
	USHC 00 H	GRAJ(5)
	USHC 00 L	GRAJ(2)
	USHC 01 H	GRAJ(3)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
RACD	RAR 00 H	PDCR, PDRF, SSRA(4)	
	RAR 01 H	PDCR, PDRF, SSRA(4)	
	RAR 02 H	PDCR, PDRF, SSRA(4)	
	RAR 03 H	PDCR, PDRF, SSRA(4)	
	RAR 04 H	PDCR, PDRF, SSRA(4)	
	RAR 05 H	PDCR, PDRF, SSRA(4)	
	RAR 06 H	PDCR, PDRF, SSRA(4)	
	RAR 07 H	PDCR, PDRF, SSRA(4)	
	RARA 00 H	RACC(4), RACD(4)	
	RARA 01 H	RACC(4), RACD(4)	
	RARA 02 H	RACC(4), RACD(4)	
	RARA 03 H	RACC(4), RACD(4)	
	ROM 15 H	RACC	
	UADR 00 L	RACL	
	UADR 01 L	RACL	
	UADR 02 L	RACL	
	UADR 03 L	RACL	
	UADR 04 L	RACL	
	UADR 05 L	RACL	
	UADR 06 L	RACL	
	UADR 07 L	RACL	
	UAFEN L	RACL	
	UBEF 02 L	RACK(5), RACL	
	UBEF 03 H	RACK(4)	
	UBEF 03 L	RACK(5), RACL	
	UBEFA 00 L	RACK(3)	
	UBEFA 01 L	RACK(3)	
	UBEFB 00 L	RACK(3)	
	UBEFB 01 L	RACK(3)	
	UBFEN L	RACL	
	UCFEN L	RACL	
	RACE	AO RAB 00 L	RACL
		AO RAB 01 L	RACL
		AO RAB 02 L	RACL
		AO RAB 03 L	RACL
		AO RAB 04 L	RACL(3)
		AO RAB 05 L	RACL(2)
AO RAB 07 L		RACL	
BF1=0 L		RACE(5), RACF, RACH(3)	
BF1=7 L		RACF, RACH(3)	
BF2=4 L		RACE	
BF2=5 L		RACE(2), RACH	
BF3=5 L		RACE, RACF	
BIN L		RACE, RACF(4), RACH	
BIN *SMO H		RACE(3), RACF, RACH	
BR INST H		RACE(2), RACH(5)	
JMP+JSR+SWAB H		RACE(4), RACH	
(MUL:ASHC + MFP) H		RACE(3)	
R CLASS H		RACE(4), RACH	
SMO L		RACE, RACH(2)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
RACF	A1 RAB 00 L	RACL	
	A1 RAB 01 L	RACL	
	A1 RAB 02 L	RACL	
	A1 RAB 05 L	RACL	
	A2 RAB 03 L	RACL	
	A2 RAB 05 L	RACL	
	BF01=0 L	RACF(2)	
	(HALT:OPCODE 7) H	RACE, RACF(3)	
	IR(11:8) 0 L	RACF(2)	
	(RTS:CCOP) H	RACF(4)	
	TRUE 1 H	RACH	
	TRUE 2 H	RACH	
	UCLASS H	RACE, RACF(3)	
	X CLASS H	RACF(4)	
	RACH	A0 RAB 07 L	RACL
		A1 RAB 04 L	RACL
A2 RAB 00 L		RACL(2)	
A2 RAB 01 L		RACL	
A2 RAB 02 L		RACL	
AFIR 09A (0) H		RACE(2), RACF(3)	
AFIR 09A (1) H		RACF(3)	
AFIR 10A (0) H		RACE(2), RACF(3)	
AFIR 10A (1) H		RACF(3), RACH	
BF2=7L		RACF(2)	
BIN *(-SM01) L		RACF	
BUST H		TMCC, UBCE(2), CCBB, CCBC, SSRK	
DF7 L		RACH	
DF7 + BRQ H		RACE(2), RACF	
DMO H		RACE, RACF, RACH(2)	
DMO L		RACF(2), RACH(4)	
NEG.B * DMO H		RACH(2)	
PSWAB H		RACE, RACH	
T CLASS L		RACE, RACH	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
RACJ	AFIR 00 (0) H	
	AFIR 00 (1) H	RACF, RACH
	AFIR 01 (0) H	
	AFIR 01 (1) H	RACF, RACH
	AFIR 02 (0) H	
	AFIR 02 (1) H	RACF, RACH
	AFIR 03 (0) H	RACE, RACH
	AFIR 03 (1) H	RACF(3)
	AFIR 04 (0) H	RACE, RACF(2), RACH
	AFIR 04 (1) H	RACF(2)
	AFIR 05 (0) H	RACE, RACF(4), RACH(2)
	AFIR 05 (1) H	RACF
	AFIR 06 (0) H	RACF(4), RACH(2)
	AFIR 06 (1) H	RACE(3), RACF, RACH
	AFIR 07 (0) H	RACE, RACF(4), RACH(3)
	AFIR 07 (1) H	RACF, RACH
	AFIR 08 (0) H	RACE(2), RACF, RACH, RACJ
	AFIR 08 (1) H	RACE(2), RACF, RACH(4)
	AFIR 09 (0) H	RACE(3), RACF(2)
	AFIR 09 (1) H	RACE, RACF, RACH(2)
	AFIR 10 (0) H	RACE(3), RACF, RACH(2)
	AFIR 10 (1) H	RACE, RACF(2), RACH
	AFIR 11 (0) H	RACE(2), RACF, RACH(3), RACJ
	AFIR 11 (1) H	RACE(4), RACF, RACH(3)
	AFIR 12 (0) H	RACE(3), RACF
	AFIR 12 (1) H	RACE(4)
	AFIR 13 (0) H	RACE(3), RACF
	AFIR 13 (1) H	RACE(4)
	AFIR 14 (0) H	RACE(3), RACF
	AFIR 14 (1) H	RACE(4)
	AFIR 15 (0) H	RACE(3), RACF(3)
	AFIR 15 (1) H	RACE, RACF(2), RACH
	BUF AFIR 08 (1) H	RACE,
BUF AFIR 11 (1) H	RACE, RACF(2)	
CLK AFIR L	RACH, RACJ(3)	
RACK	BEF (1:0) 0 H	RACK(5), RACL
	BEF (1:0) 1 H	RACL(2)
	BEF (3:2) 3 H	RACK, RACL(2)
	BRC 10 H	UBCC
	BRCAB 00 L	RACL
	BRCAB 01 L	RACL
	BRCAB 02 L	RACL
	BRCAB 04 L	RACL
	BRCAB 05 L	RACL
	BRCAB 06 L	RACL
	BRCAB 07 L	RACL
	FP REQ H	RACK
	SYNC BRC 10(1) H	RACK(3)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
RACL	RADR 00 H	RACC, RACD(2)
	RADR 01 H	RACC, RACD(2)
	RADR 02 H	RACA, RACD(2)
	RADR 03 H	RACA, RACD(2)
	RADR 04 H	RACA, RACC, RACD
	RADR 05 H	RACA, RACC, RACD
	RADR 06 H	RACA, RACC, RACD
	RADR 07 H	RACA, RACC, RACD
TIGA	CLK BR H	DAPA
	CLK BRA H	PDRB(3), PDRH
	CONT (1) H	TIGA
	MEM SYNC H	MAINTENANCE MODULE
	MEM SYNC (0) H	TIGA(2)
	MEM SYNC (1) H	TIGA(2)
	PAUSE H	TIGA(7), TIGB
	PSEUDO T3 H	TIGA, UBCA(2), SCCC(9), SCCD
	ROM INITA L	TIGA(10)
	ROM INITB L	TIGA, TIGE(5)
	S0 (0) H	TIGA
	S0 (1) H	TIGA(2)
	S1 (1) H	
	S1 (1) L	TIGA
	SNGCY H	KNLC, SCCJ
	STOP T1 L	TIGA(3)
	T1 (1) H	TIGA(3), TIGB, TIGE
	T1 (1) L	TIGA
	T1 MAT H	MAINTENANCE BOARD
	T1A (0) H	TIGA,
	T1A (1) H	TIGC
	T1A (1) L	TIGC
	T2 (0) H	TIGA(2)
	T2 (1) H	TIGA
	T2 MAT H	MAINTENANCE BOARD
	T2A (1) H	TIGC, TIGE
	T2A (1) L	TIGC
	T3 (1) H	TIGA, TIGD, TIGE
	T3 (1) L	TIGD
	T3 MAT H	MAINTENANCE BOARD
	T4 (1) H	TIGA(3), TIGB, TIGD, TIGE
	T4 (1) L	TIGD
T4 L	TIGA(2)	
T4 MAT H	MAINTENANCE BOARD	
T5 (0) H	TIGA(2)	
T5 (1) H		
T5 (1) L	TIGA	
T5 MAT H	MAINTENANCE BOARD	
T5A (1) H	TIGA, TIGD, TIG \bar{E}	
T5A (1) L	TIGB, TIGD	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
TIGB	CLOCK H CLOCK L CONT L MS EN (0) H PB SYNCH H RC EN (0) H RC H RC L ROM + UPB(1) H SINGLE CY L SOURCE CLOCK L XTAL EN (0) H XTAL H XTAL L	TIGC(4), TIGD(3) TIGC(4), TIGD(3) TIGA TIGB(2) TIGB TIGB(3) TIGB TIGA TIGA TIGB TIGB(2) TIGB(3) TIGB(2)
TIGC	T1 H T1 L T1B H T1B L T2 H T2 L T2B H T2B L TF H TF L TPB H TPB L	DAPJ, GRAB(2), GRAD, IRCA, SAPD, SAPK, SCCD, SS SSRA RACB(2), RACJ(2), RACK, UBCA, UBCB, UBCD GRAD, RACB(2), RACC TMCD RACA CCBC(2) GRAC(2), IRCH(2), CCBA, CCBC, CCBE UBCD(2) TIGA(12), TIGE(5) TIGA(9), TIGB
TIGD	T3 H T3 L T3B H T3B L T4 H T4 L T4B H T4B L T5 H T5 L T5B H T5B L	GRAD, GRAJ, PDRJ, RACA(2) UBCA, CCBC(2) GRAC, TMCC, SAPC PDRD(4) DAPJ, GRAC(4), TMCD GRAJ, SAPC, SAPD PDRD, SSRF(2)
TIGE	TS1 L TS2 L TS3 L TS4 L TS5 L	RACA, TMCC, UBCD, SSRK TMCC(2), TMCD, UBCB(2), SSRK UBCA(2), SSRK SSRK

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
TMCA	ABOVE BR7 L	TMCB(4)
	BLOCK BR4 L	TMCB
	BRQ CLR L	TMCA(2)
	CONF (0) H	TMCA(4), TMCB(2), TMCF, UBCF
	CONF(1) H	RACK, UBCE, UBCF(2)
	HONOR BR5L	TMCB, UBCD(2)
	HONOR BR6 L	TMCB, UBCD(2)
	HONOR BR7 L	TMCB, UBCD(2)
	HONOR FPTRAP L	TMCB(4)
	HONOR PIR4 L	TMCB(2)
	HONOR PIR5 L	TMCB(2)
	HONOR PIR6 L	TMCB(2)
	HONOR PIR7 L	TMCB(2)
	HONOR PWRF L	TMCB(4), TMCC, UBCE
	HONOR SEGT H	TMCA, TMCB
	HONOR SLY H	TMCC
	HONOR SLY L	TMCA, TMCB(2)
	INH BELOW BR6 L	TMCB(3)
	INH BELOW PIR4 L	TMCA, TMCB(3)
	PAR TRP (1) H	TMCA(2), TMCB
	PERF ACKN L	CCBJ
	SEG ACKN L	SSRD
	SEG + CON + PAR L	TMCA(6), TMCB
	SEGTF L	TMCA(2)
TMCB	BLOCK LEV 2 L	TMCB
	BLOCK LEV 3 L	TMCB
	BLOCK LEV 5 L	TMCA(2)
	BLOCK LEV 6 L	TMCA(2)
	BLOCK LEV 7 L	TMCA(2)
	BRQ TRUE H	RACE, RACH, TMCB
	BRQ TRUE L	RACH, RACK
	BRQ *-(T+CONF) L	RACK
	CLR INTRF L	TMCB, UBCC
	FP ACKN L	
	FPTRAP L	DAPE
	HONOR BR4 L	TMCB, UBCD(2)
	HONOR FPTRAP H	TMCB
	HONOR PIR1 L	TMCB(2)
	HONOR PIR2 L	TMCB(2)
	HONOR PIR3 L	TMCB(2)
	HONOR PWRF H	TMCB(2), TMCC
	HONOR T L	TMCB
	PART L	TMCB, UBCB
	PF(0)*(SF+TF) H	RACK
	PF(0)*(SF+-TF) H	RACK
	PIRQL	DAPE(2), TMCB(2)
	PS04 MAT H	MAINTENANCE BOARD
	PS07 (0) H	TMCA, TMCB(2)
	PWRF L	DAPE
	PWRF + INTRF L	RACK
	SEGT L	DAPE(3), TMCB
	TOK L	DAPE, TMCB
	TRAP INH L	IRCD(4)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
TMCC	ABORT CLK L	TMCC, TMCD(4)	
	ABORT CLR L	TMCC(2), TMCF	
	ABORT H	RACA, TIGA, TMCC(4), UBCB(2)	
	AC CLEAR L	UBCE	
	AERF (0) H	TMCB	
	AERF (1) H	TMCB	
	AERF MAT H	MAINTENANCE BOARD	
	BCT 01 B L	TMCC, TMCF	
	BCT 02 B L	TMCC, TMCF	
	BLOCK STROBE (1) H	TMCB, TMCC, TMCD	
	BSC 01 B L	TMCC, TMCE	
	BUS ERROR L	TMCC(2)	
	INIT + ACKN L	TMCC, TMCF	
	KERNEL R6 (0) H	TMCC	
	KERNEL R6 (1) H	TMCC, TMCD(2)	
	NEXM L	TMCC(2), TMCD, TMCE	
	ODD ADRS ERR L	TMCC, TMCD, TMCE(2)	
	PRIORITY CLR L	TMCA	
	SEG ABORTED (1) H	TMCB	
	SERF (0) H	TMCB(2), TMCC	
	SERF (1) H	TMCB, TMCC(2), TMCD(2)	
	SERF MAT H	MAINTENANCE BOARD	
	STROBE INH L	TMCE	
	TS2 CLK H	TMCC, TMCE	
	TS3 CLK H	TMCB, TMCC(3), TMCD	
	TS3 CLKA H	TMCD, TMCE(3)	
	TMCD	C1B L	TMCD(3)
		CACHE NEXM H	SCCM
		CLK LR H	PDRB(3)
		CLR ERR REG A L	TMCD(3)
CLR ERR REG B L		TMCD(3)	
DMXSO H		PDRE	
DMXS1 H		PDRE	
HI BYTE EN H		PDRE	
ILL HALT H		SCCM	
LO BYTE EN H		PDRE	
ODD ADRS H		SCCM	
SL RED ERR H		SCCM	
SL RED H		TMCD(2)	
SL RED L		TMCC(2), TMCD, TMCE(2)	
SL YEL (1) H		TMCA, TMCD	
UBUS TIMEOUT H		SCCM	
YEL TRAP H		SCCM	
YEL ZONE H		TMCD(2)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
TMCE	BRQ CLOCK H	TMCA(2)	
	BRQ STROBE H	TMCE(2)	
	BSC 00 B L	TMCE(2)	
	BSC 02 B L	TMCE	
	CO H	UBCC(2), SAPL	
	C1 H	TMCD(3), UBCC(2), SAPK, SCCC(2), SSRJ	
	CACHE BEND H	TMCE, CCBB, CCBC(2)	
	CLK CONF H	TMCA, TMCC	
	CONTROL OK H	CCBC(2)	
	FC H	DAPD	
	FLOATING ATTN H	UBCD	
	IND PAUSE H	SCCJ	
	INTR CLR L	UBCF(2)	
	INTR PAUSE H	TMCB, TMCE, UBCD	
	INTR PAUSE L	TMCE, TMCF	
	KERNEL DATI L	PDRD(6)	
	KT BEND L	SAPK(3), SSRC, SSRD, SSRE	
	MSC 01 B L	TMCE(2)	
	MSC 02 B L	TMCE(2)	
	PAUSES B H	UBCA(2), SAPC, SSRD, SSRE, SSRK(2)	
	PAUSES B L	UBCB(3)	
	PAUSES H	TMCA, TMCC(2), TMCD(2), TMCE	
	PAUSES L	TMCA, TMCC, TMCE(5) TMCF	
	ROM BEND L	TMCE(2)	
	SET CONF L	TMCA	
	SET HALT H	TMCD, TMCE	
	SET PRIORITY H	PDRD(2)	
	TMCF	CACHE ADRS H	TIGA(2), TMCE, TMCF, UBCA
		CLK HI PS L	PDRD
		CLK LO PS L	PDRD
		CLK PB H	PDRD(3)
CLK PIR H		PDRD(2)	
CLK SL H		PDRD(3)	
FP READ L		TMCF(2)	
GET OFF H		SSRJ	
IBS 00 B L		TMCF(2)	
IBS 01 B L		TMCF(2)	
IND ADRS ERR H		SCCF	
IND RUN H		SCCJ	
READ SW L		KNLC, SCCH(2), SCCJ	
SEL INT L		PDRA, TMCF	
SEL MEM L		PDRA	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
UBCA	BBSY MAT H	MAINT. CARD	
	BUS BBSY L	UBCA, UNIBUS	
	BUS MSYN L	UNIBUS	
	CLR IND (0) H	PDRH(2)	
	CPBSY (0) H	UBCA, UBCF	
	CPBSY (1) H	UBCA(2), UBCC	
	CPBSY B H	PDRE(16), UBCA, UBCC, SCCE(2), SCCL (18)	
	IND CLK H	PDRH(2)	
	MSYN (0) H	UBCA(2), UBCB, UBCC	
	MSYN (1) H	UBCA(3), UBCB(2), UBCC	
	MSYN SET H	TMCD(3), TCMF(5)	
	PASSIVE L	UBCB	
	START BUS (1) H	UBCA(2)	
	START TIMEOUT L	UBCA, UBCB	
	TS4 CLK H	UBCA(2), UBCB, UBCC, UBCF	
	UNIBUS ADRS H	TIGA, UBCA	
	UBCB	ABORT ACKN H	SSRC
		ABORT ACKN L	TMCD, UBCB(3), UBCE
ABORT L		UBCA	
ABORT RESTART L		UBCA, UBCB	
ACKN B H		TMCA(2), TMCB(2), TMCC(3)	
ACKN L		UBCB	
BCT OO B L		UBCB(2), UBCC, UBCF	
CC DATA (1) H		IRCH(8)	
CLR CC DATA L		UBCB	
CP SSYN L		UBCA, UBCB	
HI BYTE H		TCMF(3), SAPK, SSRC, SSRD	
IND PAR ERR H		SCCF, SCCJ	
LO BYTE H		TCMF(2), UBCB, SAPK, SSRE	
PAR ABORT CLR L		UBCB	
PARITY ERR L		DAPE(2), TMCB, UBCB	
PE ABORT L		RACA, TMCC(2), UBCA	
SSYN H		UBCA, UBCB(2)	
SSYN L		UBCA, UBCB	
SSYN MAT H		MAINTENANCE BOARD	
TIG RESTART H		TIGA	
TIMEOUT (0) H		UBCA, UBCB	
TIMEOUT B H		TMCD	
TIMEOUT B L		TMCC	
TS3 L		UBCA, UBCC	
TS3 CLK H		UBCB, UBCC, UBCD, UBCE(2), UBCH	
TS3 CLKA H		UBCB(2), UBCC, UBCF	
UBUS PAR ERR H		CCBJ, CCBK	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
UBCC	BUS CO L	UNIBUS
	BUS C1L	UNIBUS
	BUS SSYN L	UNIBUS
	CO B H	UBCC(2), ADMJ
	C1 B H	UBCA, UBCC(2), ADMJ
	C1 B L	UBCA, UBCC(3)
	DATI L	TMCC
	DATIP L	UBCA, UBCD
	DATO L	UBCB(4)
	DATOB L	UBCB(2)
	INTR B H	UBCC(3)
	INTR L	UBCB
	INTRF (0) H	TMCB
	INTRF (1) H	
	RESET (0) H	UBCC(2), UBCD, UBCE
	RESET (1) H	UBCE
	RESET WAIT (0) H	UBCC
	RIP + FPSYNC H	RACK
	START INIT (0) H	UBCC, UBCD
	START INIT (1) H	UBCC
	UBCD	BRQ (0) H
BRQ (1) H		UBCD
CLR BG (0) H		UBCD
CLR BG (1) H		UBCA
DSACK (0) H		UBCA(2), UBCD(2)
DSACK (1) H		
ENAB BR H		UBCD
EXT BRQ H		TIGA, UBCD(2)
EXT BRQ L		TMCB
FP ATTN H		
FP ATTN L		
FP SYNC H		UBCC
FP SYNC MAT H		MAINTENANCE BOARD
FREE CLOCK (0) H		UBCA, UBCD
GRANT BR (1) H		UBCD(2)
NO SACK (1) H		UBCD(2)
NPG (0) H		UBCA, UBCD(3)
NPG (1) L		
NPR (0) H		UBCA, UBCD
NPR (1) H		UBCD
PROC BG4 H		UNIBUS
PROC BG5 H		UNIBUS
PROC BG6 H		UNIBUS
PROC BG7 H		UNIBUS
PROC NPG H		UNIBUS
TS2 CLK H		UBCB, UBCD
SACK (0) H		UBCA, UBCD
SACK H	UBCD(2)	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
UBCE	AC LO H	UBCE (3)	
	AC LO L	UBCD, UBCE (2), ADMJ (2)	
	BLOCK DOWN (1) H		
	BUS DC LO L	UNIBUS	
	BUS INIT L	UNIBUS	
	BUST + DCLO L	UBCC, UBCE (2)	
	BUST MAT H	MAINTENANCE BOARD	
	DC LO B H	UBCE (2), MAPB	
	DC LO B L	UBCA, UBCE (4), UBCF, UBCH, ADMJ, SSRK	
	INIT H	IRCH, TMCB, UBCE, UBCF, CCBA, CCBL	
	INIT L	UBCE, UBCE, UBCH, GRAJ, PDRB, TMCC (3)	
		TMCD (2), UBCA (4), UBCB (2), UBC (5)	
	INT BUS INIT L	PDRC (3), PDRD (9), TMCA, SSRK	
	MAINT PFL	UBCE	
	PDNF (0) H	UBCE	
	PDNF (1) H	TMCA, UBCC, UBCE	
	PF CLR (0) H	TMCA	
	PF CLR (1) H	UBCE (2)	
	PUPF (0) H	UBCE (2)	
	PUPF (1) H	TMCB (2), UBCE (2)	
	ROM INIT H	RACA (2), RACC, RACL, TIGA, CCBA	
	START INIT L	UBCE, UBCH	
	UBCF	ACKN LEVEL H	UBCF, UBCH
		ACKN T4 L	UBCF (3)
		CONSL ACT (0) H	GRAB (2), TIGB, TMCC (2), UBCF, SCCJ
		CONSL ACT (1) H	GRAC (2), RACK (2), TMCF, UBCF
		CONSL RESET L	TMCE, UBCE (3)
CONSL07 (0) H		UBCF, UBCH (4)	
CONSL07 (1) H		RACK, SCCK	
CONT (0) H		UBCF, UBCH (3)	
CONT (1) H		UBCH	
DEPOS (0) H		UBCF	
DEPOS (1) H		UBCH (2)	
DISABLE NPR L		UBCD	
EXAM (0) H		UBCF	
EXAM (1) H		UBCH	
IND MASTER H		SCCJ	
REG DEP (0) H		UBCF	
REG DEP (1) H		RACK, UBCH (3)	
REG EXAM (0) H		UBCF	
REG EXAM (1) H		UBCH (3)	
START (1) H		UBCF	
START (1) L		UBCF, UBCH (2)	
START L	UBCE, UBCF		
STATUS CLR L	PDRD (2)		
STOP L	TMCA		

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<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
UBCH	CLR CONF L	TMCA
	CONSL 00 H	RACK
	CONSL 01 H	RACK
	CONSL 02 H	RACK
	DEPOSIT + REG DPST L	UBCH(2)
	EXAM + STEP EXAM H	UBCH
	MICRO ADO H	UBCH
	MICRO AD1 H	UBCH
	MICRO AD2 H	UBCH
	MSB DATA L	UBCH
	REG DEP L	UBCH
	REG DEP + REG EXM L	UBCH
	REG DEP + STEP H	UBCH(2)
	REG DEP + STEP L	UBCH(2)
	REG EXAM + STEP H	UBCH(2)
	REG EXAM + STEP L	UBCH
	REG STEP DEP + EXM L	UBCH(2)
	REG STEP EXM L	UBCH
	STEP DEP + DEP H	UBCH(2)
	STEP DEP + DEP L	UBCH
STEP DEPOSIT 2 L	UBCH	
STEP EXAM 0 L	UBCH	
ADMA	AMX 02 L	ADMA(8), ADMB(7)
	AMX 03 L	ADMA(8), ADMB(7)
	AXM 04 L	ADMA(8), ADMB(7)
	AMX 05 L	ADMA(8), ADMB(7)
	AMX 06 L	ADMA(8), ADMB(7)
	AMX 07 L	ADMA(8), ADMB(7)
	AMX 08 L	ADMA(8), ADMB(7)
	AMX 09 L	ADMA(8), ADMB(7)
	TAGO A14 L	ADMK(2)
	TAGO A15 L	ADMK(2)
	TAGO A16 L	ADMK(2)
	TAGO A17 L	ADMK(2)
	TAGO A18 L	ADMK(2)
	TAGO A19 L	ADMK(2)
	TAGO A20 L	ADMK(2)
	TAGO A21 L	ADMK(2)
	TAGO WP L	ADMA(8), ADMB(7)
ADMB	TAGO A10 L	ADMK(2)
	TAGO A11 L	ADMK(2)
	TAGO A12 L	ADMK(2)
	TAGO A13 L	ADMK(2)
	TAGO PARA H	ADMK
	TAGO PARB L	ADMK
	TAGO VALID H	ADMK(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
ADMC	AMX A02 L	ADMC(8), ADMD(7)
	AMX A03 L	ADMC(8), ADMD(7)
	AMX A04 L	ADMC(8), ADMD(7)
	AMX A05 L	ADMC(8), ADMD(7)
	AMX A06 L	ADMC(8), ADMD(7)
	AMX A07 L	ADMC(8), ADMD(7)
	AMX A08 L	ADMC(8), ADMD(7)
	AMX A09 L	ADMC(8), ADMD(7)
	TAG1 A14 L	ADMK(2)
	TAG1 A15 L	ADMK(2)
	TAG1 A16 L	ADMK(2)
	TAG1 A17 L	ADMK(2)
	TAG1 A18 L	ADMK(2)
	TAG1 A19 L	ADMK(2)
	TAG1 A20 L	ADMK(2)
	TAG1 A21 L	ADMK(2)
	TAG1 WP L	ADMC(8), ADMD(7)
ADMD	TAG1 A10 L	ADMK(2)
	TAG1 A11 L	ADMK(2)
	TAG1 A12 L	ADMK(2)
	TAG1 A13 L	ADMK(2)
	TAG1 PARA H	ADMK
	TAG1 PARB L	ADMK
	TAG1 VALID H	ADMK(2)
ADME	AMX 00 H	ADMJ(2), CCBH, DTMB(2)
	AMX 01 H	ADMJ(2), CCBH, DTMB(5)
	AMX 02 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 03 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 04 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 05 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 06 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 07 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 08 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 09 H	ADMA, ADMC, ADMJ, ADML, CCBH, DTMA
	AMX 10 H	ADMB, ADMD, ADME, ADMF, ADMJ, ADML, CCBH
	AMX 10 L	ADMK(2)
	AMX 11 H	ADMB, ADMD, ADME, ADMF, ADMJ, ADML, CCBH
	AMX 11 L	ADMK(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
ADMF	AMX 12 H	ADMB, ADMD, ADMF(2), ADMJ, ADML, CCBH	
	AMX 12 L	ADMK(2)	
	AMX 13 H	ADMB, ADMD, ADMF(2), ADMJ, ADML, CCBH	
	AMX 13 L	ADMK(2)	
	AMX 14 H	ADMA, ADMC, ADMF(2), ADMJ, ADML, CCBH	
	AMX 14 L	ADMK(2)	
	AMX 15 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 15 L	ADMK(2)	
	AMX 16 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 16 L	ADMK(2)	
	AMX 17 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 17 L	ADMK(2)	
	AMX 18 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 18 L	ADMK(2)	
	AMX 19 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 19 L	ADMK(2)	
	AMX 20 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 20 L	ADMK(2)	
	AMX 21 H	ADMA, ADMC, ADMF(2), ADML, CCBH	
	AMX 21 L	ADMK(2)	
		PARA GEN H	ADMJ
		PARA GEN L	ADMB, ADMD
		PARB GEN 1 H	ADMD
ADMH	MBC A00 H	ADME	
	MBC A01 H	ADME	
	MBC A02 H	ADME	
	BMC A03 H	ADME	
	BMC A04 H	ADME	
	MBC A05 H	ADME	
	MBC A06 H	ADME	
	MBC A07 H	ADME	
	MBC A08 H	ADME	
	MBC A09 H	ADME	
	MBC A10 H	ADME	
	MBC A11 H	ADME	
	MBC A12 H	ADMF	
	MBC A13 H	ADMF	
	MBC A14 H	ADMF	
	MBC A15 H	ADMF	
	MBC A16 H	ADMF	
	MBC A17 H	ADMF	
	MBC A18 H	ADMF	
	MBC A19 H	ADMF	
	MBC A20 H	ADMF	
MBC A21 H	ADMF		
MBC C0 H	ADMJ		
MBC C1 H	ADMJ		
MBC CXH	ADMJ		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
ADMJ	ADRS COUNT H	ADMJ	
	ADRS PARITY H	ADML	
	ALLOW WP H	ADMJ(2)	
	BYTE MASK 0 H	ADML	
	BYTE MASK 1 H	ADML	
	BYTE MASK 2 H	ADML	
	BYTE MASK 3 H	ADML	
	CO H	ADMJ(3), CCBH	
	DATIP H	CCBC, CDPH(4)	
	DATOB H	ADMJ, DTMB(2)	
	MAINT LOOP ENAB L	ADMJ	
	MBC WR DBL H	ADMJ (8)	
	PARB GENO H	ADMB, ADMJ	
	POWER UP (0) H	CCBA, CCBB, CCBM (3)	
	POWER UP (1) H	ADMJ, DTMB (4)	
	PUP A00 H	ADME	
	PUP A01 H	ADME	
	PUP A02 H	ADME	
	PUP A03 H	ADME	
	PUP A04 H	ADME	
	PUP A05 H	ADME	
	PUP A06 H	ADME	
	PUP A07 H	ADME	
	PUP WP L	ADMJ, DTMB	
	READ L	ADMJ (2), ADML, CCBD, CDPK, DTMB	
	WP H	ADMA, ADMC	
	ADMK	GROUP 0 AMATCH H	ADMK(2), CCBM
		GROUP 0 HIT L	CCBC(2), CCBD(2), DTMM
		GROUP 0 PARA OK H	ADMK(3)
GROUP 0 PARB OK H		ADMK(3)	
GROUP 1 AMATCH H		ADMK(2), CCBM	
GROUP 1 HIT L		CCBC(2), CCBD(2)	
GROUP 1 PARA OK H		ADMK(3)	
GROUP 1 PARB OK H		ADMK(3)	
HIT EITHER L		CCBC(2)	
TAGO PAR ERR H		CCBK(2), CCBM	
TAG1 PAR ERR H		CCBK(2), CCBM	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
ADML	ADRS ACK H	CCBD, CCBE	
	ADRS PAR ERR H	CCBK(3)	
	MAIN A <21:02> L	MCTA	
	MAIN ACK L	MAIN MEMORY	
	MAIN ACLO H	UBCE	
	MAIN ACLO L	MAIN MEMORY (MCTA)	
	MAIN BYTE MASK 0L	MAIN MEMORY (MCTA)	
	MAIN BYTE MASK 1L	MAIN MEMORY (MCTA)	
	MAIN BYTE MASK 2L	MAIN MEMORY (MCTA)	
	MAIN BYTE MASK 3L	MAIN MEMORY (MCTA)	
	MAIN CO L	MAIN MEMORY (MCTA)	
	MAIN C1 L	MAIN MEMORY (MCTA)	
	MAIN DCLO H	UBCE	
	MAIN DCLO L	MAIN MEMORY	
	MAIN MAR 0 L	MAIN MEMORY	
	MAIN MAR 1 L	MAIN MEMORY	
	MAIN MAR 2 L	MAIN MEMORY	
	MAIN START L	MAIN MEMORY	
	MAINA PAR L	MAIN MEMORY	
	MAINA PAR ERR L	MAIN MEMORY	
	OCC H	CDPD	
	CCBA	ARB CLK H	CCBA, CCBB(6), CCBC(7), CCBD(2), CCBE(6)
		INIT H	CCBA(3), CDPJ, CDPK
INIT A L		CCBB(3), CCBC(4)	
INIT B L		CCBC(4), CCBD(2)	
INIT C L		CCBD, CCBE(4), CCBM	
INIT D L		CCBH(2), CCBJ(4), CCBK(2), CCBL	
SYNC CLK H		CCBB(7), CCBE(2)	
CCBB	AMX S0 H	ADME(6), ADMF(5), ADMJ(2)	
	AMX S1 H	ADME(6), ADMF(5), ADMJ(2)	
	CLK MBC ADRS L	ADMH	
	CP CYCLE H	CCBC(6), CCBD(2), CCBE, CCBJ(2)	
	CP CYCLE L	CCBB, CCBK, CCBM, CDPE	
	LOCK (0) H	CCBB(3), CCBC, CCBD	
	LOCK (1) H	CCBB(2), CCBC, CCBE	
	MBC CYCLE H	CCBC, CCBD, CCBE(4)	
	MBC CYCLE L	CCBB, CCBD(2), CCBE, CCBK	
	PRE MBC (1) H	CCBB(4)	
	PRE UBUS (1) H	CCBB(3)	
	UB CYCLE H	CCBB, CCBC(4), CCBD(2), CCBH, CCBJ, CCBK	
	UB CYCLE L	CCBC	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
CCBC	BEND HOLD (0) H		
	BEND HOLD (1) H	CCBC(2)	
	BUST HOLD (0) H		
	BUST HOLD (1) H	CCBB(2), CCBC	
	CONTROL OK (0) H	CCBC, CCBD	
	CONTROL OK (1) H	CCBC(2), CCBJ	
	DONE (0) H	CCBC(5), CCBD, CCBE(2)	
	DONE (1) H	CCBC(2), CCBL	
	DONE H	CCBB, CCBD, CCBE(4)	
	MEM SYNC H	TIGA	
	NON CPU DATIP (0) H	CCBB(3), CCBC	
	NON CPU DATIP (1) H	CCBC	
	NON MBC DATIP (0) H	CCBB(3), CCBC	
	NON MBC DATIP (1) H	CCBC	
	NON UBUS DATIP (0) H	CCBB(2), CCBC	
	NON UBUS DATIP (1) H	CCBC	
	T2 DLY (0) H	CCBB, CCBC(2)	
	T2 DLY (1) H	CCBB, CCBC(4)	
	T2 L	CCBC(2)	
	UB ACKN L	MAPF	
	UB DONE H	MAPB, MAPH(3)	
	CCBD	CP TIMEOUT L	PDRH
		DMXS1 H	DTMM(10)
DONE CLR L		CCBD(2), CCBE(4)	
MBC CYCLE B H		CCBD, CCBM(2), CDPC(2)	
MBC TIMEOUT H		CDPK	
READ H		CCBC, CCBD(3), CCBE, CCBH(2)	
READ IN PROG (1) H		CCBD, CCBE	
READ L		CCBC, CCBE, CCBH(2), CCBJ, CCBM(4)	
SLOW CYCLE (0) H		CCBC(2), CCBD(3), CCBL	
SLOW CYCLE (1) H		CCBC, CCBD	
SLOW CYCLE H		DTMB(4), DTMM	
SLOW CYCLE L		CCBD, DTMB(4), DTMM(2)	
SLOW CYCLE MAT H		MAIN. BOARD	
START H		ADML	
START SLOW (0) H		CCBE(2)	
START SLOW (1) H		CCBD(3), CCBM	
TIMEOUT (0) H		CCBC, CCBE(5)	
TIMEOUT (1) H		CCBD(3), CCBK	
UB TIMEOUT L		MAPB	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
CCBE	ADRS ACKN (0) H	CCBE	
	ADRS ACKN (1) H	CCBD, CCBE	
	ALLOW TIMEOUT L	CCBD, CCBE(2)	
	CLK PRI H	CDPH, CDPJ	
	CP SYNC L	SCOPE SYNC	
	DISABLE REQ H	CCBE, CDPJ(2)	
	MBC ADRS ACKN L	RH70	
	MBC REQ ACKN L	CDPK, RH70	
	MEM SYNC SLOW (0) H		
	MEM SYNC SLOW (1) H	CCBC	
	PARITY CLK H	CCBE, CCBK	
	RESTART (0) H	CCBC, CCBE	
	RESTART (1) H	CCBK	
	RESTART SYNC (1) H	CCBD, CCBE(2)	
	T30 (0) H	CCBC, CCBE(2), CCBK	
	T30 (1) H	CCBC, CCBE(4), CCBL	
	T60 (0) H	CCBE(2), CCBK, CCBL	
	T60 (1) H	CCBE(3), CCBH(2), CCBM	
	T90 H	CCBC, CCBE, CCBJ	
	T120 (0) H	CCBE	
	T120 (1) H	CCBC, CCBE(2)	
	T150 HOLD (0) H	CCBD, CCBE(2)	
	T150 HOLD (1) H	CCBC(5)	
	WP L	ADMJ, DTMB	
	CCBF	ADRS 01 L	CCBF(9)
		ADRS 02 L	CCBF(9)
		ADRS 03 L	CCBF(9)
REG D 00 H		MAPH	
REG D 01 H		MAPH	
REG D 02 H		MAPH	
REG D 03 H		MAPH	
REG D 04 H		MAPH	
REG D 05 H		MAPH	
REG D 06 H		MAPH	
REG D 07 H		MAPH	
REG D 08 H		MAPH	
REG D 09 H		MAPH	
REG D 10 H		MAPH	
REG D 11 H		MAPH	
REG D 12 H		MAPH	
REG D 13 H		MAPH	
REG D 14 H		MAPH	
REG D 15 H	MAPH		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CCBH	CLK CONTROL REG L	CCBH(2)
	CLK MAINT REG L	CCBL(4)
	DIS TRAPS H	CCBF
	DIS TRAPS L	CCBJ
	DIS UNI TRAPS H	CCBF
	DIS UNI TRAPS L	CCBJ(2)
	ERR ADRS 00 H	CCBF
	ERR ADRS 01 H	CCBF
	ERR ADRS 02 H	CCBF
	ERR ADRS 03 H	CCBF
	ERR ADRS 04 H	CCBF
	ERR ADRS 05 H	CCBF
	ERR ADRS 06 H	CCBF
	ERR ADRS 07 H	CCBF
	ERR ADRS 08 H	CCBF
	ERR ADRS 09 H	CCBF
	ERR ADRS 10 H	CCBF
	ERR ADRS 11 H	CCBF
	ERR ADRS 12 H	CCBF
	ERR ADRS 13 H	CCBF
	ERR ADRS 14 H	CCBF
	ERR ADRS 15 H	CCBF
	ERR ADRS 16 H	CCBF
	ERR ADRS 17 H	CCBF
	ERR ADRS 18 H	CCBF
	ERR ADRS 19 H	CCBF
	ERR ADRS 20 H	CCBF
	ERR ADRS 21 H	CCBF
	ERR CO H	CCBF
	ERR C1 H	CCBF
	FORCE MISS GPO L	ADMK
	FORCE MISS GP1 L	ADMK
	FORCE MISS ONE H	CCBF, CCBH
	FORCE MISS ONE L	CCBK(3)
	FORCE MISS ZERO H	CCBF, CCBH
	FORCE MISS ZERO L	CCBK(3)
	REPLACE ONE H	CCBF
	REPLACE ONE L	CCBM
	REPLACE ZERO H	CCBF
	REPLACE ZERO L	CCBM
	WRITE ERR REG L	CCBJ

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CCBJ	AOK (0) H	CCBJ(2)
	CACHE REG L	CCBC, CCBJ
	CLK ADRS H	CCBH, CCBJ
	CLR ERR 08 L	CCBK
	CLR ERR 09 L	CCBK
	CLR ERR 13 L	CCBJ(2)
	CLR ERR REG L	CCBJ(3), CCBK(3)
	ERR REG 10 H	CCBF
	ERR REG 11 H	CCBF
	ERR REG 12 H	CCBF
	ERR REG 13 H	CCBF
	ERR REG 14 H	CCBF
	ERR REG 15 H	CCBF
	ERR REG 15 (0) H	CCBJ
	PARITY ABORT H	PDRH
	PARITY TRAP H	TMCA
	READ B H	CCBJ, CCBK(3), CCBM(3)
	REG ENAB H	CCBJ(2), CCBK
	UB MEM CYC L	CCBJ(2)
	VALID CP CYC H	CCBC, CCBJ, CCBL
CCBK	ANY ERR (0) H	CCBJ(2), CCBK
	ANY ERR (1) H	CCBJ, CCBL
	CLR ERR 00 L	CCBK
	CLR ERR 01 L	CCBK
	CLF ERR 04 L	CCBK
	CLR ERR 05 L	CCBK
	CLR ERR 06 L	CCBK
	DATA PE 0 H	CCBK(2)
	DATA PE 1 H	CCBK(2)
	ERR CLK (1) H	CCBK(3)
	ERR REG 00 H	CCBF
	ERR REG 01 H	CCBF
	ERR REG 02 H	CCBF
	ERR REG 03 H	CCBF
	ERR REG 04 H	CCBF
	ERR REG 05 H	CCBF
	ERR REG 06 H	CCBF
	ERR REG 07 H	CCBF
	ERR REG 08 H	CCBF
	ERR REG 09 H	CCBF

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CCBL	CLR MAINT L	CCBL(4)
	HIT REG 00 H	CCBF, CCBL
	HIT REG 01 H	CCBF, CCBL
	HIT REG 02 H	CCBF, CCBL
	HIT REG 03 H	CCBF, CCBL
	HIT REG 04 H	CCBF, CCBL
	HIT REG 05 H	CCBF
	MAINT 01 H	ADML, CCBF
	MAINT 02 H	ADML, CCBF
	MAINT 03 H	ADML, CCBF
	MAINT 04 H	CCBF
	MAINT 04 L	CCBL, DTMN
	MAINT 05 H	CCBF
	MAINT 05 L	CCBL, DTMN
	MAINT 06 H	CCBF
	MAINT 06 L	CCBL, DTMN
	MAINT 07 H	CCBF
	MAINT 07 L	CCBL, DTMN
	MAINT 08 H	CCBF
	MAINT 09 H	CCBF
	MAINT 10 H	CCBF
	MAINT 11 H	CCBF
	MAINT 12 H	CCBF
	MAINT 12 L	CCBL, CDPF
	MAINT 13 H	CCBF
	MAINT 13 L	CCBL, CDPF
	MAINT 14 H	CCBF
	MAINT 14 L	CCBL, CDPF
	MAINT 15 H	CCBF
	MAINT 15 L	CCBL, CDPF
	MAINT MODE H	CCBK
CCBM	ENAB WR SEL 1 H	CCBM(3)
	NON MBC READ H	CCBC, CCBM(2)
	RANDOM (1) H	CCBM
	VALID 0 INPUT L	ADMB, ADMJ(2)
	VALID 1 INPUT L	ADMD, ADMF
	WR OK H	CCBE
	WRITE SEL 0 H	ADMA, DTMB(4)
	WRITE SEL 1 H	ADMC, DTMB(4)

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<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPA	BD 00 H	CDPB
	BD 01 H	CDPB
	BD 02 H	CDPB
	BD 03 H	CDPB
	BD 04 H	CDPB
	BD 05 H	CDPB
	BD 06 H	CDPB
	BD 07 H	CDPB
	BD 08 H	CDPB
	BD 09 H	CDPB
	BD 10 H	CDPB
	BD 11 H	CDPB
	BD 12 H	CDPB
	BD 13 H	CDPB
	BD 14 H	CDPB
	BD 15 H	CDPB
	BD 16 H	CDPB
	BD 17 H	CDPB
	BD 18 H	CDPB
	BD 19 H	CDPB
	BD 20 H	CDPB
	BD 21 H	CDPB
	BD 22 H	CDPB
	BD 23 H	CDPB
	BD 24 H	CDPB
	BD 25 H	CDPB
	BD 26 H	CDPB
	BD 27 H	CDPB
	BD 28 H	CDPB
	BD 29 H	CDPB
	BD 30 H	CDPB
	BD 31 H	CDPB
	BD CLK H	CDPA(4)
	BYTE 0 PAR H	CDPF
	BYTE 1 PAR H	CDPF
	BYTE 2 PAR H	CDPF
	BYTE 3 PAR H	CDPF

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPB	EVEN MUXD00 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD01 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD02 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD03 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD04 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD05 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD06 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD07 L	CDPF, DTMD, DTMJ, DTMP
	EVEN MUXD08 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD09 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD10 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD11 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD12 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD13 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD14 L	CDPF, DTMC, DTMH, DTMP
	EVEN MUXD15 L	CDPF, DTMC, DTMH, DTMP
	ODD MUXD16 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD17 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD18 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD19 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD20 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD21 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD22 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD23 L	CDPF, DTMF, DTML, DTMP
	ODD MUXD24 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD25 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD26 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD27 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD28 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD29 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD30 L	CDPF, DTME, DTMK, DTMP
	ODD MUXD31 L	CDPF, DTME, DTMK, DTMP
	SO H	CDPB(4), CDPF

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPC	CACHE DATA EN H	CDPC(19), CDPD(18)
	CACHE DATA EN L	CDPC
	DATA RDY H	CDPA, CDPK(5)
	MAIN DATA BYTE 0-0 L	CDPC
	MAIN DATA BYTE 0-1 L	CDPC
	MAIN DATA BYTE 0-2 L	CDPC
	MAIN DATA BYTE 0-3 L	CDPC
	MAIN DATA BYTE 0-4 L	CDPC
	MAIN DATA BYTE 0-5 L	CDPC
	MAIN DATA BYTE 0-6 L	CDPC
	MAIN DATA BYTE 0-7 L	CDPC
	MAIN DATA BYTE 0-8 L	CDPC
	MAIN DATA BYTE 1-0 L	CDPC
	MAIN DATA BYTE 1-1 L	CDPC
	MAIN DATA BYTE 1-2 L	CDPD
	MAIN DATA BYTE 1-3 L	CDPC
	MAIN DATA BYTE 1-4 L	CDPC
	MAIN DATA BYTE 1-5 L	CDPC
	MAIN DATA BYTE 1-6 L	CDPD
	MAIN DATA BYTE 1-7 L	CDPC
	MAIN DATA BYTE 1-8 L	CDPC
	MBC CYCLE L	CDPC, CDPE(2)
	MBC DATA ENAB L	CDPC(19), CDPD(18)
	MEM BYTE 0 PAR H	CDPA, RH70
	MEM BYTE 1 PAR H	CDPA, RH70
	MEM D 00 H	CDPA, RH70
	MEM D 01 H	CDPA, RH70
	MEM D 02 H	CDPA, RH70
	MEM D 03 H	CDPA, RH70
	MEM D 04 H	CDPA, RH70
	MEM D 05 H	CDPA, RH70
	MEM D 06 H	CDPA, RH70
	MEM D 07 H	CDPA, RH70
	MEM D 08 H	CDPA, RH70
	MEM D 09 H	CDPA, RH70
	MEM D 10 H	CDPA, RH70
	MEM D 11 H	CDPA, RH70
	MEM D 12 H	CDPA, RH70
	MEM D 13 H	CDPA, RH70
	MEM D 14 H	CDPA, RH70
	MEM D 15 H	CDPA, RH70
	START WRITE H	CCBD, RH70

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPD	MAIN DATA BYTE 2-0 L	CDPD
	MAIN DATA BYTE 2-1 L	CDPD
	MAIN DATA BYTE 2-2 L	CDPD
	MAIN DATA BYTE 2-3 L	CDPD
	MAIN DATA BYTE 2-4 L	CDPD
	MAIN DATA BYTE 2-5 L	CDPD
	MAIN DATA BYTE 2-6 L	CDPD
	MAIN DATA BYTE 2-7 L	CDPD
	MAIN DATA BYTE 2-8 L	CDPD
	MAIN DATA BYTE 3-0 L	CDPD
	MAIN DATA BYTE 3-1 L	CDPD
	MAIN DATA BYTE 3-2 L	CDPD
	MAIN DATA BYTE 3-3 L	CDPD
	MAIN DATA BYTE 3-4 L	CDPD
	MAIN DATA BYTE 3-5 L	CDPD
	MAIN DATA BYTE 3-6 L	CDPD
	MAIN DATA BYTE 3-8 L	CDPD
	MEM BYTE 2 PAR H	CDPA, RH70
	MEM BYTE 3 PAR H	CDPA, RH70
	MEM D 16 H	CDPA, RH70
	MEM D 17 H	CDPA, RH70
	MEM D 18 H	CDPA, RH70
	MEM D 19 H	CDPA, RH70
	MEM D 20 H	CDPA, RH70
	MEM D 21 H	CDPA, RH70
	MEM D 22 H	CDPA, RH70
	MEM D 23 H	CDPA, RH70
	MEM D 24 H	CDPA, RH70
	MEM D 25 H	CDPA, RH70
	MEM D 26 H	CDPA, RH70
	MEM D 27 H	CDPA, RH70
	MEM D 28 H	CDPA, RH70
	MEM D 29 H	CDPA, RH70
	MEM D 30 H	CDPA, RH70
	MEM D 31 H	CDPA, RH70
	OCC L	CDPC(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPE	WRITE MUX 00 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 01 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 02 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 03 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 04 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 05 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 06 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 07 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 08 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 09 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 10 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 11 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 12 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 13 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 14 H	CDPB(2), CDPC, CDPD, CDPF
	WRITE MUX 15 H	CDPB(2), CDPC, CDPD, CDPF
CDPF	MAIN HI PAR OK L	CCBK, DTMM
	MAIN LO PAR OK L	CCBK, DTMM
	MUX BYTE 0 PAR L	CDPF, DTMD, DTMJ, DTMP
	MUX BYTE 1 PAR L	CDPF, DTMC, DTMH, DTMP
	MUX BYTE 2 PAR L	CDPF, DTMF, DTML, DTMP
	MUX BYTE 3 PAR L	CDPF, DTME, DTMK, DTMP
	WRITE MUX HI GEN H	CDPC, CDPD, CDPF(2)
	WRITE MUX LO GEN H	CDPC, CDPD, CDPF(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
CDPH	A REQ L	CDPJ(2)
	B REQ L	CDPJ(2)
	BLOCK A L	CDPH
	BLOCK B L	CDPH
	BLOCK C L	CDPH
	BLOCK D L	CDPH
	C REQ L	CDPJ(2)
	D REQ L	CDPJ(2)
CDPJ	B LAST (1) H	CDPJ(2)
	B LAST (1) L	CDPJ
	C or D LAST (1) H	CDPJ(5)
	C or D LAST (1) L	CDPJ(4)
	D LAST (1) H	CDPJ(2)
	D LAST (1) L	CDPJ
	INIT A L	CDPB(2), CDPE(2), CDPH, CDPJ(6)
	MBC REQ L	CCBB, CDPJ
	REQ CLK H	CDPJ(4)
	SEL DATA CTRLA H	RH70
	SEL DATA CTRLB H	RH70
	SEL DATA CTRLC H	RH70
	SEL DATA CTRLD H	RH70
	SELADRS CTRLA H	CDPH, CDPJ, CDPK
	SELADRS CTRLB H	CDPH, CDPJ, CDPK
	SELADRS CTRLC H	CDPH, CDPJ, CDPK
SELADRS CTRLD H	CDPH, CDPJ, CDPK	
CDPK	DATA RDY L	CDPK(5)
	DATA RDY CNTL A H	RH70
	DATA RDY CNTL B H	RH70
	DATA RDY CNTL C H	RH70
	DATA RDY CNTL D H	RH70
	MBC REQ ACKN H	CDPK(4)
	RDY CLK L	CCBE, CDPK
	READ B L	CDPC(3)
	READ H	CDPB(2), CDPK(5)
	RIP H	CCBD, CCBE, CDPK
	RIP L	CDPA, CDPK(4)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
DTMA	WRD 0 A02 H	DTMC(9), DTMD(9)	
	WRD 0 A03 H	DTMC(9), DTMD(9)	
	WRD 0 A04 H	DTMC(9), DTMD(9)	
	WRD 0 A05 H	DTMC(9), DTMD(9)	
	WRD 0 A06 H	DTMC(9), DTMD(9)	
	WRD 0 A07 H	DTMC(9), DTMD(9)	
	WRD 0 A08 H	DTMC(9), DTMD(9)	
	WRD 0 A09 H	DTMC(9), DTMD(9)	
	WRD 1 A02 H	DTME(9), DTMF(9)	
	WRD 1 A03 H	DTME(9), DTMF(9)	
	WRD 1 A04 H	DTME(9), DTMF(9)	
	WRD 1 A05 H	DTME(9), DTMF(9)	
	WRD 1 A06 H	DTME(9), DTMF(9)	
	WRD 1 A07 H	DTME(9), DTMF(9)	
	WRD 1 A08 H	DTME(9), DTMF(9)	
	WRD 1 A09 H	DTME(9), DTMF(9)	
	WRD 2 A02 H	DTMH(9), DTMJ(9)	
	WRD 2 A03 H	DTMH(9), DTMJ(9)	
	WRD 2 A04 H	DTMH(9), DTMJ(9)	
	WRD 2 A05 H	DTMH(9), DTMJ(9)	
	WRD 2 A06 H	DTMH(9), DTMJ(9)	
	WRD 2 A07 H	DTMH(9), DTMJ(9)	
	WRD 2 A08 H	DTMH(9), DTMJ(9)	
	WRD 2 A09 H	DTMH(9), DTMJ(9)	
	WRD 3 A02 H	DTMK(9), DTML(9)	
	WRD 3 A03 H	DTMK(9), DTML(9)	
	WRD 3 A04 H	DTMK(9), DTML(9)	
	WRD 3 A05 H	DTMK(9), DTML(9)	
	WRD 3 A06 H	DTMK(9), DTML(9)	
	WRD 3 A07 H	DTMK(9), DTML(9)	
	WRD 3 A08 H	DTMK(9), DTML(9)	
	WRD 3 A09 H	DTMK(9), DTML(9)	
	DTMB	A01 L	DTMB(4), DTMM
		CS0 L	DTMC(9), DTMD(9)
		CS1 L	DTME(9), DTMF(9)
		CS2 L	DTMH(9), DTMJ(9)
CS3 L		DTMK(9), DTML(9)	
HI BYTE WP0*1L		DTMC(9), DTME(9)	
HI BYTE WP2*3L		DTMJ(9), DTMK(9)	
LO BYTE WP0*1L		DTMD(9), DTMF(9)	
LO BYTE WP2*3L		DTMJ(9), DTML(9)	
READ H		DTMB(5)	
WRITE H	DTMB(4)		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
DTMC + E	GP0 D08 H	DTMM, DTMN
	GP0 D09 H	DTMM, DTMN
	GP0 D10 H	DTMM, DTMN
	GP0 D11 H	DTMM, DTMN
	GP0 D12 H	DTMM, DTMN
	GP0 D13 H	DTMM, DTMN
	GP0 D14 H	DTMM, DTMN
	GP0 D15 H	DTMM, DTMN
	GP0 HI PAR H	DTMM, DTMN
DTMD + F	GP0 D00 H	DTMD, DTMM, DTMN
	GP0 D01 H	DTMD, DTMM, DTMN
	GP0 D02 H	DTMD, DTMM, DTMN
	GP0 D03 H	DTMM, DTMN
	GP0 D04 H	DTMM, DTMN
	GP0 D05 H	DTMM, DTMN
	GP0 D06 H	DTMM, DTMN
	GP0 D07 H	DTMM, DTMN
	GP0 LO PAR H	DTMM, DTMN
DTMH + K	GP1 D08 H	DTMM, DTMN
	GP1 D09 H	DTMM, DTMN
	GP1 D10 H	DTMM, DTMN
	GP1 D11 H	DTMM, DTMN
	GP1 D12 H	DTMM, DTMN
	GP1 D13 H	DTMM, DTMN
	GP1 D14 H	DTMM, DTMN
	GP1 D15 H	DTMM, DTMN
	GP1 HI PAR H	DTMM, DTMN
DTMJ + L	GP1 D00 H	DTMM, DTMN
	GP1 D01 H	DTMM, DTMN
	GP1 D02 H	DTMM, DTMN
	GP1 D03 H	DTMM, DTMN
	GP1 D04 H	DTMM, DTMN
	GP1 D05 H	DTMM, DTMN
	GP1 D06 H	DTMM, DTMN
	GP1 D07 H	DTMM, DTMN
	GP1 LO PAR H	DTMM, DTMN

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
DTMM	BAD PARITY H	CCBC, CCBJ, MAPH
	CDMX D00 H	PDRA, MAPH
	CDMX D01 H	PDRA, MAPH
	CDMX D02 H	PDRA, MAPH
	CDMX D03 H	PDRA, MAPH
	CDMX D04 H	PDRA, MAPH
	CDMX D05 H	PDRA, MAPH
	CDMX D06 H	PDRA, MAPH
	CDMX D07 H	PDRA, MAPH
	CDMX D08 H	PDRA, MAPH
	CDMX D09 H	PDRA, MAPH
	CDMX D10 H	PDRA, MAPH
	CDMX D11 H	PDRA, MAPH
	CDMX D12 H	PDRA, MAPH
	CDMX D13 H	PDRA, MAPH
	CDMX D14 H	PDRA, MAPH
	CDMX D15 H	PDRA, MAPH
	DMX SO H	DTMM(1)
	HI BYTE PAR H	PDRB
	LO BYTE PAR H	PDRB
DTMN	DATA PAR 0 OK L	CCBC(2), CCBD, CCBK, CCBM, DTMM(2)
	DATA PAR 1 OK L	CCBC(2), CCBD, CCBK, CCBM, DTMM
	GPO HI PAR H	DTMN
	GPO LO PAR H	DTMN
	GP1 HI PAR H	DTMN
	GP1 LO PAR H	DTMN

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
DTMP	BYTE 0 PAR H	DTMM
	BYTE 1 PAR H	DTMM
	BYTE 2 PAR H	DTMM
	BYTE 3 PAR H	DTMM
	EVEN MUX 00 H	DTMM
	EVEN MUX 01 H	DTMM
	EVEN MUX 02 H	DTMM
	EVEN MUX 03 H	DTMM
	EVEN MUX 04 H	DTMM
	EVEN MUX 05 H	DTMM
	EVEN MUX 06 H	DTMM
	EVEN MUX 07 H	DTMM
	EVEN MUX 08 H	DTMM
	EVEN MUX 09 H	DTMM
	EVEN MUX 10 H	DTMM
	EVEN MUX 11 H	DTMM
	EVEN MUX 12 H	DTMM
	EVEN MUX 13 H	DTMM
	EVEN MUX 14 H	DTMM
	EVEN MUX 15 H	DTMM
	ODD MUX 16 H	DTMM
	ODD MUX 17 H	DTMM
	ODD MUX 18 H	DTMM
	ODD MUX 19 H	DTMM
	ODD MUX 20 H	DTMM
	ODD MUX 21 H	DTMM
	ODD MUX 22 H	DTMM
	ODD MUX 23 H	DTMM
	ODD MUX 24 H	DTMM
	ODD MUX 25 H	DTMM
	ODD MUX 26 H	DTMM
	ODD MUX 27 H	DTMM
	ODD MUX 28 H	DTMM
ODD MUX 29 H	DTMM	
ODD MUX 30 H	DTMM	
ODD MUX 31 H	DTMM	
SAPA	APR ADDR 0 H	SAPA(3)
	APR ADDR 1 H	SAPA(3)
	APR ADDR 2 H	SAPA(3)
	APR ADDR 3 H	SAPA(4)
	WRITE HIBYTE KPAR L	SAPA(2)
	WRITE LOBYTE KPAR L	SAPA(2)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPB	APR ADDR 0 H	SAPB(3)
	APR ADDR 1 H	SAPB(3)
	APR ADDR 2 H	SAPB(3)
	APR ADDR 3 SA H	SAPB(4)
	INITA B L	SAPC, SAPD(3), SAPK(2)
	WRITE HIBYTE SPAR L	SAPB(2)
	WRITE LOBYTE SPAR L	SAPB(2)
SAPC	APR ADDR 0 H	SAPC(3)
	APR ADDR 1 H	SAPC(3)
	APR ADDR 2 H	SAPC(3)
	APR ADDR 3 UA H	SAPC(4)
	PULSE BC9B H	SAPA(2), SAPB(2), SAPC(2)
	PULSE BC9C H	SAPD(2), SAPE(2), SAPF(2)
	PULSE BC9D H	SAPC, SAPD(2), SAPK
	WRITE HIBYTE UPAR L	SAPC(2)
WRITE LOBYTE UPAR L	SAPC(2)	
SAPA+B+C	PAF 06 H	SAPJ, SAPM, SAPN
	PAF 07 H	SAPJ, SAPM, SAPN
	PAF 08 H	SAPJ, SAPM, SAPN
	PAF 09 H	SAPJ, SAPM, SAPN
	PAF 10 H	SAPJ, SAPM, SAPN
	PAF 11 H	SAPJ, SAPM, SAPN
	PAF 12 H	SAPJ, SAPM, SAPN
	PAF 13 H	SAPJ, SAPM, SAPN
	PAF 14 H	SAPJ, SAPM, SAPN
	PAF 15 H	SAPJ, SAPM, SAPN
	PAF 16 H	SAPJ, SAPM, SAPN
	PAF 17 H	SAPJ, SAPM, SAPN
	PAF 18 H	SAPJ, SAPM, SAPN
PAF 19 H	SAPJ, SAPM, SAPN	
PAF 20 H	SAPJ, SAPM, SAPN	
PAF 21 H	SAPJ, SAPM, SAPN	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPD	ACF 0 L	SAPL(3)
	ACF 1 L	SAPL(5)
	ACF 2 L	SAPL(4)
	APR ADDR 0 H	SAPD(4)
	APR ADDR 1 H	SAPD(4)
	APR ADDR 2 H	SAPD(4)
	APR ADDR 3 KD H	SAPD(4)
	ATTN (0) H	SAPD
	ATTN (1) H	
	ATTN DATA L	SAPD, SAPE, SAPF
	BR 12 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 13 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 14 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 15 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BUSTC (1) H	SAPD
	GATE ATTN H	SAPD
	GATE WRTN H	SAPD
	MM AF L	SAPD
	MM WC L	SAPD
	PGE EXPN DOWN L	SAPL(2)
	WR A + W L	SAPD, SAPE, SAPF
	WRITE HIBYTE KPDR L	SAPD(2)
	WRITE LOBYTE KPDR L	SAPD
	WRTN DATA L	SAPD, SAPE, SAPF
	WRTN INTO (0) H	SAPD
	WRTN INTO (1) H	
SAPE	APR ADDR0 H	SAPE(4)
	APR ADDR1 H	SAPE(4)
	APR ADDR2 H	SAPE(4)
	APR ADDR3 SD H	SAPE(4)
	BUF INT REG L	SAPE(2)
	KERNEL CS L	SAPD(4)
	KERNEL PAR L	SAPA(4)
	SUPER CS L	SAPE(4)
	SUPER PAR L	SAPB(4)
	USER CS L	SAPF(4)
	USER PAR L	SAPC(4)
	WRITE HIBYTE SPDR L	SAPE(2)
	WRITE LOBYTE SPDR L	SAPE
SAPF	APR ADDR 0 H	SAPF(4)
	APR ADDR 1 H	SAPF(4)
	APR ADDR 2 H	SAPF(4)
	APR ADDR 3 UD H	SAPF(4)
	WRITE HIBYTE UPDR L	SAPF(2)
	WRITE LOBYTE UPDR L	SAPF

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPD+E+F	ACF 0 H	SAPD, SAPL(4), SAPM
	ACF 1 H	SAPD, SAPL(2), SAPM
	ACF 2 H	SAPD, SAPL(2), SAPM
	PGE EXPN DOWN H	SAPD, SAPL(2), SAPM
	PLF 0 H	SAPL, SAPM
	PLF 1 H	SAPL, SAPM
	PLF 2 H	SAPL, SAPM
	PLF 3 H	SAPL, SAPM
	PLF 4 H	SAPL, SAPM
	PLF 5 H	SAPL, SAPM
	PLF 6 H	SAPL, SAPM
	PLFE H	SAPM
	RAM ATTN H	SAPD, SAPM
	RAM WRTN INTO H	SAPD, SAPM
	SAPH	EX MEM FLAG H
VA 01 B H		SAPK(2), SSRH
VA 02 B H		SAPK(2), SSRH
VA 03 B H		SAPK(2), SSRH
VA 05 B H		TMCD, SAPM
VA 06 B H		SAPJ, SAPL, SAPN
VA 07 B H		SAPJ, SAPL, SAPN
VA 08 B H		SAPJ, SAPL, SAPN
VA 09 B H		SAPJ, SAPL, SAPN
VA 10 B H		SAPJ, SAPL, SAPN
VA 11 B H		SAPJ, SAPL, SAPN
VA 12 B H		SAPJ, SAPL, SAPN
VA 13 B H		SAPH, SAPJ, SSRH
VA 14 B H		SAPH, SAPJ, SSRH
VA 15 B H		SAPH, SAPJ, SSRH

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPJ	ADRS 13 H	SAPJ(2), SAPN
	ADRS 14 H	SAPJ
	ADRS 15 H	SAPJ
	ADRS 16 H	SAPJ(2)
	ADRS 17 H	SAPJ(3)
	ADRS 18 H	SAPJ
	ADRS 19 H	SAPJ
	ADRS 20 H	SAPJ
	ADRS 21 H	SAPJ
	COUT B L	SAPJ(2)
	PA 06 H	ADME, SCCA
	PA 07 H	ADME, SCCA
	PA 08 H	ADME, SCCA
	PA 09 H	ADME, SCCA
	PA 10 H	ADME, SCCA, SCCB
	PA 11 H	ADME, SCCA
	PA 12 H	ADMF, SCCA, SCCB, SCCL
	PA 13 H	ADMF, SCCA, SCCB, SCCL
	PA 14 H	ADMF, SCCA, SCCB, SCCL
	PA 15 H	ADMF, SCCA, SCCB, SCCL
	PA 16 H	ADMF, SCCB, SCCK, SCCL
	PA 17 H	ADMF, SCCB, SCCK, SCCL
	PA 18 H	ADMF, SCCK
	PA 19 H	ADMF, SCCK
	PA 20 H	ADMF, SCCK
	PA 21 H	ADMF, SCCK
	PA <17:14> H	SAPJ(2), SAPN
	PA <21:18> H	SAPJ, SAPN
	RELOC B H	SAPJ(4)
	RELOC H	SAPD, SAPJ(4)
	SEL 0 H	SAPJ(4), SAPN(2)
	SEL 0 L	SAPN(2)
	SEL 1 H	SAPJ(5), SAPN(2)
	SEL 1 L	SAPN(2)
	18 BIT UB ADRS H	SAPJ(4)
	18 BIT WRAPAROUND L	SAPJ, SAPN
	22 BIT MAPPING H	SAPN
	22 BIT WRAPAROUND L	SAPJ, SAPN

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPK	APR ADDRO L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	APR ADDR1 L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	APR ADDR2 L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	APR ADDR3 K L	SAPA, SAPD
	APR ADDR3 S L	SAPB, SAPE
	APR ADDR3 U L	SAPC, SAPF
	APR ADDRAO L	SAPA, SAPB, SAPC
	APR ADDRA1 L	SAPA, SAPB, SAPC
	APR ADDRA2 L	SAPA, SAPB, SAPC
	BR 00 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 01 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 02 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 03 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 04 C L	SAPA, SAPB, SAPC
	BR 05 C L	SAPA, SAPB, SAPC
	BR 06 C L	SAPA, SAPB, SAPC
	BR 07 C L	SAPA, SAPB, SAPC
	BR 08 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 09 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 10 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	BR 11 C L	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	C1 B H	SAPK(3)
	C1 B L	SAPK(3)
	DSK H	SAPK(2)
	DSS H	SAPK(2)
	DSU H	SAPK(2)
	HI BYTE B H	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF
	I SPACE CLK L	SAPK(2)
	I SPACEA (0) H	SAPK(3)
	I SPACEB (0) H	SAPK(3)
	IND DATA H	SCCJ, SSRB, SSRE
	IND DATA L	SAPK
	KERNEL SPACE H	SAPK
	LO BYTE B H	SAPA, SAPB, SAPC, SAPD, SAPE, SAPF, SAPK
	REG WR PULSE H	SCCL
	SUPER SPACE H	SAPK
	USER SPACE H	SAPK
	WR OK H	SAPA(2), SAPB(2), SAPC(2)
	WR OKA H	SAPD(2)
	WR OKB H	SAPD, SAPE(2), SAPF(2), SAPK

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SAPL	ABORT COND H	SSRC(3)
	DATIP L	SAPL
	KEY=1*WRI L	SAPL
	KEY=1WR L	SAPL
	KEY=2WR L	SAPL
	KEY=4 L	SAPL
	KEY=5WR L	SAPL
	LENGTH FAULT L	SAPL(2)
	MEM MGMT H	SAPD, SSRD
	MM LENGTH FAULT H	SSRC
	NON RES FAULT H	SAPL, SSRC
	READ ONLY FAULT H	SAPL, SSRC
	READ ONLY FAULT L	SAPL
	WRITE CYCLE H	SAPD, SAPL(4)
	WRITE CYCLE L	SAPL
SAPM	APR BIT 00 H	SSRJ
	APR BIT 01 H	SSRJ
	APR BIT 02 H	SSRJ
	APR BIT 03 H	SSRJ
	APR BIT 04 H	SSRJ
	APR BIT 05 H	SSRJ
	APR BIT 06 H	SSRJ
	APR BIT 07 H	SSRJ
	APR BIT 08 H	SSRJ
	APR BIT 09 H	SSRJ
	APR BIT 10 H	SSRJ
	APR BIT 11 H	SSRJ
	APR BIT 12 H	SSRJ
	APR BIT 13 H	SSRJ
	APR BIT 14 H	SSRJ
	APR BIT 15 H	SSRJ
SAPN	ADRS OVERFLOW H	SAPN
	NOT CACHE ADRS H	TIGA(2), TMCC, TCMF
	SR <21:18> H	SAPN
	UB ADRS L	SAPN(2)
	UNIBUS ADRS L	TMCC, UBCA, SCCB, SCCE, SSRC
	16 BIT MAPPING H	SAPN
18 BIT OVERFLOW H	SAPN	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SCCA	PA 06 B H	SCCB(2), SCCE, SCCL
	PA 06 L	SCCA, SCCB(2), SCCK
	PA 07 B H	SCCB(3), SCCE, SCCL
	PA 07 L	SCCA, SCCB, SCCK
	PA 08 B H	SCCB(2), SCCE, SCCL
	PA 08 L	SCCA, SCCB(2), SCCK
	PA 09 B H	SCCB, SCCE, SCCL
	PA 09 L	SCCA, SCCB, SCCK
	PA 10 B H	SCCL
	PA 10 L	SCCA, SCCK
	PA 11 B H	SCCB, SCCL
	PA 11 L	SCCA, SCCB, SCCE, SCCK
	PA 12 L	SCCK
	PA 13 L	SCCK
	PA 14 L	SCCK
	PA 15 L	SCCK
	VA 00 A H	UBCB(2), SCCJ, SCCL
	VA 00 A L	SCCA, SCCB
	VA 01 A H	SCCB, SCCE(2), SCCJ, SCCL
	VA 01 A L	SCCA, SCCB(2)
	VA 02 A H	SCCB, SCCE(2), SCCJ, SCCL
	VA 02 A L	SCCA, SCCB
	VA 03 A H	SCCB, SCCE(2), SCCJ, SCCL
	VA 03 A L	SCCA, SCCB, SCCE(3)
	VA 04 A H	SCCB, SCCE(2), SCCF, SCCL
	VA 04 A L	SCCA, SCCB
	VA 05 A H	SCCB, SCCE(2), SCCF, SCCL
	VA 05 A L	SCCA, SCCB
	VA 06 A H	SCCE
	VA 06 A L	SCCA, SCCK
	VA 07 A H	SCCE
	VA 07 A L	SCCA, SCCK
	VA 08 A H	SCCE
	VA 08 A L	SCCA, SCCK
	VA 09 A H	SCCE
VA 09 A L	SCCA, SCCK	
VA 10 A H	SCCE	
VA 10 A L	SCCA, SCCK	
VA 11 A H	SCCE	
VA 11 A L	SCCA, SCCK	
VA 12 A H	SCCE	
VA 12 A L	SCCA, SCCK	
VA 13 C L	SCCK	
VA 14 C L	SCCK, SCCN	
VA 15 C L	SCCK, SCCN	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SCCB	KERNEL PAR ADRS L	SCCC, SCCD
	KERNEL PDR ADRS L	SCCC, SCCD
	MMR ADRS L	SCCC
	MMR3 ADRS L	SCCC, SCCD
	PA <8:6> =5 H	SCCB(4)
	PAR ADRS H	SCCB(6)
	PDR ADRS H	SCCB(4)
	SUPER PAR ADRS L	SCCC, SCCD
	SUPER PDR ADRS L	SCCC, SCCD
	SW REG ADRS L	SCCC
	SWR + MMR ADRS L	SCCD
	UB REF H	SCCB(2)
	UB REF L	SCCB, SCCE
	UNIBUS ADRS H	SCCB(2)
	USER PAR ADRS L	SCCC, SCCD
	USER PDR ADRS L	SCCC, SCCD
	SCCC	C1 B L
INT REG H		SAPD, SAPE, SAPN, SCCC(2), SSRJ
INT REGA L		SAPK(3)
INT REGB L		SAPD(3), SAPN, SSRC, SSRD, SSRE
KERNEL PAR (0) H		SAPE(2), SCCC, SCCD
KERNEL PAR (1) H		SAPA(2)
KERNEL PDR (0) H		SAPE, SCCC, SCCD
KERNEL PDR (1) H		SAPD(2)
MMR REG (0) H		SCCC
MMR REG (1) H		SCCC, SSRJ(2)
MMR3 (0) H		SCCC, SSRJ
MMR3 (1) H		SCCC, SCCL
READ MMR3 L		SCCH(2)
SUPER PAR (0) H		SAPE(2), SCCC, SCCD
SUPER PAR (1) H		SAPB(2)
SUPER PDR (0) H		SAPE, SCCC, SCCD
SUPER PDR (1) H		SAPE(2)
SW REG L		
SW REG (0) H		SCCH(2)
USER PAR (0) H		SAPE(2), SCC, SCCD
USER PAR (1) H		SAPC(2)
USER PDR (0) H		SAPE, SCCC, SCCD
USER PDR (1) H		SAPF(2)
WRITE MMRO REG H		SSRC, SSRE
SCCD	APR REG H	SAPD, SCCD
	APR REG L	SSRJ(2)
	INITA B L	SCCF, SCCK, SCCL
	INT CLR A L	SCCC(5)
	INT CLR B L	SCCC(4), SCCD
	INT REG ADRS H	SCCD
	INTD REG L	SCCM(4), SCCN(2)
	INTD REG (1) L	TIGA, TMTF, UBCA(2), SSCD

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SCCE	ERR REG ADRS H	TMCD
	ERR REG ADRS L	SCCE, SCCM(2)
	INTERNAL ADRS H	TMCD, UBCC
	PB ADRS H	TMCD, TMCF
	PIR ADRS H	TMCD, TMCF
	PS ADRS H	TMCD, TMCF(2), UBCB
	SL ADRS H	TMCD, TMCF
	STACK OVERFLOW H	TMCD
	SYS ID ADRS L	SCCM(3), SCCN(3)
	SYS INT REG L	SCCD
	SYS SIZH ADRS L	SCCM
	SYS SIZL ADRS L	SCCM(2), SCCN
	SCCF	CNSL CLR L
CONT H		TIGB(2), UBCF
DEP H		UBCF
DISP ADRS04 H		SCCJ
DISP ADRS05 H		SCCJ
EXAM H		UBCF
GEN REG ADRS H		SCCF
GEN REG (0) H		SCCF(4), SCCK(2)
GEN REG (1) H		SCCF(2), SCCK(3)
HALT H		TIGB, UBCE, UBCF(3)
IND ADRS ERR H		SCCJ
IND 16 BIT MAPPING H		SCCJ
IND 18 BIT MAPPING H		SCCJ
IND 22 BIT MAPPING H		SCCJ
LOAD ADRS H		UBCF
REG EXAM H		UBCF
REG DEP H		UBCF
SINGLE CYCLE H		TIGB, UBCF(2)
START H		UBCF(2)
SCCH	BUS INTD00 L	PDRA
	BUS INTD01 L	PDRA
	BUS INTD02 L	PDRA
	BUS INTD03 L	PDRA
	BUS INTD04 L	PDRA
	BUS INTD05 L	PDRA
	BUS INTD06 L	PDRA
	BUS INTD07 L	PDRA
	BUS INTD08 L	PDRA
	BUS INTD09 L	PDRA
	BUS INTD10 L	PDRA
	BUS INTD11 L	PDRA
	BUS INTD12 L	PDRA
	BUS INTD13 L	PDRA
	BUS INTD14 L	PDRA
	BUS INTD15 L	PDRA
	ENAB INTD DATA H	SCCH(11)

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SCCJ	CONT SW H	SCCF (2)
	DEP SW H	SCCF (2)
	DISP ADRS SELO H	SSRK
	DISP ADRS SEL1 H	SSRK
	DISP ADRS SEL2 H	SSRK
	EXAM SW H	SCCF (2)
	HALT SW H	SCCF
	LOAD ADRS SW H	SCCF (2)
	PNL LOCK H	
	SGL BUS CYC SW H	SCCF
	START SW H	SCCF
	SWR00 H	SCCH
	SWR01 H	SCCH
	SWR02 H	SCCH
	SWR03 H	SCCH
	SWR04 H	SCCF, SCCH
	SWR05 H	SCCF, SCCH
	SWR06 H	SCCF, SCCH
	SWR07 H	SCCF, SCCH
	SWR08 H	SCCF, SCCH
	SWR09 H	SCCF, SCCH
	SWR10 H	SCCF, SCCH
	SWR11 H	SCCF, SCCH
	SWR12 H	SCCF, SCCH
	SWR13 H	SCCF, SCCH
	SWR14 H	SCCF, SCCH
	SWR15 H	SCCF, SCCH
	SWR16 H	SCCF, SCCK
	SWR17 H	SCCF, SCCK
	SWR18 H	SCCF, SCCK
	SWR19 H	SCCF, SCCK
	SWR20 H	SCCF, SCCK
	SWR21 H	SCCF, SCCK

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SCCK	DISP ADRS06 H	SCCJ
	DISP ADRS07 H	SCCJ
	DISP ADRS08 H	SCCJ
	DISP ADRS09 H	SCCJ
	DISP ADRS10 H	SCCJ
	DISP ADRS11 H	SCCJ
	DISP ADRS12 H	SCCJ
	DISP ADRS13 H	SCCJ
	DISP ADRS14 H	SCCJ
	DISP ADRS15 H	SCCJ
	DISP ADRS16 H	SCCJ
	DISP ADRS17 H	SCCJ
	DISP ADRS18 H	SCCJ
	DISP ADRS19 H	SCCJ
	DISP ADRS20 H	SCCJ
	DISP ADRS21 H	SCCJ
	ROM110 L	SCCK
	SR CLR L	SCCK
	SWR 16 B(1) H	SAPJ, SCCK, SCCN
	SWR 17 B(1) H	SAPJ, SCCK, SCCN
	SWR 18 B(1) H	SAPJ, SAPN, SCCK, SCCN
SWR 19 B(1) H	SAPJ, SAPN, SCCK, SCCN	
SWR 20 B(1) H	SAPJ, SAPN, SCCK, SCCN	
SWR 21 B(1) H	SAPJ, SAPN, SCCK, SCCN	
SCCL	BRO0 C H	SCCL
	BRO1 C H	SCCL
	BRO2 C H	SCCL
	BRO4 C H	SCCL
	BUS A <17:00> L	UNIBUS
	ENAB UB MAP H	SCCH, MAPD(3)
	ENAB 22 BIT MAPPING H	SAPJ, SCCF(2), SCCH
	ENBL D K (1) H	SAPK, SCCH
	ENBL D S (1) H	SAPK, SCCH
	ENBL D U (1) H	SAPK, SCCH
MMR3 CLK L	SCCL	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
SCCM	BUS INTD00 L	PDRA	
	BUS INTD01 L	PDRA	
	BUS INTD02 L	PDRA	
	BUS INTD03 L	PDRA	
	BUS INTD04 L	PDRA	
	BUS INTD05 L	PDRA	
	BUS INTD06 L	PDRA	
	BUS INTD07 L	PDRA	
	DATA EN H	SCCM(8)	
	SELO H	SCCM(4)	
	SEL1 H	SCCM(4)	
	SYS ID00 H	SCCM	
	SYS ID01 H	SCCM	
	SYS ID02 H	SCCM	
	SYS ID03 H	SCCM	
	SYS ID04 H	SCCM	
	SYS ID05 H	SCCM	
	SYS ID06 H	SCCM	
	SYS ID07 H	SCCM	
	SCCN	BUS INTD. <15:08> L	PDRA
		CNSL OVERFLOW H	SAPN
		SWR16 L	SCCN
		SWR17 L	SCCN
		SWR18 L	SCCN
		SWR19 L	SCCN
		SWR20 L	SCCN
		SWR21 L	SCCN
		SYS ID08 H	SCCN
SYS ID09 H		SCCN	
SYS ID10 H		SCCN	
SYS ID11 H		SCCN	
SYS ID12 H		SCCN	
SYS ID13 H		SCCN	
SYS ID14 H		SCCN	
SYS ID15 H		SCCN	
SYS SIZE14 H		SAPN, SCCN(2)	
SYS SIZE15 H		SAPN, SCCN(2)	
SYS SIZE16 H		SAPN, SCCN(2)	
SYS SIZE17 H		SAPN, SCCN(2)	
SYS SIZE18 H		SAPN(2), SCCN(2)	
SYS SIZE19 H		SAPN(2), SCCN(2)	
SYS SIZE20 H		SAPN(2), SCCN(2)	
SYS SIZE21 H	SAPN(2), SCCN(2)		

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SSRA	AUTO DEC (0) H	SSRF
	AUTO DEC (1) H	SSRA, SSRF(2)
	BRK:30 (0) H	SSRF
	BRK:30 (1) H	SSRA, SSRE, SSRH
	CLR PSR (1) H	SSRA
	DST (1) H	SSRE
	INH PWE L	GRAC(2)
	KY PH MEM AC(0) H	SSRE(2)
	KY PH MEM AC(1) H	SAPJ, SAPN(2)
	ONE CHANGED (1) H	SSRF(2)
	PS RESTORE (1) H	PDRD, RACK
	ROM 001 H	SSRA
	ROM 010 + 011 H	SSRA
	ROM 011 H	SSRA
	ROM 110 H	SCCK
	ROM OUT 01 H	SSRA(5)
	ROM OUT 02 H	SSRA(2)
	ROM OUT 03 H	SSRA(2)
	ROM OUT 04 H	SSRA
	ROM OUT 05 H	SSRB(4)
	ROM OUT 06 H	SSRA, SSRB
	ROM OUT 06 L	SSRB(2)
	ROM OUT 07 H	SAPD, SAPK, SCCD(2), SSRB
	ROM OUT 08 H	SSRB
	ROM OUT 09 H	SSRB
	ROM OUT 10 H	
	ROM OUT 11 H	SSRB
	ROM OUT 12 H	SSRA, SSRB(5)
	ROM OUT 13 H	SSRB
	ROM OUT 14 H	SSRB
	ROM OUT 15 H	SSRB
	ROM OUT 16 H	SSRB
	ROM OUT 100 H	SSRA
	ROM OUT 101 H	SSRA
	ROM OUT 110 H	SSRA
	ROM OUT 111 H	SSRA

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
SSRB	DSTF7 H	SSRB(3)	
	ENABLE IF NOT U/U H	SSRB	
	I SPACEA L	SAPK	
	I SPACEB L	SAPK	
	IR 15 L	SSRB	
	KERNEL SPACE(1) L	SAPE(2), SAPK, SSRB	
	KS L	SSRB	
	MF/TP SPACE L	SSRB(3)	
	MFP + MTP L	SSRB	
	MMRO MODE0 H	SCCJ, SSRE	
	MMRO MODE1 H	SCCJ, SSRE	
	PMODE= USER H	SSRB(2)	
	PREV = I H	SSRB	
	PS 14 B H	SSRB(2)	
	ROM OUT 12 L	SSRB(3)	
	SRCF 7 H	SSRB	
	SS L	SSRB	
	SUPER SPACE(1) L	SAPE(2), SAPK, SSRB(2)	
	US L	SSRB	
	USER MODE H	SSRB(2)	
	USER SPACE(1) L	SAPE(2), SAPK, SSRB(2)	
	USER/USER L	SSRB	
	SSRC	ABORT FLG B L	SSRD
		ABT FLG (0) H	SSRC
		ABT FLG (1) H	SSRC
CLK MMRO HIB H		SSRC	
CLR ABORT FLG L		PDRH, SSRC	
DATA 13 H		SSRC	
DATA 14 H		SSRC	
DATA 15 H		SSRC	
INH PSEUDO T3 L		TIGA	
KT ABORT FLG H		SSRA(2), SSRC	
KT ABORT FLG L		TMCC(3), TMCE, SAPD(2), SSRC(2), SSRD	
MMRO BIT 13 H		SSRJ	
MMRO BIT 13 L		SSRC	
MMRO BIT 14 H		SSRJ	
MMRO BIT 14 L		SSRC	
MMRO BIT 15 H		SSRJ	
MMRO BIT 15 L		SSRC	
NO ERROR (1) H		SSRE(3), SSRF(2), SSRH	
PRE MMRO BIT 13 H		SSRC	
PRE MMRO BIT 14 H		SSRC	
PRE MMRO BIT 15 H		SSRC	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
SSRD	CLK TRAP H	SSRD(2)
	DATA 12 H	SSRD
	ENABLE MGMT (1) H	SSRD(2), SSRJ
	MEM MGMT B L	SSRD(2)
	MEM MGMT DET (1) H	SSRJ
	MEM MGMT DET (1) L	SSRD
	MEM MGMT TRAP L	TMCA
	MGT TP DET H	SSRD
	MGT TP DET DLY H	SSRD
	MGT TP DET DLY L	SSRD(2)
	MMRO HIB CLK L	SSRD(2), SSRE
	SET MGMT HOLD L	SSRD
	SSRE	CLK MMRO L
CLR INST COMP L		SSRE
DATA 00 H		SSRE
DATA 08 H		SSRE
DST MODE (1) H		SSRE, SSRJ
INST COMP (1) H		SSRE, SSRJ
MMRO BIT 01 H		SSRJ,
MMRO BIT 02 H		SSRJ
MMRO BIT 03 H		SSRJ
MMR 0 BIT 04 H		SSRJ
MMRO BIT 05 H		SSRJ
MMR 0 BIT 06 H		SSRJ
MMRO LOB CLK L		SSRE
RELOC H		SSRC(2), SSRD, SSRE
RELOC L		SAPJ(3), SBCF(2), SSRE
RELOCATING (1) H		SSRE, SSRJ
SET INST COMP L		SSRE
STROBE OK L	SSRC, SSRE	
WRITE MMRO H	SSRC, SSRD, SSRE	
SSRF	CLK BYTE 0 H	SSRF(2)
	CLR MMR1 L	SSRF(5)
	CONST 00 H	SSRF
	CONST (0+1) H	SSRF
	CONST 01 H	SSRF
	CONST 02 H	SSRF
	CONST 03 H	SSRF
	GRA 0 H	SSRF(2)
	GRA 0 B H	SSRF(2)
	GRA 1 B H	SSRF(3)
	GRA 2 B H	SSRF(3)
	GRA 3 B H	SSRF
	MMR1 BIT <15:00> H	SSRJ
	ONE AUTOED (0) H	SSRF
	ONE AUTOED (1) H	SSRF
ONE AUTOED CLK L	SSRF	
RESET MMR1 H	SSRF	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
SSRH	LOAD IR H	SSRE	
	LOAD IR L	SSRF, SSRH(2)	
	MMR2 BIT <15:00> H	SSRJ	
	NEW SEQUENCE CLK H	SSRH(3)	
	VA01 C H	SSRC, SSRH, SSRJ	
	VA01 C L	SSRE, SSRH, SSRJ	
	VA02 C H	SSRC, SSRH, SSRJ(2)	
	VA02 C L	SSRE, SSRH	
	VA13 C H	SSCA, SSRE, SSRH	
	VA14 C H	SSCA, SSRE, SSRH	
	VA15 C H	SSCA, SSRE, SSRH	
	SSRJ	BUS INTD <15:00> L	PDRA
		C1 B L	SSRJ
		ENABLE MX L	SSRJ(8)
		GET OFF L	SCCC, SCCM, SCCN, SSRJ
INT BUS DATA <15:00> H		SSRJ	
MX ADDR SELO H		SSRJ(8)	
MX ADDR SEL1 H		SSRJ(8)	
SSRK		ADRS 1 L	SSRK
	ADRS 2 L	SSRK(2)	
	ADRS 4 L	SSRK	
	ADRS 5 L	SSRK(2)	
	ADRS 6 L	SSRK	
	ADRS 7 L	SSRK(2)	
	BOOT VECTOR L	DAPE, SSRK	
	CONSL I SPACE H	SSRB	
	CONSL KERNEL H	SSRB	
	CONSL PHYS ADRS H	SSRA	
	CONSL PHYS DSPLY L	SSCK, SSRK	
	CONSL SUPER H	SSRB	
	CONSL USER H	SSRB	
	INIT B H	SAPB, SCCD(3), SSRK(3)	
	INIT B L	SSRA(5), SSRB(4), SSRC(3), SSRE(2), SSRF	
	INITA B L	SSRD(3), SSRE(2), SSRH(3)	
	PRGM PHYS DSPLY L	SCCK(4), SSRK	
	PULSE 23 H	SSRA(2), SSRE	
	PULSE 51 H	SSRF	
	PULSE BC 45 H	SSRB, SSRK	
	PULSE BC 89 H	SSRC(2), SSRD, SSRE	
	PULSE BC9 10 H	SSRK	
	PULSE BC9 10 L	SSRC	
	TS3 H	SSRD, SSRE, SSRK	
	TS4 H	SSRH, SSRK	

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
MAPA	ADRS 01 B H	MAPJ
	ADRS 01 H	CCBF(2), CCBH, MAPA, MAPB, MAPE
	ADRS 01 L	MAPA, MAPB, MAPJ
	ADRS 02 H	CCBF, CCBH, MAPB, MAPC, MAPE
	ADRS 03 H	CCBF(2), MAPB, MAPC, MAPE
	ADRS 04 H	MAPA, MAPC, MAPE
	ADRS 04 L	MAPB,
	ADRS 05 H	MAPB, MAPC, MAPE
	ADRS 06 H	MAPB, MAPC, MAPE
	ADRS 07 H	MAPB, MAPE
	ADRS 08 H	MAPA, MAPB, MAPE
	ADRS 08 L	MAPB
	ADRS 09 H	MAPA, MAPB, MAPE
	ADRS 09 L	MAPB
	ADRS 10 H	MAPA, MAPB, MAPE
	ADRS 10 L	MAPB
	ADRS 11 H	MAPA, MAPB, MAPE
	ADRS 11 L	MAPB
	ADRS 12 H	MAPB, MAPE
	ADRS 13 B H	MAPF(2)
	ADRS 13 H	MAPA, MAPB, MAPC, MAPE
	ADRS 14 B H	MAPF(2)
	ADRS 14 H	MAPA, MAPB, MAPC, MAPE
	ADRS 15 B H	MAPF(2)
	ADRS 15 H	MAPA, MAPB, MAPC, MAPE
	ADRS 16 B H	MAPF(2)
	ADRS 16 H	MAPA, MAPB, MAPC, MAPE
	ADRS 17 B H	MAPF(2)
	ADRS 17 H	MAPA, MAPB, MAPC, MAPE
	CA00 H	ADME
	DATA 00 H	CCBH, CCBK, CDPE, MAPA
	DATA 00 L	MAPC, MAPD
	DATA 01 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 01 L	MAPC(2), MAPD(2)
	DATA 02 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 02 L	MAPC(2), MAPD(2)
	DATA 03 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 03 L	MAPC(2), MAPD(2)
	DATA 04 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 04 L	MAPC(2), MAPD(2)
	DATA 05 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 05 L	MAPC(2), MAPD(2)
	DATA 06 H	CCBK, CCBL, CDPE, MAPA
	DATA 06 L	MAPC, MAPD
	DATA 07 H	CCBK, CCBL, CDPE, MAPA
	DATA 07 L	MAPC, MAPD
	DATA 08 H	CCBJ, CCBL, CDPE, MAPA
	DATA 08 L	MAPC, MAPD
	DATA 09 H	CCBJ, CCBL, CDPE, MAPA
	DATA 09 L	MAPC, MAPD

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
MAPA	ADRS 01 B H	MAPJ
	ADRS 01 H	CCBF(2), CCBH, MAPA, MAPB, MAPE
	ADRS 01 L	MAPA, MAPB, MAPJ
	ADRS 02 H	CCBF, CCBH, MAPB, MAPC, MAPE
	ADRS 03 H	CCBF(2), MAPB, MAPC, MAPE
	ADRS 04 H	MAPA, MAPC, MAPE
	ADRS 04 L	MAPB,
	ADRS 05 H	MAPB, MAPC, MAPE
	ADRS 06 H	MAPB, MAPC, MAPE
	ADRS 07 H	MAPB, MAPE
	ADRS 08 H	MAPA, MAPB, MAPE
	ADRS 08 L	MAPB
	ADRS 09 H	MAPA, MAPB, MAPE
	ADRS 09 L	MAPB
	ADRS 10 H	MAPA, MAPB, MAPE
	ADRS 10 L	MAPB
	ADRS 11 H	MAPA, MAPB, MAPE
	ADRS 11 L	MAPB
	ADRS 12 H	MAPB, MAPE
	ADRS 13 B H	MAPF(2)
	ADRS 13 H	MAPA, MAPB, MAPC, MAPE
	ADRS 14 B H	MAPF(2)
	ADRS 14 H	MAPA, MAPB, MAPC, MAPE
	ADRS 15 B H	MAPF(2)
	ADRS 15 H	MAPA, MAPB, MAPC, MAPE
	ADRS 16 B H	MAPF(2)
	ADRS 16 H	MAPA, MAPB, MAPC, MAPE
	ADRS 17 B H	MAPF(2)
	ADRS 17 H	MAPA, MAPB, MAPC, MAPE
	CA00 H	ADME
	DATA 00 H	CCBH, CCBK, CDPE, MAPA
	DATA 00 L	MAPC, MAPD
	DATA 01 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 01 L	MAPC(2), MAPD(2)
	DATA 02 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 02 L	MAPC(2), MAPD(2)
	DATA 03 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 03 L	MAPC(2), MAPD(2)
	DATA 04 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 04 L	MAPC(2), MAPD(2)
	DATA 05 H	CCBH, CCBK, CCBL, CDPE, MAPA
	DATA 05 L	MAPC(2), MAPD(2)
	DATA 06 H	CCBK, CCBL, CDPE, MAPA
	DATA 06 L	MAPC, MAPD
	DATA 07 H	CCBK, CCBL, CDPE, MAPA
	DATA 07 L	MAPC, MAPD
	DATA 08 H	CCBJ, CCBL, CDPE, MAPA
	DATA 08 L	MAPC, MAPD
	DATA 09 H	CCBJ, CCBL, CDPE, MAPA
	DATA 09 L	MAPC, MAPD

<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>	
MAPA (Cont.)	DATA 10 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 10 L	MAPC, MAPD	
	DATA 11 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 11 L	MAPC, MAPD	
	DATA 12 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 12 L	MAPC, MAPD	
	DATA 13 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 13 L	MAPC, MAPD	
	DATA 14 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 14 L	MAPC, MAPD	
	DATA 15 H	CCBJ, CCBL, CDPE, MAPA	
	DATA 15 L	MAPC, MAPD	
	MAPB	BUS PBL	UNIBUS
		BUS SSYN L	UNIBUS
CO H		ADMJ	
C1 H		ADMJ, MAPB(2), MAPJ	
CACHE REG H		CCBC, CCBD, CCBH, CCBJ	
CACHE REG L		MAPB, MAPF, MAPH(4)	
MSYN H		MAPB, MAPF(2), MAPH(3), MAPJ	
MSYN MAT H		MAINTENANCE BOARD	
PB DATA H		CCBJ(3), MAPB	
REG OP H		MAPC, MAPJ	
REG OP L		MAPB, MAPC(2), MAPJ	
REG SSYN L		MAPB(3)	
TIMEOUT (0) H		MAPB	
WRITE HI WORD L		MAPC(2), MAPD(2)	
WRITE LO WORD L		MAPC(4), MAPD(4)	
77X <X1X> XXH		MAPB(3)	
MAPC		EN HI REG L	MAPC, MAPD(6)
	EN LO REG L	MAPC(6)	
	INDA 1 H	MAPC(6), MAPD(6)	
	INDA 1 L	MAPC	
	INDA 2 H	MAPC(6), MAPD(6)	
	INDA 2 L	MAPC	
	INDA 3 H	MAPC(6), MAPD(6)	
	INDA 3 L	MAPC	
	INDA 4 H	MAPC(6), MAPD(6)	
	INDA 4 L	MAPC	
MAPD	ENAB MAP A H	MAPE(2)	
	ENAB MAP A L	MAPD, MAPE	
	ENAB MAP B H	MAPE	
	ENAB MAP B L	MAPD, MAPE	
	ENAB MAP C H	MAPE	
	ENAB MAP C L	MAPD, MAPE(2)	

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<u>PRINT</u>	<u>SIGNAL NAME</u>	<u>PRINT WHERE USED</u>
MAPC + D	RA <20:01> H RA 21 H	MAPE, MAPJ MAPE(2), MAPJ
MAPE	CA <11:01> H CA <21:12> H COUT 2 L COUT 4 L	ADME ADMF MAPE MAPE
MAPF	CACHE BUS ADRS L HI13 JUMPER H HI14 JUMPER H HI15 JUMPER H HI16 JUMPER H HI17 JUMPER H LO13 JUMPER H LO14 JUMPER H LO15 JUMPER H LO16 JUMPER H LO17 JUMPER H LO LIM OK H PAR ADRS OK H UB REQUEST H UB REQUEST (1) L UP LIM OK H	MAPJ MAPF MAPF MAPF MAPF MAPF MAPF MAPF MAPF MAPF MAPF MAPF(2) MAPB MAPF CCBB MAPF(2)
MAPH	CA DATA <15:00> (1) H PAR ERR (1) H	MAPJ MAPB
MAPJ	BUS D <15:00> L C1 L DATI L EN BUS H	UNIBUS MAPB, MAPJ MAPJ (6) MAPB (4), MAPJ (16)