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1. PURPOSE

THE 1800 RELOCATABLE DIAGNOSTIC LOADER IS USED TO LOAD THE DIAGNOSTIC MONITOR AND PROGRAMS WHICH RUN UNDER CONTROL OF THE DIAGNOSTIC MONITOR. THE LOADER ALSO LOADS NON-MONITOR PROGRAMS WHOSE OBJECT DECKS ARE IN THE SAME FORM AS THE OUTPUT OF THE 1800 ASSEMBLER (12-4 FORMAT). (THE 1800 RELOCATABLE DIAGNOSTIC LOADER WILL NOT LOAD PROGRAMS WHOSE OBJECT DECK IS COMPATIBLE WITH EITHER THE 1800 BASIC DIAGNOSTIC LOADER OR THE 1800 AUXILIARY STORAGE LOADER.)

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES.

PROGRAMS MAY NOT HAVE ORG ADDRESSES OF /3000 TO /7500 SINCE THIS IS RESERVED FOR WAITS AND ERROR TRAPS.

2.2 EQUIPMENT PREREQUISITES

- A. 1801 OR 1802 PROCESSOR CONTROLLER
- B. 1442 CARD READER/PUNCH

3. USE PROCEDURE

3.1 LOADING AND OPERATING

THE 1800 RELOCATABLE LOADER DECK CONSISTS OF SEVEN (8-8 FORMAT) CARDS. THE RELOCATABLE LOADER IS CAPABLE OF LOADING-

- 1. ABSOLUTE BINARY DECKS (12-4 FORMAT)
- 2. RELOCATABLE BINARY DECKS (WHOSE ORIGIN IS /07FF)
- 3. ABSOLUTE HEX CORRECTION CARDS
- 4. RELOCATABLE HEX CORRECTION CARDS
- 5. ABSOLUTE HEX TRANSFER CARDS
- 6. EDIT CARDS (OF THE TYPE REQUIRED FOR PROGRAMS WHICH RUN UNDER

CONTROL OF THE DIAGNOSTIC MONITORS)

TO LOAD A PROGRAM-

A. AT 1442 CARD READ/PUNCH-

- 1. DEPRESS NPRO PUSHBUTTON TO EJECT ANY CARDS LEFT IN MACHINE.
- 2. PLACE 1800 RELOCATABLE LOADER IN FRONT OF DECK(S) TO BE LOADED AND PLACE CARDS IN HOPPER. PLACE CARDS FACE DOWN WITH NINE EDGE FORWARD.
- 3. DEPRESS START PUSHBUTTON ON 1442. READY INDICATOR SHOULD LIGHT.

B. USE 1800 PC CONSOLE TO CLEAR STORAGE AS FOLLOWS-

- 1. SET DATA ENTRY SWITCHES TO /7OFF.
- 2. SET CHECK STOP SWITCH TO 'OFF'.
- 3. SET WRITE STG PRO SWITCH TO 'YES'.
- 4. HOLD CLEAR STORAGE PUSHBUTTON DOWN AND DEPRESS START PUSHBUTTON TO CLEAR STORAGE.
- 5. DEPRESS STOP PUSHBUTTON TO STOP CLEARING OPERATION.

C. TO INITIATE PROGRAM LOADING-

- 1. SET OPERATIONS MONITOR SWITCH TO 'OFF'.
- 2. SET DISABLE INTERRUPT SWITCH TO 'OFF'.
- 3. SET CHECK STOP SWITCH TO 'ON'.
- 4. SET WRITE STOR PROT BITS SWITCH TO 'YES'.
- 5. SET 1800 PC SENSE/PROGRAM AND DATA ENTRY SWITCHES AS REQUIRED BY PROGRAM TO BE LOADED.
- 6. DEPRESS RESET PUSHBUTTON.
- 7. DEPRESS PROGRAM LOAD PUSHBUTTON. (PROGRAM SHOULD LOAD)

3.2 WAITS

SEE THE WAIT SECTION OF THE LISTING FOR REGISTER VALUES AND WAIT DESCRIPTION.

4. PRINTOUTS (NONE)

5. COMMENTS

5.1 THE FOLLOWING ARE THE MAJOR ELEMENTS OF THE 1800 RELOCATABLE DIAGNOSTIC LOADER-

- 5.1.1 BOOTSTRAP ROUTINE-- IS A SET OF INSTRUCTIONS ENTERED INTO THE PROCESSOR CONTROLLED BY THE IPL (INITIAL PROGRAM LOADER) MODE WHOSE FUNCTION IS TO READ IN THE REMAINDER OF THE LOADER.
- 5.1.2 READ ROUTINE -- CHECKS 1442 FOR PROPER STATUS, READS A CARD INTO LOCATION /0000 THROUGH /004F, CHECKS FOR SATISFACTORY COMPLETION OF THE READ OPERATION, AND DETERMINES WHETHER THE CARD READ IS A BINARY CARD OR A HEXADECIMAL (CORRECTION OR EDIT CARD).
- 5.1.3 BINARY PACK ROUTINE -- TAKES DATA FOUND IN LOCATIONS /0000 THROUGH /004F (12 BITS PER CORE WORD) AND PACKS IT INTO LOCATION /0000 THROUGH /0035 (16 BITS PER CORE WORD).
- 5.1.4 CHECKSUM ROUTINE -- COMPUTES CHECKSUM OF A BINARY CARD, WAITS IF CHECKSUM IS IN ERROR.
- 5.1.5 MOVE ROUTINE -- MOVES DATA FROM /0000 THROUGH /0035 TO PROPER CORE LOCATION. CHECKS FOR EXCEEDING CORE SIZE. ADDS IN RELOCATION FACTOR WHEN REQUIRED.
- 5.1.6 RELOCATABLE HEADER ROUTINE -- ENTERED WHEN A RELOCATABLE HEADER CARD

IS FOUND. COMPUTES A RELOCATION FACTOR FOR PROGRAM THAT FOLLOWS.

- 5.1.7 ABSOLUTE HEADER ROUTINE -- ENTERED WHEN AN ABSOLUTE HEADER CARD IS FOUND. SETS RELOCATION FACTOR TO ZERO.
- 5.1.8 TRANSFER ROUTINE -- ENTERED WHEN A TRANSFER CARD IS FOUND. COMPUTES THE NEXT LOCATION AVAILABLE FOR LOADING IF ANOTHER PROGRAM FOLLOWS. TRANSFERS CONTROL TO THE LOCATION SPECIFIED ON THE TRANSFER CARD.
- 5.1.9 HEX TO BINARY CONVERSION ROUTINE -- CONVERTS A HEXADEcimal CARD TO BINARY. ADDS IN RELOCATION FACTOR IF REQUIRED.

5.2 CARD RECOGNITION

THE FOLLOWING ARE CARDS WHICH CAN BE LOADED BY THE 1800 RELOCATABLE DIAGNOSTIC LOADER.

- 5.2.1 ABSOLUTE HEADER CARDS HAVE A 1 PUNCH IN COLUMN 4.
- 5.2.2 RELOCATABLE HEADER CARDS HAVE A 0 (ZERO) PUNCH IN COLUMN 4.
- 5.2.3 NORMAL DATA CARDS HAVE NO PUNCHES IN ROW 12 IN COLUMN 1. AN ADDRESS IN ROWS 11 THROUGH 9 IN COLUMN 1 AND ROWS 12 THROUGH 1 IN COLUMN 2. A CHECKSUM IN ROWS 2 THROUGH 9 OF COLUMN 2 AND ROWS 12 THROUGH 5 OF COLUMN 3. A 12, 0 PUNCH IN COLUMN 4, A WORD COUNT IN ROWS 4 THROUGH 9 OF COLUMN 4. A RELOCATION FIELD (WHICH MAY BE BLANK) IN COLUMN 5 THROUGH 12. DATA IN COLUMNS 13 THROUGH 72. A SEQUENCE NUMBER IN COLUMNS 73-80.
- 5.2.4 BINARY TRANSFER CARDS HAVE 12, 11, 0, 1 PUNCHES IN COLUMN 4 AND A WORD COUNT OF ZERO (NO PUNCHES IN ROWS 4 THROUGH 9 IN COLUMN 4).
- 5.2.5 HEXADEcimal TRANSFER CARDS HAVE A 12 PUNCH IN COLUMN 1, A TRANSFER ADDRESS IN COLUMN 2 THROUGH 5 AND NO PUNCHES IN COLUMNS 6 AND 7.
- 5.2.6 HEXADEcimal CORRECTION CARDS HAVE A 12 PUNCH IN COLUMN 1. AN ADDRESS IN COLUMN 2 THROUGH 5. DATA IN COLUMNS 6 THROUGH 80. DATA IS GROUPED 5 COLUMNS TO ONE CORE WORD. THE FIRST COLUMN OF EACH GROUP SPECIFIES WHETHER OR NOT THE GROUP REQUIRES A RELOCATION FACTOR. IF THE FIRST COLUMN OF A GROUP IS BLANK A RELOCATION FACTOR WILL NOT BE ADDED. IF THE FIRST COLUMN OF A GROUP CONTAINS AN R (11,9 PUNCH) A RELOCATION FACTOR WILL BE ADDED TO THE FIELD. LOADING OF THE CARD IS TERMINATED BY TWO SEQUENTIAL BLANK COLUMNS.
- 5.2.7 EDIT CARDS HAVE 12, 5 PUNCHES IN COLUMN 1. DATA IS GROUPED 4 COLUMNS PER CORE WORD WITH A BLANK COLUMN AFTER EACH GROUP. LOADING OF THE CARD IS TERMINATED BY TWO SEQUENTIAL BLANK COLUMNS.

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```

      ABS      8B100020
      ORG      /30F8 8B100030
      *        8B100040
      *        8B100050
      *        8B100060
      *        8B100070
      *        8B100080
      *        8B100090
      *        8B100100
      *        8B100110
      *        8B100120
      *        8B100130
      *        8B100140
      *        8B100150
      *        8B100160
      *        8B100170
      *        8B100180
      *        8B100330
      *        8B100340
      *        8B100350
      *        8B100360
      *        8B100370
      *        8B100380
      *        8B100390
      *        8B100400
      *        8B100410
      *        8B100420
      *        8B100430
      *        8B100440
      *        8B100450
      *        8B100460
      *        8B100470
      *        8B100480
      *        8B100490
      *        8B100500
      *        8B100510
      *        8B100520
      *        8B100530
      *        8B100540
      *        8B100550
      *        8B100560
      *        8B100570
      *        8B100580
      *        8B100590
      *        8B100600
      *        8B100610
      *        8B100620
      *        8B100630
      *        8B100640
      *        8B100650
      *        8B100660
      *        8B100670
      *        8B100680
      *        8B100690
      *        8B100700
      *        8B100710
      *        8B100720
      *        8B100730
      *        8B100740
      *        8B100750
      *        8B100760
      *        8B100770
      *        8B100780
      *        8B100790
      *        8B100800
      *        8B100810
      *        8B100820
      *        8B100830

```

```

002C 0 6100
002D 0 C007
002E 0 D1FF
002F 0 7500 1000
0031 0 1000
0032 0 1010
0033 0 D1FF
0034 0 C400 FFFF
0036 0 4C20 002F
0038 0 71FF
0039 0 1000
003A 0 6D00 0126
003C 0 7013
0050
0050 0 0817
0051 0 0818
0052 0 C01D
0053 0 4CA0 0123
0055 0 0816
0056 0 0819
0057 0 4C04 0056
0059 0 1003
005A 0 4828
005B 0 6814
005C 0 4C02 0072
005E 0 C0A1
005F 0 4C28 00F0
0061 0 1810
0062 0 D07A
0063 0 7010
0064 0 3000
0065 0 1084
0066 0 1088
0067 0 108C
0068 0000
0068 0 FFFF
0069 0 0480
006A 0 FFFF
006B 0 0481
006C 0 0000
006D 0 1600
006E 0 0001
006F 0 07FF
0070 0 0000
0071 0 1701
0072 0 30FB
0073 0 70DE
0074

```

```

*        4K TO 65K CORE SIZE DETERMINATION RTN *
*        *
*****
LD36 LDX 1 0      SET CONTROL INDEX
      LD   STGCK&1 GET CONSTANT FFFF & SET
      STO 1 -1     IN MAXIMUM ADDRESS
*
STGLP MDX L1 /1000 ADVANCE CONTROL INDEX
      NOP 0        SAFETY NOP FOR 32K CORE
      SLA 16      CLEAR ACCUMULATOR AND SET
      STO 1 -1     IN 4K CORE BLOCK MAX ADDR
*
STGCK LD L /FFFF  GET MAX CORE ADDRESS DATA
      BSC L STGLP,Z CHECK IT FOR ZERO
*
*
MDX 1 -1         DECREMENT X1 TO ACTUAL
*
*
NOP 0           SAFETY NOP FOR 32K CORE
STX L1 ULIM     STORE MAX CORE SIZE
MDX RD05        BR TO CONTINUE
*****
ORG 80          CARD 2
*
* THIS ROUTINE READS THE OBJECT CARDS
*
RD05 XIO MSK MASK
      XIO MSK2 MASK 2ND SET
RD20 LD DSW CK LAST CARD SW
      BSC I MLC0,Z GO TO MONITOR IF ON
      XIO READ READ A CARD
RD25 XIO DSW CK READY
      BSC L RD25,E XFER IF NOT READY
      SLA 3 ERR TO CARRY, LAST CD= 80
      BSC &Z IS LAST CARD ON
      STX DSW SET LAST CARD SW
      BSC L W30FB,C ERROR BIT ON
      LD LD22
      BSC L HB05,&Z
      SRA 16
      STO HEXSW INDICATE BINARY CARD
      MDX SB05
*
K3000 DC /3000 CONSTANT
SL SLT 4
      SLT 8
      SLT 12
      BSS E 0
MSK DC /FFFF
      DC /0480
MSK2 DC /FFFF
      DC /0481
      DC LD22
      DC /1600
ONE DC 1
*
RLBA DC 2047 **KEEP AT /6F, BASE ADDRESS
*
DSW DC 0 SENSE DSW & LAST CD SW
      DC /1701
*
W30FB DC /30FB ERROR LIST ON IN DSW
      MDX RD20
*****
ORG 116 CARD 3
*
*

```

FL

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```

* THIS RT PACKS BINARY DATA AND LEAVES IT IN
* LOCATIONS 0000-0040 .
*
0074 0 61B8 SB05 LDX 1 -72
0075 0 6300 LDX 3 0
0076 0 62FD SB06 LDX 2 -3
0077 0 C268 SB07 LD 2 SL&3
0078 0 D004 STO SB10
0079 0 C149 LD 1 73 CDIN&73
007A 0 18D0 RTE 16
007B 0 C148 LD 1 72 CDIN&72
007C 0 1804 SRA 4
007D 0 1000 SB10 SLA 0
007E 0 D300 STO 3 0 CDIN
007F 0 7301 MDX 3 1
0080 0 7101 MDX 1 1
0081 0 7201 MDX 2 1
0082 0 70F4 MDX SB07 FINISHED
0083 0 7101 MDX 1 1
0084 0 70F1 MDX SB06

*
* THIS RT DETERMINES WHETHER THE DATA CARD IS
* 1 ABSOLUTE HDR CARD 2 RELOCATABLE HDR CARD
*
0085 0 C102 LD 1 2 CK FOR HDR CARDS
0086 0 4C18 0050 BSC L RD05,&- IGNORE BLANK CARD
0088 0 E052 AND LB20
0089 0 9052 S LB25
008A 0 4C18 00E1 BSC L ABHED,&- BCH IF ABSOL HEAD CARD
008C 0 904F S LB25
008D 0 4C18 00DF BSC L RLHED,&- BCH IF RELOC HEAD CARD

*
008F 0 7400 00D8 MDX L SW,0
0091 0 7005 MDX TAKE

*
0092 0 C100 LD 1 0 GET CARD ADRS
0093 0 B0D0 CMP K3000 CMP 3000
0094 0 B068 CMP K74FF GRTR 3000 CMP 74FF
0095 0 7001 MDX TAKE LESS 3000 SAME GRTR 74FF
0096 0 704C MDX PASS EQUAL 3000 SAME LESS 74FF
0097 0 6840 TAKE STX SW LOAD PROGRAM
*****
0098 ORG 152 CARD 4

*
0098 0 C040 LD CDCT
0099 0 62CA LDX 2 -54
009A 0 8236 HSCK A 2 54
009B 0 4802 BSC C
009C 0 80D1 A ONE
009D 0 7201 MDX 2 1
009E 0 70FB MDX HSCK
009F 0 80CE A ONE
00A0 0 4820 BSC Z
00A1 0 30F9 W30F9 DC /30F9 CHECK SUM ERROR
00A2 0 C102 LD 1 2 GET CDIN&2
00A3 0 E036 AND LB15
00A4 0 D039 STO PCAM
00A5 0 4818 BSC &- SKIP IF NOT XFER CARD
00A6 0 703F MDX XFRCD BRANCH TO XFER RT

*
* THIS RT PLACES DATA FIELDS INTO THE CORRECT
* CORE LOCATIONS ANY ADDS IN A RELOCATION
* FACTOR IF REQUIRED.
*
00A7 0 6209 LDX 2 9 SET FOR 1ST DATA WD
00A8 0 6100 LDX 1 0
00A9 0 C033 LD HEXSW CK FOR EDIT CARD
00AA 0 1007 SLA 7

```

```

8B101520
8B101530
8B101540
8B101550
8B101560
8B101570
8B101580
8B101590
8B101600
8B101610
8B101620
8B101630
8B101640
8B101650
8B101660
8B101670
8B101680
8B101690
8B101700
8B101710
8B101720
8B101730
8B101740
8B101750
8B101760
8B101770
8B101780
8B101790
8B101800
8B101810
8B101820
8B101830
8B101840
8B101850
8B101860
8B101870
8B101880
8B101890
8B101900
8B101910
8B101920
8B101930
8B101940
8B101950
8B101960
8B101970
8B101980
8B101990
8B102000
8B102010
8B102020
8B102030
8B102040
8B102050
8B102060
8B102070
8B102080
8B102090
8B102100
8B102110
8B102120
8B102130
8B102140
8B102150
8B102160
8B102170
8B102180
8B102190

```

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```

00AB 0 4CA8 0124 BSC 1 MECD,&Z XFER IF EDIT CARD 8B102200
00AD 0 6A06 STX 2 LB10&1 8B102210
00AE 0 C100 LD 1 0 8B102220
00AF 0 8077 A UPPER 8B102230
00B0 0 D100 STO 1 0 STO IN CDIN 8B102240
00B1 0 6680 0000 LDX 12 LD22 8B102250
00B3 0 C500 0000 LB10 LD L1 0 8B102260
00B5 0 D200 STO 2 0 8B102270
00B6 0 6A72 STX 2 TEMP CK FOR ECEEDING CORE 8B102280
00B7 0 C071 LD TEMP 8B102290
00B8 0 F06D EOR ULIM 8B102300
00B9 0 4820 BSC Z 8B102310
00BA 0 7002 MDX OVER1 8B102320
00BB 0 30FC W30FC DC /30FC ERROR EXCEEDED CORE SIZE 8B102330
***** 8B102340
00BC ORG 188 CARD 5 8B102350
*
00BC 0 70FE MDX W30FC 8B102360
00BD 0 7201 OVER1 MDX 2 1 8B102370
00BE 0 7101 MDX 1 1 8B102380
00BF 0 74FF 00DE MDX L PCAM,-1 SKIP IF WORD COUNT ZERO 8B102390
00C1 0 70F1 MDX LB10 8B102400
00C2 0 C01A LD HEXSW 8B102410
00C3 0 4C28 0050 BSC L RD05,&Z 8B102420
00C5 0 6780 0000 RLDAT LDX 13 LD22 SET X3 TO START ADDR 8B102430
00C7 0 62FA LDX 2 -6 SET FOR 6 CONTROL WD 8B102440
00C8 0 6108 LDX 1 8 8 LOCS PER WD 8B102450
00C9 0 C209 CKFLD LD 2 9 GET RELOC CONTROL 8B102460
00CA 0 1002 SLA 2 8B102470
00CB 0 D209 STO 2 9 8B102480
00CC 0 4C02 00D4 BSC L RLCAT,C BRANCH IF RELOC FIELD 8B102490
00CE 0 7301 INCRE MDX 3 1 8B102500
00CF 0 71FF MDX 1 -1 TEST FOR CNTRL WD END 8B102510
00D0 0 70F8 MDX CKFLD NO 8B102520
00D1 0 7201 MDX 2 1 TEST FOR FIN ALL WDS 8B102530
00D2 0 70F5 MDX CKFLD-1 NO 8B102540
00D3 0 700F MDX PASS 8B102550
00D4 0 C300 RLCAT LD 3 0 ADD IN RELOCATION FACTOR 8B102560
00D5 0 8051 A UPPER 8B102570
00D6 0 D300 STO 3 0 8B102580
00D7 0 70F6 MDX INCRE 8B102590
00D8 0 0000 SW DC /0000 IF ZERO CK FOR WAITS-TRAPS 8B102600
00D9 0 0000 CDCT DC 0 8B102610
00DA 0 003F LB15 DC /003F 8B102620
00DB 0 0F00 LB20 DC /0F00 8B102630
00DC 0 0100 LB25 DC /0100 8B102640
00DD 0 0000 HEXSW DC 0 8B102650
00DE 0 0000 PCAM DC 0 WORD COUNT 8B102660
* 8B102670
* THIS RT. HANDLES RELOCATABLE HDR CARDS. 8B102680
* 8B102690
00DF 0 C045 RLHED LD NLOC COMPUTE RELOC FACTOR 8B102700
***** 8B102710
00E0 ORG 224 CARD 6 8B102720
* 8B102730
00E0 0 908E S RLBA 8B102740
* 8B102750
* THIS RT. HANDLES ABSOLUTE HDR CARDS. 8B102760
* 8B102770
00E1 0 D045 ABHED STO UPPER 8B102780
00E2 0 69F6 STX 1 CDCT 8B102790
00E3 0 7401 00D9 PASS MDX L CDCT,1 INCR CARD COUNT 8B102800
00E5 0 6050 LDX 80 8B102810
* 8B102820
* THIS RT. HANDLES BINARY AND HEX XFER CARDS 8B102830
* 8B102840
00E6 0 C100 XFRCD LD 1 0 8B102850
00E7 0 8086 A ONE 8B102860
8B102870

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RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```

00E8 0 803E      A      UPPER      8B102880
00E9 0 D03B      STO     NLOC      SET NEXT AVAIL LOC 8B102890
00EA 0 C103      LD      1 3      SET UP TO XFER     8B102900
00EB 0 803B      XFR2   A.      UPPER      8B102910
00EC 0 69EB      STX    1 SW      CLEAR SW          8B102920
00ED 0 D001      STO     XFER&1    8B102930
00EE 0 4C00 0000 XFER   BSC L 0    8B102940
*
* THIS RT CONVERTS HEX TO BINARY AND LEAVES I
* IN LOCATIONS 0000-0010 , FIELDS ARE RELOCATED
* IF REQUIRED.
*
00F0 0 61AF      HB05   LDX 1 -81   8B102950
00F1 0 D0EB      STO     HEXSW     SET FOR HEX OR EDIT CD 8B102960
00F2 0 1810      SRA    16         8B102970
00F3 0 D0EA      STO     PCAM      8B102980
00F4 0 1810      HB06   SRA    16   8B102990
00F5 0 D032      STO     RLREQ     8B103000
00F6 0 C151      LD      1 81      CK FOR RELOC FIELD 8B103010
00F7 0 1001      SLA    1          8B103020
00F8 0 4828      BSC    &Z        8B103030
00F9 0 682E      STX    RLREQ     8B103040
00FA 0 7101      MDX    1 1       8B103050
00FB 0 7006      MDX    HB07      8B103060
*
* THIS RT DETERMINES WHETHER A HEX CARD IS A
* 1 DATA CD 2 EDIT CD 3 XFER CARD .
*
00FC 0 6201      LH05   LDX 2 1    SET X2 # CDIN&1    8B103070
00FD 0 74FF 00DE K74FF MDX L PCAM,-1 8B103080
00FE 0 70A8      MDX    LB06      8B103090
0100 0 C2FF      LD      2 -1     CDIN              8B103100
0101 0 70E9      MDX    XFR2      BRANCH TO XFER RT 8B103110
0102 0 6204      HB07   LDX 2 4    8B103120
0103 0 1004      HB10   SLA 4      8B103130
*****
0104             ORG    260     CARD 7          8B103140
*
0104 0 D025      STO     TEMP1     8B103150
0105 0 C151      LD      1 81      CDIN&81          8B103160
0106 0 4C18 00FC BSC L LH05,&-    8B103170
0108 0 6300      LDX    3 0        CONVERT 1 HEX CUL TO BIN 8B103180
0109 0 4828      BSC    &Z        SKIP IF NOT A-F      8B103190
010A 0 7309      MDX    3 9        ADD 9 FOR ALPHA     8B103200
010B 0 1003      SLA    3          ELIMINATE ZONE BITS 8B103210
010C 0 4C18 0115 BSC L HTBZ,&-    XFER IF HEX CHAR # 0 8B103220
010E 0 7301      MDX    3 1        8B103230
010F 0 4C28 0113 HTOB1 BSC L HTBX,&Z XFER IF BIT IS FOUND 8B103240
0111 0 1001      SLA    1          PREPARE TO LK AT NEXT BIT 8B103250
0112 0 70FB      MDX    HTOB1-1   8B103260
0113 0 6815      HTBX   STX 3 TEMP 8B103270
0114 0 C014      LD      TEMP     LOAD BINARY BITS    8B103280
0115 0 E814      HTBZ   OR  TEMP1  ADD TO PREVIOUS CHARS 8B103290
0116 0 7101      MDX    1 1       8B103300
0117 0 72FF      MDX    2 -1      8B103310
0118 0 70EA      MDX    HB10      8B103320
0119 0 6780 00DE LDX I3 PCAM      8B103330
011B 0 7400 0128 MDX L RLREQ,0    8B103340
011D 0 8009      A      UPPER     8B103350
011E 0 D300      STO    3 0       8B103360
011F 0 7401 00DE MDX L PCAM,1     8B103370
0121 0 70D2      MDX    HB06      8B103380
*
0122 0 30F8      W30F8 DC /30F8   EDIT CARD ERROR    8B103390
*
0123 0 0122      MLCDC DC W30F8   **KEEP AT /123, CHG BY USER 8B103400
0124 0 0122      MECDC DC W30F8   **KEEP AT /124, CHG BY USER 8B103410
0125 0 07FF      NLOC  DC /07FF   **KEEP AT /125, NEXT LOCATIO 8B103420

```

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

```

0126 0 1000      ULIM   DC /1000   **KEEP AT /126, UPPER LIMIT 8B103560
0127 0 0000      UPPER DC /0000   **KEEP AT /127, UPPER LIMIT 8B103570
0128 0 0000      RLREQ  DC /0000   I COUNTER STORAGE     8B103580
0129 0 0000      TEMP  DC /0000   TEMP STORAGE          8B103590
012A 0 0000      TEMPI  DC /0000   TEMP STORAGE          8B103600
*****
012C 0000      END    LD22      TRANSFER TO START    8B103610
NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY 8B103620

```

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

RELOCATABLE DIAGNOSTIC LOADER (4K-65K CARDS)

ABHED 00E1 008A
 CDCT 00D9 0098 00E2 00E3
 CKFLD 00C9 00D0 00D2
 CT 0011 0009
 DSW 0070 0052 0056 005B
 DSW1 0014 0001 0004
 HB05 00F0 005F
 HB06 00F4 0121
 HB07 0102 00FB
 HB10 0103 0118
 HEXSW 00DD 0062 00A9 00C2 00F1
 HSCK 009A 009E
 HTBX 0113 010F
 HTBZ 0115 010C
 HT081 010F 0112
 INCRE 00CE 00D7
 K3000 0064 0093
 K74FF 00FD 0094
 LB06 00A8 00FF
 LB10 00B3 00AD 00C1
 LB15 00DA 00A3
 LB20 00DB 0088
 LB25 00DC 0089 008C
 LD22 0000 0008 005E 006C 0081 00C5 012C
 LD25 0001 0002 0010
 LD30 000D 0006
 LD36 002C 000C
 LH05 00FC 0106
 MECD 0124 00AB
 MLC0 0123 0053
 MSK 0068 0050
 MSK2 006A 0051
 MSK5 0016 000D
 NLOC 0125 00DF 00E9
 ONE 006E 009C 009F 00E7
 OVER1 008D 008A
 PASS 00E3 0096 00D3
 PCAM 00DE 00A4 00BF 00F3 00FD 0119 011F
 RD05 0050 003C 0086 00C3
 RD20 0052 0073
 RD25 0056 0057
 READ 006C 0055
 READ1 0012 0000 0007
 RLBA 006F 00E0
 RLCAT 00D4 00CC
 RLDAT 00C5
 RLHED 00DF 008D
 RLREQ 0128 00F5 00F9 0118
 SB05 0074 0063
 SB06 0076 0084
 SB07 0077 0082
 SB10 007D 0078
 SL 0065 0077
 STGCK 0034 002D
 STGLP 002F 0036
 SW 00D8 008F 0097 00EC
 TAKE 0097 0091 0095
 TEMP 0129 0086 0087 0113 0114
 TEMP1 012A 0104 0115
 ULIM 0126 003A 00B8
 UPPER 0127 00AF 00D5 00E1 00E8 00EB 011D
 W30FA 000E 0005 000F 30FA
 W30FB 0072 005C 30FB
 W30FC 008B 008C 30FC
 W30F8 0122 0123 0124 30F8
 W30F9 00A1 30F9
 XFER 00EE 00ED
 XFRCD 00E6 00A6

XFR2 00EB 0101
 END OF ASSEMBLY

----- LAST PAGE -----

INTERVAL TIMER FUNCTION TEST

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INTERVAL TIMER FUNCTION TEST

1. PURPOSE

THE TIMER FUNCTION TEST IS USED TO DETERMINE WHETHER THE INTERVAL TIMER CIRCUITS IN THE 1801 OR 1802 PROCESS/CONTROLLER ARE OPERATING PROPERLY. TIMER STEPPING, TIMER INTERRUPTS, DSW, AND ILSW ARE TESTED.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE BASIC DIAGNOSTIC LOADER IS REQUIRED TO LOAD THIS PROGRAM.

2.2 EQUIPMENT PREREQUISITES

THE FOLLOWING EQUIPMENT IS REQUIRED,

- A. 1800 PROCESSOR/CONTROLLER.
- B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
- C. EITHER A 1053/1816 OR 1443 PRINTER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

REFER TO 1800 BASIC DIAGNOSTIC LOADER DOCUMENTATION PARAGRAPH 3.1, FOR LOADING INSTRUCTIONS.

3.2 PROGRAM OPERATION

AFTER LOADING THE P.C. STOPS AT WAIT 1 (B REG=3001). WITH P.C. STOPPED AT WAIT 1, PROCEED AS FOLLOWS,

- A. SET CHECK STOP SWITCH TO OFF.
 - B. SET DISABLE INTERRUPT SWITCH TO OFF.
 - C. SET WRITE STORAGE PROTECT SWITCH TO YES.
 - D. SELECT PROGRAM OPTIONS. REFER TO TABLE 1 SECTION 3.2 .
 - E. IF LOOP ROUTINE IS DESIRED, REFER TO LOOP ROUTINE OPTION TABLE 2 SECTION 3.2 .
 - F. DEPRESS START BUTTON. PROGRAM SHOULD START EXECUTION.
1. IF LOOP ROUTINE OR LOOP PROGRAM WERE NOT SPECIFIED, ROUTINES 1 THROUGH 6 WILL BE EXECUTED ONCE. THE PROGRAM WILL PRINT MESSAGE **A002 PROGRAM COMPLETE** AND THEN STOP AT WAIT 2. (B REG=3002).
 2. IF A ROUTINE WAS SELECTED FOR LOOPING, THEN THAT ROUTINE WILL LOOP UNTIL THE PROGRAM IS TERMINATED, OR THE LOOP ROUTINE FUNCTION IS CHANGED OR CLEARED. IF THE LOOP ROUTINE FUNCTION IS CHANGED, THEN THE NEW ROUTINE SELECTION WILL BE LOOPED. IF THE LOOP ROUTINE FUNCTION IS CLEARED, THE PROGRAM WILL CONTINUE FROM THE PRESENT ROUTINE TO COMPLETION.
 3. IF LOOP PROGRAM WAS SELECTED, ROUTINES 1 THROUGH 6 WILL BE RUN IN SEQUENCE IN A LOOP FASHION. AT THE END OF EACH PROGRAM PASS, MESSAGE **A003 PASS COMPLETE** WILL BE PRINTED.
 4. IF THE SCOPING ROUTINE WAS SELECTED, MESSAGE **A001 SCOPE RTN SELECTED** WILL BE PRINTED, FOLLOWED BY MESSAGE **C002 ENTER STARTING COUNT**. THE PROGRAM WILL STOP AT WAIT 5 (B REG=3005). THE PRINTOUT EXPLANATIONS SHOULD BE CONSULTED FROM THIS POINT FOR THE OPERATION OF THE SCOPING ROUTINE. PRINTOUT SECTION 4.2 COMMAND MESSAGES.

INTERVAL TIMER FUNCTION TEST

TABLE 1
PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE
THE OPTIONS FOR SELECTING OUTPUT DEVICE, THE SCOPING ROUTINE, OR THE CORE SPEED WILL BE HONORED ONLY IF THEY ARE ENTERED WHEN PROGRAM IS STOPPED AT WAIT 1 (B REG=3001).

DATA ENTRY SWITCHES	OPTION DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15	
1	HALT ON ERROR
1	BYPASS ERROR PRINT
1	LOOP ON ERROR
1	LOOP PROGRAM
1	USE 1443 AS OUTPUT DEVICE
1	SELECT SCOPING ROUTINE
1	4 USEC MACHINE

TABLE 2
LOOP ROUTINE OPTION - SENSE/PROGRAM SWITCHES

PROGRAM/SENSE	DESCRIPTION
0 1 2 3 4 5 6 7	
X X X	ROUTINE NUMBER TO LOOP. NUMBER MUST BE IN HEX. NUMBER MAY BE CHANGED AT ANY TIME.

3.3 TERMINATING PROCEDURE

IF THE LOOP-PROGRAM, OR LOOP-ROUTINE OPTIONS ARE NOT SELECTED THE PROGRAM WILL BE EXECUTED ONCE AND WILL STOP AT WAIT 2 FOLLOWING THE PROGRAM COMPLETE PRINTOUT. DEPRESSING THE START PUSHBUTTON WILL BRANCH THE PROGRAM TO WAIT 1, WHICH IS THE BEGINNING OF THE PROGRAM.

IF THE PROGRAM IS IN A LOOPING MODE, IT MAY BE TERMINATED BY:

1. DEPRESSING THE STOP BUTTON. DEPRESSING RESET AND START BUTTONS WILL RETURN PROGRAM TO WAIT 1.
2. CLEARING THE LOOP FUNCTION, TO ALLOW PROGRAM TO RUN TO ITS COMPLETION.

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

INTERVAL TIMER FUNCTION TEST

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```

.....
3001 0 01ED          DC      WAIT1+1
                                WAIT 1
                                ONE OF THE METERED I/O UNITS
                                FAILED TO SEND A RESPONSE
                                INTERRUPT TO THE PROGRAM. INDEX
                                REGISTER 1 WILL HAVE THE ADDRESS
                                OF THE IOCC. THE AREA CODE WILL
                                INDICATE THE I/O UNIT NOT READY.
                                IF A 2401/02 DRIVE IS NOT READY,
                                PROGRAM WILL NOT STOP AT WAIT 1.
.....

```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS

THE VARIOUS PRINTOUTS THAT MAY OCCUR DURING EXECUTION OF THE TIMER FUNCTION TEST FOLLOW.

4.1 STATUS MESSAGES

A001 SCOPE RTN SELECTED

THIS PRINTOUT INDICATES THAT THE SCOPE RTN HAS BEEN SELECTED AS A RESULT OF SETTING ON DATA ENTRY SWITCH NO. 8 WHEN PROGRAM STOPPED AT WAIT 1.

A002 PROGRAM COMPLETE

FOLLOWING THIS PRINTOUT PROGRAM STOPS AT WAIT 2. DEPRESSING START PUSHBUTTON CAUSES PROGRAM TO BRANCH TO WAIT 1. AT THIS POINT PROGRAM MAY BE REPEATED AGAIN.

A003 PASS COMPLETE

PRINTED AT THE END OF EACH PROGRAM PASS WHEN THE LOOP PROGRAM OPTION IS SELECTED.

4.2 COMMAND MESSAGES

C001 RUN SCOPE RTN

THIS PRINTOUT INSTRUCTS THE OPERATOR TO RUN THE PROGRAM IN SCOPE RTN, AND IS CAUSED IF THE CONTROL ROUTINE IS UNABLE TO DETERMINE TIMER INTERRUPT LEVEL DUE TO ALL TIMERS FAILING TO INTERRUPT, OR STEP.

INTERVAL TIMER FUNCTION TEST

C002 ENTER STARTING COUNT

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED STARTING COUNT IN THE DATA ENTRY SWITCHES. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
X X X X X X X X X X X X X X X X	...DESIRED STARTING COUNT IN HEX

C003 ENTER NUMBER OF STEPS

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED NUMBER OF STEPS IN THE DATA ENTRY SWITCHES. A COUNT OF ZERO IS INVALID AND WILL CAUSE MESSAGE C003 TO BE PRINTED AGAIN. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
X X X X X X X X X X X X X X X X	...DESIRED NUMBER OF STEPS IN HEX

C004 ENTER TIMER NUMBER

THIS PRINTOUT IS A SCOPE ROUTINE REQUEST FOR OPERATOR ACTION. ENTER THE DESIRED TIMER SELECTION IN DATA ENTRY SWITCHES. ONLY 1 TIMER AT A TIME MAY BE SELECTED. TIMER C IS USED IF NO ENTRY IS MADE. CLEAR ALL UNUSED SWITCHES. PUSH START AFTER ENTRY.

DATA ENTRY SWITCHES	DESCRIPTION
0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *	
0 0 1.....	RUN TIMER C
0 1 0.....	RUN TIMER B
1 0 0.....	RUN TIMER A
1.....	CHANGE INPUT PARAMETERS. (SEE NOTE 1)

NOTE 1. AFTER ROUTINE IS LOOPING, AND IT IS DESIRED TO CHANGE STARTING COUNT, NUMBER OF STEPS, OR TIMER NUMBER, SET SWITCH 2 ON. ROUTINE RESTARTS AND PRINTS MESSAGE C002.

C005 REPAIR FAILURE BEFORE CONTINUING

PRINTED FOLLOWING ERRORS E008 AND E00C. PROGRAM GOES TO WAIT 1 AFTER THIS PRINTOUT TO ALLOW CE TO SELECT SCOPING ROUTINE. ERRORS E008 AND E00C CAN CAUSE PROGRAM TO LOOSE CONTROL OR INDICATE FALSE ERRORS.

INTERVAL TIMER FUNCTION TEST

4.3 DATA MESSAGES

D001 TIMERS ON INTRP LEVEL XX

THIS PRINTOUT INDICATES THE INTERRUPT LEVEL OF THE TIMERS. XX WILL BE THE ACTUAL INTERRUPT LEVEL NUMBER, IN DECIMAL.

4.4 ERROR MESSAGES

E001 SEQUENCE ERROR

THIS PRINTOUT OCCURS WHEN THE ROUTINE JUST RUN DOES NOT AGREE WITH THE ROUTINE SELECTED BY THE CONTROL ROUTINE.

E002 TIMERS FAIL TO STEP

THIS PRINTOUT OCCURS WHEN THE CONTROL SECTION IS UNABLE TO DETERMINE THE TIMER INTERRUPT LEVEL DUE TO TIMERS NOT STEPPING. THIS IS FOLLOWED BY A **RUN MANUAL MODE** PRINTOUT.

E003 TIMERS FAIL TO INTRP

THIS PRINTOUT OCCURS WHEN THE CONTROL SECTION IS UNABLE TO DETERMINE THE TIMER INTERRUPT LEVEL DUE TO TIMERS FAILING TO INTERRUPT. THIS PRINTOUT IS FOLLOWED BY A **RUN MANUAL MODE** PRINTOUT.

E004 RTN 2 TIMER X FAILED TO TURN ON

THIS PRINTOUT RESULTS IF A TIMER (A, B, OR C) FAILS TO STEP AFTER BEING TURNED ON. 10 CONSECUTIVE PRINTOUTS MAY RESULT, AS 10 TRIES ARE MADE ON EACH TIMER.

E005 RTN 2 TIMER X FAILED TO TURN OFF

THIS PRINTOUT RESULTS WHEN A TIMER (A, B, OR C) CONTINUES TO STEP AFTER A TURN OFF COMMAND. 10 CONSECUTIVE PRINTOUTS ARE POSSIBLE.

E006 RTN 3 TIMER X FAILED TO INTRP

THIS PRINTOUT OCCURS WHEN A TIMER (A, B, OR C) FAILS TO INTERRUPT.

E007 RTN 3 TIMER X DSW XXXX

THIS PRINTOUT OCCURS WHEN THE DSW BIT FOR THE INDICATED TIMER (A, B, OR C) IS IN ERROR.

E008 RTN 3 TIMER X ILSW XXXX

ONE PRINTOUT FOR EACH TIMER WILL OCCUR IF THE ILSW BIT IS NOT THE SAME FOR ALL TIMERS.

E009 RTN 4 TIMER X WAS XXXX SHOULD BE XXXX

THIS PRINTOUT OCCURS WHEN EXPECTED AND ACTUAL TIMER COUNTS DO NOT AGREE.

INTERVAL TIMER FUNCTION TEST

E00A RTN 5 TX WAS XXXX EXPCTD XXXX PASS X

THIS PRINTOUT OCCURS WHEN EXPECTED AND ACTUAL TIMER COUNTS DO NOT AGREE. PASS 1 IS RUN WITH INTERRUPT OFF, PASS 2 IS RUN WITH INTERRUPT ON.

E00B RTN1 DOUBLE INCR OF I CTR DURING TMR X CS CYCLE

THE DESIGNATED TIMER CAUSED THE I COUNTER TO BE INCREMENTED DURING THE SECOND TIMER CYCLE STEAL CYCLE. THIS PRINTOUT WILL BE FOLLOWED BY MESSAGE C005.

E00C ILLEGAL RTN ENTRY

A ROUTINE NUMBER OF 7 WAS ENTERED INTO PROGRAM SWITCHES 5, 6, AND 7. SEVEN IS AN INVALID ROUTINE NUMBER. PROGRAM RETURNS TO WAIT 1 AFTER THIS PRINTOUT.

E00D RTN3 TIMER X ILSW WAS ZERO

WHILE CHECKING THE DESIGNATED TIMER, AN INTERRUPT WAS RECEIVED ON THE TIMER INTERRUPT LEVEL AND THE ILSW WAS BLANK.

E00E RTN1 A REG CHANGED ON TMR X CS CYCLE

THE CONTENTS OF THE A REGISTER WERE DESTROYED DURING THE DESIGNATED TIMER CYCLE STEAL CYCLE. THE A REG IS LOADED TO FFFF PRIOR TO TURNING THE TIMER ON.

E00F RTN6 TIMER X FAILED TO INCREMENT

THE TIMER SPECIFIED FAILED TO INCREMENT WITHIN 70 MSEC. AFTER BEING TURNED ON. THE TIMERS ARE STORAGE PROTECTED DURING THIS CHECK.

E010 RTN6 SPV INTRPT ON TIMER X CS CYCLE

A STORAGE PROTECT VIOLATE INTERRUPT WAS RECEIVED DURING THE DESIGNATED TIMER CYCLE STEAL CYCLE. TIMERS ARE STORAGE PROTECTED DURING THIS CHECK.

E011 RTN6 NO INTRP ON VIOLATE TIMER X

A STORE INSTRUCTION WAS ISSUED TO THE DESIGNATED PROTECTED TIMER. A STORAGE PROTECT VIOLATE INTERRUPT DID NOT OCCUR.

5. COMMENTS

THE TIMER FUNCTION TEST CONSISTS OF A CONTROL ROUTINE, SIX TEST ROUTINES, AND A SCOPING ROUTINE.

THE CONTROL ROUTINE DETERMINES THE TIMER INTERRUPT LEVEL AND OUTPUTS THE INFORMATION FOR OPERATOR OBSERVATION. THE CONTROL ROUTINE ALSO SEQUENCES THE TESTING ROUTINES AND ACCOMPLISHES THE PROGRAM OPTIONS SPECIFIED BY THE OPERATOR.

INTERVAL TIMER FUNCTION TEST

ROUTINE 1 CHECKS FOR DOUBLE INCREMENTING OF THE I COUNTER, AND FOR A CHANGE IN A REG CONTENTS DURING A TIMER C.S. CYCLE. IF EITHER OF THESE FAILURES OCCURS, THE ERROR PRINTOUT IS FOLLOWED BY A MESSAGE INSTRUCTING THE CE TO REPAIR THE FAILURE BEFORE CONTINUING. THIS IS DONE SINCE EITHER OF THESE FAILURES CAN CAUSE THE PROGRAM TO LOOSE CONTROL OR INDICATE FALSE ERRORS. IF IT IS DETERMINED BY THE CONTROL ROUTINE, THAT ANY TIMER FAILS TO INTERRUPT, THEN THAT TIMER(S) WILL NOT BE CHECKED IN ROUTINE 1.

ROUTINE 2 CHECKS THE ON-OFF ACTION OF ALL THREE TIMERS. EACH TIMER IS CHECKED 10 TIMES.

ROUTINE 3 CHECKS THE TIMERS FOR INTERRUPT, DSW, AND ILSW.

ROUTINE 4 CHECKS THE TIMERS FOR PROPER STEPPING. EACH TIMER IS ALLOWED TO STEP 50 TIMES WITH 16 DIFFERENT STARTING COUNTS.

ROUTINE 5 IS A TWO PASS ROUTINE. THE FIRST PASS IS WITH TIMER INTERRUPT OFF, AND THE SECOND PASS IS TIMER INTERRUPT ON. ALL TIMERS ARE RUN TOGETHER, AND EACH ONE IS CHECKED FOR PROPER STEPPING FOR 1000 STEPS.

ROUTINE 6 CHECKS THE TIMERS WHILE STORAGE PROTECTED. EACH TIMER IS CHECKED TO INSURE IT INCREMENTS WHILE STORAGE PROTECTED WITHOUT CAUSING A STORAGE PROTECT VIOLATE ERROR. THE CHECK IS MADE 10 TIMES PER TIMER. A FURTHER CHECK IS MADE TO INSURE AN SPV INTERRUPT DOES OCCUR WHEN TRYING TO STORE INTO A PROTECTED TIMER.

THE SCOPING ROUTINE ALLOWS THE OPERATOR TO SELECT A TIMER (A, B, OR C) STARTING COUNT, AND THE NUMBER OF STEPS THE TIMER SHOULD BE STEPPED. AFTER ALL DATA IS ENTERED, AND THE START BUTTON HAS BEEN DEPRESSED, THE ROUTINE WILL LOOP UNTIL DATA ENTRY SWITCH 2 IS TURNED ON, AT WHICH POINT THE ROUTINE RETURNS TO WAIT 5. ONLY ONE TIMER MAY BE RUN AT A TIME. IF A TIMER NUMBER IS NOT ENTERED, TIMER C WILL BE USED.

INTERVAL TIMER FUNCTION TEST

```

02BC      ABS      88200010
          ORG      /3001      88200020
          *          88200030
          *          ** PROGRAM WAITS **      88200040
          *          88200050
          *          DC      WT1+1    WAIT 1      88200060
          *          88200070
          *          WAIT OCCURS AFTER PROGRAM      88200080
          *          HAS LOADED. ENTER PROGRAM      88200090
          *          OPTIONS IN DATA ENTRY SMS.      88200100
          *          AND DEPRESS START.      88200110
          *          88200120
          *          DC      WT2+1    WAIT 2      88200130
          *          88200140
          *          PROGRAM RAN TO COMPLETION.      66200150
          *          DEPRESSING START RETURNS      88200160
          *          PROGRAM TO WAIT 1.      88200170
          *          88200180
          *          DC      WT3+1    WAIT 3      88200190
          *          88200200
          *          PROGRAM SEQUENCE ERROR.      88200210
          *          SUPERVISOR SECTION OF      88200220
          *          PROGRAM DETECTED AN ERROR      88200230
          *          IN ROUTINE SEQUENCING.      88200240
          *          DEPRESS START TO RETURN      88200250
          *          TO WAIT 1.      88200260
          *          88200270
          *          DC      WT4+1    WAIT 4      88200280
          *          88200290
          *          TIMER C FAILED TO INTER-      88200300
          *          RUPT IN ROUTINE 4.      88200310
          *          88200320
          *          DC      WT5+1    WAIT 5      88200330
          *          88200340
          *          SCOPE ROUTINE WAIT. ENTER      88200350
          *          STARTING TIMER COUNT IN      88200360
          *          DATA ENTRY SWITCHES. PUSH      88200370
          *          START BUTTON.      88200380
          *          88200390
          *          DC      WT6+1    WAIT 6      88200400
          *          88200410
          *          SCOPE ROUTINE WAIT. ENTER      88200420
          *          NUMBER OF DESIRED TIMER      88200430
          *          STEPS IN DATA ENTRY SWITCH      88200440
          *          DEPRESS START BUTTON.      88200450
          *          88200460
          *          DC      WT7+1    WAIT 7      88200470
          *          88200480
          *          SCOPE ROUTINE WAIT. ENTER      88200490
          *          TIMER NUMBER IN DATA ENTRY      88200500
          *          SWITCHES.      88200510
          *          *          88200520
          *          BIT 4 = TIMER A      88200530
          *          BIT 5 = TIMER B      88200540
          *          BIT 6 = TIMER C      88200550
          *          *          88200560
          *          CLEAR ALL UNUSED BIT SMS.      88200570
          *          DEPRESS START TO CONTINUE.      88200580
          *          88200590
          *          DC      WT8+1    WAIT 8      88200600
          *          88200610
          *          WAIT FOR TIMER INTERRUPT      88200620
          *          IN ROUTINE 1.      88200630
          *          88200640
          *          DC      WT9+1    WAIT 9      88200650
          *          88200660
          *          HALT ON ERROR REQUESTED.      88200670
          *          PUSH START TO CONTINUE.      88200680
  
```

INTERVAL TIMER FUNCTION TEST

```

300A 0 0551      DC      WTA+1    WAIT A      88200690
          *          88200700
          *          1443 NOT READY. MAKE READY      88200710
          *          AND PUSH START.      88200720
          *          88200730
          *          DC      WTB+1    WAIT B      88200740
          *          88200750
          *          1443 BUSY. THIS IS AN      88200760
          *          ERROR CONDITION. DETERMINE      88200770
          *          CAUSE, THEN PUSH START TO      88200780
          *          CONTINUE.      88200790
          *          88200800
          *          DC      WTC+1    WAIT C      88200810
          *          88200820
          *          1053/1016 NUMBER 1 NOT      88200830
          *          READY. MAKE 1053/1016      88200840
          *          READY AND DEPRESS START.      88200850
          *          88200860
          *          DC      WTD+1    WAIT D      88200870
          *          88200880
          *          DSW FAILED TO RESET AFTER      88200890
          *          INTERRUPT IN ROUTINE 2.      88200900
          *          88200910
          *          DC      WTE+1    WAIT E      88200920
          *          88200930
          *          AN INTERNAL INTERRUPT WAS      88200940
          *          RECEIVED. THE I CTR. AT      88200950
          *          INTERRUPT IS IN THE Q REG.      88200960
          *          THE ILSW IS IN THE A REG.      88200970
          *          DEPRESS START BUTTON TO      88200980
          *          RESTART THE PROGRAM.      88200990
          *          88201000
          *          DC      WTF+1    WAIT F      88201010
          *          88201020
          *          INTERNAL INTERRUPT, OTHER      88201030
          *          THAN SPV WAS RECEIVED      88201040
          *          DURING ROUTINE 6. I COUNT      88201050
          *          IS IN Q REG. ILSW IN A REG      88201060
          *          PRESS START TO RESTART      88201070
          *          PROGRAM.      88201080
          *          88201090
          *          3010      ORG      300      88201100
          *          012C 0 8200      DC      /B200    PID      88201110
          *          *          *****      88201120
          *          *          *          88201130
          *          *          *          88201140
          *          *          *          ** TIMER FUNCTION TEST **      88201150
          *          *          *          ** TIMFT **      88201160
          *          *          *          *          88201170
          *          *          *          *****      88201180
          *          *          *          88201190
          *          *          *          88201200
          *          *          *          88201210
          *          *          *          88201220
          *          *          *          CLR ROUTINE NUMBER      88201230
          *          *          *          CLEAR INTERRUPT SW      88201240
          *          *          *          AND INOP INDICATORS      88201250
          *          *          *          88201260
          *          *          *          88201270
          *          *          *          88201280
          *          *          *          88201290
          *          *          *          88201300
          *          *          *          CLEAR INTERVAL TIMER      88201310
          *          *          *          AND INTERRUPT LOCATN      88201320
          *          *          *          88201330
          *          *          *          88201340
          *          *          *          88201350
          *          *          *          88201360
          *          *          *          SET RESTART INSTRUCN
          *          *          *          88201360
  
```

INTERVAL TIMER FUNCTION TEST

0142 0 10A0 SLT 32 CLEAR A AND G 88201370
 0143 0 C05F LD CONST+2 SET LEVEL ERRGR 88201380
 0144 00 D4000008 STO L /0008 INTERRUPT ADDRESS 88201390
 * 88201400
 0146 0 3001 WT1 WAIT 1 OP ENTER CONTROL 88201410
 * INFORMATION PUSH 88201420
 * START TO CONTINUE 88201430
 * 88201440
 * 88201450
 0147 0 0864 XIO BSW READ BIT SWITCHES 88201460
 0148 0 C067 LD BSWA 88201470
 0149 0 188B SRT 11 88201480
 014A 0 1010 SLA 16 88201490
 014B 0 1081 SLT 1 SET CORE SPEED 88201500
 014C 0 D067 STO SPEED *INDICATOR 88201510
 014D 0 1084 SLT 4 88201520
 014E 0 100F SLA 15 88201530
 014F 0 D063 STO OPIND SET OPT DEVICE INDIC 88201540
 0150 00 2C400004 STS L 4, /40 CLEAR STORAGE 88201550
 0152 00 2C400005 STS L 5, /40 *PROTECT BITS 88201560
 0154 00 2C400006 STS L 6, /40 88201570
 * 88201580
 * 88201590
 0156 0 C059 LD BSWA SAVE SW INPUT 88201600
 0157 0 10G8 SLA 8 88201610
 0158 00 4C280179 BSC L CTLO3,+2 BRNCH IF MANUAL MODE 88201620
 * 88201630
 * *****
 015A 00 440004AD BSI L TIINT GO DETERMINE TIMER SRC 88201640
 * INTERRUPT LEVEL 88201650
 * *****
 * SET UP INTERRUPT ADDRESSES
 * *****
 015C 00 0C00026A XIO L MASKO MASK INTERRUPT LVLS 88201670
 015E 00 0C00026C XIO L MASK1 88201680
 0160 0 621A LDX 2 26 88201690
 0161 0 C042 LD CONST+3 ADDRESS SVINT 88201700
 0162 00 D6000008 STO L2 8 88201710
 0164 0 72FF MDX 2 -1 88201720
 0165 0 70FC MDX *-4 88201730
 0166 0 0841 XIO UMSKO UNMASK INTERRUPTS 88201740
 0167 0 0842 XIO UMSK1 88201750
 * 88201760
 * 88201770
 * 88201780
 * 88201790
 0168 0 0845 RTRN XIO SNSWS CHECK IF A ROUTINE 88201800
 0169 0 E038 AND CONST+1 *IS SPECIFIED TO 88201810
 016A 0 1808 SRA 8 *LOOP 88201820
 016B 0 4808 BSC + SKIP IF LOOP ROUTINE 88201830
 016C 0 7002 MDX CTLO4 88201840
 016D 0 D043 STO RTNNO SET IN ROUTINE SW 88201850
 016E 0 7006 MDX CTLO2+2 GO EXECUTE ROUTINE 88201860
 * 88201870
 * 88201880
 016F 0 C041 CTLO4 LD RTNNO CK IF ALL ROUTINES 88201890
 0170 0 9030 S CONST HAVE RUN 88201900
 0171 0 4818 BSC + 88201910
 0172 0 7008 MDX CTLO5 BRNCH IF ALL RTN RUN 88201920
 * 88201930
 * 88201940
 0173 00 74010181 CTLO2 MDX L RTNNO,1 ADD 1 TO ROUTINE NO 88201950
 0175 00 65800181 LDX 11 RTNNO 88201960
 0177 00 4D800195 BSC 11 RTN-1 EXIT TO ROUTINE 88201970
 * 88201980
 * *****
 * MANUAL MODE SELECTED
 * *****
 0179 00 4400053C CTLO3 BSI L LOG PRINT MANUAL MODE SRC 88201990
 017B 0 0710 DC TMM01 SELECTED 88202000
 * 88202010
 * 88202020
 017C 00 4C000456 BSC L TIMAN GO TO MANUAL ROUTINE 88202030
 * 88202040

INTERVAL TIMER FUNCTION TEST

017E 0 082D * ALL ROUTINES HAVE RUN 88202050
 017F 0 C030 * CTLO5 XIO BSW READ BIT SWITCHES 88202060
 0180 0 1804 LD BSWA CHECK IF LOOP PRGRM 88202070
 0181 0 4804 SRA 4 IS SPECIFIED 88202080
 0182 0 7005 BSC E 88202090
 * MDX LPPGM BRANCH IF LOOP PGRM 88202100
 * 88202110
 * 88202120
 * *****
 0183 00 44C0053C BSI L LOG PRINT PROGRAM SRC 88202130
 0185 0 072C DC TMM03 COMPLETE 88202140
 * 88202150
 * *****
 0186 0 3002 WT2 WAIT 2 PROGRAM COMPLETE 88202160
 * 88202170
 * 88202180
 * 88202190
 * MDX TISRT PUSHING START RESULT 88202200
 * IN BRANCH TO START 88202210
 * OF PROGRAM 88202220
 * 88202230
 0188 0 1010 LPPGM SLA 16 CLEAR ROUTINE NUMBER 88202240
 0189 0 D027 STG RTNNO 88202250
 * 88202260
 * *****
 018A 00 4400U53C BSI L LOG PRINT PASS COMPLETE SRC 88202270
 018C U 08B6 DC TMM23 88202280
 * 88202290
 * *****
 018D 0 70E5 MDX CTLO2 LOOP PROGRAM 88202300
 * 88202310
 * 88202320
 * 88202330
 * 88202340
 * 88202350
 * ROUTINES RETURN HERE
 * *****
 018E 0 C023 RTNRT LD SEQCK 88202360
 018F 0 4818 BSC + 88202370
 0190 0 7007 MDX RTRN BRANCH ON GOOD CHECK 88202380
 * 88202390
 * *****
 0191 00 4400053C BSI L LOG PRINT SEQUENCE ERROR SRC 88202400
 0193 0 073A DC TMM04 88202410
 * 88202420
 * *****
 0194 0 3003 WT3 WAIT 3 SEQUENCE ERROR 88202430
 * 88202440
 * 88202450
 * 88202460
 * 88202470
 * MDX TISRT PUSH START FOR RESRT 88202480
 * 88202490
 * *****
 * ROUTINE ADDRESSES
 * *****
 0196 0 018A RTN DC TIM00 ROUTINE 1 88202500
 0197 0 0217 DC TIM01 ROUTINE 2 88202510
 0198 0 0270 DC TIM02 ROUTINE 3 88202520
 0199 0 02E0 DC TIM03 ROUTINE 4 88202530
 019A 0 0355 DC TIM04 ROUTINE 5 88202540
 019B 0 03E8 DC TIM05 ROUTINE 6 88202550
 019C 0 019D DC TIMER ILLEGAL ROUTINE 88202560
 * 88202570
 * *****
 019D 00 4400053C TIMER BSI L LOG PRINT ILLEGAL ENTRY SRC 88202580
 019F 0 087A DC TMM20 MESSAGE ID 88202590
 * 88202600
 * 88202610
 * *****
 01A0 0 70A5 MDX WT1 88202620
 * 88202630
 * 88202640
 * 88202650
 * *****
 * CONTROL CONSTANTS
 * *****
 01A1 0 0006 CONST DC 6 88202660
 01A2 0 0700 DC /0700 88202670
 01A3 0 06C8 DC ERINT LVL ERR INTRUPT ADRS 88202680
 01A4 0 06DF DC SVINT 88202690
 * 88202700
 * 88202710
 * 88202720

INTERVAL TIMER FUNCTION TEST

```

01A6 0000      BSS E 0
01A6 0 4C00    RESRT DC /4C00  RESTART INSTRUCTION
01A7 0 012D    DC TISRT
01A8 0 0000    UM SKO DC /0000  UNMASK INTERRUPT
01A9 0 0480    DC /0480  IOCC
01AA 0 0000    UM SK1 DC /0000
01AB 0 0481    DC /0481
01AC 0 0180    BSW DC BSWA  READ BIT SWITCH IOCC
01AD 0 0240    DC /0240
01AE 0 0000    SNSWS DC /0000  READ SENSE SW IOCC
01AF 0 0760    DC /0760
01B0 0 0000    BSWA DC 0  BIT SW READ IN AREA
01B1 0 0000    RTNNO DC 0  ROUTINE NUMBER
01B2 0 0000    SEQCK DC 0
01B3 0 0000    DPIND DC 0  OUTPUT DEVICE INDCTR
01B4 0 0000    SPEED DC 0  CORE SPEED INDICATOR
01B5 0 04A4    TIBCN DC TIMAI
01B6 0 2000    DC /2000
01B7 0 3100    DC /3100  TIMER A
01B8 0 3200    DC /3200  TIMER B
01B9 0 3300    DC /3300  TIMER C
*****
ROUTINE NUMBER ONE
CHECK FOR DOUBLE INCR
OF I CTR DURING TIMER
CYCLE STEAL CYCLE
*****
01EA 00 0C00026A  TIM00 XIO L MASKO  MASK INTERRUPTS
01EB 00 0C00026C  XIO L MASK1
01EC 0 1010      SLA 16  CLEAR ERROR SWITCH
01ED 0 0053      STO ERRSW
01EE 0 00F4      LD TIBCN  SET INTERRUPT
01EF 00 678006CA  LDX I3 INVLV *TRANSFER VECTOR
01F0 00 07000000  STO L3 0
01F1 00 6700FFFF  LDA L3 /FFFF
01F2 00 6F000004  STX L3 4  SET ALL TIMERS TO
01F3 00 6F000005  STX L3 5  *FFFF
01F4 00 6F000006  STX L3 6
01F5 00 0C0001A8  XIO L UM SKO  UNMASK INTERRUPTS
01F6 0 08DA      XIO UM SK1
01F7 0 6103      LDX I3  SET TIMER INDEX
01F8 0 00E4      LD TIBCN+1 INITIALIZE IOCC
01F9 00 04000500  STO L NIOCC
01FA 0 7005      MDX RTN00+5
01FB 00 04000500  RTN00 LD L NIOCC  MODIFY IOCC FOR
01FC 0 1001      SLA I  *NEXT TIMER
01FD 00 04000500  STO L NIOCC
01FE 00 05000186  LD LI TIBCN+1  THR NMBR TO MESSAGE
01FF 00 0400085E  STO L TMM18+24
0200 00 05000213  RTN01 LD LI TMA-1  CHECK IF TIMER OPRTV
0201 0 4820      BSC Z  SKIP IF OK
0202 0 7016      MDX RTN05
0203 00 0400026A  LD L MASKO  SET A TO FFFF
0204 00 0C000500  XIO L NIOCC  TURN TIMER ON
0205 0 3008      MTB WAIT 8  WAIT FOR I/TERRUPT
0206 0 7008      MDX RTN02  NORMAL INTRP RETURN
0207 00 0C000502  XIO L FIOCC  ERROR INTRP RETURN

```

INTERVAL TIMER FUNCTION TEST

```

01EA 00 74010213 MDX L ERRSW,1  SET ERROR INDICATOR
*****
01EB 00 44000511 BSI L ERROR  PRINT ERROR  SRC
01EE 0 0846      DC TMM18  MESSAGE ID
01EF 0 01DE      DC RTN01  LOOP ERROR RETURN
*****
01F0 00 0C000502 RTN02 XIO L FIOCC  TIMERS OFF
01F2 00 040004AC  LD L ACS  CHECK IF ACCUMULATOR
01F4 00 0400026A  EOR L MASKO *WAS DESTROYED ON
01F6 0 4820      BSC Z  *TIMER C.S.CYCLE
01F7 0 700F      MDX RTN04
01F8 0 71FF      RTN05 MDX I -1
01F9 0 700B      MDX RTN00  BRN TO CHECK NXT THR
01FA 0 0018      LD ERRSW
01FB 00 04C180202 BSC L RTN03,+  BRANCH IF NO ERROR
*****
01FD 00 4400053C BSI L LOG  PRINT FIX COMMAND  SRC
01FF 0 0864      DC TMM19  MESSAGE ID
*****
0200 00 04C000146 BSC L M71
0202 00 04000181 RTN03 LD L RTNNO  PREPARE SEQUENCE
0204 0 F00D      EOR RT00  *CHECK
0205 0 00AC      STO SEQCK
0206 0 7087      MDX RTN01  RETURN TO CONTROL  SX
0207 00 05000186 RTN04 LD LI TIBCN+1  SET THR NMBR IN MSG
0209 00 04000880  STO L TMM22+19
*****
020B 00 44000511 BSI L ERROR  PRINT A DESTROYED  SRC
020D 0 089D      DC TMM22
020E 0 01DE      DC RTN01
*****
020F 00 74010213 MDX L ERRSW,1  SET ERROR SWITCH
0211 0 70E6      MDX RTN05
0212 0 0001      RT00 DC I
0213 0 0000      ERRSW DC 0  ANY FAILURE SWITCH
0214 0 0000      TMA DC 0  A INOP SW
0215 0 0000      DC 0  B INOP SW
0216 0 0000      DC 0  C INOP SW
*****
ROUTINE NUMBER TWO
CHECK ON/OFF ACTION OF
TIMERS
*****
0217 0 0852      TIM01 XIO MASKO  MASK ALL INTERRUPTS
0219 0 0853      XIO MASK1
021A 00 04000004  SLA I6  CLEAR ALL TIMERS
021C 00 04000005  STO L /0005
021E 00 04000006  STO L /0006
0220 0 6103      LDX I3  TIMER INDEX
0221 0 0094      LD TIBCN+1  SET TIMER IOCC
0222 00 04000500  STO L NIOCC
0224 0 1010      RTN10 SLA I6  CLEAR TIMER COUNT
0225 0 0048      STO TMCNT  WORK LOCATION

```

INTERVAL TIMER FUNCTION TEST

```

0226 0 620A      *      LDX  2  IC      PASS INDEX
0227 00 C5000186 *      LD   LI T1BCN+1  SET TIMER NUMBER
0229 00 D4000784 *      STO  L TMM08+11  IN MESSAGE
0228 00 D400079A *      STO  L TMM09+11
0220 00 0C000500 *      RTN11 XIO L NIOCC  TURN TIMER ON
*
*.....*
022F 00 44000504 *      BSI  L DEL20      GO DELAY          SRC
*.....*
0231 00 0C000502 *      XIO  L FIOCC      TURN TIMER OFF
0233 00 C5000003 *      LD   LI /0003     GET TIMER CONTENTS
0235 0  F038      *      EDR  TMCNT      CHECK IF COUNT CHNGD
0236 00 4C180256 *      BSC  L RTN12,+   BRANCH IF ZERO
0238 00 C5000003 *      LD   LI /0003     SET PRESENT COUNT IN
023A 0  D033      *      STO  TMCNT      WORK AREA
*
*.....*
023B 00 44000504 *      BSI  L DEL20      GO DELAY          SRC
*.....*
023D 00 C5000003 *      LD   LI /0003     GET TIMER CONTENTS
023F 0  F02E      *      EDR  TMCNT      CHECK IF COUNT CHNGD
0240 00 4C20025D *      BSC  L RTN13,2   BRANCH IF NOT ZERO
0242 00 C5000003 *      LD   LI /0003     SAVE PRESENT COUNT
0244 0  D029      *      STO  TMCNT
0245 0  72FF      *      RTN15 MDX  2  -1
0246 0  70E6      *      MDX  RTN11      GO MAKE ANOTHER PASS
0247 0  71FF      *      MDX  1  -1
0248 0  701B      *      MDX  RTN14      SET UP FOR NEXT TIMR
0249 00 0C0006C8 *      XIO  L DSM        PREVENT INTERRUPT
0248 00 0C0001A8 *      XIO  L UMSKO      UNMASK INTERRUPTS
0240 00 0C0001AA *      XIO  L UMSK1
024F 00 C40001B1 *      LD   L  RTNNO     PREPARE SEQUENCE
0251 0  F01D      *      EDR  RT100      CHECK
0252 00 D40001B2 *      STO  L SEQCK
0254 00 4C00018E *      BSC  L RTNRT     RETURN TO CONTROL
*
*.....*
0256 00 44000511 *      RTN12 BSI  L ERROR PRINT TIMER FAILED SRC
0258 0  0779      *      DC   TMM08      TO TURN ON
0259 0  022D      *      DC   RTN11     -LOOP-ERROR-RETURN
*.....*
025A 0  080F      *      XIO  MASKO      REMASK AFTER PRINT
0258 0  0810      *      XIO  MASK1
025C 0  70E8      *      MDX  RTN15
*
*.....*
025D 00 44000511 *      RTN13 BSI  L ERROR PRINT TIMER FAILED SRC
025F 0  078F      *      DC   TMM09      TO TURN /FF
0260 0  022D      *      DC   RTN11     LOOP ERROR RETURN
*.....*
0261 0  0808      *      XIO  MASKO      REMASK AFTER PRINT
0262 0  0809      *      XIO  MASK1

```

INTERVAL TIMER FUNCTION TEST

```

0263 0 70E1      *      MDX  RTN15
0264 00 C4000500 *      RTN14 LD  L NIOCC  SET IOCC FOR NEXT
0266 0  1001      *      SLA  1          TIMER
0267 00 D4000500 *      STO  L NIOCC
*
*.....*
0269 0  708A      *      MDX  RTN10      GO CHECK NEXT TIMER
*
*.....*
026A 00 0000      *      BSS  E  0
*
026A 0  FFFF      *      MASKO DC  /FFFF  MASK INTERRUPT IOCC
0268 0  0480      *      DC  /0480
026C 0  FFFF      *      MASK1 DC  /FFFF
026D 0  0481      *      DC  /0481
026E 0  0000      *      TMCNT DC  0
026F 0  0002      *      RT100 DC  2
*
*.....*
*      ROUTINE NUMBER THREE *
*      CHECK TIMER INTERRUPTS *
*      AND OSW *
*.....*
0270 0  C06A      *      TIMO2 LD  RT201  SET TRAP ROUTINE
0271 00 678006CA *      LOX  13 INLVL  ADDRESS IN INTERRUPT
0273 00 D7000000 *      STO  L3  0      LOCATION
*
0275 0  6103      *      LOX  1  3      TIMER INDEX
*
0276 00 C4000186 *      LD   L  T1BCN+1  SET TIMER IOCC TO C
0278 00 D4000500 *      STO  L NIOCC
027A 0  7005      *      MDX  RTN20+5
*
027B 00 C4000500 *      RTN20 LD  L NIOCC  SET IOCC FOR NEXT
027D 0  1001      *      SLA  1          TIMER
027E 00 D4000500 *      STO  L NIOCC
*
0280 00 C5000186 *      LD   LI T1BCN+1  SET TIMER NUMBER
0282 00 D40007B1 *      STO  L TMM10+11 IN MESSAGE
0284 00 D40007C7 *      STO  L TMM11+11
*
0286 0  C053      *      RTN21 LD  RT200  SET TIMER TO FFFF
0287 00 D5000003 *      STO  LI /0003
*
0289 00 0C000500 *      XIO  L NIOCC      TURN TIMER ON
*
*.....*
028B 00 44000504 *      BSI  L DEL20      GO WAIT FOR INTERUPT SRC
*.....*
*      IF TIMER FAILS TO INTEPUPT *
*      CONTINUE FROM THIS POINT *
*.....*
028D 00 0C000502 *      XIO  L FIOCC      TURN TIMER OFF
*
*.....*
028F 00 44000511 *      BSI  L ERROR      PRINT TIMER FAILED
0291 0  07A6      *      DC   TMM10      TO INTERRUPT
0292 0  0286      *      DC   RTN21     LOOP ERROR RETURN
*.....*
0293 0  7018      *      MDX  RTN27      GO CHECK IF DONE
*
*.....*
*      INTERRUPT TRAP ROUTINE *
*      RETURNS HERE *

```

```

88204090
88204100
88204110
88204120
88204130
88204140
88204150
88204160
88204170
88204180
88204190
88204200
88204210
88204220
88204230
88204240
88204250
88204260
88204270
88204280
88204290
88204300
88204310
88204320
88204330
88204340
88204350
88204360
88204370
88204380
88204390
88204400
88204410
88204420
88204430
88204440
88204450
88204460
88204470
88204480
88204490
88204500
88204510
88204520
88204530
88204540
88204550
88204560
88204570
88204580
88204590
88204600
88204610
88204620
88204630
88204640
88204650
88204660
88204670
88204680
88204690
88204700
88204710
88204720
88204730
88204740
88204750
88204760

```

```

88204770
88204780
88204790
88204800
88204810
88204820
88204830
88204840
88204850
88204860
88204870
88204880
88204890
88204900
88204910
88204920
88204930
88204940
88204950
88204960
88204970
88204980
88204990
88205000
88205010
88205020
88205030
88205040
88205050
88205060
88205070
88205080
88205090
88205100
88205110
88205120
88205130
88205140
88205150
88205160
88205170
88205180
88205190
88205200
88205210
88205220
88205230
88205240
88205250
88205260
88205270
88205280
88205290
88205300
88205310
88205320
88205330
88205340
88205350
88205360
88205370
88205380
88205390
88205400
88205410
88205420
88205430
88205440

```

INTERVAL TIMER FUNCTION TEST

0294 00 C4000653 RTN22 LD L TRP02 CHECK IF PROPER DSW
0296 00 F50002DC EOR L1 RT202
0298 0 4818 BSC +- SKIP IF NOT ZERO
0299 0 700E MDX RTN23 DSW OK CONTINUE
*
* WRONG DSW AFTER INTERRUPT
*
029A 00 C4000653 LD L TRP02 SET DSW FOR CONVRSM
029C 00 D4000629 STO L HEXWD
*

029E 00 44000609 BSI L HEXCV CONVERT HEX TO 43 CD SRC

02A0 00 CC00062E LDD L HEXCD ERROR DSW TO MESSAGE
02A2 00 DC0007CA STO L TMM11+14
*

02A4 00 44000511 BSI L ERROR PRINT DSW ERROR SRC
02A6 0 078C DC TMM11
02A7 0 0286 DC RTN21 LOOP ERROR RETURN

02A8 00 C500064F RTN23 LD L1 TRP01-1 CHECK IF TIMER ILSW
02AA 0 4818 BSC +- *IS ZERO
02AB 0 7025 MDX RTN26
02AC 0 71FF RTN27 MDX 1 -1 MAKE ANCTHER PASS IF
02AD 0 70CD MDX RTN20 ALL TIMERS NOT CKD
*
* ALL TIMERS CKD CK ILSW
*
02AE 00 C4000650 LD L TRP01 CHECK THAT ALL TIMER
02B0 00 F4000651 EOR L TRP01+1 *ILSW BITS ARE THE
02B2 00 E4000652 AND L TRP01+2 *SAME
02B4 0 4818 BSC +-
02B5 0 7014 MDX RTN25 ILSW OK EXIT
*
* ILSW ERROR
*
02B6 0 6303 LDX 3 3
02B7 00 C700064F RTN24 LD L3 TRP01-1 SET ILSW FOR CONVRSM
02B9 00 D4000629 STO L HEXWD
*

02BB 00 44000609 BSI L HEXCV CONVERT HEX TO 43 CD SRC

02BD 00 CC00062E LDD L HEXCD SET ILSW IN MESSAGE
02BF 00 DC0007DE STO L TMM12+16
02C1 00 C70001B6 LD L3 TIBCN+1 TIMER NMBR TO MESSAG
02C3 00 D40007D9 STO L TMM12+11
*

02C5 00 4400053C BSI L LOG PRINT ILSW SRC
02C7 0 07CE DC TMM12

02C8 0 73FF MDX 3 -1
02C9 0 70ED MDX RTN24 ALL TIMERS CHECKED EXIT
*
02CA 00 C40001B1 RTN25 LD L RTNNO
02CC 0 F00F EOR RT202
02CD 00 D40001B2 STO L SEQCK
*
02CF 00 4C00018E BSC L RTNRT RETURN TO CONTROL
*
02D1 00 C50001B6 RTN26 LD L1 TIBCN+1

INTERVAL TIMER FUNCTION TEST

02D3 00 D4000894 STO L TMM21+11

02D5 00 44000511 BSI L ERRDR PRINT ILSW ZERO SRC
02D7 0 0889 DC TMM21 MESSAGE ID
02D8 0 0286 DC RTN21 LOOP ERROR RETURN

02D9 0 70D2 MDX RTN23+4
*
* CONSTANTS
*
02DA 0 FFFF RT200 DC /FFFF
02DB 0 0640 RT201 DC TRAP2 INTERRUPT ADDRESS
02DC 0 0003 RT202 DC 3
02DD 0 8000 DC /8000 A DSW
02DE 0 4000 DC /4000 B DSW
02DF 0 2000 DC /2000 C DSW
*

ROUTINE FOUR
CHECK TIMERS FOR
PROPER STEPPING

02E0 00 0C00026A TIM03 XIO L MASK0 MASK INTERRUPTS
02E2 00 0C00026C XIO L MASK1
02E4 0 6103 LDX 1 3 TIMER INDEX
02E5 00 C40001B6 LD L TIBCN+1 IOCC TO TIMER C
02E7 00 D4000500 STO L NIOCC
02E9 0 7005 MDX RTN30+5
*
02EA 00 C4000500 RTN30 LD L NIOCC SET IOCC FOR NEXT
02EC 0 1001 SLA 1 TIMER
02ED 00 D4000500 STO L NIOCC
02EF 0 6210 LDX 2 16 SET PASS INDEX
02F0 00 C50001B6 LD L1 TIBCN+1 SET TIMER NUMBER
02F2 00 D40007ED STO L TMM13+11 IN ERROR MESSAGE
*
02F4 0 6332 RTN31 LDX 3 50 SET STEP INDEX
02F5 00 C6000344 LD L2 CTIBL-1 SET STARTING COUNT
02F7 00 D5000003 STO L1 /0003 *IN TIMER AND IN
02F9 0 0048 STO RT300 *CHECK REGISTER
*
02FA 00 0C000500 XIO L NIOCC TURN TIMER ON
*
* ** CHECK STEPPING **
*
02FC 00 C5000003 RTN32 LD L1 /0003 GET TIMER CONTENTS
02FE 0 9043 S RT300 *LOOP UNTIL TIMER
02FF 0 4818 BSC +- *AND CHECK REG ARE
0300 0 70FB MDX RTN32 *UNLIKE
*
0301 0 F042 EOR RT302 CHECK IF DIFF IS 1
0302 0 4818 BSC +-
0303 0 7026 MDX RTN33 COUNT OK CONTINUE
*
* ** COUNT IN ERROR **
*
0304 00 0C000502 XIO L FIOCC TURN TIMER OFF
0306 00 0C0006C8 XIO L DSW RESET DSW AVOID INTP
*
0308 0 C039 LD RT300 CONVERT EXPECTED
0309 0 803A A RT302 *COUNT
030A 00 D4000629 STO L HEXWD
*

030C 00 44000609 BSI L HEXCV GO CONVERT WORD SRC

INTERVAL TIMER FUNCTION TEST

```

*****
030E 00 CC00062E      LDD L HEXCD      SET EXPECTED COUNT
0310 00 DC0007F8      STD L TMM13+22   IN MESSAGE
*****
0312 00 C5000003      LD L1 /0003      CONVERT ACTUAL
0314 00 D4000629      STO L HEXWD      COUNT TO 1443 HEX
*****
0316 00 44000609      BSI L HEXCV      GO CONVERT WORD      SRC
*****
0318 00 CC00062E      LDD L HEXCD      SET ACTUAL COUNT
031A 00 DC0007F8      STD L TMM13+14   IN MESSAGE
*****
031C 00 44000511      BSI L ERROR      GO PRINT COUNT ERROR SRC
031E 0 07E2          DC TMM13
031F 0 02F4          DC RTN31          LOOP ERROR ADDRESS
*****
0320 00 0C00026A      XIO L MASKO      REMASK AFTER PRINT
0322 00 0C00026C      XIO L MASK1
*****
0324 00 C5000003      LD L1 /0003      SET PRESENT TIMER
0326 0 001B          STO RT300        *COUNT IN CHECK REG
*****
0327 00 0C000500      XIO L NIOCC      TURN TIMER ON
0329 0 7002          MDX RTN33+2
*****
** CHECK STEP,PASS AND **
** TIMER INDEX FOR RTN **
** COMPLETE **
*****
032A 00 74010342      RTN33 MDX L RT300,1 CHECK REG TO NEXT CT
032C 0 1000          NOP
032D 0 73FF          MDX 3 -1
032E 0 70CD          MDX RTN32        BRNCH NOT 50 STEPS
032F 0 72FF          MDX 2 -1
0330 0 70C3          MDX RTN31        BRNCH NOT 16 PASSES
*****
0331 00 0C000502      XIO L FIOCC      TURN TIMER OFF
*****
0333 0 71FF          MDX 1 -1
0334 0 70B5          MDX RTN30        BRNCH NOT ALL TIMRS
*****
ROUTINE COMPLETE
*****
0335 00 C40001B1      LD L RTNNO      PREPARE SEQUENCE
0337 0 F00B          EDR RT301      CHECK
0338 00 D40001B2      STO L SEQCK
*****
033A 00 0C0006C8      XIO L DSW       PREVENT INTERRUPT
033C 00 0C0001A8      XIO L UMSKO     UNMASK INTERRUPTS
033E 00 0C0001AA      XIO L UMSK1
*****
0340 00 4C00018E      BSC L RTNRT     RETURN TO CONTROL
*****
CONSTANTS
*****
0342 0 0000          RT300 DC 0
0343 0 0004          RT301 DC 4
0344 0 0001          RT302 DC 1
*****
TIMER STARTING COUNT TABL
*****
0345 0 FFFF          CTTBL DC /FFFF
0346 0 FFF0          DC /FFFO

```

INTERVAL TIMER FUNCTION TEST

```

0347 0 FFOF          DC /FFOF
0348 0 FF00          DC /FF00
0349 0 F0FF          DC /F0FF
034A 0 F0F0          DC /F0F0
034B 0 F00F          DC /F00F
034C 0 F000          DC /F000
034D 0 0FFF          DC /0FFF
034E 0 0FF0          DC /0FF0
034F 0 0F0F          DC /0F0F
0350 0 0F00          DC /0F00
0351 0 00FF          DC /00FF
0352 0 00F0          DC /00F0
0353 0 000F          DC /000F
0354 0 0000          DC /0000
*****
ROUTINE NUMBER FIVE
CHECK TIMERS WHILE STEP-
ING TOGETHER, AND WHILE
IN INTERRUPT MODE
*****
0355 00 678006CA      TIM04 LDX I3 INLVL SET TIMER INTERRUPT
0357 00 C40003DE      LD L RT400      LEVEL
0359 00 D7000000      STO L3 0
035B 0 1010          SLA 16          CLEAR INTERRUPT PASS
035C 00 D40003E5      STO L INTSW     *INDICATOR
035E 00 C40003DF      LD L RT401      SET PASS 1 IN ERROR
0360 00 D4000813      STO L TMM14+23 *MESSAGE
*****
0362 00 0C00026A      XIO L MASKO     MASK INTERRUPTS FOR
0364 00 0C00026C      XIO L MASK1     *1ST PASS
*****
0366 0 6103          RTN40 LDX I 3   SET TIMER INDEX
0367 00 C40004FD      LD L T1103      SET IOCC TO ALL TMRS
0369 00 D4000500      STO L NIOCC
*****
0368 00 C50001B6      LD L1 T1BCN+1  TIMER NUMBER TO
036D 00 D4000805      STO L TMM14+9  *ERROR MESSAGE
*****
036F 00 660003E8      RTN41 LDX L2 /03E8 SET STEP INDEX 1000
0371 0 C070          LD RT404        SET ALL TIMERS AND
0372 0 D06E          STO RT403      *THE CHECK REGISTER
0373 0 6303          LDX 3 3        *TO HEX FEOB
0374 00 D7000003      STO L3 3
0376 0 73FF          MDX 3 -1
0377 0 70FC          MDX *-4
*****
0378 00 0C000500      XIO L NIOCC     TURN ALL TIMERS ON
*****
037A 00 C5000003      RTN42 LJ L1 3   GET TIMER CONTENTS
037C 0 9064          S RT403        *LOOP UNTIL TIMER
037D 0 4818          BSC +-         *AND CHECK REG ARE
037E 0 70FB          MDX RTN42     *UNLIKE
*****
037F 0 F064          EDR RT406      CHECK IF DIFF IS 1
0380 0 4818          BSC +-
0381 0 702C          MDX RTN43     COUNT OK CONTINUE
*****
** COUNT IN ERROR **
*****
0382 00 0C000502      XIO L FIOCC     TURN TIMERS OFF
0384 0 C060          LD INTSW
0385 00 4C200389      BSC L *+2,Z
0387 00 0C0006C8      XIO L DSW       RESET DSW IF 1ST PAS

```

INTERVAL TIMER FUNCTION TEST

```

0389 0 C057      LD      RT403  CONVERT EXPECTED
038A 0 R059      A       RT406  *COUNT
038B 00 D4000629 STO L  HEXWD

*
038D 00 44000609 *****
BSI L  HEXCV  GO CONVERT          SRC
*****

038F 00 CC00067E LDD L  HFVCD  EXPECTED WCRD TO ERR
0391 00 DC00080E STD L  TMM14+18 MESSAGE

*
0393 00 C5000003 LD  L1 /0003  ACTUAL COUNT TO
0395 00 D4000629 STO L  HEXWD  CONVERT

*
0397 00 44000609 *****
BSI L  HEXCV  GO CONVERT          SRC
*****

0399 00 CC00062E LDD L  HFVCD  ACTUAL COUNT TO ERR
039B 00 DC00080E STD L  TMM14+12 MESSAGE

*
039D 00 44000511 *****
BSI L  ERROR  PRINT ERROR        SRC
DC    TMM14
DC    RTN41   LOOP ERROR ADDRESS
*****

03A1 00 C5000003 LD  L1 3      SET PRESENT TIMER
03A3 0 D03D      STO  RT403  *CNT IN CHECK REG

*
03A4 0 C040      LD      INTSM
03A5 00 4C2003AB BSC L  *+4,2
03A7 00 0C00026A XIO L  MASKO  REMASK AFTER PRINT
03A9 00 0C00026C XIO L  MASK1  *ON 1ST PASS

*
03AB 00 0C000500 XIO L  NIOCC  TURN TIMERS ON
03AD 0 7902      MDX  RTN43+2

*
03AE 00 740103E1 RTN43 MDX L  RT403,1 SET CHECK REG FOR
03B0 0 1000      NOP
03B1 0 72FF      MDX  2 -1  *NEXT COUNT
03B2 0 70C7      MDX  RTN42  BRNCH NOT 1000 STEPS

*
03B3 00 0C000502 XIO L  FIOCC  TURN TIMERS OFF

*
03B5 0 71FF      MDX  1 -1
03B6 0 70B4      MDX  RTN41-4 BRANCH NOT ALL TIMRS

*
03B7 0 C02D      LD      INTSM
03B8 0 4820      BSC  Z
03B9 0 7019      MDX  RTN44  SKIP IF 1ST PASS
BRNCH IF INT PASS

*
03BA 00 0C0006C8 XIO L  DSW
03BC 00 0C0001A8 XIO L  UMSKO  UNMASK INTERRUPT
03BE 00 0C0001AA XIO L  UMSK1  LEVELS
03C0 03 740103E5 MDX L  INTSM,1 INDICATE INT PASS
03C2 0 C01D      LD      RT402  SET PASS 2 IN ERROR
03C3 00 D4000813 STO L  TMM14+23 *MESSAGE
03C5 00 C40001B6 LD  L  TIBCN+1  SET TIMER C IOCC
03C7 00 D4000500 STO L  NIOCC
03C9 00 6700FFFF LDX L3 /FFFF  GET TIMER INTERRUPT
03CB 00 6F000006 STX L3 6      LEVEL SET

*
03CD 00 0C000500 XIO L  NIOCC  TIMER ON FOR INTRPT

*
03CF 0 3004      MT4  WAIT  4      WAIT FOR INTEPRUPT

*
03D0 00 0C000502 XIO L  FIOCC  TURN TIMER OFF

```

INTERVAL TIMER FUNCTION TEST

```

03D2 0 7093      *      MDX  RTN40  RERUN ROUTINE
*
*      ** ROUTINE COMPLETE **
*
03D3 00 0C0006C8 RTN44 XIO L  DSW  RESET DSW
03D5 00 4C4003D7 BOSC L *      TURN INTERRUPT OFF

*
03D7 00 C40001B1 LD  L  RTNNO  PREPARE SEQUENCE
03D9 0 F009      EOR  RT405  *CHECK
03DA 00 D40001B2 STO L  SEQCK

*
03DC 00 4C00018E BSC L  RTNRT  RETURN TO CONTROL

*
*      CONSTANTS
*
03DE 0 03E6      RT400 DC  TRA4A  TIMER LEVEL
03DF 0 0100      RT401 DC  /0100  43 CODED 1
03E0 0 0200      RT402 DC  /0200  43 CODED 2
03E1 0 0000      RT403 DC  0
03E2 0 FE0B      RT404 DC  /FE0B  TIMER STARTING COUNT
03E3 0 0005      RT405 DC  5
03E4 0 0001      RT406 DC  1
03E5 0 0000      INTSW DC  0  INTERRUPT SWITCH

*
03E6 0 0000      TRA4A DC  0  TIMER TRAP ROUTINE
03E7 00 0C0005C6 XIO L  ILSW
03E9 00 4C8003E6 BSC I  TRA4A

*
*****
*      ROUTINE SIX
*      CHECK STORAGE PRO-
*      TECTED TIMERS
*****
03EB 00 670006D3 TIMOS LDX L3 SPVTP  LOAD TRAP ADDRESS
03ED 00 6F000008 STX L3 8
03EF 00 670004A4 LDX L3 TMM1  ...
03F1 00 6F8006CA STX I3 INLVL
03F3 0 6103      LDX  1 3  SET TIMER INDEX
03F4 0 1010      SLA  16  CLEAR ALL TIMERS
03F5 0 D05E      STO  SPVCK *AND VIOLATE SWITCH
03F6 00 D4000004 STO L  4
03F8 00 D4000005 STO L  5
03FA 00 D4000006 STO L  6
03FC 00 2C410004 STS L  4, /41  STORAGE PROTECT ALL
03FE 00 2C410005 STS L  5, /41  *TIMERS
0400 00 2C410006 STS L  6, /41
0402 00 C40001B6 LD  L  TIBCN+1  INITIALIZE TIMER
0404 00 D4000500 STO L  NIOCC  *IOCC
0406 0 7005      MDX  RTN50+5

*
0407 00 C4000500 RTN50 LG L  NIOCC  MODIFY IOCC FOR
0409 0 1001      SLA  1  *NEXT TIMER
040A 00 D4000500 STO L  NIOCC
040C 0 1010      SLA  16  CLEAR INCREMENT
040D 0 D047      STO  INCCY *SWITCH
040E 0 620A      LDX  2 10  SET PASS INDEX
040F 00 C50001B6 LD  L1 TIBCN+1 SET TIMER NUMBER IN
0411 00 D40008CE STO L  TMM24+11 *MESSAGES
0413 00 D40008EC STO L  TMM25+18

*
0415 00 0C000500 RTN51 XIO L  NIOCC  TURN TIMER ON
0417 00 44000504 BSI L  DEL20  ALLOW FOR INCREMENT SRC
0419 00 0C000502 XIO L  FIOCC  TURN TIMER OFF

*
041B 00 C5000003 LD  L1 3
041D 0 F037      EOR  INCCY  CHECK IF TIMER
*INCREMENTED

```

INTERVAL TIMER FUNCTION TEST

```

041E 0 4820      BSC      Z      OK CONTINUE      88209530
041F 0 7004      MDX      RTN52
*
*****
0420 00 44000511 BSI L ERROR PRINT ERROR SRC 88209540
0422 0 08C3      DC      TMM24      88209550
0423 0 0415      DC      RTN51      LOOP ERROR RETURN 88209560
*****
0424 00 C5000003 RTN52 LD L1 3 MODIFY INCREMENT 88209570
0426 0 002E      STO     INCCY      *SWITCH 88209580
0427 0 72FF      MDX     2 -1      SKIP IF 10 PASSES 88209590
0428 0 70EC      MDX     RTN51      88209600
0429 0 71FF      MDX     1 -1      SKIP IF LAST TIMER 88209610
042A 0 70DC      MDX     RTN50      GO CK NEXT TIMER 88209620
*
*****
VIOLATE PROTECTED TIMERS
*
0428 00 74010454 MDX L SPVCK+1 SET CHECK SWITCH 88209630
042D 0 610J      LDX     1 3      SET TIMER INDEX 88209640
042E 00 C5000186 RTN53 LD L1 T1BCN+1 SET TIMER NUMBER 88209650
0430 00 D4000907 STO L TMM26+21 *IN MESSAGE 88209660
*
*****
0432 00 J5000003 STO L1 3 VIOLATE TIMER 88209670
*
*****
0434 00 44000511 BSI L ERROR PRINT INTRP FAILED SRC 88209680
0436 0 08F2      DC      TMM26      88209690
0437 0 042E      DC      RTN53      LOOP ERROR RETURN 88209700
*****
0438 0 71FF      RTN54 MDX 1 -1 SKIP IF ALL TIMERS 88209710
0439 0 70F4      MDX     RTN53      *CHECKED 88209720
*
*****
043A 00 670006C8 LDX L3 ERINT RESTORE INTERRUPT 88209730
043C 00 6F000008 STX L3 8 *VECTOR 88209740
043E 00 C4000181 LD L RTNNO PREPARE SEQUENCE 88209750
0440 0 F012      EOR     RT500      *CHECK 88209760
0441 00 D4000182 STO L SEQCK 88209770
0443 00 2C400004 STS L 4,740 CLEAR PROTECTED 88209780
0445 00 2C400005 STS L 5,740 *TIMERS 88209790
0447 00 2C400006 STS L 6,740 88209800
0449 00 4C00018E BSC L RTNRT RETURN TO CONTROL 88209810
*****
*****
INTERRUPT RETURN
*
0448 0 C008      RTN55 LD SPVCK CHECK IF SPV PASS 88209820
044C 0 4820      BSC     Z      SKIP IF ERROR 88209830
044D 0 70EA      MDX     RTN54
*
*****
044E 00 44000511 BSI L ERROR PRINT ERROR SRC 88209840
0450 0 08DA      DC      TMM25      88209850
0451 0 042E      DC      RTN53      LOOP ERROR RETURN 88209860
*****
*
*****
CONSTANTS
*
0453 0 0006      RT500 DC 6 CONSTANT 6 88209870
0454 0 0000      SPVCK DC 0 SPV CK SWITCH 88209880
0455 0 0000      INCCY DC 0 INCREMENT SWITCH 88209890
*
*****
SCOPING ROUTINE
*****

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INTERVAL TIMER FUNCTION TEST

```

0456 0 6318      TIMAN LDX 3 27 SE 88210210
0457 0 C042      LD      TIX01      88210220
0458 00 D7000007 TIMAA STO L3 7 SET ALL INTRS FOR 88210230
045A 0 73FF      MDX     3 -1      *RETURN TO MANUAL 88210240
045B 0 70FC      MDX     TIMAA      *ROUTINE 88210250
*
*****
REQUEST SW ENTRY FOR START TIME CT
*
045C 00 4400053C BSI L LOG GO PRINT REQUEST SRC 88210260
045E 0 0815      DC      TMM15      ADRS OF MSG 88210270
045F 0 3005      WAIT    5      88210280
0460 00 0C0001AC XIO L BSW READ BIT SWITCHS 88210290
0462 00 C4000180 LD L BSW GET BIT SWITCHS 88210300
0464 0 D036      STO     TIX02      88210310
*
*****
REQUEST SW ENTRY FOR NUMBER OF STEPS
*
0465 00 4400053C TIMAB BSI L LOG GO PRINT REQUEST SRC 88210320
0467 0 0825      DC      TMM16      ADRS OF MSG 88210330
0468 0 3006      WAIT    6      88210340
0469 00 0C0001AC XIO L BSW READ BIT SWS 88210350
046B 00 C4000180 LD L BSW GET BIT SWS 88210360
046D 0 002E      STO     TIX03      88210370
046E 0 4818      BSC     +-      WAS ENTRY ZERO 88210380
046F 0 70F5      MDX     TIMAB      YES-REQ AGAIN 88210390
*
*****
REQUEST SW ENTRY FOR TIMER
*
0470 00 4400053C BSI L LOG GO PRINT REQUEST SRC 88210400
0472 0 0836      DC      TMM17      ADRS OF MSG 88210410
0473 0 3007      WAIT    7      88210420
0474 00 0C0001AC XIO L BSW READ BIT SWS 88210430
0476 00 C4000180 LD L BSW GET BIT SWS 88210440
0478 0 E029      AND     TIX07      SAVE BITS 4 5 AND 6 88210450
0479 0 180A      SRA     10      SET UP TIMER INDEX 88210460
047A 0 F028      EOR     TIX08      88210470
047B 0 0021      STO     TIX04      88210480
*
*****
SET UP TIMER TO DESIRED VALUE
*
047C 0 C020      TIMAL LD TIX04 GET TIMER ENTRY 88210490
047D 0 D001      STO     TIMAC+1 88210500
047E 00 67000000 TIMAC LDX L3 0 IX 3 = TIMER 88210510
0480 0 C01A      LD      TIX02      GET STARTING COUNT 88210520
0481 00 D7000003 STO L3 3 SET IN TIMER 88210530
0483 0 8018      A       TIX03      ADD DESIRED COUNTS 88210540
0484 0 D019      STO     TIX05      SAVE 88210550
*
*****
TURN ON TIMER FOR DESIRED NUMBER OF COUNTS
*
0485 00 C700049E LD L3 TIX06-1 GET TURN ON CONSTANT 88210560
0487 00 D4000500 STO L NIOCC SET IN IOCC 88210570
0489 00 0C000500 XIO L NIOCC TURN ON TIMER 88210580
*
*****
CK TIMER FOR COUNT
*
048B 00 C7000003 TIMAD LD L3 3 GET COUNTER CONTENTS 88210590
048D 0 9010      S       TIX05      SUB START + NO CNTS 88210600
048E 0 4820      BSC     Z      SKIP = DESIRED COUNT 88210610
048F 0 70FB      MDX     TIMAD      LOOP 88210620
*
*****
GOT DESIRED CT
*
0490 00 0C000502 XIO L FIOCC TURN OFF CTR 88210630

```


INTERVAL TIMER FUNCTION TEST

```

0492 00 0C0001AC      XIO L BSW      CHECK IF OPERATOR      88210890
0494 00 C40001B0      LD L BSWA     *DESIRES TO CHANGE     88210900
0496 0 1002          SLA 2         *ENTRIES                88210910
0497 0 4810          BSC -         -                88210920
0498 0 70E3          MDX TIMAL     LOOP PRESENT SETUP     88210930
0499 0 70BC          MDX TIMAN     GO CHANGE ENTRIES    88210940
                                88210950
                                88210960
                                88210970
                                88210980
                                88210990
                                88211000
                                88211010
                                88211020
                                88211030
                                88211040
                                88211050
                                88211060
                                88211070
                                88211080
                                88211090
                                88211100
                                88211110
                                88211120
                                88211130
                                88211140
                                88211150
                                88211160
                                88211170
                                88211180
                                88211190
                                88211200
                                88211210
                                88211220
                                88211230
                                88211240
                                88211250
                                88211260
                                88211270
                                88211280
                                88211290
                                88211300
                                88211310
                                88211320
                                88211330
                                88211340
                                88211350
                                88211360
                                88211370
                                88211380
                                88211390
                                88211400
                                88211410
                                88211420
                                88211430
                                88211440
                                88211450
                                88211460
                                88211470
                                88211480
                                88211490
                                88211500
                                88211510
                                88211520
                                88211530
                                88211540
                                88211550
                                88211560

CONSTANTS
049A 0 04A4      TIX01 DC TIMAI  INTERRUPT ADRS      88210980
049B 0 C000      TIX02 DC 0      STARTIG CT STORAGE    88210990
049C 0 0000      TIX03 DC 0      NUMBER STEPS STORAGE  88211000
049D 0 0000      TIX04 DC 0      TIMER STORAGE         88211010
049E 0 0000      TIX05 DC 0      START CT + NO. STEPS  88211020
049F 0 8000      TIX06 DC /8000  TURN ON TIMER A       88211030
04A0 0 4000      DC /4000        TURN ON TIMER B       88211040
04A1 0 2000      DC /2000        TURN ON TIMER C       88211050
04A2 0 0E00      TIX07 DC /0E00  /2000                 88211060
04A3 0 0003      TIX08 DC /0003  /8000                 88211070

INTERRUPT ROUTINE
04A4 0 0000      TIMAI DC 0      IE
04A5 0 D006      STO ACS        SAVE ACCUMULATOR    88211110
04A6 00 0C0006C6  XIO L ILSW     SENSE ILSW            88211120
04A8 00 0C0006C8  XIO L DSW     SENSE DSW-RESET      88211130
04AA 00 4CC004A4  BOSC I TIMAI  EXIT                  88211140
04AC 0 0000      ACS DC 0      ACCUMULATOR SAVE     88211150

ROUTINE TO DETERMINE TIMER INTERRUPT LEVEL
04AD 0 0000      TIINT DC 0      SE
04AE 0 6100      LDX 1 0      LOAD INTERRUPT TRAP  88211210
04AF 0 6218      LDX 2 24     ADDRESSES             88211220
04B0 0 C049      LD TI100     ADDRESS INTRP        88211230
04B1 00 D50000GB  STO L1 /000B  BUMP ADDRESS BY 4    88211240
04B3 0 8047      A TI101     88211250
04B4 0 7101      MDX 1 1     88211260
04B5 0 72FF      MDX 2 -1    88211270
04B6 0 70FA      MDX TIINT+4 88211280

04B7 0 6303      LDX 3 3     88211290
04B8 0 C045      LD TI104     88211300
04B9 00 D7000003  STO L3 /0003  SET ALL TIMERS TO    88211310
04BB 0 73FF      MDX 3 -1    FFFF                 88211320
04BC 0 70FC      MDX *-4     88211330
04BD 0 6103      LDX 1 3     SET TIMER INDEX     88211340
04BE 0 C03D      LD TI102     SET TIMER IOCC      88211350
04BF 0 D040      STO NIOCC    88211360
04C0 00 0C0001A8  XIO L UMSKO  UNMASK INTERRUPTS   88211370
04C2 00 0C0001AA  XIO L UMSK1  88211380

04C4 0 083B      TIIN1 XIO NIOCC START TIMER 88211390
04C5 0 403E      BSI DEL20    GO DELAY 20 MILLISEC SRC 88211400
                                88211410
                                88211420
                                88211430
                                88211440
                                88211450
                                88211460
                                88211470
                                88211480
                                88211490
                                88211500
                                88211510
                                88211520
                                88211530
                                88211540
                                88211550
                                88211560

IF TIMER FAILS TO INTERRUPT CONTINUE FROM THIS POINT
04C6 0 083B      XIO FIOCC    TURN TIMER OFF     88211510
04C7 00 C4000212  LD L RT00    88211520
04C9 00 D5000213  STO L1 TIMA-1 SET TMR INOP SM 88211530
04CB 0 C034      TIINS LD NIOCC 88211540

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INTERVAL TIMER FUNCTION TEST

```

04CC 0 1001      SLA 1        SET IOCC FOR NEXT     88211570
04CD 0 D032      STO NIOCC    TIMER                88211580
04CE 0 71FF      MDX 1 -1     88211590
04CF 0 70F4      MDX TIIN1    TRY NEXT TIMER 88211600
                                88211610
                                88211620
                                88211630
                                88211640
                                88211650
                                88211660
                                88211670
                                88211680
                                88211690
                                88211700
                                88211710
                                88211720
                                88211730
                                88211740
                                88211750
                                88211760
                                88211770
                                88211780
                                88211790
                                88211800
                                88211810
                                88211820
                                88211830
                                88211840
                                88211850
                                88211860
                                88211870
                                88211880
                                88211890
                                88211900
                                88211910
                                88211920
                                88211930
                                88211940
                                88211950
                                88211960
                                88211970
                                88211980
                                88211990
                                88212000
                                88212010
                                88212020
                                88212030
                                88212040
                                88212050
                                88212060
                                88212070
                                88212080
                                88212090
                                88212100
                                88212110
                                88212120
                                88212130
                                88212140
                                88212150
                                88212160
                                88212170
                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04D0 00 C40003E5  TIIN2 LD L INTSW  BYPASS STEP CK IF    88211650
04D2 00 4C2004F5  BSC L TIIN6,Z *ANY TMR INTERRUPTED 88211660
04D4 0 C026      LD TI103     SET IOCC TO TURN ON  88211670
04D5 0 D02A      STO NIOCC    ALL TIMERS      88211680
                                88211690
                                88211700
                                88211710
                                88211720
                                88211730
                                88211740
                                88211750
                                88211760
                                88211770
                                88211780
                                88211790
                                88211800
                                88211810
                                88211820
                                88211830
                                88211840
                                88211850
                                88211860
                                88211870
                                88211880
                                88211890
                                88211900
                                88211910
                                88211920
                                88211930
                                88211940
                                88211950
                                88211960
                                88211970
                                88211980
                                88211990
                                88212000
                                88212010
                                88212020
                                88212030
                                88212040
                                88212050
                                88212060
                                88212070
                                88212080
                                88212090
                                88212100
                                88212110
                                88212120
                                88212130
                                88212140
                                88212150
                                88212160
                                88212170
                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04D6 0 1010      SLA 16       CLEAR TIMERS          88211710
04D7 00 D4000004  STO L /0004  88211720
04D9 00 D4000005  STO L /0005  88211730
04DB 00 D4000006  STO L /0006  88211740

04DD 0 0822      XIO NIOCC    TURN ALL TIMERS ON 88211750
                                88211760
                                88211770
                                88211780
                                88211790
                                88211800
                                88211810
                                88211820
                                88211830
                                88211840
                                88211850
                                88211860
                                88211870
                                88211880
                                88211890
                                88211900
                                88211910
                                88211920
                                88211930
                                88211940
                                88211950
                                88211960
                                88211970
                                88211980
                                88211990
                                88212000
                                88212010
                                88212020
                                88212030
                                88212040
                                88212050
                                88212060
                                88212070
                                88212080
                                88212090
                                88212100
                                88212110
                                88212120
                                88212130
                                88212140
                                88212150
                                88212160
                                88212170
                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04DE 0 4025      BSI DEL20    GO DELAY          SRC 88211780
                                88211790
                                88211800
                                88211810
                                88211820
                                88211830
                                88211840
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                                88211890
                                88211900
                                88211910
                                88211920
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                                88211960
                                88211970
                                88211980
                                88211990
                                88212000
                                88212010
                                88212020
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                                88212110
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                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04DF 0 0822      XIO FIOCC    TURN ALL TIMERS OFF 88211810
                                88211820
                                88211830
                                88211840
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                                88211890
                                88211900
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                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04E0 0 6303      LDX 3 3     88211880
04E1 00 C7000003  LD L3 /0003  88211890
04E3 00 4C2004F0  BSC L TIIN3,Z BRNCH IF TIMER STEPD 88211900
04E5 0 73FF      MDX 3 -1    88211910
04E6 0 70FA      MDX *-6     88211920

04E7 00 44000511  BSI L ERROR  PRINT TIMERS FAIL SRC 88211980
04E9 0 0747      DC THM05    TO STEP              88211990
04EA 0 04B7      DC TIINT+10 LOOP ERROR RETURN 88212000

04EB 00 4400053C  TIIN4 BSI L LOG  PRINT RUN MANUAL SRC 88212040
04ED 0 071F      DC THM02    88212050

04EE 00 4C000146  BSC L CTL01+12 GO TO WAIT 1 88212080
                                88212090
                                88212100
                                88212110
                                88212120
                                88212130
                                88212140
                                88212150
                                88212160
                                88212170
                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04F0 00 44000511  TIIN3 BSI L ERROR  PRINT TIMERS FAIL SRC 88212110
04F2 0 0757      DC THM06    TO INTERRUPT     88212120
04F3 0 04B7      DC TIINT+10 LOOP ERROR RETURN 88212130

04F4 0 70F6      MDX TIIN4   88212140
                                88212150
                                88212160
                                88212170
                                88212180
                                88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04F5 00 4400053C  TIIN6 BSI L LOG  PRINT TIMER INTERRUPT SRC 88212180
04F7 0 0767      DC THM07    LEVEL              88212190
                                88212200
                                88212210
                                88212220
                                88212230
                                88212240

04F8 00 4C8004AD  BSC I TIINT  RETURN TO USER   SX 88212220
                                88212230
                                88212240

TIINT SUBROUTINE CONSTANTS

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INTERVAL TIMER FUNCTION TEST

04FA 0 0654	T1100 DC	INTRP	TRAP RTNS START ADRS	88212250
04FB 0 0004	T1101 DC	/0004		88212260
04FC 0 2000	T1102 DC	/2000		88212270
04FD 0 E000	T1103 DC	/E000		88212280
04FE 0 FFFF	T1104 DC	/FFFF		88212290
0500 0000	BSS E 0			88212300
0500 0 0000	NIOCC DC	/0000	TURN TIMER ON IOCC	88212310
0501 0 0420	DC	/0420		88212320
0502 0 0000	FIOCC DC	/0000	TURN TIMER OFF IOCC	88212330
0503 0 0420	DC	/0420		88212340
			70 MILLISEC DELAY ROUTINE	88212350
0504 0 0000	DEL20 DC	0		88212360
0505 00 740001B4	MDX L	SPEED,0	SKIP IF 2 USEC CORE	88212370
0507 0 7003	MDX	*+3		88212380
0508 00 670036B0	LDX L3	/36B0	2 USEC INDEX	88212390
050A 0 7002	MDX	*+2		88212400
050B 00 670032C8	LDX L3	/32C8	4 USEC INDEX	88212410
050D 0 73FF	MDX	*-1		88212420
050E 0 70FE	MDX	*-2		88212430
050F 00 4C800504	BSC I	DEL20		88212440
			*****	88212450
			*****	88212460
			*****	88212470
			*****	88212480
			*****	88212490
			*****	88212500
			*****	88212510
			*****	88212520
			*****	88212530
			*****	88212540
			*****	88212550
			*****	88212560
0511 0 0000	ERROR DC	0		88212570
0512 00 C4800511	LD I	ERROR	SET MESSAGE ADDRESS	88212580
0514 0 D00F	STO	ERR01+1	IN LOG CALL	88212590
0515 00 74010511	MDX L	ERROR,1		88212600
0517 00 C4800511	LD I	ERROR	LOAD LOOP ON ERROR	88212610
0519 0 D020	STO	LPERR+1	ADDRESS	88212620
051A 00 7401053B	MDX L	ERRID,1	SET ERR CALL INDOCTOR	88212630
051C 00 0C0001AC	XIO L	BSW	READ BIT SWITCHES	88212640
051E 00 C40001B0	LD L	BSWA	CHECK IF BYPASS	88212650
0520 0 1802	SRA	2	*ERROR PRINT	88212660
0521 0 4804	BSC	E		88212670
0522 0 7002	MDX	ERR02		88212680
0523 0 4018	ERR01 BSI	LOG	GO PRINT ERROR	88212690
0524 0 0000	DC	0		88212700
0525 0 1010	ERR02 SLA	16	CLEAR ERROR CALL	88212710
0526 0 D014	STO	ERRID	INDICATOR	88212720
0527 00 C40001B0	LD L	BSWA	CHECK IF HALT ON ERR	88212730
0529 0 1801	SRA	1		88212740
052A 0 4804	BSC	E		88212750
052B 0 700B	MDX	WT9	HALT BIT ON	88212760
052C 00 0C0001AC	ERR03 XIO	L BSW	READ BIT SWITCHES	88212770
052E 00 C40001B0	LD L	BSWA	CHECK IF LOOP ON	88212780
0530 0 1803	SRA	3	*ERROR REQUESTED	88212790
0531 0 4804	BSC	E		88212800
0532 0 7006	MDX	LPERR	LOOP ERROR	88212810
0533 00 74010511	MDX L	ERROR,1	ADD 1 TO RETURN	88212820
0535 00 4C800511	BSC I	ERR0R	RETURN TO USER	88212830

INTERVAL TIMER FUNCTION TEST

0537 0 3009	WT9	WAIT	9	ERROR HALT REQUESTED	88212930
0538 0 70F3	MDX	ERR03			88212940
				*****	88212950
				*****	88212960
				*****	88212970
				*****	88212980
				*****	88212990
0539 00 4C000000	LPERR BSC	L 0		LOOP ERROR REQUESTED	88213000
053B 0 0000	ERRID DC	0		*****	88213010
				*****	88213020
				*****	88213030
				*****	88213040
				*****	88213050
				*****	88213060
				*****	88213070
053C 0 00C^	LOG DC	0		LOG ROUTINE	88213080
053D 0 681D	LOG01 STX	3	LOG06+1	SAVE IX 3	88213090
053E 00 0C00026A	XIO L	MASK0		MASK INTERRUPTS	88213100
0540 03 0C00026C	XIO L	MASK1			88213110
0542 00 C40001B3	LD L	OPIND		CK WHICH OUTPUT DVC	88213120
0544 00 4C180564	BSC L	TWRTR,+		*BRANCH IF 1053/1816	88213130
0546 00 C480053C	LD I	LOG		GET MESSAGE ADDRESS	88213140
0548 0 D055	STO	PRWRT		SET IN IOCC	88213150
0549 0 0850	LOG02 XIO	PRSNS		CHECK PRINTER READY	88213160
054A 00 4C040550	BSC L	WTA,E		BRANCH IF NOT READY	88213170
054C 0 1801	SRA	1			88213180
054D 00 4C040552	BSC L	WTR,E		BRANCH IF BUSY	88213190
054F 0 7004	MDX	LOG05		READY AND NOT BUSY	88213200
0550 0 300A	WTA	WAIT	10	NOT READY	88213210
0551 0 70F7	MDX	LOG02		CHECK AGAIN	88213220
0552 0 300B	WTR	WAIT	11	BUSY	88213230
0553 0 70F5	MDX	LOG02		CHECK AGAIN	88213240
0554 0 0849	LOG05 XIO	PRWRT		OUTPUT MESSAGE	88213250
0555 0 0846	XIO	PRSN		CHECK FOR OP COMPLY	88213260
0556 0 1002	SLA	2			88213270
0557 0 4810	BSC	-			88213280
0558 0 70FC	MDX	*-4			88213290
0559 0 0840	XIO	PRSNS		RESET DSW	88213300
055A 00 67000000	LOG06 LDX	L3 0		RESTORE IX 3	88213310
055C 00 0C0001A8	XIO L	UMSK0		UNMASK INTERRUPTS	88213320
055E 00 0C0001AA	XIO L	UMSK1			88213330
0560 00 7401053C	MDX L	LOG,1		BUMP RETURN	88213340
0562 00 4C80053C	BSC I	LOG		RETURN TO USER	88213350
0564 0 1010	TWRTR SLA	16			88213360
0565 0 D032	STO	WRDSW			88213370
0566 0 0839	XIO	TWSNS		CHECK IF TYPEWRITER	88213380
0567 0 1005	SLA	5		READY	88213390
0568 0 180F	SRA	15			88213400
0569 00 4C18056D	BSC L	TWR01,+			88213410
056B 0 300C	WTC	WAIT	12	NOT READY	88213420
056C 0 70F9	MDX	TWRTR+2			88213430
056D 0 C028	TWR01 LD	TWRTO		CARRIAGE RETURN AND	88213440
056E 0 D02A	STO	IOARA		LINE SPACE TO IO ARA	88213450

INTERVAL TIMER FUNCTION TEST

056F 0 0832	XIO	TWR2	CARG RETURN/LINE SP	88213610
0570 0 082F	XIO	TWSNS	HANG TILL NOT BUSY	88213620
0571 0 180B	SRA	11		88213640
0572 0 4804	BSC	E		88213650
0573 0 70FC	MDX	**4		88213660
0574 0 6301	LDX	3 1	BYPASS 1443 WORD COUNT	88213670
0575 00 C480053C	LD	1 LOG	GET MESSAGE ADDRESS	88213680
0577 0 0001	STO	TWR02+1		88213690
0578 00 C7000000	TWR02 LD	L3 0	GET WORD TO PRINT	88213700
057A 00 D40005D4	STO	L CODVD	SET IN CONVERSION RT	88213710
057C 0 F01A	EOR	TWR1	CHECK IF TERMINATOR	88213720
057D 00 4C18055A	BSC	L LOGO6,+	BRANCH IF TERMINATOR	88213730
057F 00 440005A4	BSI	L CODCV	GO CONVERT 43 TO TW SRC	88213740
0581 00 C40005D4	LD	L CODWD		88213750
0583 0 0015	STO	IOARA		88213760
0584 0 081D	XIOWR XIO	TWR2	WRITE CHARACTER	88213770
0585 0 081A	XIOSN XIO	TWSNS	HANG ON BUSY	88213780
0586 0 180B	SRA	11		88213790
0587 0 4804	BSC	E		88213800
0588 0 70FC	MDX	XIOSN	BUSY	88213810
0589 0 C00E	LD	WRDSW	GFT 1/2 WORD SWITCH	88213820
058A 0 4804	BSC	E		88213830
058B 0 7006	MDX	TWR03	GO SET UP NEXT WORD	88213840
058C 0 C00C	LD	IOARA		88213850
058D 0 1008	SLA	8	POSITION 2ND 1/2 WD	88213860
058E 0 000A	STO	IOARA		88213870
058F 00 74010598	MDX	L WRDSW,1	BUMP WORD SWITCH	88213880
0591 0 70F2	MDX	XIOWR	GO WRITE 2ND 1/2 WD	88213890
0592 0 7301	TWR03 MDX	3 1	NEXT WORD INDEX	88213900
0593 00 74010598	MDX	L WRDSW,1	BUMP WORD SWITCH	88213910
0595 0 70E2	MDX	TWR02	GO GET NEXT WORD	88213920
0596 0 8103	TWR0 DC	/8103	LINE SP/CARRIAGE RTN	88213930
0597 0 FFFF	TWR1 DC	/FFFF	TERMINATOR	88213940
0598 0 0000	WRDSW DC	0	1/2 WORD SWITCH	88213950
0599 0 0000	IOARA DC	0	OUTPUT AREA	88213960
059A 0000	BSS	E 0		88213970
059A 0 0000	PRNS DC	/0000	PRINTER SENSE IOCC	88213980
059B 0 3701	DC	/3701		88213990
059C 0 0000	PRSN DC	0	NON RESET SENSE	88214000
059D 0 3700	DC	/3700		88214010
059E 0 0000	PRWR DC	/0000	PRINTER WRITE IOCC	88214020
059F 0 3500	DC	/3500		88214030

INTERVAL TIMER FUNCTION TEST

05A0 0 0000	TWSNS DC	/0000	TYPEWTR SENSE IOCC	88214290
05A1 0 0F03	DC	/0F03		88214300
05A2 0 0599	TWR2 DC	IOARA	TYPEWTR WRITE IOCC	88214310
05A3 0 0902	DC	/0902		88214320
05A4 0 0000	CODCV DC	0		88214330
05A5 0 6927	STX	1 CODC4+1	SAVE INDEX REGS SE	88214340
05A6 0 6A28	STX	2 CODC4+3		88214350
05A7 0 6B79	STX	3 CODC4+5		88214360
05A8 0 1010	SLA	16	CLEAR LEFT HALF WORD	88214370
05A9 0 D02B	STO	LHIND	*INDICATOR	88214380
05AA 0 6300	LDX	3 0		88214390
05AB 0 C028	CODC1 LD	CODWD	GET WORD TO CONVERT	88214400
05AC 0 1890	SRT	16	SET IN Q	88214410
05AD 0 C027	LD	LHIND		88214420
05AE 0 4820	BSC	Z	SKIP IF LEFT HALF	88214430
05AF 0 1088	SLT	8	POSITION RIGHT HALF	88214440
0580 0 1010	SLA	16		88214450
0581 0 1084	SLT	4	ZONE TO ACCUM	88214460
0582 0 D023	STO	COD00		88214470
0583 00 659005D6	LDX	11 COD00	IX 1 = ZONE	88214480
0585 0 1010	SLA	16		88214490
0586 0 1084	SLT	4	DIGIT TO ACCUM	88214500
0587 0 D01E	STO	COD00		88214510
0588 00 668005D6	LDX	12 COD00	IX 2 = DIGIT	88214520
058A 00 C50005D9	LD	L1 ZONE	GET ZONE TABLE ADDR	88214530
058C 0 D001	STO	CODC2+1	SET IN CONVERSION WD	88214540
058D 00 C6000000	CODC2 LD	L2 0	GET CONVERTED CODE	88214550
058F 00 D70005D7	STO	L3 COD01		88214560
05C1 0 C013	LD	LHIND		88214570
05C2 00 4C2005C8	BSC	L CODC3,2	BRNCH IF RIGHT HALF	88214580
05C4 00 740105D5	MDX	L LHIND,1		88214590
05C6 0 7301	MDX	3 1		88214600
05C7 0 70E3	MDX	CODC1	GO CONVERT RIGHT HLF	88214610
05C8 0 C00E	CODC3 LD	COD01	PACK CONVERTED CODES	88214620
05C9 0 1008	SLA	8		88214630
05CA 0 E80D	OR	COD02		88214640
05CB 0 D008	STO	CODWD		88214650
05CC 00 65000000	CODC4 LDX	L1 0	RESTORE INDEX REGS	88214660
05CE 00 66000000	LDX	L2 0		88214670
05D0 00 67000000	LDX	L3 0		88214680
05D2 00 4C8005A4	BSC	I CODCV	RETURN TO USER SX	88214690
05D4 0 0000	CODWD DC	0	CONSTANTS	88214700
05D5 0 0000	LHIND DC	0	WORD LOCATION	88214710
05D6 0 0000	COD00 DC	0	LEFT HALF INDICATOR	88214720
05D7 0 0000	COD01 DC	0	WORK AREA	88214730
05D8 0 0000	COD02 DC	0	CONVERTED LH CHARACT	88214740
			CONVERTED RH CHARACT	88214750
				88214760
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				88214800
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INTERVAL TIMER FUNCTION TEST

INTERVAL TIMER FUNCTION TEST

1443 TO 1816/1053 CODE
CONVERSION TABLES

05D9 0 05DD	ZONE DC	ZONEN	NO ZONE
05DA 0 05FA	DC	ZONE1	0 ZONE
05DB 0 05F3	DC	ZONE2	11 ZONE
05DC 0 05FD	DC	ZONE3	12 ZONE

05DD 0 0021	ZONEN DC	/0021	SPACE
05DE 0 00FC	DC	/00FC	1
05DF 0 00D8	DC	/00D8	2
05E0 0 00DC	DC	/00DC	3
05E1 0 00F0	DC	/00F0	4
05E2 0 00F4	DC	/00F4	5
05E3 0 00D0	DC	/00D0	6
05E4 0 00D4	DC	/00D4	7
05E5 0 00E4	DC	/00E4	8
05E6 0 00E0	DC	/00E0	9
05E7 0 00C4	DC	/00C4	0

ZONE1 DC 0

05EA 0 009A	DC	/009A	S
05EB 0 009E	DC	/009E	T
05EC 0 0082	DC	/0082	U
05ED 0 0086	DC	/0086	V
05EE 0 0092	DC	/0092	W
05EF 0 0096	DC	/0096	X
05F0 0 00A6	DC	/00A6	Y
05F1 0 00A2	DC	/00A2	Z

ZONE2 DC 0

05F4 0 007E	DC	/007E	J
05F5 0 005A	DC	/005A	K
05F6 0 005E	DC	/005E	L
05F7 0 0072	DC	/0072	M
05F8 0 0076	DC	/0076	N
05F9 0 0052	DC	/0052	O
05FA 0 0056	DC	/0056	P
05FB 0 0066	DC	/0066	Q
05FC 0 0062	DC	/0062	R

ZONE3 DC 0

05FE 0 003E	DC	/003E	A
05FF 0 001A	DC	/001A	B
0600 0 001E	DC	/001E	C
0601 0 0032	DC	/0032	D
0602 0 0036	DC	/0036	E
0603 0 0012	DC	/0012	F
0604 0 0016	DC	/0016	G
0605 0 0026	DC	/0026	H
0606 0 0022	DC	/0022	I
0607 0 0086	DC	/0086	0 ERROR
0608 0 0000	DC	/0000	PERIOD

 * HEXADECIMAL TO 1443 CODED *
 * HEXADECIMAL CONVERSION *
 * ROUTINE *

0609 0 0000	HEXCV DC	0	
060A 0 6A19	STX	2 HEXC2+1	SAVE INDEX 2 AND 3
060B 0 6B1A	STX	3 HEXC2+3	
060C 0 6204	LDX	2 4	CONVERSION INDEX

060D 0 C018	LD	HEXWD	GET WORD TO CONVERT
060E 0 1890	SRT	16	SET A IN Q
060F 0 1010	SLA	16	
0610 0 1084	HEXC1 SLT	4	GET CHARACTER

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0611 0 D001	STD	HEXC1+3	
0612 00 67000000	LDX	L3 0	SET CODE TABLE INDEX

0614 00 C7000630	LD	L3 CODEM	GET CODED CHARACTER
0616 00 D6000629	STO	L2 HEX00-1	AND SAVE
0618 0 1010	SLA	16	

0619 0 72FF	MDX	2 -1	CHECK IF DONE
061A 0 70F5	MDX	HEXC1	

0618 0 C011	LD	HEXC0+3	PACK CODED WORDS
061C 0 1008	SLA	8	
061D 0 E80E	OR	HEXC0+2	
061E 0 D00F	STO	HEXC0	
061F 0 C008	LD	HEXC0+1	
0620 0 1008	SLA	8	
0621 0 E808	OR	HEXC0	
0622 0 D00C	STO	HEXC0+1	

0623 00 66000000	HEXC2 LDX	L2 0	RESTORE INDEX
0625 00 67000000	LDX	L3 0	

0627 00 4C800609	BSC	I HEXCV	RETURN TO USER
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 * CONSTANTS *

0629 0 0000	HEXWD DC	0	WORD TO CONVERT
062A 0 0000	HEX00 DC	0	
062B 0 0000	DC	0	* UNPACKED CODED
062C 0 0000	DC	0	* WORD
062D 0 0000	DC	0	*

062E 0000	BSS	E 0	
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062E 0 00C0	HEXC0 DC	0	* PACKED CODED WORD
062F 0 0000	DC	0	*

 * CONVERSION TABLE *

0630 0 000A	CJDEH DC	/000A	0
0631 0 0001	DC	/0001	1
0632 0 0002	DC	/0002	2
0633 0 0003	DC	/0003	3
0634 0 0004	DC	/0004	4
0635 0 0005	DC	/0005	5
0636 0 0006	DC	/0006	6
0637 0 0007	DC	/0007	7
0638 0 0008	DC	/0008	8
0639 0 0009	DC	/0009	9
063A 0 0031	DC	/0031	A
063B 0 0032	DC	/0032	B
063C 0 0033	DC	/0033	C
063D 0 0034	DC	/0034	D
063E 0 0035	DC	/0035	E
063F 0 0036	DC	/0036	F

 * ROUTINE TWO INTERRUPT *
 * TRAP ROUTINE *

0640 0 0000	TRAP2 DC	0	
0641 00 0C000502	XIO L	FIOCC	TURN TIMER OFF

0643 00 0C0006C6	XIO L	ILSW	SENSE ILSW AND SAVE
0645 00 D500064F	STO	L1 TRP01-1	

0647 00 0C0006C8	XIO L	DSW	SENSE DSW AND SAVE
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88216210
88216220
88216230
88216240
88216250
88216260
88216270
88216280
88216290
88216300
88216310
88216320

INTERVAL TIMER FUNCTION TEST

```

0649 0 0009          STO   TRP02
          *
064A 00 0C0006CB    XIO  L  DSW   CHECK IF DSW RESET
064C 0 4820          BSC   Z      ON FIRST SENSE
          *
064D 0 300D          WTD   WAIT  13   DSW DID NOT RESET
          *                   DSW IN ACCUMULATOR
064E 00 4C400296    BOSC  L  RTN22  RETURN TO CALLER
          *
0650 0 0000          TRP01 DC    0      ILSM
0651 0 0000          DC      0
0652 0 0000          DC      0
0653 0 0000          TRP02 DC    0      DSW
          *
          TRAP ROUTINES TO DETERMINE
          TIMER INTERRUPT LEVEL
          *
          ** LEVEL 0 **
0654 0 0000          INTRP DC    0
0655 0 405E          BSI   CMTRP  COMN RTN
0656 0 0A0A          DC     /0A0A  00 1443
0657 0 000B          DC     /000B  INTRP ADRS
          *
          ** LEVEL 1 **
0658 0 0000          DC      0
0659 0 405A          BSI   CMTRP  COMN RTN
065A 0 0A01          DC     /0A01  01 1443
065B 0 000C          DC     /000C  INTRP ADRS
          *
          ** LEVEL 2 **
065C 0 0000          DC      0
065D 0 4056          BSI   CMTRP  COMN RTN
065E 0 0A02          DC     /0A02  02 1443
065F 0 000D          DC     /000D  INTRP ADRS
          *
          ** LEVEL 3 **
0660 0 0000          DC      0
0661 0 4052          BSI   CMTRP  COMN RTN
0662 0 0A03          DC     /0A03  03 1443
0663 0 000E          DC     /000E  INTRP ADRS
          *
          ** LEVEL 4 **
0664 0 0000          DC      0
0665 0 404E          BSI   CMTRP  COMN RTN
0666 0 0A04          DC     /0A04  04 1443
0667 0 000F          DC     /000F  INTRP ADRS
          *
          ** LEVEL 5 **
0668 0 0000          DC      0
0669 0 404A          BSI   CMTRP  COMN RTN
066A 0 0A05          DC     /0A05  05 1443
066B 0 0010          DC     /0010  INTRP ADRS
          *
          ** LEVEL 6 **
066C 0 0000          DC      0
066D 0 4046          BSI   CMTRP  COMN RTN
066E 0 0A06          DC     /0A06  06 1443
066F 0 0011          DC     /0011  INTRP ADRS
          *
          ** LEVEL 7 **

```

```

8B216330
8B216340
8B216350
3B216360
8B216370
8B216380
8B216390
8B216400
8B216410
8B216420
8B216430
8B216440
8B216450
8B216460
8B216470
8B216480
8B216490
8B216500
8B216510
8B216520
8B216530
8B216540
8B216550
8B216560
8B216570
8B216580
8B216590
8B216600
8B216610
8B216620
8B216630
8B216640
8B216650
8B216660
8B216670
8B216680
8B216690
8B216700
8B216710
8B216720
8B216730
8B216740
8B216750
8B216760
8B216770
8B216780
8B216790
8B216800
8B216810
8B216820
8B216830
8B216840
8B216850
8B216860
8B216870
8B216880
8B216890
8B216900
8B216910
8B216920
8B216930
8B216940
8B216950
8B216960
8B216970
8B216980
8B216990
8B217000

```

INTERVAL TIMER FUNCTION TEST

```

0670 0 0000          DC      0
0671 0 4042          BSI   CMTRP  COMN RTN
0672 0 0A07          DC     /0A07  07 1443
0673 0 0012          DC     /0012  INTRP ADRS
          *
          ** LEVEL 8 **
0674 0 0000          DC      0
0675 0 403E          BSI   CMTRP  COMN RTN
0676 0 0A08          DC     /0A08  08 1443
0677 0 0013          DC     /0013  INTRP ADRS
          *
          ** LEVEL 9 **
0678 0 0000          DC      0
0679 0 403A          BSI   CMTRP  COMN RTN
067A 0 0A09          DC     /0A09  09 1443
067B 0 0014          DC     /0014  INTRP ADRS
          *
          ** LEVEL 10 **
067C 0 0000          DC      0
067D 0 4036          BSI   CMTRP  COMN RTN
067E 0 010A          DC     /010A  10 1443
067F 0 0015          DC     /0015  INTRP ADRS
          *
          ** LEVEL 11 **
0680 0 0000          DC      0
0681 0 4032          BSI   CMTRP  COMN RTN
0682 0 0101          DC     /0101  11 1443
0683 0 0016          DC     /0016  INTRP ADRS
          *
          ** LEVEL 12 **
0684 0 0000          DC      0
0685 0 402E          BSI   CMTRP  COMN RTN
0686 0 0102          DC     /0102  12 1443
0687 0 0017          DC     /0017  INTRP ADRS
          *
          ** LEVEL 13 **
0688 0 0000          DC      0
0689 0 402A          BSI   CMTRP  COMN RTN
068A 0 0103          DC     /0103  13 1443
068B 0 0018          DC     /0018  INTRP ADRS
          *
          ** LEVEL 14 **
068C 0 0000          DC      0
068D 0 4026          BSI   CMTRP  COMN RTN
068E 0 0104          DC     /0104  14 1443
068F 0 0019          DC     /0019  INTRP ADRS
          *
          ** LEVEL 15 **
0690 0 0000          DC      0
0691 0 4022          BSI   CMTRP  COMN RTN
0692 0 0105          DC     /0105  15 1443
0693 0 001A          DC     /001A  INTRP ADRS
          *
          ** LEVEL 16 **
0694 0 0000          DC      0
0695 0 401E          BSI   CMTRP  COMN RTN
0696 0 0106          DC     /0106  16 1443
0697 0 001B          DC     /001B  INTRP ADRS

```

INTERVAL TIMER FUNCTION TEST

```

** LEVEL 17 **
0698 0 0000      DC      0
0699 0 401A     BSI     CMTRP   COMN RTN
069A 0 0107      DC     /0107   17 1443
069B 0 001C      DC     /001C   INTRP ADRS

** LEVEL 18 **
069C 0 0000      DC      0
069D 0 4016     BSI     CMTRP   COMN RTN
069E 0 0108      DC     /0108   18 1443
069F 0 001D      DC     /001D   INTRP ADRS

** LEVEL 19 **
06A0 0 0000      DC      0
06A1 0 4012     BSI     CMTRP   COMN RTN
06A2 0 FCE0      DC     /FCE0   19 1443
06A3 0 001E      DC     /001E   INTRP ADRS

** LEVEL 20 **
06A4 0 0000      DC      0
06A5 0 400E     BSI     CMTRP   COMN RTN
06A6 0 020A      DC     /020A   20 1443
06A7 0 001F      DC     /001F   INTRP ADRS

** LEVEL 21 **
06A8 0 0000      DC      0
06A9 0 400A     BSI     CMTRP   COMN RTN
06AA 0 0201      DC     /0201   21 1443
06AB 0 0020      DC     /0020   INTRP ADRS

** LEVEL 22 **
06AC 0 0000      DC      0
06AD 0 4006     BSI     CMTRP   COMN RTN
06AE 0 0202      DC     /0202   22 1443
06AF 0 0021      DC     /0021   INTRP ADRS

** LEVEL 23 **
06B0 0 0000      DC      0
06B1 0 4002     BSI     CMTRP   COMN RTN
06B2 0 0203      DC     /0203   23 1443
06B3 0 0022      DC     /0022   INTRP ADRS

COMMON TRAP ROUTINE USED
BY TIINT
06B4 0 0000      CMTRP DC      0
06B5 00 0C000502 XIO L FI0CC   TURN TIMERS OFF SE
06B7 0 080E      XIO     ILSW   RESET ILSW
06B8 0 080F      XIO     DSW    RESET DSW
06B9 00 C48006B4 LD I CMTRP   GET INTRP LEVEL NUMB
06C0 00 D4000777 STO L TMM07+1C SET IN PRINT MESSAGE
06BD 00 740106B4 MDX L CMTRP,1
06BF 00 C48006B4 LD I CMTRP   GET INTRP ADDRESS
06C1 0 D008      STO     INLVL  SAVE
06C2 00 740103E5 MDX L INTSW,1 SET INTERRUPT SWITCH
06C4 00 4C4004CB BOSC L TIINS  RETURN TO MAIN FLOW SX

```

INTERVAL TIMER FUNCTION TEST

```

CONSTANTS
06C6 0000      BSS E 0
06C6 0 0000     ILSW DC /0000   SENSE ILSW IOCC
06C7 0 0300     DC     /0300
06C8 0 0000     DSW  DC /0000   SENSE/RESET DSW IOCC
06C9 0 0721     DC     /0721
06CA 0 0000     INLVL DC 0      TIMER INTERRUPT ADRS
06CB 0 0000     ERINT DC 0
06CC 0 COFE     LD     ERINT   SET I CTR IN Q REG
06CD 0 1890     SRT    16
06CE 00 0C0006C6 XIO L ILSW
06D0 0 300E     WTE   WAIT 14   ILSW IN A REG
06D1 00 4C40012D BOSC L TISRT
06D3 0 0000     SPVTP DC 0      INTERRUPT ENTRY IE
06D4 0 COFE     LD     SPVTP   I COUNT TO Q REG
06D5 0 1890     SRT    16
06D6 0 08EF     XIO   ILSW   SENSE ILSW
06D7 0 F006     EOR   SPVCN  CHECK FOR SPV
06D8 00 4C58044B BOSC L RTN55,+ BRANCH IF SPV
06DA 0 F003     EOR   SPVCN  RESTORE ILSW
06DB 0 300F     WTF   WAIT 15   NOT SPV ERROR
06DC 00 4C40012D BOSC L TISRT   RESTART PROGRAM IX
06DE 0 2000     SPVCN DC /2000  SPV ILSW
06DF 0 0000     SVINT DC 0
06E0 0 D020     STO   SVIO   SAVE ACCUMULATOR IE
06E1 00 0C0006C6 XIO L ILSW   RESET ILSW
06E3 00 7402070D MDX L SV7,2  SET PASS SWITCH
06E5 0 1010     SLA   16
06E6 0 D023     STO   SV4   CLEAR AFEA CODE CNTR
06E7 0 C020     LD    SV2
06E8 0 D023     STO   SV6   SET IOCC IN USE SW
06E9 0 C01D     SVINO LD SV1
06EA 0 D020     STO   SV5   SET MODIFIER COUNTER
06EB 0 C01E     SVINI LD SV4
06EC 0 1008     SLA   11
06ED 0 E81D     OR    SV5
06EE 0 E81D     OR    SV6
06EF 0 D01F     STO   SVIO+1
06F0 0 081D     XIO   SVIO
06F1 00 74FF070B MDX L SV5,-1
06F3 0 70F7     MDX   SVINI
06F4 00 7401070A MDX L SV4,1  BRANCH IF NOT ALL MD
06F6 0 C013     LD    SV4   INCREMENT AREA CODE
06F7 0 900E     S     SV0
06F8 0 4808     BSC   +
06F9 0 70EF     MDX   SVINO  CHECK IF ALL AC USED
06FA 00 74FF070D MDX L SV7,-1  SKIP IF ALL AC USED
06FC 0 7001     MDX   **1    GO SENSE WITH NXT AC
                                           SKIP IF SECOND PASS

```


INTERVAL TIMER FUNCTION TEST

```

06FD 0 7005          MDX   SVEXT-1
06FE 0 C00A          LD    SV3
06FF 0 D00C          STO   SV6      SET IOCC FOR PI
0700 0 1010          SLA   16
0701 0 D008          STO   SV4      SET AC FOR NEXT
0702 0 70E6          MDX   SVINO    *PASS
0703 0 C00A          LD    SVIO     RESTORE ACCUMULATOR
0704 00 4CC006DF     SVEXT BOSC I SVINT  EXIT

```

** CONSTANTS **

```

0706 0 001F          SV0   DC      /001F  NUMBER OF AREA CODES
0707 0 00FF          SV1   DC      /00FF  NUMBER OF MODIFIERS
0708 0 0701          SV2   DC      /0701  SENSE/RESET DSM
0709 0 0700          SV3   DC      /0700  SENSE/RESET PISM
070A 0 0000          SV4   DC      0       AREA CODE INDICATOR
070B 0 0000          SV5   DC      0       MODIFIER INDICATOR
070C 0 0000          SV6   DC      0       IOCC IN USE
070D 0 0000          SV7   DC      0       PASS SWITCH
070E 0 0000          BSS   E      0
070F 0 0000          SVIO  DC      0       SENSE DSM IOCC

```

PRINT MESSAGES
1443 CODED

```

0710 0 0000          TMM01 DC /0000  WORD COUNT
0711 0 310A          DC   /310A  AO
0712 0 0A01          DC   /0A01  O1
0713 0 0C00          DC   /0C00  SPACE
0714 0 0000          DC   /0000  SPACE
0715 C 1233          DC   /1233  SC
0716 0 2627          DC   /2627  OP
0717 0 3500          DC   /3500  E
0718 0 2913          DC   /2913  RT
0719 0 2500          DC   /2500  N
071A 0 1235          DC   /1235  SE
071B 0 2335          DC   /2335  LE
071C 0 3313          DC   /3313  CT
071D 0 3534          DC   /3534  ED
071E 0 FFFF          DC   /FFFF  TERM

071F 0 0008          TMM02 DC /0008  WORD COUNT
0720 0 330A          DC   /330A  CO
0721 0 0A01          DC   /0A01  O1
0722 0 0000          DC   /0000  SPACE
0723 0 0000          DC   /0000  SPACE
0724 0 2914          DC   /2914  RU
0725 0 2500          DC   /2500  P
0726 0 1233          DC   /1233  SC
0727 0 2627          DC   /2627  OP
0728 0 3500          DC   /3500  E
0729 0 2913          DC   /2913  RT
072A 0 2500          DC   /2500  N
072B 0 FFFF          DC   /FFFF  TERM

072C 0 000C          TMM03 DC /000C  WORD COUNT
072D 0 310A          DC   /310A  AO
072E 0 0A02          DC   /0A02  O2
072F 0 0000          DC   /0000  SPACE
0730 0 0000          DC   /0000  SPACE
0731 0 2729          DC   /2729  PR
0732 0 2637          DC   /2637  OG
0733 0 2931          DC   /2931  RA
0734 0 2400          DC   /2400  M

```

INTERVAL TIMER FUNCTION TEST

```

0735 0 3326          DC   /3326  CO
0736 0 2427          DC   /2427  HP
0737 0 2335          DC   /2335  LE
0738 0 1335          DC   /1335  TE
0739 0 FFFF          DC   /FFFF  TERM

073A 0 0008          * TMM04 DC /0008  WORD COUNT
073B 0 350A          DC   /350A  EO
073C 0 0A01          DC   /0A01  O1
073D 0 0000          DC   /0000  SPACE
073E 0 0000          DC   /0000  SPACE
073F 0 1235          DC   /1235  SE
0740 0 2814          DC   /2814  QU
0741 0 3525          DC   /3525  EN
0742 0 3335          DC   /3335  CE
0743 0 0035          DC   /0035  E
0744 0 2929          DC   /2929  RR
0745 0 2629          DC   /2629  OR
0746 0 FFFF          DC   /FFFF  TERM

0747 0 000E          * TMM05 DC /000E  WORD COUNT
0748 0 350A          DC   /350A  EO
0749 0 0A02          DC   /0A02  O2
074A 0 0000          DC   /0000  SPACE
074B 0 0000          DC   /0000  SPACE
074C 0 1339          DC   /1339  TI
074D 0 2435          DC   /2435  ME
074E 0 2912          DC   /2912  RS
074F 0 0036          DC   /0036  F
0750 0 3139          DC   /3139  AI
0751 0 2300          DC   /2300  L
0752 0 1326          DC   /1326  TO
0753 0 0012          DC   /0012  S
0754 0 1335          DC   /1335  TE
0755 0 2700          DC   /2700  P
0756 0 FFFF          DC   /FFFF  TERM

0757 0 000E          * TMM06 DC /000E  WORD COUNT
0758 0 350A          DC   /350A  EO
0759 0 0A03          DC   /0A03  O3
075A 0 0000          DC   /0000  SPACE
075B 0 0000          DC   /0000  SPACE
075C 0 1339          DC   /1339  TI
075D 0 2435          DC   /2435  ME
075E 0 2912          DC   /2912  RS
075F 0 0036          DC   /0036  F
0760 0 3139          DC   /3139  AI
0761 0 2300          DC   /2300  L
0762 0 1326          DC   /1326  TO
0763 0 0039          DC   /0039  I
0764 0 2513          DC   /2513  NT
0765 0 2927          DC   /2927  RP
0766 0 FFFF          DC   /FFFF  TERM

0767 0 0010          * TMM07 DC /0010  WORD COUNT
0768 0 340A          DC   /340A  DO
0769 0 0A01          DC   /0A01  O1
076A 0 0000          DC   /0000  SPACE
076B 0 0000          DC   /0000  SPACE
076C 0 1339          DC   /1339  TI
076D 0 2435          DC   /2435  ME
076E 0 2912          DC   /2912  RS
076F 0 0026          DC   /0026  O
0770 0 2500          DC   /2500  N
0771 0 3925          DC   /3925  IN
0772 0 1329          DC   /1329  TR
0773 0 2700          DC   /2700  P
0774 0 2335          DC   /2335  LE

```

INTERVAL TIMER FUNCTION TEST

0775 0 1535	DC	/1535	VE	88220410
0776 0 2300	DC	/2300	L	88220420
0777 0 0000	DC	/0000	LEVEL NUMBER	88220430
0778 0 FFFF	DC	/FFFF	TERM	88220440
* TMM08				
0779 0 0014	DC	/0014	WORD COUNT	88220450
077A 0 350A	DC	/350A	EO	88220460
077B 0 0A04	DC	/0A04	04	88220470
077C 0 0000	DC	/0000	SPACE	88220480
077D 0 0000	DC	/0000	SPACE	88220490
077E 0 2913	DC	/2913	RT	88220500
077F 0 2500	DC	/2500	N	88220510
0780 0 0200	DC	/0200	Z	88220520
0781 0 1339	DC	/1339	TI	88220530
0782 0 2435	DC	/2435	ME	88220540
0783 0 2900	DC	/2900	R	88220550
0784 0 0000	DC	/0000	TIMER NUMBER	88220560
0785 0 0036	DC	/0036	F	88220570
0786 0 3139	DC	/3139	AI	88220580
0787 0 2335	DC	/2335	LE	88220590
0788 0 3400	DC	/3400	D	88220600
0789 0 1326	DC	/1326	TO	88220610
078A 0 0013	DC	/0013	T	88220620
078B 0 1429	DC	/1429	UR	88220630
078C 0 2500	DC	/2500	N	88220640
078D 0 2625	DC	/2625	ON	88220650
078E 0 FFFF	DC	/FFFF	TERM	88220660
* TMM09				
078F 0 0015	DC	/0015	WORD COUNT	88220670
0790 0 350A	DC	/350A	EO	88220680
0791 0 0A05	DC	/0A05	05	88220690
0792 0 0000	DC	/0000	SPACE	88220700
0793 0 0000	DC	/0000	SPACE	88220710
0794 0 2913	DC	/2913	RT	88220720
0795 0 2500	DC	/2500	N	88220730
0796 0 0200	DC	/0200	Z	88220740
0797 0 1339	DC	/1339	TI	88220750
0798 0 2435	DC	/2435	ME	88220760
0799 0 2900	DC	/2900	R	88220770
079A 0 0000	DC	/0000	TIMER NUMBER	88220780
079B 0 0036	DC	/0036	F	88220790
079C 0 3139	DC	/3139	AI	88220800
079D 0 2335	DC	/2335	LE	88220810
079E 0 3400	DC	/3400	D	88220820
079F 0 1326	DC	/1326	TO	88220830
07A0 0 0013	DC	/0013	T	88220840
07A1 0 1429	DC	/1429	UR	88220850
07A2 0 2500	DC	/2500	N	88220860
07A3 0 2636	DC	/2636	OF	88220870
07A4 0 3600	DC	/3600	F	88220880
07A5 0 FFFF	DC	/FFFF	TERM	88220890
* TMM10				
07A6 0 0013	DC	/0013	WORD COUNT	88220900
07A7 0 350A	DC	/350A	EO	88220910
07A8 0 0A06	DC	/0A06	06	88220920
07A9 0 0000	DC	/0000	SPACE	88220930
07AA 0 0000	DC	/0000	SPACE	88220940
07AB 0 2913	DC	/2913	RT	88220950
07AC 0 2500	DC	/2500	N	88220960
07AD 0 0300	DC	/0300	Z	88220970
07AE 0 1339	DC	/1339	TI	88220980
07AF 0 2435	DC	/2435	ME	88220990
07B0 0 2900	DC	/2900	R	88221000
07B1 0 0000	DC	/0000	TIMER NUMBER	88221010
07B2 0 0036	DC	/0036	F	88221020
07B3 0 3139	DC	/3139	AI	88221030
07B4 0 2335	DC	/2335	LE	88221040
07B5 0 3400	DC	/3400	D	88221050
				88221060
				88221070
				88221080

INTERVAL TIMER FUNCTION TEST

07B6 0 1326	DC	/1326	TO	88221090
07B7 0 0039	DC	/0039	I	88221100
07B8 0 2513	DC	/2513	NT	88221110
07B9 0 2927	DC	/2927	RP	88221120
07BA 0 FFFF	DC	/FFFF	TERM	88221130
* BSS E 0				
07BC 0000				88221140
* TMM11				
07BC 0 000F	DC	/000F	WORD COUNT	88221150
07BD 0 350A	DC	/350A	EO	88221160
07BE 0 0A07	DC	/0A07	07	88221170
07BF 0 0000	DC	/0000	SPACE	88221180
07C0 0 0000	DC	/0000	SPACE	88221190
07C1 0 2913	DC	/2913	RT	88221200
07C2 0 2500	DC	/2500	N	88221210
07C3 0 0300	DC	/0300	Z	88221220
07C4 0 1339	DC	/1339	TI	88221230
07C5 0 2435	DC	/2435	ME	88221240
07C6 0 2900	DC	/2900	R	88221250
07C7 0 0000	DC	/0000	TIMER NUMBER	88221260
07C8 0 3412	DC	/3412	DS	88221270
07C9 0 1600	DC	/1600	W	88221280
07CA 0 0000	DC	/0000	DSW IN	88221290
07CB 0 0000	DC	/0000	*ERROR	88221300
07CC 0 FFFF	DC	/FFFF	TERM	88221310
* BSS E 0				
07CE 0000				88221320
* TMM12				
07CE 0 0011	DC	/0011	WORD COUNT	88221330
07CF 0 350A	DC	/350A	EO	88221340
07D0 0 0A08	DC	/0A08	08	88221350
07D1 0 0000	DC	/0000	SPACE	88221360
07D2 0 0000	DC	/0000	SPACE	88221370
07D3 0 2913	DC	/2913	RT	88221380
07D4 0 2500	DC	/2500	N	88221390
07D5 0 0300	DC	/0300	Z	88221400
07D6 0 1339	DC	/1339	TI	88221410
07D7 0 2435	DC	/2435	ME	88221420
07D8 0 2900	DC	/2900	R	88221430
07D9 0 0000	DC	/0000	TIMER NUMBER	88221440
07DA 0 0039	DC	/0039	I	88221450
07DB 0 2312	DC	/2312	LS	88221460
07DC 0 1600	DC	/1600	W	88221470
07DD 0 0000	DC	/0000	BLANK	88221480
07DE 0 0000	DC	/0000	ILSW	88221490
07DF 0 0000	DC	/0000	*ON INTRP	88221500
07E0 0 FFFF	DC	/FFFF	TERM	88221510
* BSS C 0				
07E2 0000				88221520
* TMM13				
07E2 0 0017	DC	/0017	WORD COUNT	88221530
07E3 0 350A	DC	/350A	EO	88221540
07E4 0 0A09	DC	/0A09	09	88221550
07E5 0 0000	DC	/0000	SPACE	88221560
07E6 0 0000	DC	/0000	SPACE	88221570
07E7 0 2913	DC	/2913	RT	88221580
07E8 0 2500	DC	/2500	N	88221590
07E9 0 0400	DC	/0400	Z	88221600
07EA 0 1339	DC	/1339	TI	88221610
07EB 0 2435	DC	/2435	ME	88221620
07EC 0 2900	DC	/2900	R	88221630
07ED 0 0000	DC	/0000	TIMER NUMBER	88221640
07EE 0 1631	DC	/1631	WA	88221650
07EF 0 1200	DC	/1200	S	88221660
07F0 0 0000	DC	/0000	TIMER	88221670
07F1 0 0000	DC	/0000	*CONTENTS	88221680
07F2 0 0012	DC	/0012	S	88221690
07F3 0 3826	DC	/3826	HO	88221700
				88221710
				88221720
				88221730
				88221740
				88221750
				88221760

INTERVAL TIMER FUNCTION TEST

07F4 0 1423	DC	/1423	UL	88221770
07F5 0 3400	DC	/3400	D	88221780
07F6 0 3235	DC	/3235	BE	88221790
07F7 0 0000	DC	/0000	BLANK	88221800
07F8 0 0000	DC	/0000	EXPECTED	88221810
07F9 0 0000	DC	/0000	*COUNT	88221820
07FA 0 FFFF	DC	/FFFF	TERM	88221830
* BSS E 0				
07FC 0 0000	DC	/0000		88221840
* TMM14				
07FC 0 0017	DC	/0017	WORD COUNT	88221850
07FD 0 350A	DC	/350A	EO	88221860
07FE 0 0A31	DC	/0A31	OA	88221870
07FF 0 0000	DC	/0000	SPACE	88221880
0800 0 0000	DC	/0000	SPACE	88221890
0801 0 2913	DC	/2913	RT	88221900
0802 0 2500	DC	/2500	N	88221910
0803 0 0500	DC	/0500	S	88221920
0804 0 1300	DC	/1300	T	88221930
0805 0 0000	DC	/0000	TIMER NUMBER	88221940
0806 0 1631	DC	/1631	WA	88221950
0807 0 1200	DC	/1200	S	88221960
0808 0 0000	DC	/0000	ACTUAL	88221970
0809 0 0000	DC	/0000	*COUNT	88221980
080A 0 0035	DC	/0035	E	88221990
080B 0 1727	DC	/1727	XP	88222000
080C 0 3313	DC	/3313	CT	88222010
080D 0 3400	DC	/3400	D	88222020
080E 0 0000	DC	/0000	EXPECTED	88222030
080F 0 0000	DC	/0000	*COUNT	88222040
0810 0 0027	DC	/0027	P	88222050
0811 0 3112	DC	/3112	AS	88222060
0812 0 1200	DC	/1200	S	88222070
0813 0 0000	DC	/0000	PASS NUMBER	88222080
0814 0 FFFF	DC	/FFFF	TERM	88222090
* TMM15				
0815 0 000E	DC	/000E	WORD COUNT	88222100
0816 0 330A	DC	/330A	CO	88222110
0817 0 0A02	DC	/0A02	O2	88222120
0818 0 0000	DC	/0000	SPACE	88222130
0819 0 0000	DC	/0000	SPACE	88222140
081A 0 3525	DC	/3525	EN	88222150
081B 0 1335	DC	/1335	TE	88222160
081C 0 2900	DC	/2900	R	88222170
081D 0 1213	DC	/1213	ST	88222180
081E 0 3129	DC	/3129	AR	88222190
081F 0 1339	DC	/1339	TI	88222200
0820 0 2537	DC	/2537	NG	88222210
0821 0 0033	DC	/0033	C	88222220
0822 0 2614	DC	/2614	OU	88222230
0823 0 2513	DC	/2513	NT	88222240
0824 0 FFFF	DC	/FFFF	TERM	88222250
* TMM16				
0825 0 000F	DC	/000F	WORD COUNT	88222260
0826 0 330A	DC	/330A	CO	88222270
0827 0 0A03	DC	/0A03	O3	88222280
0828 0 0000	DC	/0000	SPACE	88222290
0829 0 0000	DC	/0000	SPACE	88222300
082A 0 3525	DC	/3525	EN	88222310
082B 0 1335	DC	/1335	TE	88222320
082C 0 2900	DC	/2900	R	88222330
082D 0 2514	DC	/2514	NU	88222340
082E 0 2432	DC	/2432	NB	88222350
082F 0 3529	DC	/3529	ER	88222360
0830 0 0026	DC	/0026	O	88222370
0831 0 3600	DC	/3600	F	88222380
0832 0 1213	DC	/1213	ST	88222390
0833 0 3527	DC	/3527	EP	88222400

INTERVAL TIMER FUNCTION TEST

0834 0 1200	DC	/1200	S	88222450
0835 0 FFFF	DC	/FFFF	TERM	88222460
* TMM17				
0836 0 000E	DC	/000E	WORD COUNT	88222470
0837 0 330A	DC	/330A	CO	88222480
0838 0 0A04	DC	/0A04	O4	88222490
0839 0 0000	DC	/0000	SPACE	88222500
083A 0 0000	DC	/0000	SPACE	88222510
083B 0 3525	DC	/3525	EN	88222520
083C 0 1335	DC	/1335	TE	88222530
083D 0 2900	DC	/2900	R	88222540
083E 0 0013	DC	/0013	T	88222550
083F 0 3924	DC	/3924	IM	88222560
0840 0 3529	DC	/3529	ER	88222570
0841 0 0025	DC	/0025	N	88222580
0842 0 1424	DC	/1424	UM	88222590
0843 0 3235	DC	/3235	BE	88222600
0844 0 2900	DC	/2900	R	88222610
0845 0 FFFF	DC	/FFFF	TERM	88222620
* TMM18				
0846 0 001C	DC	/001C	WORD COUNT	88222630
0847 0 350A	DC	/350A	EO	88222640
0848 0 0A32	DC	/0A32	OB	88222650
0849 0 0000	DC	/0000	SPACE	88222660
084A 0 0000	DC	/0000	SPACE	88222670
084B 0 2913	DC	/2913	RT	88222680
084C 0 2500	DC	/2500	N	88222690
084D 0 0100	DC	/0100	I	88222700
084E 0 3426	DC	/3426	DO	88222710
084F 0 1432	DC	/1432	UB	88222720
0850 0 2335	DC	/2335	LE	88222730
0851 0 0039	DC	/0039	I	88222740
0852 0 2533	DC	/2533	MC	88222750
0853 0 2900	DC	/2900	R	88222760
0854 0 2636	DC	/2636	OF	88222770
0855 0 0039	DC	/0039	I	88222780
0856 0 0033	DC	/0033	C	88222790
0857 0 1329	DC	/1329	TR	88222800
0858 0 0034	DC	/0034	D	88222810
0859 0 1429	DC	/1429	UR	88222820
085A 0 3925	DC	/3925	IV	88222830
085B 0 3700	DC	/3700	G	88222840
085C 0 1324	DC	/1324	TM	88222850
085D 0 2900	DC	/2900	R	88222860
085E 0 0000	DC	/0000	TIMER NUMBER	88222870
085F 0 3312	DC	/3312	CS	88222880
0860 0 0033	DC	/0033	C	88222890
0861 0 1833	DC	/1833	YC	88222900
0862 0 2335	DC	/2335	LE	88222910
0863 0 FFFF	DC	/FFFF	TERM	88222920
* TMM19				
0864 0 0014	DC	/0014	WORD COUNT	88222930
0865 0 330A	DC	/330A	CO	88222940
0866 0 0A05	DC	/0A05	O5	88222950
0867 0 0000	DC	/0000	SPACE	88222960
0868 0 0000	DC	/0000	SPACE	88222970
0869 0 2935	DC	/2935	RE	88222980
086A 0 2731	DC	/2731	PA	88222990
086B 0 3929	DC	/3929	IR	88223000
086C 0 0036	DC	/0036	F	88223010
086D 0 3139	DC	/3139	AI	88223020
086E 0 2314	DC	/2314	LU	88223030
086F 0 2935	DC	/2935	RE	88223040
0870 0 0012	DC	/0012	B	88223050
0871 0 3536	DC	/3536	EF	88223060
0872 0 2629	DC	/2629	OR	88223070
0873 0 3500	DC	/3500	E	88223080
0874 0 3326	DC	/3326	CO	88223090

INTERVAL TIMER FUNCTION TEST

0875 0 2513	DC	/2513	NT	88223130
0876 0 3925	DC	/3925	IN	88223140
0877 0 1439	DC	/1439	UI	88223150
0878 0 2537	DC	/2537	NG	88223160
0879 0 FFFF	DC	/FFFF	TERM	88223170
* TMM20				
087A 0 000D	DC	/000D	WORD COUNT	88223180
087B 0 350A	DC	/350A	EO	88223190
087C 0 0A33	DC	/0A33	OC	88223200
087D 0 0000	DC	0	SPACE	88223210
087E 0 0000	DC	0	SPACE	88223220
087F 0 3923	DC	/3923	IL	88223230
0880 0 2335	DC	/2335	LE	88223240
0881 0 3731	DC	/3731	GA	88223250
0882 0 2300	DC	/2300	L	88223260
0883 0 2913	DC	/2913	RT	88223270
0884 0 2500	DC	/2500	N	88223280
0885 0 3525	DC	/3525	EN	88223290
0886 0 1329	DC	/1329	TR	88223300
0887 0 1800	DC	/1800	Y	88223310
0888 0 FFFF	DC	/FFFF	TERM	88223320
* TMM21				
0889 0 0012	DC	/0012	WORD COUNT	88223330
088A 0 350A	DC	/350A	EO	88223340
088B 0 0A34	DC	/0A34	OD	88223350
088C 0 0000	DC	0	SPACE	88223360
088D 0 0000	DC	0	SPACE	88223370
088E 0 2913	DC	/2913	RT	88223380
088F 0 2500	DC	/2500	N	88223390
0890 0 0300	DC	/0300	3	88223400
0891 0 1339	DC	/1339	TI	88223410
0892 0 2435	DC	/2435	ME	88223420
0893 0 2900	DC	/2900	R	88223430
0894 0 0000	DC	0	TIMER NUMBER	88223440
0895 0 3923	DC	/3923	IL	88223450
0896 0 1216	DC	/1216	SW	88223460
0897 0 0016	DC	/0016	W	88223470
0898 0 3112	DC	/3112	AS	88223480
0899 0 0019	DC	/0019	Z	88223490
089A 0 3529	DC	/3529	ER	88223500
089B 0 2600	DC	/2600	O	88223510
089C 0 FFFF	DC	/FFFF	TERM	88223520
* TMM22				
089D 0 0017	DC	/0017	WORD COUNT	88223530
089E 0 350A	DC	/350A	EO	88223540
089F 0 0A35	DC	/0A35	OE	88223550
08A0 0 0000	DC	0	SPACE	88223560
08A1 0 0000	DC	0	SPACE	88223570
08A2 0 2913	DC	/2913	RT	88223580
08A3 0 2500	DC	/2500	N	88223590
08A4 0 0100	DC	/0100	I	88223600
08A5 0 3100	DC	/3100	A	88223610
08A6 0 2935	DC	/2935	RE	88223620
08A7 0 3700	DC	/3700	G	88223630
08A8 0 3338	DC	/3338	CH	88223640
08A9 0 3125	DC	/3125	AN	88223650
08AA 0 3735	DC	/3735	GE	88223660
08AB 0 3400	DC	/3400	D	88223670
08AC 0 2625	DC	/2625	ON	88223680
08AD 0 0013	DC	/0013	T	88223690
08AE 0 3924	DC	/3924	IM	88223700
08AF 0 2900	DC	/2900	R	88223710
08B0 0 0000	DC	/0000	TIMER NUMBER	88223720
08B1 0 3312	DC	/3312	CS	88223730
08B2 0 0033	DC	/0033	C	88223740
08B3 0 1833	DC	/1833	YC	88223750
08B4 0 2335	DC	/2335	LE	88223760
08B5 0 FFFF	DC	/FFFF	TERM	88223770
				88223780
				88223790
				88223800

INTERVAL TIMER FUNCTION TEST

0886 0 000B	DC	/000B	WORD COUNT	88223810
0887 0 310A	DC	/310A	AO	88223820
0888 0 0A03	DC	/0A03	OS	88223830
0889 0 0000	DC	0	SPACE	88223840
088A 0 0000	DC	0	SPACE	88223850
088B 0 2731	DC	/2731	PA	88223860
088C 0 1212	DC	/1212	SS	88223870
088D 0 0033	DC	/0033	C	88223880
088E 0 2624	DC	/2624	OM	88223890
088F 0 2723	DC	/2723	PL	88223900
08C0 0 3513	DC	/3513	ET	88223910
08C1 0 3500	DC	/3500	E	88223920
08C2 0 FFFF	DC	/FFFF	TERM	88223930
* TMM23				
08C3 0 0015	DC	/0015	WORD COUNT	88223940
08C4 0 350A	DC	/350A	EO	88223950
08C5 0 0A36	DC	/0A36	OF	88223960
08C6 0 0000	DC	0	SPACE	88223970
08C7 0 0000	DC	0	SPACE	88223980
08C8 0 2913	DC	/2913	RT	88223990
08C9 0 2500	DC	/2500	N	88224000
08CA 0 0600	DC	/0600	6	88224010
08CB 0 1339	DC	/1339	TI	88224020
08CC 0 2435	DC	/2435	ME	88224030
08CD 0 2900	DC	/2900	R	88224040
08CE 0 0000	DC	0	TIMER NUMBER	88224050
08CF 0 3631	DC	/3631	FA	88224060
08D0 0 3923	DC	/3923	IL	88224070
08D1 0 3534	DC	/3534	ED	88224080
08D2 0 0013	DC	/0013	T	88224090
08D3 0 2600	DC	/2600	O	88224100
08D4 0 3925	DC	/3925	IN	88224110
08D5 0 3329	DC	/3329	CR	88224120
08D6 0 3524	DC	/3524	EM	88224130
08D7 0 3525	DC	/3525	EN	88224140
08D8 0 1300	DC	/1300	T	88224150
08D9 0 FFFF	DC	/FFFF	TERM	88224160
* TMM24				
08DA 0 0016	DC	/0016	WORD COUNT	88224170
08DB 0 350A	DC	/350A	EO	88224180
08DC 0 010A	DC	/010A	10	88224190
08DD 0 0000	DC	0	SPACE	88224200
08DE 0 0000	DC	0	SPACE	88224210
08DF 0 2913	DC	/2913	RT	88224220
08E0 0 2500	DC	/2500	N	88224230
08E1 0 0600	DC	/0600	6	88224240
08E2 0 1227	DC	/1227	SP	88224250
08E3 0 1500	DC	/1500	V	88224260
08E4 0 3925	DC	/3925	IN	88224270
08E5 0 1329	DC	/1329	TR	88224280
08E6 0 2713	DC	/2713	PT	88224290
08E7 0 0026	DC	/0026	O	88224300
08E8 0 2500	DC	/2500	N	88224310
08E9 0 1339	DC	/1339	TI	88224320
08EA 0 2435	DC	/2435	ME	88224330
08EB 0 2900	DC	/2900	R	88224340
08EC 0 0000	DC	0	TIMER NUMBER	88224350
08ED 0 3312	DC	/3312	CS	88224360
08EE 0 0033	DC	/0033	C	88224370
08EF 0 1833	DC	/1833	YC	88224380
08F0 0 2335	DC	/2335	LE	88224390
08F1 0 FFFF	DC	/FFFF	TERM	88224400
* TMM25				
08F2 0 0015	DC	/0015	WORD COUNT	88224410
08F3 0 350A	DC	/350A	EO	88224420
08F4 0 0101	DC	/0101	11	88224430
08F5 0 0000	DC	0	SPACE	88224440
				88224450
				88224460
				88224470
				88224480

INTERVAL TIMER FUNCTION TEST

08F6 0 0000	DC 0	SPACE	88224490
08F7 0 2913	DC /2913	RT	88224500
08F8 0 2500	DC /2500	N	88224510
08F9 0 0600	DC /0600	6	88224520
08FA 0 2526	DC /2526	NO	88224530
08FB 0 0039	DC /0039	I	88224540
08FC 0 2513	DC /2513	NT	88224550
08FD 0 2927	DC /2927	RP	88224560
08FE 0 0026	DC /0026	O	88224570
08FF 0 2500	DC /2500	N	88224580
0900 0 1539	DC /1539	VI	88224590
0901 0 2623	DC /2623	OL	88224600
0902 0 3113	DC /3113	AT	88224610
0903 0 3500	DC /3500	E	88224620
0904 0 1339	DC /1339	TI	88224630
0905 0 2435	DC /2435	ME	88224640
0906 0 2900	DC /2900	R	88224650
0907 0 0000	DC 0	TIMER NUMBER	88224660
0908 0 FFFF	DC /FFFF	TERM	88224670
090A 012D	END	TISRT	88224680
			88224690

INTERVAL TIMER FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
ACS	04AC	01F2, 04A5
BSW	01AC	0147, 017E, 0460, 0469, 0474, 0492, 051C, 052C
BSWA	0180	0148, 0156, 017F, 01AC, 0462, 046B, 0476, 0494, 051E, 0527, 052E
CMTRP	06B4	0655, 0659, 065D, 0661, 0665, 0669, 066D, 0671, 0675, 0679, 067D, 0681, 0685, 0689, 068D, 0691, 0695, 0699, 069D, 06A1, 06A5, 06A9, 06AD, 06B1, 06B9, 06BD, 06BF
CODCV	05A4	057F, 05D2
CODC1	05AB	05C7
CODC2	05BD	058C
CODC3	05C8	05C2
CODC4	05CC	05A5, 05A6, 05A7
CODEH	0630	0614
CODWD	05D4	057A, 0581, 05AB, 05CB
COD00	05D6	0582, 0583, 0587, 0588
COD01	05D7	058F, 05C8
COD02	05D8	05CA
CONST	01A1	0143, 0161, 0169, 0170
CTLO1	013A	013E, 04EE
CTLO2	0173	016E, 0180
CTLO3	0179	0158
CTLO4	016F	016C
CTLO5	017E	0172
CTTBL	0345	02F5
DEL20	3504	022F, 0238, 0288, 0417, 04C5, 04DE, 050F
DSW	06C8	0249, 0306, 033A, 0387, 038A, 03D3, 04A8, 0647, 064A, 0688
ERINT	06CB	01A3, 043A, 06CC
ERRID	0538	051A, 0526
ERROR	0511	01EC, 020B, 0256, 025D, 028F, 02A4, 02D5, 031C, 039D, 0420, 0434, 044E, 04E7, 04F0, 0512, 0515, 0517, 0533, 0535
ERRSW	0213	0138, 01BF, 01EA, 01FA, 020F
ERR01	0523	0514
ERR02	0525	0522
ERR03	052C	0538
FIOLC	0502	01E8, 01F0, 0231, 028D, 0304, 0331, 0382, 0383, 03D0, 0419, 0490, 04C6, 04DF, 0641, 0685
HEXCD	062E	02A0, 02ED, 030E, 0318, 036F, 0399, 061E, 0622
HEXCV	0609	029E, 028B, 030C, 0316, 038D, 0397, 0627
HEXC1	0610	0611, 061A
HEXC2	0623	060A, 060B
HEXWD	0629	029C, 02B9, 030A, 0314, 038B, 0395, 060D
HEX00	062A	0616, 061B, 061D, 061F, 0621
ILSW	06C6	03E7, 04A6, 0643, 06B7, 06CE, 06D6, 06E1
INCCT	0455	040D, 041D, 0426
INLVL	06CA	01C1, 0271, 0355, 03F1, 06C1
INTRP	0654	04FA
INTSW	03E5	0130, 035C, 0384, 03A4, 0387, 03C0, 04D0, 06C2
IQARA	0599	056E, 0583, 058C, 058E, 05A2
LHIND	05D5	05A9, 05AD, 05C1, 05C4
LOG	053C	0179, 0183, 018A, 0191, 019D, 01FD, 02C5, 044C, 0465, 0470, 04E8, 04F5, 0523, 0546, 0560, 0562, 0575
LOG01	053D	0551, 0553
LOG02	0549	054F
LOG05	0554	053D, 057D
LOG06	055A	0519, 0532
LPERR	0539	0182
LPPGM	0188	015C, 018A, 01E2, 01F4, 0217, 025A, 0261, 02E0, 0320, 0362, 03A7, 053E
MASK0	026A	015E, 018C, 0218, 0258, 0262, 02E2, 0322, 0364, 03A9, 0540
MASK1	026C	01D2, 01D5, 01D8, 01F4, 0222, 022D, 0264, 0267, 0278, 027B, 027E, 0289, 02E7, 02LA, 021D, 02FA, 0327, 0369, 0378, 03AB, 03C7, 03CD, 04D4, 04D7, 04DA, 0415, 0487, 0489, 04BF, 04C4, 04CB, 04CD, 04D5, 04DD
NIOCC	0500	014F, 0542
OPIND	0183	

INTERVAL TIMER FUNCTION TEST

PRSN	059C	0555
PRSNS	059A	0549, 0559
PRWRT	059E	0548, 0554
RESRT	01A6	013F
RTN	0196	0177
RTNNO	01B1	012E, 016D, 016F, 0173, 0175, 0189, 0202, 024F, 02CA, 0335, 03D7, 043E
RTNRT	018E	0206, 0254, 02CF, 0340, 03DC, 0449
RTNO0	01D5	01D4, 01F9
RTNO1	01DE	01EF, 020E
RTNO2	01F9	01E7
RTNO3	0202	01FB
RTNO4	0207	01F7
RTNO5	01F8	01E1, 0211
RTN10	0224	0269
RTN11	022D	0246, 0259, 0260
RTN12	0256	0236
RTN13	025D	0240
RTN14	0264	0248
RTN15	0245	025C, 0263
RTN20	0278	027A, 02AD
RTN21	0286	0292, 02A7, 02B8
RTN22	0294	064E
RTN23	02A8	0299, 02D9
RTN24	0287	02C9
RTN25	02CA	0285
RTN26	02D1	02A8
RTN27	02AC	0293
RTN30	02EA	02E9, 0334
RTN31	02F4	031F, 0330
RTN32	02FC	0300, 032E
RTN33	032A	0303, 0329
RTN40	0366	03D2
RTN41	036F	03A0, 0386
RTN42	037A	037E, 0382
RTN43	03AE	0381, 03AD
RTN44	03D3	0389
RTN50	0407	0406, 042A
RTN51	0415	0423, 0428
RTN52	0424	041F, 0452
RTN53	042E	0437, 0439, 0451
RTN54	0438	044D
RTN55	0448	06D8
RTRN	0168	0190
RT00	0212	0204, 04C7
RT100	026F	0251
RT200	02DA	0786
RT201	02DB	0270
RT202	02DC	0296, 02CC
RT300	0342	02F9, 02FE, 0308, 0326, 032A
RT301	0343	0337
RT302	0344	0301, 0309
RT400	03DE	0357
RT401	03DF	035E
RT402	03E0	03C2
RT403	03E1	0372, 037C, 0389, 03A3, 03AE
RT404	03E2	0371
RT405	03E3	03D9
RT406	03E4	037F, 038A
RT500	0453	0440
SECK	0182	018E, 0205, 0252, 02CD, 0338, 03DA, 0441
SNSWS	01AE	0168
SPEED	0184	014C, 0505
SPVCK	0454	03F5, 0428, 0448
SPVCN	06DE	06D7, 06DA
SPVTP	06D3	03EB, 06D4
SVEXT	0704	06FD
SVINT	06DF	01A4, 0704

INTERVAL TIMER FUNCTION TEST

SVINO	06E9	06F9, 0702
SVINI	06EB	06F3
SVIQ	070E	06E0, 06EF, 06F0, 0703
SVO	0706	06F7
SV1	0707	06E9
SV2	0708	06E7
SV3	0709	06FE
SV4	070A	06E6, 06EB, 06F4, 06F6, 0701
SV5	070B	06EA, 06ED, 06F1
SV6	070C	06EB, 06EE, 06FF
SV7	070D	06E3, 06FA
TIBCN	01B5	01C0, 01D1, 01DA, 0207, 0221, 0227, 0276, 0280, 02C1, 02D1, 02E5, 02F0, 036B, 03C5, 0402, 040F, 042E, 015A, 04B6, 04EA, 04F3, 04FB, 04CF
TIINT	04AD	
TIIN1	04C4	
TIIN2	04D0	
TIIN3	04F0	04E3
TIIN4	04EB	04F4
TIIN5	04CB	06C4
TIIN6	04F5	04D2
TI100	04FA	0480
TI101	04FB	0483
TI102	04FC	048E
TI103	04FD	0367, 04D4
TI104	04FE	0488
TIMA	0214	0132, 0134, 0136, 01DE, 04C9
TIMAA	0458	045B
TIMAB	0465	046F
TIMAC	047E	047D
TIMAD	048B	048F
TIMAI	04A4	01B5, 03EF, 049A, 04AA
TIMAL	047C	0498
TIMAN	0456	017C, 0499
TIMER	019D	019C
TIM00	01BA	0196
TIM01	0217	0197
TIM02	0270	019A
TIM03	02E0	0199
TIM04	0355	019A
TIM05	03EB	019B
TISR1	012D	0187, 0195, 01A7, 06D1, 06DC, 0909
TIX01	049A	0457
TIX02	049B	0464, 0480
TIX03	049C	046D, 0483
TIX04	049D	0478, 047C
TIX05	049E	0484, 048D
TIX06	049F	0485
TIX07	04A2	0478
TIX08	04A3	047A
TMCNT	026E	0225, 0235, 023A, 023F, 0244
TMM01	0710	0178
TMM02	071F	04ED
TMM03	072C	0185
TMM04	073A	0193
TMM05	0747	04E9
TMM06	0757	04F2
TMM07	0767	04F7, 0688
TMM08	0779	0229, 0258
TMM09	078F	0228, 025F
TMM10	07A6	0282, 0291
TMM11	07BC	0284, 02A2, 02A6
TMM12	07CE	028F, 02C3, 02C7
TMM13	07E2	02F2, 0310, 031A, 031E
TMM14	07FC	0360, 036D, 0391, 039B, 039F, 03C3
TMM15	0815	045E
TMM16	0825	0467
TMM17	0836	0472
TMM18	0846	01DC, 01EE

INTERVAL TIMER FUNCTION TEST

TMM19	0864	01FF
TMM20	087A	019F
TMM21	0889	0203, 0207
TMM22	089D	0209, 0200
TMM23	08B6	018C
TMM24	08C3	0411, 0422
TMM25	08DA	0413, 0450
TMM26	08F2	0430, 0436
TRAP2	0640	02DB
TRA4A	03E6	03DE, 03E9
TRP01	0650	02A8, 02AE, 02B0, 02B2, 02B7, 0645
TRP02	0653	0294, 029A, 0649
TWRTR	0564	0544, 056C
TWRT0	0596	056D
TWRT1	0597	C57C
TWRO1	056D	0569
TWRO2	0578	0577, 0595
TWRO3	0592	058B
TWSNS	05A0	0566, 057D, 0585
TWWRT	05A2	056F, 0586
UMSK0	01A8	0166, 01CD, 0248, 033C, 038C, 04C0, 055C
UMSK1	01AA	0167, 01CF, 024D, 033E, 038E, 04C2, 055E
WRDSW	0598	0565, 0589, 059F, 0593
WTA	0550	300A, 054A
WTB	0552	300B, 054B
WTC	056B	300C
WTD	064D	300D
WTE	06D0	300E
WTF	06D8	300F
WT1	0146	3001, 01AC, 02C0
WT2	0186	3002
WT3	0194	3003
WT4	03CF	3004

INTERRUPT FUNCTION TEST

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INTERRUPT FUNCTION TEST

1. PURPOSE

THE INTERRUPT FUNCTION TEST CHECKS THE OPERATING CONDITION OF THE INTERRUPT CIRCUITS IN THE PROCESSOR/CONTROLLER. AUTOMATIC AND MANUAL INTERRUPTS, INTERRUPT PRIORITY, MASK REGISTER, DISABLE INTERRUPT SWITCH, AND TRACE MODE ARE TESTED.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE 1800 BASIC DIAGNOSTIC LOADER PROGRAM IS REQUIRED TO LOAD THE INTERRUPT FUNCTION TEST PROGRAM.

2.2 EQUIPMENT PREREQUISITES

THE FOLLOWING EQUIPMENT IS REQUIRED.

- A. 1000 PROCESSOR/CONTROLLER.
- B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
- C. EITHER A 1053/1016, OR 1443 PRINTER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

REFER TO 1800 BASIC DIAGNOSTIC LOADER DOCUMENTATION PARAGRAPH 3.1, FOR LOADING INSTRUCTIONS.

3.2 PROGRAM OPERATION

AFTER LOADING PROCESSOR STOPS AT WAIT 1 (B REG = 3001) WITH PROCESSOR STOPPED AT WAIT 1, PROCEED AS FOLLOWS -

- A. SET DISABLE INTERRUPT SWITCH TO OFF.
- B. SET CHECK STOP SWITCH TO OFF.
- C. SET WRITE STORAGE PROTECT BITS SWITCH TO YES.
- D. AT THE CE PANEL, INSURE THAT THE CE INTERRUPT SWITCH IS SET TO INTERRUPT TO MAIN STORAGE.
- E. SELECT PROGRAM OPTIONS FROM TABLE 1.
- F. IF LOOP ROUTINE IS DESIRED, REFER TO TABLE 2.
- G. DEPRESS START PUSHBUTTON. PROGRAM SHOULD START EXECUTION.

- 1. OPERATOR SHOULD PERFORM THE ACTIONS REQUESTED BY THE PROGRAM. THE ACTIONS TO BE PERFORMED ARE INDICATED BY A PRINTOUT.
- 2. IF THE OPTIONS OF LOOP ROUTINE OR LOOP PROGRAM ARE NOT SELECTED, THEN ROUTINE 1 THROUGH 6 WILL BE EXECUTED ONCE, FOLLOWED BY PRINTOUT A001 PROGRAM COMPLETE. THE PROGRAM STOPS AT WAIT 2 B REG = 3002. PRESSING START RETURNS PROGRAM TO WAIT 1.
- 3. IF A ROUTINE WAS SELECTED FOR LOOPING, THEN THAT ROUTINE WILL LOOP UNTIL THE PROGRAM IS TERMINATED OR THE LOOP ROUTINE FUNCTION IS CHANGED OR CLEARED. IF THE LOOP ROUTINE FUNCTION IS CHANGED, THEN THE NEW ROUTINE SELECTED WILL BE LOOPED. IF THE LOOP ROUTINE FUNCTION IS CLEARED, THE PROGRAM WILL CONTINUE FROM THE PRESENT ROUTINE TO COMPLETION. FOR ROUTINE LOOPING WITH BYPASS MANUAL CHECKS, REFER TO NOTE 1 TABLE 2.
- 4. IF LOOP PROGRAM WAS SELECTED, AND THE MANUAL CHECKS WERE NOT BYPASSED, THEN ROUTINES 1 THROUGH 6 WILL BE RUN IN SEQUENCE IN LOOP FASHION.
- 5. IF LOOP PROGRAM WAS SELECTED AND THE MANUAL CHECKS ARE BYPASSED, THEN THE PROGRAM WILL RUN ROUTINE 1, 1ST PASS OF ROUTINE 2 AND ALL OF ROUTINE 3 AND 4 IN SEQUENTIAL LOOP FASHION. ROUTINE 5 AND 6 ARE NOT RUN UNDER THIS SETUP.

INTERRUPT FUNCTION TEST

TABLE 1
PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE

THE OPTIONS OF SELECT 1443 AS OUTPUT DEVICE, BYPASS MANUAL CHECKS, AND NUMBER OF INTERRUPT LEVELS SELECTED WILL BE HONORED ONLY IF THEY ARE ENTERED WHILE THE PROCESSOR IS STOPPED AT WAIT 1 (B REG = 3001).

DATA ENTRY SWITCHES															OPTION DESCRIPTION	
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
.	HALT ON ERROR
.	BYPASS ERROR PRINT
.	LOOP ON ERROR
.	LOOP PROGRAM
.	USE 1443 AS OUTPUT DEVICE
.	BYPASS MANUAL CHECKS - NOTE 1.
.	BYPASS ROUTINE 3 PRIORITY PRINTOUT
0	0	12 INTERRUPT LEVELS
1	0	18 INTERRUPT LEVELS
0	1	24 INTERRUPT LEVELS

NOTE 1 -- MANUAL CHECKS INCLUDE DISABLE SWITCH, CONSOLE INTERRUPT PUSHBUTTON, CE INTERRUPT BUTTON AND TRACE MODE OPERATION. SWITCH 8 BYPASSES THESE CHECKS BY PREVENTING PASS 2 OF ROUTINE 2 AND ALL OF ROUTINES 5 AND 6 FROM OPERATING. SWITCH 8 IN CONJUNCTION WITH OPTION SWITCHES 7 AND 11 WILL RUN THE PROGRAM IN A MODE ADAPTABLE TO SCOPING.

TABLE 2

SENSE / PROGRAM	OPTION DESCRIPTION							
0	1	2	3	4	5	6	7	
X	X	X	ROUTINE NUMBER TO LOOP. NUMBER MUST BE IN HEX AND MAY BE CHANGED AT ANY TIME.

NOTE -- IF ROUTINE ENTRY IS 5 OR 6 AND THE PROGRAM OPTION TO BYPASS MANUAL CHECKS HAS BEEN SELECTED, THEN THE PROGRAM WILL LOOP SELECTING THAT ROUTINE BUT NOT ALLOWING IT TO EXECUTE. IF ROUTINE ENTRY IS 2 AND BYPASS MANUAL CHECKS IS SELECTED, THEN ROUTINE 2 WILL LOOP WITHOUT CHECKING THE DISABLE INTERRUPT SWITCH.

3.3 PROGRAM TERMINATION

A NORMAL PROGRAM RUN TERMINATES BY PROGRAM STOPPING AT WAIT 2 FOLLOWING 'PROGRAM COMPLETE' PRINTOUT. DEPRESSION OF THE START PUSHBUTTON WILL CAUSE PROGRAM TO BRANCH TO WAIT 1 TO PERMIT PROGRAM TO BE REPEATED, IF DESIRED.

THE PROGRAM MAY ALSO BE TERMINATED AT ANY TIME BY DEPRESSING THE IMIED STOP PUSHBUTTON. DEPRESSING RESET AND START PUSHBUTTONS WILL BRANCH PROGRAM TO WAIT 1 TO PERMIT PROGRAM TO BE REPEATED.

INTERRUPT FUNCTION TEST

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS (IN LISTING)

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORHAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```

*****
3001 0 01ED          DC      WAIT1+1      WAIT 1
          *
          *
          *      ONE OF THE METERED I/O UNITS
          *      FAILED TO SEND A RESPONSE
          *      INTERRUPT TO THE PROGRAM. INDEX
          *      REGISTER 1 WILL HAVE THE ADDRESS
          *      OF THE IOCC. THE AREA CODE WILL
          *      INDICATE THE I/O UNIT NOT READY.
          *      IF A 2401/02 DRIVE IS NOT READY,
          *      PROGRAM WILL NOT STOP AT WAIT 1.
          *
*****

```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS

THE VARIOUS PRINTOUTS THAT MAY OCCUR DURING EXECUTION OF THIS PROGRAM FOLLOW.

4.1 STATUS MESSAGES

A001 PROGRAM COMPLETE

ONE PASS THROUGH THE PROGRAM HAS BEEN COMPLETED. DEPRESS START TO RETURN TO WAIT 1.

4.2 COMMAND MESSAGES

CO01 TURN DISABLE SW ON PUSH START

COMMAND TO OPERATOR.

CO02 TURN DISABLE SW OFF

COMMAND TO OPERATOR. PROGRAM SHOULD START EXECUTIONG WHEN SWITCH IS TURNED OFF. IF IT DOES NOT, PUSH START TO CONTINUE.

INTERRUPT FUNCTION TEST

C003 PUSH CE INTERRUPT BUTTON

COMMAND TO OPERATOR.

C004 PUSH CONS INTRP BUTTON

COMMAND TO OPERATOR. THE PROGRAM DELAYS FOR 1 MINUTE WAITING FOR THE BUTTON TO BE PUSHED. IF THE BUTTON IS NOT PUSHED, OR IF IT FAILS TO INTERRUPT, AN ERROR PRINTOUT WILL OCCUR.

C005 SET TRACE MODE PUSH START

COMMAND TO OPERATOR.

C006 SET RUN MODE PUSH START

COMMAND TO OPERATOR.

C007 SET DISABLE ON HIT CE AND CONS BTNS SET TRACE AND START

COMMAND TO OPERATOR TO CHECK CE AND CONSOLE INTERRUPT BUTTONS FOR INTERRUPTS WITH DISABLE INTERRUPT SWITCH ON.

C008 REPAIR FAILURE BEFORE CONTINUING

THIS PRINTOUT WILL FOLLOW ERROR MESSAGE E00B. THE FAILURE INDICATED BY MESSAGE E00B CAN CAUSE LOSS OF PROGRAM CONTROL IF THE PROGRAM IS CONTINUED AND SHOULD THEREFORE BE REPAIRED BEFORE THE REMAINDER OF THE PROGRAM IS RUN.

4.3 DATA MESSAGES

D001 RTN OX PRIORITY CHECK

PRIORITY CHECK HEADING PRINTOUT. ROUTINE NUMBER CAN BE 3 OR 4.

REQ SEQUENCE XX XX XX XX XX
SRVC SEQUENCE XX XX XX XX XX

THESE TWO PRINTOUTS WILL OCCUR FOLLOWING THE HEADING PRINTOUT. REQUEST SEQUENCE INDICATES THE ORDER (READING FROM LEFT TO RIGHT) IN WHICH THE INTERRUPTS WERE RECEIVED BY THE TRAP ROUTINES. SERVICED SEQUENCE INDICATES (READING FROM LEFT TO RIGHT) THE ORDER IN WHICH THE INTERRUPTS WERE SERVICED BY THE TRAP ROUTINES.

THE FIRST INTERRUPT IS ISSUED BY ROUTINE 3, OR WITH EITHER THE TRACE MODE OR CE INTERRUPT BUTTON BY ROUTINE 5. THE FIRST TRAP ROUTINE ENTERED, WILL ISSUE AN INTERRUPT TO THE NEXT HIGHER LEVEL ETC, UNTIL LEVEL 00 IS REACHED. LEVEL 00 WILL CAUSE AN OP CODE VIOLATE TO INTERRUPT TO LEVEL ERROR. LEVEL ERROR WILL BE SERVICED IMMEDIATELY AND CAUSE THE SERVICING OF ALL NESTED INTERRUPTS IN THE OPPOSITE ORDER THAN THEY WERE RECEIVED.

IF THE FIRST INTERRUPT REQUEST FAILS, BOTH THE REQUEST AND SERVICED SEQUENCE PRINTOUTS WILL BE BLANK. IF A REQUEST FAILS TO INTERRUPT FROM A TRAP ROUTINE, THEN, THE LEVEL FROM WHICH THE REQUEST WAS ISSUED WILL BE THE FIRST ONE SERVICED.

INTERRUPT FUNCTION TEST

TYPICAL CORRECT PRINTOUTS FOLLOW.

FOR ROUTINE 3 AND SYSTEM WITH 12 INTERRUPT LEVELS,

REQ SEQUENCE 11 10 09 0802 01 00 ER
SRVC SEQUENCE ER 00 01 0208 09 10 11

FOR ROUTINE 3 AND SYSTEM WITH 18 INTERRUPT LEVELS,

REQ SEQUENCE 17 16 15 1402 01 00 ER
SRVC SEQUENCE ER 00 01 0214 15 16 17

FOR ROUTINE 3 AND SYSTEM WITH 24 INTERRUPT LEVELS,

REQ SEQUENCE 23 22 21 2002 01 00 ER
SRVC SEQUENCE ER 00 01 0220 21 22 23

FOR ROUTINE 5 AND SYSTEM WITH 12 INTERRUPT LEVELS,

REQ SEQUENCE CE (OR TR) 11 10 0902 01 00 ER
SRVC SEQUENCE ER 00 01 0209 10 11 CE (OR TR)

FOR ROUTINE 5 AND SYSTEM WITH 18 INTERRUPT LEVELS,

REQ SEQUENCE CE (OR TR) 17 16 1502 01 00 ER
SRVC SEQUENCE ER 00 01 0215 16 17 CE (OR TR)

FOR ROUTINE 5 AND SYSTEM WITH 24 INTERRUPT LEVELS,

REQ SEQUENCE CE (OR TR) 23 22 2102 01 00 ER
SRVC SEQUENCE ER 00 01 0221 22 23 CE (OR TR)

D002 CONSOLE BUTTON ON LEVEL XX ILSW BIT X

THIS PRINTOUT IS GIVEN BY ROUTINE 4 TO INDICATE THE LEVEL TO WHICH THE CONSOLE INTERRUPT PUSHBUTTON INTERRUPTS. THE ILSW BIT IS IN DECIMAL. IF NO ILSW BIT WAS ON, IT WILL BE INDICATED BY AN "N".

4.4 ERROR MESSAGES

E001 RTN OX LEVEL XX FAILED TO INTRP

ROUTINE NUMBER (RTN) CAN BE 2, 4, OR 5.

THIS ERROR PRINTOUT INDICATES THAT,

- A. THE LEVEL SPECIFIED FAILED TO RESPOND TO A PROGRAMMED INTERRUPT.
- B. IF RTN 03, LEVEL ER, OP CODE VIOLATE FAILED TO INTERRUPT.
- C. IF RTN 05, LEVEL ER, VIOLATING A STORAGE PROTECTED LOCATION FAILED TO CAUSE AN INTERRUPT.
- D. IF RTN 04, THE LEVEL SPECIFIED FAILED TO INTERRUPT AFTER THE MASK REGISTER WAS RESET OFF.

E002 RTN OX REQ XX GIVEN LEVL XX SRVCD

ROUTINE NUMBER CAN BE 2, 4, OR 5. PRINTOUT OCCURS WHEN THE INTERRUPT GIVEN INTERRUPTS TO THE WRONG LEVEL.

INTERRUPT FUNCTION TEST

E003 RTN 0X REQ XX INTRPD WITH DISABLE SW ON

ROUTINE NUMBER CAN BE 2 OR 5. THE REQUEST NUMBER SPECIFIED RESULTED IN AN INTERRUPT WHILE THE DISABLE INTERRUPT SWITCH WAS ON.

E004 RTN 0X WRONG ILSW ON ERR INTRP

IF ROUTINE 2, THE ILSW WAS WRONG FOR AN OP CODE VIOLATE. IF ROUTINE 5, THE ILSW WAS WRONG FOR A STORAGE PROTECT VIOLATION.

E005 SEQUENCE ERROR RTN 0X

ROUTINE NUMBER SPECIFIED SHOULD HAVE BEEN RUN, BUT WAS NOT. PUSH START TO GO TO WAIT 1.

E006 RTN 4 LEVEL XX INTRPD WHILE MASKED

THE LEVEL SPECIFIED INTERRUPTED WHILE THE MASK REGISTER WAS SET ON.

E007 RTN 2 INT XX ILSW NOT ZERO

THE ILSW FOR THE LEVEL INDICATED WAS NOT ZERO AFTER A PROGRAMMED INTERRUPT. FOLLOWING THIS PRINTOUT, IF THE PROGRAM IS IN A NORMAL PROGRAM RUN, IT WILL STOP AT WAIT 15 WITH ILSW IN A REGISTER.

E008 RTN 6 TRACE DID NOT INTRPT ON PASS XX

TEN PASSES ARE MADE THROUGH THIS ROUTINE. IF ANY PASS IS MADE WITHOUT RECEIVING A TRACE INTERRUPT THIS PRINTOUT WILL OCCUR, INDICATING THE PASS NUMBER.

E009 RTN 6 EXPECTED INTRPT FROM INSTRM XX GOT XX

THE ROUTINE HAS 10 INSTRUCTIONS WHICH SHOULD INTERRUPT IN SEQUENCE. IF THE SEQUENCE IS DESTROYED DUE TO A SKIPPED INSTRUCTION OR BECAUSE TRACE FAILED TO INTERRUPT, THE PRINTOUT WILL OCCUR, INDICATING THE INSTRUCTION THAT INTERRUPTED, AND THE INSTRUCTION THAT SHOULD HAVE INTERRUPTED.

THE INSTRUCTIONS USED IN THE ROUTINE FOLLOW.

1	LD	6	M
2	RTE	7	A
3	STO	8	BSC
4	S	9	NOP
5	EOR	10	MDX

E00A CONSOLE BUTTON FAILED

IF THE CONSOLE BUTTON DOES NOT CAUSE AN INTERRUPT, OR IF THE CONSOLE BUTTON IS NOT DEPRESSED WITHIN 1 MINUTE AFTER THE OPERATOR WAS REQUESTED TO DO SO, THIS PRINTOUT WILL OCCUR.

E00B RTN1 INTRPT NOT INHIBITED AFTER XXX

XXX REPRESENTS EITHER XIO OR BSI. THIS PRINTOUT INDICATES THAT AN INTERRUPT WAS NOT INHIBITED FOR 1 INSTRUCTION FOLLOWING THE EXECUTION OF AN XIO OR BSI INSTRUCTION.

INTERRUPT FUNCTION TEST

E00C ILLEGAL RTN ENTRY

AN ENTRY OF 7 WAS ENTERED IN THE SENSE PROGRAM SWITCHES. THIS IS AN INVALID ENTRY. PROGRAM RETURNS TO WAIT 1, B REG = 3001.

5. COMMENTS

THE INTERRUPT FUNCTION TEST CONSISTS OF A CONTROL ROUTINE AND SIX TESTING ROUTINES. INTERRUPT LEVELS 0 THROUGH 23 ARE CHECKED USING THE PROGRAMMED INTERRUPT FEATURE. LEVEL INTERNAL IS CHECKED BY ISSUING OP CODE VIOLATES AND STORAGE PROTECT VIOLATES. LEVELS TR AND CE ARE CHECKED OUT THROUGH THEIR ASSOCIATED HARDWARE. THE CONSOLE INTR PUSHBUTTON IS ALSO CHECKED.

THE CONTROL ROUTINE SEQUENCES THE TEST ROUTINES AND ACCOMPLISHES THE PROGRAM OPTIONS SPECIFIED BY THE OPERATOR.

ROUTINE 1 CHECKS TO INSURE THAT THE INTERRUPT IS DELAYED FOR 1 INSTRUCTION FOLLOWING THE EXECUTION OF AN XIO AND BSI. THE XIO CHECK IS MADE FIRST. THE BSI CHECK IS THEN MADE BY EXECUTING THE BSI IMMEDIATELY AFTER AN XIO. IF AN ERROR IS DETECTED, THE CE WILL RECEIVE A FIX COMMAND AND THE PROGRAM WILL GO TO WAIT 1.

ROUTINE 2 CHECKS THE BASIC OPERATION OF INTERRUPT LEVELS 0 THROUGH 23 AND INTERRUPT LEVEL INTERNAL. PASS 1 CHECKS FOR PROPER INTERRUPTING AND PASS 2 CHECKS FOR NO INTERRUPTS WITH THE DISABLE INTERRUPT SWITCH SET TO ON. LEVEL INTERNAL IS CHECKED WITH AN OP CODE VIOLATE, AND IS ALSO CHECKED FOR PROPER ILSW BIT ON. LEVELS 0 THROUGH 23 ARE CHECKED FOR NO ILSW BIT BEING ON AFTER PROGRAM INTERRUPT. EACH PASS IS RUN 500 TIMES.

ROUTINE 3 CHECKS LEVELS 0 THROUGH 23 AND LEVEL INTERNAL FOR PROPER PRIORITY SEQUENCING. THE METHOD USED TO CHECK PRIORITY SEQUENCING IS EXPLAINED IN PARAGRAPH 4. PRINTOUTS, UNDER THE 'RTN 0X PRIORITY CHECK' PRINTOUT.

ROUTINE 4 CHECKS THE MASK REGISTER. THE FIRST PASS CHECKS THAT THE MASK REG. CAN BE SET ON, AND THE SECOND PASS CHECKS THAT IT CAN BE RESET OFF. THE CHECK IS MADE 500 TIMES.

ROUTINE 5 CHECKS THE HARDWARE INTERRUPT FEATURES, THAT IS THE CONSOLE INTR AND CE INTERRUPT PUSHBUTTONS, AND THE TRACE MODE CIRCUITRY. PROPER INTERRUPTING, PRIORITY, AND INTERRUPT DISABLE ARE CHECKED. LEVEL INTERNAL ILSW IS CHECKED FOR PROPER BIT ON AFTER A STORAGE PROTECT VIOLATION.

ROUTINE 6 IS A CHECK OF TRACE MODE WHILE RUNNING A 10 INSTRUCTION ROUTINE. TEN PASSES ARE MADE THROUGH THIS ROUTINE. THE 10 INSTRUCTIONS USED ARE LISTED IN PARAGRAPH 4. PRINTOUTS, UNDER 'RTN05 EXPECTED INTRPT FROM INSTRM XX GOT XX' PRINTOUT.

INTERRUPT FUNCTION TEST

028C	ABS ORG	/3001	88300010 88300020 88300030 88300040 88300050 88300060 88300070 88300080 88300090 88300100 88300110 88300120 88300130 88300140 88300150 88300160 88300170 88300180 88300190 88300200 88300210 88300220 88300230 88300240 88300250 88300260 88300270 88300280 88300290 88300300 88300310 88300320 88300330 88300340 88300350 88300360 88300370 88300380 88300390 88300400 88300410 88300420 88300430 88300440 88300450 88300460 88300470 88300480 88300490 88300500 88300510 88300520 88300530 88300540 88300550 88300560 88300570 88300580 88300590 88300600 88300610 88300620 88300630 88300640 88300650 88300660 88300670 88300680
		** PROGRAM WAITS **	
3001 0 013D	DC	WT1+1 WAIT 1	
		WAIT OCCURS AFTER PROGRAM HAS LOADED. PERFORM SETUP, ENTER DESIRED OPTIONS IN DATA ENTRY SWITCHES AND DEPRESS START.	
3002 0 0175	DC	WT2+1 WAIT 2	
		PROGRAM RAN TO COMPLETION. DEPRESSING START RETURNS PROGRAM TO WAIT 1.	
3003 0 0184	DC	WT3+1 WAIT 3	
		PROGRAM SEQUENCE ERROR. SUPERVISOR SECTION OF PROGRAM DETECTED AN ERROR IN ROUTINE SEQUENCING.	
3004 0 027F	DC	WT4+1 WAIT 4	
		ROUTINE 2 WAIT. TURN THE DISABLE INTERRUPT SWITCH ON AND DEPRESS THE START PUSHBUTTON.	
3005 0 0250	DC	WT5+1 WAIT 5	
		ROUTINE 2 WAIT. TURN THE DISABLE INTERRUPT SWITCH OFF. PROGRAM SHOULD START EXECUTION. IF IT DOES NOT (DUE TO INTERNAL INTERRUPT FAILURE) PRESS START BUTTON TO CONTINUE.	
3006 0 035C	DC	WT6+1 WAIT 6	
		ROUTINE 5 WAIT. WRONG ILSW WAS SENSED ON STORAGE PROTECT VIOLATE INTERRUPT. THE ILSW IS IN THE A REG. PUSH START TO CONTINUE.	
3007 0 0386	DC	WT7+1 WAIT 7	
		ROUTINE 5 WAIT. SET MODE SWITCH TO TRACE AND PRESS START. PROGRAM WILL CHECK TRACE INTERRUPT.	
3008 0 038E	DC	WT8+1 WAIT 8	
		ROUTINE 5 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
3009 0 03CF	DC	WT9+1 WAIT 9	
		ROUTINE 5 WAIT. SET MODE SWITCH TO TRACE AND PRESS	

INTERRUPT FUNCTION TEST

				START. PROGRAM WILL MAKE AN INTERRUPT PRIORITY CHECK IN TRACE MODE OPERATION.	88300690 88300700 88300710 88300720 88300730 88300740 88300750 88300760 88300770 88300780 88300790 88300800 88300810 88300820 88300830 88300840 88300850 88300860 88300870 88300880 88300890 88300900 88300910 88300920 88300930 88300940 88300950 88300960 88300970 88300980 88300990 88301000 88301010 88301020 88301030 88301040 88301050 88301060 88301070 88301080 88301090 88301100 88301110 88301120 88301130 88301140 88301150 88301160 88301170 88301180 88301190 88301200 88301210 88301220 88301230 88301240 88301250 88301260 88301270 88301280 88301290 88301300 88301310 88301320 88301330 88301340 88301350 88301360
300A 0 03D6	DC	WTA+1 WAIT A		ROUTINE 5 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
300B 0 03E6	DC	WTB+1 WAIT B		ROUTINE 5 WAIT. DEPRESS C.E. INTERRUPT BUTTON. PROGRAM WILL MAKE AN INTERRUPT PRIORITY CHECK WITH THE C.E. INTERRUPT LEVEL.	
300C 0 03F7	DC	WTC+1 WAIT C		ROUTINE 5 WAIT. SET THE DISABLE INTERRUPT SWITCH ON, THEN DEPRESS THE C.E. INTERRUPT AND CONSOLE INTERRUPT BUTTONS. NO INTERRUPT SHOULD OCCUR. THEN SET TRACE MODE AND DEPRESS START.	
300D 0 040A	DC	WTD+1 WAIT D		ROUTINE 5 WAIT. SET THE MODE SWITCH TO RUN AND DEPRESS START.	
300E 0 040E	DC	WTE+1 WAIT E		ROUTINE 5 WAIT. TURN THE DISABLE INTERRUPT SWITCH OFF. PROGRAM SHOULD CONTINUE. IF IT DOES NOT (DUE TO DISABLE INTERRUPT IN-OPERATIVE) THEN PUSH START TO CONTINUE.	
300F 0 045D	DC	WTF+1 WAIT F		ROUTINE 6 WAIT. SET MODE SWITCH TO TRACE AND PRESS START BUTTON. PROGRAM WILL CHECK TRACE MODE OPERATION	
3010 0 0475	DC	WT10+1 WAIT 10		ROUTINE 6 WAIT. SET MODE SWITCH TO RUN AND DEPRESS START BUTTON.	
3011 0 051F	DC	WT11+1 WAIT 11		HALT ON ERROR OPTION REQUESTED. DEPRESS START BUTTON TO CONTINUE.	
3012 0 0538	DC	WT12+1 WAIT 12			

INTERRUPT FUNCTION TEST

```

*          1443 NOT READY. MAKE 1443
*          READY AND DEPRESS START.
*
3013 0 053A      DC      WT13+1      WAIT 13
*
*          1443 BUSY. THIS IS AN
*          ERROR CONDITION. REMEDY
*          CAUSE, THEN PUSH START TO
*          CONTINUE.
*
3014 0 0553      DC      WT14+1      WAIT 14
*
*          1816/1053 NUMBER 1 NOT
*          READY. MAKE READY AND PUSH
*          START TO CONTINUE.
*
3015 0 06C8      DC      WT15+1      WAIT 15
*
*          THIS WAIT WILL OCCUR
*          DURING ROUTINE 2 IF OPTION
*          BIT SWITCH 8 IS NOT ON, AND
*          AN ILSW ERROR IS DETECTED
*          ON A PROG. GENERATED INTER-
*          RUPT. THE ILSW IS IN THE
*          A REG. DEPRESS START TO
*          CONTINUE.
*
3016
012C 0 8300      ORG      300
                  DC      /8300      PID
*
*          * INTERRUPT FUNCT TEST *
*          * ** INTRP **          *
*
*          *****
*
CONTROL ROUTINE
*
012D 00 4400048E  START BSI L INTST      SET SPURIOUS INT ADR
*
012F 0 C864      LDD      CNC02      SET RESTART ADDRESS
0130 00 DC000026  STD      L /0026      IN LOCATION 26
0132 0 C863      LDD      CNC03
0133 00 DC000000  STD      L /0000
*
0135 0 1010      SLA      16          CLEAR BIT SWITCH
0136 0 D058      STO      BSW00      *READ IN AREA AND BY
0137 0 D061      STO      RUNSW      *PASS MAN CKS SWITCH
0138 00 D4000428  STD      L CN400      CLEAR TRACE INDICATO
013A 0 2C40      DC      /2C40      INSURE SP AREA IS
013B 0 0429      DC      CN401      *CLEAR
*
013C 0 3001      WT1      WAIT      1          ENTER PROG.OPTIONS
*
013D 0 084C      XIO      BSW0      READ BIT SWITCHES
*
013E 0 10A0      CON01  SLT      32          CLEAR A AND Q
013F 0 C052      LD      BSW00      GET OPTION ENTRY
0140 0 18C8      RTE      8
0141 0 1010      SLA      16
0142 0 1081      SLT      1
0143 0 D055      STO      RUNSW      SET RUN SWITCH
0144 0 1010      SLA      16
0145 0 1081      SLT      1
0146 0 D053      STO      OPIND      SET OUTPUT DEVICE ID
*
*
*          DETERMINE NUMBER OF LEVELS
*

```

```

88301370
88301380
88301390
88301400
88301410
88301420
88301430
88301440
88301450
88301460
88301470
88301480
88301490
88301500
88301510
88301520
88301530
88301540
88301550
88301560
88301570
88301580
88301590
88301600
88301610
88301620
88301630
88301640
88301650
88301660
88301670
88301680
88301690
88301700
88301710
88301720
88301730
88301740
88301750
88301760
88301770
88301780
88301790
88301800
88301810
88301820
88301830
88301840
88301850
88301860
88301870
88301880
88301890
88301900
88301910
88301920
88301930
88301940
88301950
88301960
88301970
88301980
88301990
88302000
88302010
88302020
88302030
88302040

```

INTEPRUPT FUNCTION TEST

```

0147 0 C04A      LD      BSW00      GET OPTION ENTRIES
0148 0 4810      BSC      -          SKIP IF BIT 0 ON
0149 0 7004      MDX      CTRL1      BRANCH ON NOT BIT 0
014A 0 6311      LDX      3 17      SET LEVEL INDICATOR
014B 0 684C      STX      3 LVSAV      *FOR 18 LEVELS
014C 0 6302      LDX      3 2      SET INDEX FOR 18 LVL
014D 0 700A      MDX      CTRL3      CONTINUE
014E 0 1001      CTRL1  SLA      1      CHECK FOR BIT 1
014F 0 4810      BSC      -          SKIP IF BIT 1 ON
0150 0 7004      MDX      CTRL2      BRANCH ON NOT BIT 1
0151 0 6317      LDX      3 23      SET LEVEL INDICATOR
0152 0 6845      STX      3 LVSAV      *FOR 24 LEVELS
0153 0 6304      LDX      3 4      SET INDEX FOR 24 LVL
0154 0 7003      MDX      CTRL3      CONTINUE
0155 0 630B      CTRL2  LDX      3 11      SET LEVEL INDICATOR
0156 0 6841      STX      3 LVSAV      *FOR 12 LEVELS
0157 0 6300      LDX      3 0      SET INDEX FOR 12 LVL
0158 0 6835      CTRL3  STX      3 LVLIX      SAVE INDEX SETTING
*
0159 0 1010      CON06  SLA      16
015A 0 D034      SFO      RTNNO      CLEAR ROUTN. NUMBER
*
015B 0 082C      CNTRL  XIO      SNSWS      READ SENSE SWITCHES
015C 0 1005      SLA      5          CHECK FOR LOOP RTN
015D 0 180D      SRA      13
015E 0 4808      BSC      +          SKIP IF LOOP ROUTINE
015F 0 7002      MDX      **2
0160 0 D02E      STO      RTNNO
0161 0 7006      MDX      CON05+2      GO EXECUTE ROUTINE
0162 0 C02C      LD      RTNNO
0163 0 902C      S      SIX          CK IF ALL RTNS RUN
0164 0 4818      BSC      +-
0165 0 7006      MDX      CON03      ALL ROUTINES HAVE RN
0166 00 7401018F  CON05  MDX      L RTNNO,1      ADD 1 TO RTN.NO.
0168 00 6580018F  LDX      I1 RTNNO
016A 00 4080019A  BSC      I1 RTN-1      EXIT TO ROUTINE
*
*          ALL ROUTINES HAVE RUN
*
016C 0 081D      CON03  XIO      BSW0      READ BIT SWITCHES
016D 0 C024      LD      BSW00      GET BIT SWITCHES
016E 0 1804      SRA      4          CK LOOP PROGRAM
016F 0 4804      BSC      E
0170 0 70E8      MDX      CON06      LOOP PROGRAM
*
*****
0171 00 44000523  BSI      L LOG      PRINT PROGRAM      SRC
0173 0 09C9      DC      INM07      IS COMPLETE
*****
0174 0 3002      WT2      WAIT      2          PROGRAM COMPLETE
0175 00 4C00012D  BSC      L START      PROGRAM RESTART
*
*          *** ROUTINE RETURN ***
*
0177 0 C019      RTNRT  LD      SEQCK      SEQUENCE CHECK
0178 0 4818      BSC      +-
0179 0 70E1      MDX      CNTRL      CHECK OK
*
017A 00 6780018F  LDX      I3 RTNNO
017C 00 C700092F  LD      L3 INLVT+1      GET HEX-VALUE OF RTN
017E 00 D4000A2C  STO      L INM12+15      SET IN MESSAGE
*
*****
0180 00 44000523  BSI      L LOG      PRINT SEQUENCE      SRC
0182 0 0A1D      DC      INM12      ERROR
*****
*

```


INTERRUPT FUNCTION TEST

```

0183 0 3003      WT3  WAIT  3      SEQUENCE ERROR      88302730
0184 00 4C00012D  BSC  L  START                                88302740
*                                                         88302750
*          CONTROL ROUTINE CONSTANTS                       88302760
*                                                         88302770
0186 00 00000000          DEC  0                                88302780
0188 0 0000      SNSWS DC  /0000      READ SENSE SW IOCC  88302790
0189 0 0760          DC  /0760                                88302800
018A 0 0192      BSW0 DC  BSW00      READ BIT SWITCH IOCC  88302810
018B 0 0240          DC  /0240                                88302820
*                                                         88302830
018C 0 0193      BSW1 DC  BSW01      READ BIT SWITCH IOCC  88302840
018D 0 0240          DC  /0240                                88302850
*                                                         88302860
018E 0 0000      LVLIX DC  0          NO.OF LEVELS INDEX  88302870
018F 0 0000      RTNNO DC  0          ROUTINE NUMBER      88302880
0190 0 0006      SIX  DC  6          CONSTANT 6           88302890
0191 0 0000      SEQCK DC  0          SEQUENCE CHECK SAVE  88302900
*                                                         88302910
0192 0 0000      BSW00 DC  0          BIT SW. CONTROL DATA 88302920
0193 0 0000      BSW01 DC  0          NO INTERRUPT LEVELS  88302930
*                                                         88302940
0194 0 4C00      CNC02 DC  /4C00      RESTART INSTRUCTIONS 88302950
0195 0 012D          DC  START                                88302960
0196 0 7025      CNC03 DC  /7025                                88302970
0197 0 4400          DC  /4400                                88302980
0198 0 0000      LVSAV DC  0          NO-INTR.LVLS SAVE  88302990
0199 0 0000      RUNSW DC  0                                88303000
019A 0 0000      OPIND DC  0          OUTPUT DEVICE INDCTR  88303010
*                                                         88303020
*          ROUTINE ADDRESSES                               88303030
*                                                         88303040
019B 0 01A6      RTN  DC  INT00      ROUTINE 1           88303050
019C 0 01FE          DC  INT01      ROUTINE 2           88303060
019D 0 0292          DC  INT02      ROUTINE 3           88303070
019E 0 0285          DC  INT03      ROUTINE 4           88303080
019F 0 0328          DC  INT04      ROUTINE 5           88303090
01A0 0 0446          DC  INT05      ROUTINE 6           88303100
01A1 0 01A2          DC  INTER      INVALID ENTRY       88303110
*                                                         88303120
*****                                                    88303130
01A2 00 44000523 INTER BSI L LOG      PRINT INVALID ENTRY SRC 88303140
01A4 C 089F          DC  INM25                                88303150
*****                                                    88303160
*                                                         88303170
01A5 0 7096          MDX  WT1      RETURN TO WAIT 1       88303180
*                                                         88303190
*****                                                    88303200
*          ROUTINE NUMBER ONE                             88303210
*****                                                    88303220
01A6 00 0C000320 INT00 XIO L MASK0      MASK INTERRUPTS      88303230
01A8 00 0C000322          XIO L MASK1                                88303240
*                                                         88303250
01AA 0 631B          LDX  3 27      SET INTERRUPT        88303260
01AB 00 C40001F5      LD  L  VCTOR  *TRANSFER VECTOR  88303270
01AD 00 D7000007      STO  L3 7                                88303280
01AF 0 73FF          MDX  3 -1                                88303290
01B0 0 70FC          MDX  *-4                                88303300
*                                                         88303310
01B1 0 C848          LDD  XIO      SET UP MESSAGE      88303320
01B2 00 DC000886      STD  L  INM23+22                                88303330
01B4 00 CC00028C      LDD  L  XIOCC      SET UP IOCC          88303350
01B6 00 DC00028A      STD  L  ISINT                                88303360
01B8 0 630C          LDX  3 12      SET INTRP INDEX  88303370
01B9 00 650001C5      LDX  L1 PL1+1      SET UP TRAP ROUTINE 88303380
01BB 00 6D000608      STX  L1 PLEXT+1    RETURN              88303390
01BD 00 0C000324      XIO  L  UMSKO      UNMASK INTERRUPTS 88303400

```

INTERRUPT FUNCTION TEST

```

01BF 00 0C000326          XIO  L  UMSKI      88303410
01C1 00 0C00028A IN001 XIO  L  ISINT      ISSUE INTRP CHECK  88303420
01C3 0 1000          NOP          *POLL ON XIO      88303430
01C4 0 701C          PL1  MDX  FAIL      INTERRUPT FAILED  88303440
01C5 0 C030          LD  ICTR      CHECK FOR PROPER I  88303450
01C6 0 F030          EOR  XIOCK      *COUNT ON INTERRUPT 88303460
01C7 00 4C2001E9          BSC  L  POLER,Z  BRANCH ON WRONG I CT 88303470
*                                                         88303480
01C9 0 C832          LDD  BSI      SET UP MESSAGE      88303490
01CA 00 DC000886      STD  L  INM23+22                                88303500
01CC 00 650001D6      LDX  L1 PL2+1      SET UP TRAP ROUTINE 88303510
01CE 00 6D000608          STX  L1 PLEXT+1    *RETURN              88303520
*                                                         88303530
01D0 00 0C00028A          XIO  L  ISINT      ISSUE INTRP CHECK  88303540
01D2 0 4000          BSI  *          *POLL ON BSI      88303550
01D3 0 1000          NOP          88303560
01D4 0 1000          NOP          88303570
01D5 0 7004          PL2  MDX  **4      BRNCH IF INTRP FAILD 88303580
01D6 0 C01F          LD  ICTR      CHECK FOR PROPER I  88303590
01D7 0 F020          EOR  BSICK      *COUNT ON INTERRUPT 88303600
01D8 00 4C2001E9          BSC  L  POLER,Z  BRANCH ON WRONG I CT 88303610
*                                                         88303620
01DA 0 C084          IN002 LD  RTNNO      PREPARE SEQUENCE CK  88303630
01DB 0 F01D          EOR  CNO01                                88303640
01DC 0 D084          STO  SEQCK      88303650
01DD 00 4400048E          BSI  L  INTST      SETUP XFER VECTORS SRC 88303660
*                                                         88303670
01DF 00 4C000177          BSC  L  RTNRT      RETURN TO CONTROL  88303680
01E1 00 C400028A          FAIL LD  L  ISINT      MODIFY IOCC FOR  88303690
01E3 0 1001          SLA  1          *NEXT INTERRUPT  88303700
01E4 00 D400028A          STD  L  ISINT      88303710
01E6 0 73FF          MDX  3 -1      88303720
01E7 0 7009          MDX  IN001      CONTINUE          88303730
01E8 0 70F1          MDX  IN002      END ROUTINE        88303740
*                                                         88303750
01E9 00 650001A6          POLER LDX L1 INT00 SET LOOP ERROR  88303760
01EB 00 6D000521          STX  L1 LPERR+1  *RETURN              88303770
*                                                         88303780
*****                                                    88303790
01ED 00 440004F5          BSI  L  ERROR      PRINT POLL ERROR SRC 88303800
01EF 0 0870          DC  INM23      MESSAGE TAG      88303810
01F0 00 44000523          BSI  L  LOG        PRINT FIX COMMAND  88303820
01F2 0 0889          DC  INM24      88303830
*****                                                    88303840
*                                                         88303850
01F3 00 4C00013C          BSC  L  WT1      GO TO WAIT 1       88303860
*                                                         88303870
*          ROUTINE 1 CONSTANTS                             88303880
*                                                         88303890
01F5 0 0601          VCTOR DC  POLL      TRANSFER VECTOR  88303900
01F6 0 0000          ICTR DC  0          I COUNT ON INTERRUPT 88303910
01F7 0 0104          XIOCK DC  PL1      XIO CHECK CONSTANT  88303920
01F8 0 0105          BSICK DC  PL2      BSI CHECK CONSTANT  88303930
01F9 0 0001          CNO01 DC  1          CONSTANT 1          88303940
01FA 0 0000          BSS  E          88303950
01FA 0 0017          XIO  DC  /0017      X          88303960
01FB 0 3926          DC  /3926      IO          88303970
01FC 0 0032          BSI  DC  /0032      B          88303980
01FD 0 1239          DC  /1239      SI          88303990
*                                                         88304000
*****                                                    88304010
*          ROUTINE NUMBER TWO                             88304020
*****                                                    88304030
01FE 00 C4000931 INT01 LD  L  INLVT+3 GET HEX 2          88304040
0200 00 D4000975      STO  L  INM03+7  SET ROUTINE NUMBER 88304050
0202 00 D400098A      STO  L  INM04+7  IN ERROR MESSAGES  88304060
0204 00 D40009A1      STO  L  INM05+7  88304070

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INTERRUPT FUNCTION TEST

```

0206 00 D400098B      STO L INM06+7      88304090
*                               88304100
0209 00 440004AA      BSI L LVLST        GO SET INTERPT ADRSS 88304110
*                               88304120
020A 00 C4000280      LD L CN101         88304130
020C 00 D4000281      STO L CN102        SET 1ST PASS SWITCH 88304140
020E 00 C4000284      LD L CN105        SET PASS SWITCH     88304150
0210 00 D4000285      STO L PSSW        88304160
*                               88304170
0212 00 C4000282      RT100 LD L CN103   SE LVL ER CHECK SW. 88304180
0214 00 D4000286      STO L ECKSW       88304190
0216 00 65800198      LDX I1 LVSAV      88304200
0218 0 7101           MDX 1 1           IX 1 = NO.LEVELS +1 88304210
0219 00 66800198      LDX I2 LVSAV      88304220
021B 0 7201           MDX 2 1           IX 2 = NO.LEVELS +1 88304230
021C 00 67000226      LDX L3 RT101      88304240
021E 00 6F000521      STX L3 LPERR+1    SET LOOP ERR RETURN 88304250
*                               88304260
0220 00 6780018E      LDX I3 LVLIX      SET UP INTERRUPT    88304270
0222 00 CF00028C      LDD L3 XIOCC      IOCC                88304280
0224 00 DC00028A      STO L ISINT       88304290
*                               88304300
0226 00 C600092E      RT101 LD L2 INLVT  SET REQUEST NUMBER  88304310
0228 00 D4000979      STO L INM03+11    IN FRROR MESSAGES   88304320
022A 00 D400098E      STO L INM04+11    88304330
022C 00 D40009A5      STO L INM05+11    88304340
022E 00 D4000A4F      STO L INM14+10    88304350
*                               88304360
0230 0 0859           XIO ISINT         ISSUE PROGMD INTRPT 88304370
0231 0 1000           NOP               88304380
0232 00 440006D7      BSI L SERVC       PRGM OPERATION PROT SRC 88304390
*                               88304400
0234 00 C4000281      RT109 LD L CN102   REQUEST DID NOT INRP 88304410
0236 0 4818           BSC ←            88304420
0237 0 7003           MDX RT104        NOT 1ST PASS OK     88304430
*                               88304440
*****                               88304450
0238 00 440004F5      BSI L ERROR       PRINT REQUEST FAILED SRC 88304460
023A 0 096E           DC INM03         TO INTERRUPT        88304470
*****                               88304480
*                               88304490
*                               88304500
*                               88304510
*                               88304520
023B 0 71FF          PT104 MDX 1 -1     CK IF ALL LVLS DONE 88304530
023C 0 701C          MDX RT105         NO                   88304540
023D 00 74FF0286      MDX L ECKSW,-1    LEVEL ERROR CHECKED 88304550
023F 0 7027          MDX RT106         NO                   88304560
0240 00 74FF0285      MDX L PSSW,-1     SKIP IF 500 PASSES  88304570
0242 0 70CF          MDX RT100        88304580
*                               88304590
*                               88304600
*                               88304610
*                               88304620
0243 00 C4000199      LJ L RUNSW        GET RUN SWITCH      88304630
0245 00 4C200250      BSC L RT110,Z     RUN WITH OUT STOPS 88304640
*                               88304650
*                               88304660
*                               88304670
0247 0 C039          LD CN102         GET 1ST PASS SWITCH 88304680
0248 0 4820          BSC Z           88304690
0249 0 702F          MDX RT107        1ST PASS           88304700
*                               88304710
*                               88304720
*                               88304730
024A 00 440004A0      BSI L NEST1       SET NEST ADDRESSES  88304740
*                               88304750
*****                               88304760
024C 00 44000523      BSI L LOG         PRINT TURN DISABLE SRC 88304770

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INTERRUPT FUNCTION TEST

```

024E 0 095E          DC INM02         SWITCH OFF          88304770
*****                               88304780
*                               88304790
024F 0 3005          WT5 WAIT 5       TURN DISABLE SW OFF 88304800
*                               88304810
0250 00 C400018F      RT110 LD L RTNND   PREPARE SEQUENCE CK 88304820
0252 0 902F          S CN103          88304830
0253 00 D4000191      STO L SEQCK      88304840
*                               88304850
0255 00 4400048E      BSI L INTST      SET SPURIOUS INT ADR 88304860
*                               88304870
0257 00 4C000177      BSC L RTNRT      RETURN TO CONTROL    88304880
*                               88304890
0259 0 C030          RT105 LD ISINT    GET IOCC ADDRS WD    88304900
025A 0 4808          BSC +           CK BIT 0 = 1        88304910
025B 0 7004          MDX RT108        BIT 0 = 1           88304920
025C 0 1001          SLA 1           88304930
025D 0 D02C          STO ISINT       88304940
025E 0 72FF          MDX 2 -1        SET FOR NEXT LVL. NO 88304950
025F 0 70C6          MDX RT101        CONTINUE             88304960
*                               88304970
0260 0 C02A          RT108 LD ISINT+1 CLEAR BIT 15 FROM    88304980
0261 0 901E          S CN101         IOCC CONTROL WORD   88304990
0262 0 D028          STO ISINT+1     88305000
0263 0 C01C          LD CN101        SET BIT 13 IN IOCC  88305010
0264 0 1002          SLA 2           ADDRESS WORD        88305020
0265 0 D024          STO ISINT       88305030
0266 0 70F7          MDX RT105+5     88305040
*                               88305050
0267 0 72FF          RT106 MDX 2 -1   88305060
0268 0 1000          NOP             88305070
0269 00 C600092E      LD L2 INLVT     LEVEL ERROR          88305080
026B 00 D4000979      STO L INM03+11  SET IN ERROR         88305090
026D 00 D400098E      STO L INM04+11 *MESSAGES           88305100
026F 00 D40009A5      STO L INM05+11 88305110
0271 00 D4000A4F      STO L INM14+10 88305120
0273 00 67000277      LDX L3 RT106+16 88305130
0275 00 6F000521      STX L3 LPERR+1  LOOP ERROR RETURN    88305140
*                               88305150
0277 0 0100          DC /0100        ILLEGAL OP CODE     88305160
*                               88305170
*                               88305180
*                               88305190
*                               88305200
*                               88305210
0278 0 708B          MDX RT109       88305220
*                               88305230
0279 0 1010          RT107 SLA 16     CLEAR 1ST PASS SW.  88305240
027A 0 D006          STO CN102       88305250
*                               88305260
*****                               88305270
027B 00 44000523      BSI L LOG        PRINT TURN DISABLE SRC 88305280
027D 0 0949          DC INM01         SWITCH ON           88305290
*****                               88305300
*                               88305310
027E 0 3004          WT4 WAIT 4       SET DISABLE SW ON    88305320
*                               88305330
027F 0 708E          MDX RT100-4     GO MAKE 2ND PASS    88305340
*                               88305350
*                               88305360
*                               88305370
*                               88305380
0280 0 0001          CN101 DC 1       88305390
0281 0 0000          CN102 DC 0       1ST PASS SWITCH     88305400
0282 0 0002          CN103 DC 2       88305410
0283 0 06D7          CN104 DC SERVC   88305420
0284 0 01F4          CN105 DC /01F4   PASS CONSTANT        88305430
0285 0 0000          PSSW DC 0        PASS SWITCH          88305440
*                               88305450

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INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

```

0286 0 0000      ECKSW DC      0      LEVEL ERR CHECK SW.      88305450
*
0288 00 00000000      DEC      0
028A 0 0000      ISINT DC      0      PROGRAMED INTERRUPT      88305470
028B 0 0000      DC      0      IOCC      88305480
*
028C 0 0010      XIOCC DC      /0010      12 LEVELS OF INTRPT      88305490
028D 0 04A0      DC      /04A0      88305500
028E 0 1000      DC      /1000      18 LEVELS OF INTRP      88305510
028F 0 04A1      DC      /04A1      88305520
0290 0 0040      DC      /0040      24 LEVELS OF INTRPT      88305530
0291 0 04A1      DC      /04A1      88305540
*
*
*****
*
ROUTINE NUMBER THREE
*****
*
0292 00 C4000932      INT02 LD      L      INIVT+4      GET HEX 3      88305550
0294 00 D4000A5F      STO      L      INM15+7      SET IN LOG MESSAGE      88305560
*
0296 00 44000488      BSI      L      PRIST      GO SET TRAP ADDRESS SRC      88305570
*
0298 00 6580018E      LDX      I1      LVLIX      SET IOCC FOR LOWEST      88305580
029A 00 C000028C      LDD      L1      XIOCC      INTERRUPT LEVEL      88305590
029C 0 0815      STD      CN200      88305600
*
029D 00 67800198      LDX      I3      LVSAY      NUMBER OF INTERRUPTS      88305610
029F 0 7302      MDX      3      2      TO BE GENERATED      88305620
*
02A0 0 6100      LDX      1      0      PRINT TABLE INDEX      88305630
02A1 0 6200      LDX      2      0      88305640
*
02A2 0 080F      XIO      CN200      ISSUE INTERRUPT      88305650
02A3 0 1000      NOP      88305660
*
*
RETURN FROM TRAP ROUTINES
*
02A4 00 440004E2      BSI      L      PRIPT      GO OUTPUT PRIO. SEQ      88305670
*
02A6 00 4400048E      BSI      L      INTST      SET SPURIOUS INT ADR      88305680
*
02A8 00 C400018F      LD      L      RTNNO      PREPARE SEQUENCE CK      88305690
02AA 0 9009      S      CN201      88305700
02AB 00 D4000191      STO      L      SEQCK      88305710
*
02AD 00 4C000177      BSC      L      RTNRT      RETURN TO CONTROL      88305720
*
*
ROUTINE THREE CONSTANTS
*
02B0 00 00000000      DEC      0      88305730
02B2 0 0000      CN200 DC      0      INTERRUPT IOCC      88305740
02B3 0 0000      DC      0      88305750
*
02B4 0 0003      CN201 DC      3      CONSTANT 3      88305760
*
*****
*
ROUTINE NUMBER FOUR
*****
*
02B5 00 C4000933      INT03 LD      L      INLVT+5      GET HEX 4      88305770
02B7 00 D4000975      STO      L      INM03+7      SET RTN NO. IN ERROR      88305780
02B9 00 D400098A      STO      L      INM04+7      *MESSAGES      88305790
*
02B8 00 440004AA      BSI      L      LVLST      GO SET UP TRAP ADDR      88305800
02BD 0 C0C6      LD      CN105      SET PASS SWITCH      88305810
02BE 0 D0C6      STO      PSSW      88305820
*

```

```

028F 00 C4000280      RT300 LD      L      CN101      88306130
02C1 0 0057      STO      CN300      SET 1ST PASS SW.      88306140
*
02C2 00 650002D0      LDX      L1      RT301      SET UP LOOP ON      88306150
02C4 00 6D000521      STX      L1      LPERR+1      ERROR RETURN      88306160
*
02C6 0 0859      XIO      MASK0      MASK UPPEP LEVELS      88306170
02C7 0 085A      XIO      MASK1      MASK LOWER LEVELS      88306180
*
02C8 00 6780018E      RT306 LDX      I3      LVLIX      SET UP INITIAL IOCC      88306190
02CA 00 CF00028C      LDD      L3      XIOCC      FOR LOWEST LEVEL      88306200
02CC 0 0851      STD      CN301      88306210
*
02CD 00 67800198      LDX      I3      LVSAY      SET IX FOR NO.OF      88306220
02CF 0 7301      MDX      3      1      INTERRUPTS      88306230
*
02D0 00 C700092E      RT301 LD      L3      INLVT      GET REQUEST NUMBER      88306240
02D2 00 D4000979      STO      L      INM03+11      SET IN ERROR      88306250
02D4 00 D400098E      STO      L      INM04+11      *MESSAGES      88306260
02D6 00 D4000A39      STO      L      INM13+11      88306270
*
02D8 0 0845      RT302 XIO      CN301      ISSUE INTERRUPT      88306280
02D9 0 1000      NOP      88306290
*
02DA 0 C03E      LD      CN300      GET 1ST PASS SWITCH      88306300
02DB 0 4808      BSC      +      88306310
02DC 0 7011      MDX      RT303      NOT 1ST PASS INTR ER      88306320
*
02DD 0 73FF      RT305 MDX      3      -1      CHECK IF ALL LVLS      88306330
02DE 0 701A      MDX      RT304      NO      88306340
02DF 0 C039      LD      CN300      YES      88306350
02E0 0 4820      BSC      2      88306360
02E1 0 7012      MDX      RT311      1ST PASS COMPL.CNTNU      88306370
02E2 00 74FF0285      MDX      L      PSSW,-1      SKIP IF 500 PASSES      88306380
02E4 0 70DA      MDX      RT300      88306390
*
*
ROUTINE COMPLETED
*
02E5 00 4400048E      BSI      L      INTST      SET SPURIOUS INT ADR      88306400
*
02E7 00 C400018F      LD      L      RTNNO      PREPARE SEQUENCE CK      88306410
02E9 0 9031      S      CN303      88306420
02EA 00 D4000191      STO      L      SEQCK      88306430
*
02EC 00 4C000177      BSC      L      RTNRT      RETURN TO CONTROL      88306440
*
*
REQ DID NOT INTRP MASK OFF
*
*****
*
02EE 00 440004F5      RT303 BSI      L      ERROR      LOG REQUEST FAILED SRC      88306450
02F0 0 096E      DC      INM03      TO INTERRUPT      88306460
*
*****
*
02F1 00 44000609      BSI      L      SVINT      88306470
02F3 0 70E9      MDX      RT305      88306480
*
*
SET UP FOR 2ND PASS
*
02F4 0 1010      RT311 SLA      16      88306490
02F5 0 D023      STO      CN300      CLEAR 1ST PASS SWITC      88306500
*
02F6 0 082D      XIO      UMSKO      UNMASK UPPER LEVELS      88306510
02F7 0 082E      XIO      UMSK1      UNMASK LOWER LEVELS      88306520
*
02F8 0 70CF      MDX      RT306      GO MAKE SECOND PASS      88306530
*
02F9 0 C024      RT304 LD      CN301      MODIFY IOCC FOR      88306540

```


INTERRUPT FUNCTION TEST

```

028C      ABS      88300010
          ORG      /3001 88300020
          *          88300030
          *          ** PROGRAM WAITS ** 88300040
          *          88300050
          *          88300060
          *          DC      WT1+1   WAIT 1 88300070
          *          88300080
          *          WAIT OCCURS AFTER PROGRAM 88300090
          *          HAS LOADED. PERFORM SETUP, 88300100
          *          ENTER DESIRED OPTIONS IN 88300110
          *          DATA ENTRY SWITCHES AND 88300120
          *          DEPRESS START.          88300130
          *          88300140
          *          DC      WT2+1   WAIT 2 88300150
          *          88300160
          *          PROGRAM RAN TO COMPLETION. 88300170
          *          DEPRESSING START RETURNS 88300180
          *          PROGRAM TO WAIT 1.      88300190
          *          88300200
          *          DC      WT3+1   WAIT 3 88300210
          *          88300220
          *          PROGRAM SEQUENCE ERROR. 88300230
          *          SUPERVISOR SECTION OF 88300240
          *          PROGRAM DETECTED AN ERROR 88300250
          *          IN ROUTINE SEQUENCING. 88300260
          *          88300270
          *          DC      WT4+1   WAIT 4 88300280
          *          88300290
          *          ROUTINE 2 WAIT. TURN THE 88300300
          *          DISABLE INTERRUPT SWITCH 88300310
          *          ON AND DEPRESS THE START 88300320
          *          PUSHBUTTON.           88300330
          *          88300340
          *          DC      WT5+1   WAIT 5 88300350
          *          88300360
          *          ROUTINE 2 WAIT. TURN THE 88300370
          *          DISABLE INTERRUPT SWITCH 88300380
          *          OFF. PROGRAM SHOULD START 88300390
          *          EXECUTION. IF IT DOES NOT 88300400
          *          (DUE TO INTERNAL INTERRUPT 88300410
          *          FAILURE)PRESS START BUTTON 88300420
          *          TO CONTINUE.          88300430
          *          88300440
          *          DC      WT6+1   WAIT 6 88300450
          *          88300460
          *          ROUTINE 5 WAIT. WRONG ILSW 88300470
          *          WAS SENSED ON STORAGE 88300480
          *          PROTECT VIOLATE INTERRUPT. 88300490
          *          THE ILSW IS IN THE A REC. 88300500
          *          PUSH START TO CONTINUE. 88300510
          *          88300520
          *          DC      WT7+1   WAIT 7 88300530
          *          88300540
          *          ROUTINE 5 WAIT. SET MODE 88300550
          *          SWITCH TO TRACE AND PRESS 88300560
          *          START. PROGRAM WILL CHECK 88300570
          *          TRACE INTERRUPT.      88300580
          *          88300590
          *          DC      WT8+1   WAIT 8 88300600
          *          88300610
          *          ROUTINE 5 WAIT. SET MODE 88300620
          *          SWITCH TO RUN AND DEPRESS 88300630
          *          START BUTTON.          88300640
          *          88300650
          *          DC      WT9+1   WAIT 9 88300660
          *          88300670
          *          ROUTINE 5 WAIT. SET MODE 88300680
          *          SWITCH TO TRACE AND PRESS
    
```

INTERRUPT FUNCTION TEST

```

          *          START. PROGRAM WILL MAKE 88300690
          *          AN INTERRUPT PRIORITY 88300700
          *          CHECK IN TRACE MODE 88300710
          *          OPERATION.          88300720
          *          88300730
          *          DC      WTA+1   WAIT A 88300740
          *          88300750
          *          ROUTINE 5 WAIT. SET MODE 88300760
          *          SWITCH TO RUN AND DEPRESS 88300770
          *          START BUTTON.        88300780
          *          88300790
          *          DC      WTB+1   WAIT B 88300800
          *          88300810
          *          ROUTINE 5 WAIT. DEPRESS 88300820
          *          C.E. INTERRUPT BUTTON. 88300830
          *          PROGRAM WILL MAKE AN 88300840
          *          INTERRUPT PRIORITY CHECK 88300850
          *          WITH THE C.E. INTERRUPT 88300860
          *          LEVEL.              88300870
          *          88300880
          *          DC      WTC+1   WAIT C 88300890
          *          88300900
          *          ROUTINE 5 WAIT. SET THE 88300910
          *          DISABLE INTERRUPT SWITCH 88300920
          *          ON, THEN DEPRESS THE C.E. 88300930
          *          INTERRUPT AND CONSOLE 88300940
          *          INTERRUPT BUTTONS. NO 88300950
          *          INTERRUPT SHOULD OCCUR. 88300960
          *          THEN SET TRACE MODE AND 88300970
          *          DEPRESS START.       88300980
          *          88300990
          *          DC      WTD+1   WAIT D 88301000
          *          88301010
          *          ROUTINE 5 WAIT. SET THE 88301020
          *          MODE SWITCH TO RUN AND 88301030
          *          DEPRESS START.        88301040
          *          88301050
          *          DC      WTE+1   WAIT E 88301060
          *          88301070
          *          ROUTINE 5 WAIT. TURN THE 88301080
          *          DISABLE INTERRUPT SWITCH 88301090
          *          OFF. PROGRAM SHOULD CONTI- 88301100
          *          NUE. IF IT DOES NOT(DUE 88301110
          *          TO DISABLE INTERRUPT IN- 88301120
          *          OPERATIVE* ,HEN PUSH START 88301130
          *          TO CONTINUE.          88301140
          *          88301150
          *          DC      WTF+1   WAIT F 88301160
          *          88301170
          *          ROUTINE 6 WAIT. SET MODE 88301180
          *          SWITCH TO TRACE AND PRESS 88301190
          *          START EUTTON. PROGRAM WILL 88301200
          *          CHECK TRACE MODE OPERATION 88301210
          *          88301220
          *          DC      WT10+1  WAIT 10 88301230
          *          88301240
          *          ROUTINE 6 WAIT. SET MODE 88301250
          *          SWITCH TO RUN AND DEPRESS 88301260
          *          START BUTTON.          88301270
          *          88301280
          *          DC      WT11+1  WAIT 11 88301290
          *          88301300
          *          HALT ON ERROR OPTION 88301310
          *          REQUESTED. DEPRESS START 88301320
          *          BUTTON TO CONTINUE.    88301330
          *          88301340
          *          DC      WT12+1  WAIT 12 88301350
          *          88301360
    
```


INTERRUPT FUNCTION TEST

```

*          1443 NOT READY. MAKE 1443
*          READY AND DEPRESS START.
3013 0 053A      DC      WT13+1      WAIT 13
*
*          1443 BUSY. THIS IS AN
*          ERROR CONDITION. REMEDY
*          CAUSE, THEN PUSH START TO
*          CONTINUE.
3014 0 0553      DC      WT14+1      WAIT 14
*
*          1816/1053 NUMBER 1 NOT
*          READY. MAKE READY AND PUSH
*          START TO CONTINUE.
3015 0 06C8      DC      WT15+1      WAIT 15
*
*          THIS WAIT WILL OCCUR
*          DURING ROUTINE 2 IF OPTION
*          BIT SWITCH 8 IS NOT ON, AND
*          AN ILSW ERROR IS DETECTED
*          ON A PROG. GENERATED INTER-
*          RUPT. THE ILSW IS IN THE
*          A REG. DEPRESS START TO
*          CONTINUE.
3016
012C 0 8300      ORG      300
                  DC      /8300      PID
*
*          * INTERRUPT FUNCT TEST *
*          * ** INTRP **          *
*          * *****
*
*          CONTROL ROUTINE
012D 00 4400048E  START BSI L INTST      SET SPURIOUS INT ADR
*
012F 0 C864      LDD      CNC02      SET RESTART ADDRESS
0130 00 DC000026  STD      L /0026      IN LOCATION 26
0132 0 C863      LDD      CNC03
0133 00 DC000000  STD      L /0000
*
0135 0 1010      SLA      16          CLEAR BIT SWITCH
0136 0 D058      STO      BSW00      *READ IN AREA AND BY
0137 0 D061      STO      RUNSW      *PASS MAN CKS SWITCH
0138 00 D4000428  STD      L CN400      CLEAR TRACE INDICATO
013A 0 2C40      DC      /2C40      INSURE SP AREA IS
013B 0 0429      DC      CN401      *CLEAR
*
013C 0 3001      WT1      WAIT      1          ENTER PROG.OPTIONS
*
013D 0 084C      XIO      BSW0      READ BIT SWITCHES
*
013E 0 10A0      CON01  SLT      32          CLEAR A AND Q
013F 0 C052      LD      BSW00      GET OPTION ENTRY
0140 0 18C8      RTE      8
0141 0 1010      SLA      16
0142 0 1081      SLT      1
0143 0 D055      STO      RUNSW      SET RUN SWITCH
0144 0 1010      SLA      16
0145 0 1081      SLT      1
0146 0 D053      STO      OPIND      SET OUTPUT DEVICE ID
*
*          DETERMINE NUMBER OF LEVELS

```

```

88301370
88301380
88301390
88301400
88301410
88301420
88301430
88301440
88301450
88301460
88301470
88301480
88301490
88301500
88301510
88301520
88301530
88301540
88301550
88301560
88301570
88301580
88301590
88301600
88301610
88301620
88301630
88301640
88301650
88301660
88301670
88301680
88301690
88301700
88301710
88301720
88301730
88301740
88301750
88301760
88301770
88301780
88301790
88301800
88301810
88301820
88301830
88301840
88301850
88301860
88301870
88301880
88301890
88301900
88301910
88301920
88301930
88301940
88301950
88301960
88301970
88301980
88301990
88302000
88302010
88302020
88302030
88302040

```

INTEPRUPT FUNCTION TEST

```

0147 0 C04A      LD      BSW00      GET OPTION ENTRIES
0148 0 4810      BSC      -          SKIP IF BIT 0 ON
0149 0 7004      MDX      CTRL1     BRANCH ON NOT BIT 0
014A 0 6311      LDX      3 17      SET LEVEL INDICATOR
014B 0 684C      STX      3 LVSAV    *FOR 18 LEVELS
014C 0 6302      LDX      3 2        SET INDEX FOR 18 LVL
014D 0 700A      MDX      CTRL3     CONTINUE
014E 0 1001      CTRL1  SLA      1    CHECK FOR BIT 1
014F 0 4810      BSC      -          SKIP IF BIT 1 ON
0150 0 7004      MDX      CTRL2     BRANCH ON NOT BIT 1
0151 0 6317      LDX      3 23      SET LEVEL INDICATOR
0152 0 6845      STX      3 LVSAV    *FOR 24 LEVELS
0153 0 6304      LDX      3 4        SET INDEX FOR 24 LVL
0154 0 7003      MDX      CTRL3     CONTINUE
0155 0 6308      CTRL2  LDX      3 11 SET LEVEL INDICATOR
0156 0 6841      STX      3 LVSAV    *FOR 12 LEVELS
0157 0 6300      LDX      3 0        SET INDEX FOR 12 LVL
0158 0 6835      CTRL3  STX      3 LVLI X SAVE INDEX SETTING
*
0159 0 1010      CON06  SLA      16
015A 0 D034      SIO      RTNNO      CLEAR ROUTN. NUMBER
*
015B 0 082C      CNTRL  XIO      SNSWS  READ SENSE SWITCHES
015C 0 1005      SLA      5          CHECK FOR LOOP RTN
015D 0 180D      SRA      13
015E 0 4808      BSC      +          SKIP IF LOOP ROUTINE
015F 0 7002      MDX      **2
0160 0 D02E      STO      RTNNO
0161 0 7006      MDX      CON05+2    GO EXECUTE ROUTINE
0162 0 C02C      LD      RTNNO
0163 0 902C      S      SIX          CK IF ALL RTNS RUN
0164 0 4818      BSC      +-
0165 0 7006      MDX      CON03     ALL ROUTINES HAVE RN
0166 00 7401018F  CON05  MDX      L RTNNO,1 ADD 1 TO RTN.NO.
0168 00 6580018F  LDX      I1 RTNNO
016A 00 4080019A  BSC      I1 RTN-1   EXIT TO ROUTINE
*
*          ALL ROUTINES HAVE RUN
*
016C 0 081D      CON03  XIO      BSW0  READ BIT SWITCHES
016D 0 C024      LD      BSW00      GET BIT SWITCHES
016E 0 1804      SRA      4          CK LOOP PROGRAM
016F 0 4804      BSC      E
0170 0 70E8      MDX      CON06     LOOP PROGRAM
*
*****
0171 00 44000523  BSI      L LOG      PRINT PROGRAM      SRC
0173 0 09C9      DC      INM07      IS COMPLETE
*****
0174 0 3002      WT2      WAIT      2          PROGRAM COMPLETE
0175 00 4C00012D  BSC      L START    PROGRAM RESTART
*
*          *** ROUTINE RETURN ***
*
0177 0 C019      RTNRT  LD      SEQCK  SEQUENCE CHECK
0178 0 4818      BSC      +-
0179 0 70E1      MDX      CNTRL     CHECK OK
*
017A 00 6780018F  LDX      I3 RTNNO
017C 00 C700092F  LD      L3 INLVT+1  GET HEX.VALUE OF RTN
017E 00 D4000A2C  STO      L INM12+15 SET IN MESSAGE
*
*****
0180 00 44000523  BSI      L LOG      PRINT SEQUENCE      SRC
0182 0 0A1D      DC      INM12      ERROR
*****
*
88302050
88302060
88302070
88302080
88302090
88302100
88302110
88302120
88302130
88302140
88302150
88302160
88302170
88302180
88302190
88302200
88302210
88302220
88302230
88302240
88302250
88302260
88302270
88302280
88302290
88302300
88302310
88302320
88302330
88302340
88302350
88302360
88302370
88302380
88302390
88302400
88302410
88302420
88302430
88302440
88302450
88302460
88302470
88302480
88302490
88302500
88302510
88302520
88302530
88302540
88302550
88302560
88302570
88302580
88302590
88302600
88302610
88302620
88302630
88302640
88302650
88302660
88302670
88302680
88302690
88302700
88302710
88302720

```

INTERRUPT FUNCTION TEST

```

0183 0 3003      WT3  WAIT  3      SEQUENCE ERROR      88302730
0184 00 4C00012D BSC  L  START                               88302740
*
*          CONTROL ROUTINE CONSTANTS                       88302750
*
0186 00 00000000          DEC  0                               88302760
0188 0 0000      SNSWS DC  /0000      READ SENSE SW IOCC  88302770
0189 0 0760      DC  /0760                               88302780
018A 0 0192      BSW0 DC  BSW00      READ BIT SWITCH IOCC  88302790
018B 0 0240      DC  /0240                               88302800
*
018C 0 0193      BSW1 DC  BSW01      READ BIT SWITCH IOCC  88302810
018D 0 0240      DC  /0240                               88302820
*
018E 0 0000      LVLIX DC  0          NO.OF LEVELS INDEX  88302830
018F 0 0000      RTNNO DC  0          ROUTINE NUMBER     88302840
0190 0 0006      SIX  DC  6          CONSTANT 6           88302850
0191 0 0000      SEQCK DC  0          SEQUENCE CHECK SAVE  88302860
*
0192 0 0000      BSW00 DC  0          BIT SW. CONTROL DATA 88302870
0193 0 0000      BSW01 DC  0          NO INTERRUPT LEVELS  88302880
*
0194 0 4C00      CNC02 DC  /4C00      RESTART INSTRUCTIONS 88302890
0195 0 012D      DC  START                               88302900
0196 0 7025      CNC03 DC  /7025      NO.INTR.LVLS SAVE   88302910
0197 0 4400      DC  /4400                               88302920
0198 0 0000      LVSAV DC  0          NO.INTR.LVLS SAVE   88302930
0199 0 0000      RUNSW DC  0          NO INTERRUPT LEVELS  88302940
019A 0 0000      OPIND DC  0          OUTPUT DEVICE INDCTR 88302950
*
*          ROUTINE ADDRESSES                               88302960
*
019B 0 01A6      RTN  DC  INT00      ROUTINE 1           88302970
019C 0 01FE      DC  INT01      ROUTINE 2           88302980
019D 0 0292      DC  INT02      ROUTINE 3           88302990
019E 0 0285      DC  INT03      ROUTINE 4           88303000
019F 0 0328      DC  INT04      ROUTINE 5           88303010
01A0 0 0446      DC  INT05      ROUTINE 6           88303020
01A1 0 01A2      DC  INTER      INVALID ENTRY        88303030
*
*          PRINT INVALID ENTRY SRC                         88303040
01A2 00 44000523 INTER BSI L LOG      PRINT INVALID ENTRY SRC 88303050
01A4 C 089F      DC  INM25                               88303060
*
01A5 0 7096      MDX  WT1          RETURN TO WAIT 1      88303070
*
*          ROUTINE NUMBER ONE                             88303080
*
01A6 00 0C000320 INT00 XIO L MASK0      MASK INTERRUPTS     88303090
01A8 00 0C000322 XIO  L MASK1                               88303100
*
01AA 0 631B      LDX  3 27          SET INTERRUPT       88303110
01AB 00 C40001F5 LD  L VCTOR      *TRANSFER VECTOR  88303120
01AD 00 D7000007 STO  L3 7                               88303130
01AF 0 73FF      MDX  3 -1          *TRANSFER VECTOR  88303140
01B0 0 70FC      MDX  * -4          *TRANSFER VECTOR  88303150
*
01B1 0 C848      LDD  XIO          SET UP MESSAGE       88303160
01B2 00 DC000886 STD  L INM23+22      SET UP MESSAGE       88303170
01B4 00 CC00028C LDD  L XIOCC      SET UP IOCC         88303180
01B6 00 DC00028A STD  L ISINT      SET UP IOCC         88303190
01B8 0 630C      LDX  3 12          SET INTRP INDEX    88303200
01B9 00 650001C5 LDX  L1 PL1+1      SET UP TRAP ROUTINE 88303210
01BB 00 6D000608 STX  L1 PLEXT+1      RETURN              88303220
01BD 00 0C000324 XIO  L UMSK0      UNMASK INTERRUPTS  88303230

```

INTERRUPT FUNCTION TEST

```

01BF 00 0C000326          XIO  L  UMSK1      88303410
01C1 00 0C00028A IN001 XIO  L  ISINT      ISSUE INTRP CHECK  88303420
01C3 0 1000          NOP          *POLL ON XIO     88303430
01C4 0 701C          PL1  MDX   FAIL      INTERRUPT FAILED  88303440
01C5 0 C030          LD  ICTR      CHECK FOR PROPER I  88303450
01C6 0 F030          EOR  XIOCK     *COUNT ON INTERRUPT 88303460
01C7 00 4C2001E9 BSC  L  POLER,Z  BRANCH ON WRONG I CT 88303470
*
01C9 0 C832          LDD  BSI          SET UP MESSAGE     88303480
01CA 00 DC000886 STD  L INM23+22      SET UP MESSAGE     88303490
01CC 00 650001D6 LDX  L1 PL2+1      SET UP TRAP ROUTINE 88303500
01CE 00 6D000608 STX  L1 PLEXT+1      *RETURN           88303510
*
01D0 00 0C00028A          XIO  L  ISINT      ISSUE INTRP CHECK  88303520
01D2 0 4000          BSI  *          *FOLL ON BSI   88303530
01D3 0 1000          NOP          *FOLL ON BSI   88303540
01D4 0 1000          NOP          *FOLL ON BSI   88303550
01D5 0 7004          PL2  MDX   **4      BRNCH IF INTRP FAILD 88303560
01D6 0 C01F          LD  ICTR      CHECK FOR PROPER I  88303570
01D7 0 F020          EOR  BSICK     *COUNT ON INTERRUPT 88303580
01D8 00 4C2001E9 BSC  L  POLER,Z  BRANCH ON WRONG I CT 88303590
*
01DA 0 C0B4          IN002 LD  RTNNO      PREPARE SEQUENCE CK  88303600
01DB 0 F01D          EOR  CN001      PREPARE SEQUENCE CK  88303610
01DC 0 D0B4          STO  SEQCK     PREPARE SEQUENCE CK  88303620
01DD 00 4400048E BSI  L  INTST     SETUP XFER VECTORS SRC 88303630
*
01DF 00 4C000177          BSC  L  RTNRT     RETURN TO CONTROL  88303640
01E1 00 C400028A FAIL  LD  L ISINT     MODIFY IOCC FOR     88303650
01E3 0 1001          SLA  1          *NEXT INTERRUPT   88303660
01E4 00 D400028A          STO  L ISINT     *NEXT INTERRUPT   88303670
01E6 0 73FF          MDX  3 -1          *NEXT INTERRUPT   88303680
01E7 0 70D9          MDX  IN001      CONTINUE           88303690
01E8 0 70F1          MDX  IN002      END ROUTINE         88303700
*
01E9 00 650001A6 POLER LDX  L1 INT00     SET LOOP ERROR     88303710
01EB 00 6D000521 STX  L1 LPERR+1     *RETURN           88303720
*
01ED 00 440004F5          BSI  L  ERROR     PRINT POLL ERROR SRC 88303730
01EF 0 0870          DC  INM23      MESSAGE TAG         88303740
01F0 00 44000523          BSI  L  LOG       PRINT FIX COMMAND   88303750
01F2 0 0889          DC  INM24      PRINT FIX COMMAND   88303760
*
01F3 00 4C00013C          BSC  L  WT1       GO TO WAIT 1     88303770
*
*          ROUTINE 1 CONSTANTS                            88303780
*
01F5 0 0601          VCTOR DC  POLL      TRANSFER VECTOR     88303790
01F6 0 0000          ICTR DC  0          I COUNT ON INTERRUPT 88303800
01F7 0 0104          XIOCK DC  PL1      XIO CHECK CONSTANT  88303810
01F8 0 0105          BSICK DC  PL2      BSI CHECK CONSTANT  88303820
01F9 0 0001          CN001 DC  1          CONSTANT 1         88303830
01FA 0 0000          BSS  E          *CONSTANT 1         88303840
01FA 0 0017          XIO  DC  /0017      X                   88303850
01FB 0 3926          DC  /3926      IO                   88303860
01FC 0 0032          BSI  DC  /0032      B                   88303870
01FD 0 1239          DC  /1239      SI                   88303880
*
*          ROUTINE NUMBER TWO                             88303890
*
01FE 00 C4000931 INT01 LD  L INLVT+3     GET HEX 2           88303900
0200 00 D4000975 STO  L INM03+7      SET ROUTINE NUMBER  88303910
0202 00 D400098A STO  L INM04+7      IN ERROR MESSAGES  88303920
0204 00 D40009A1 STO  L INM05+7      IN ERROR MESSAGES  88303930

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INTERRUPT FUNCTION TEST

0206 00 D400098B	STO L INM06+7	88304090
	*	88304100
0209 00 440004AA	BSI L LVLST GO SET INTERPT ADRSS	88304110
	*	88304120
020A 00 C4000280	LD L CN101	88304130
020C 00 D4000281	STO L CN102 SET 1ST PASS SWITCH	88304140
020E 00 C4000284	LD L CN105 SET PASS SWITCH	88304150
0210 00 D4000285	STO L PSSW	88304160
	*	88304170
0212 00 C4000282	RT100 LD L CN103	88304180
0214 00 D4000286	STO L ECKSW SE LVL ER CHECK SW.	88304190
0216 00 65800198	LDX I1 LVSAV	88304200
0218 0 7101	MDX 1 1 IX 1 = NO.LEVELS +1	88304210
0219 00 66800198	LDX I2 LVSAV	88304220
021B 0 7201	MDX 2 1 IX 2 = NO.LEVELS +1	88304230
021C 00 67000226	LDX L3 RT101	88304240
021E 00 6F000521	STX L3 LPERR+1 SET LOOP ERR RETURN	88304250
	*	88304260
0220 00 6780018E	LDX I3 LVLIX SET UP INTERRUPT	88304270
0222 00 CF00028C	LDD L3 XIOCC IOCC	88304280
0224 00 DC00028A	STO L ISINT	88304290
	*	88304300
0226 00 C600092E	RT101 LD L2 INLVT SET REQUEST NUMBER	88304310
0228 00 D4000979	STO L INM03+11 IN FRROR MESSAGES	88304320
022A 00 D400098E	STO L INM04+11	88304330
022C 00 D40009A5	STO L INM05+11	88304340
022E 00 D4000A4F	STO L INM14+10	88304350
	*	88304360
0230 0 0859	XIO ISINT ISSUE PROGMD INTRPT	88304370
0231 0 1000	NOP	88304380
0232 00 440006D7	BSI L SERVC PRGM OPERATION PROT SRC	88304390
	*	88304400
0234 00 C4000281	RT109 LD L CN102 REQUEST DID NOT INRP	88304410
0236 0 4818	BSC ←	88304420
0237 0 7003	MDX RT104 NOT 1ST PASS OK	88304430
	*	88304440
	*****	88304450
0238 00 440004F5	BSI L ERROR PRINT REQUEST FAILED SRC	88304460
023A 0 096E	DC INM03 TO INTERRUPT	88304470
	*****	88304480
	*	88304490
	*	88304500
	*	88304510
	RETURN FROM TRAP ROUTINES	88304520
023B 0 71FF	PT104 MDX 1 -1 CK IF ALL LVLS DONE	88304530
023C 0 701C	MDX RT105 NO	88304540
023D 00 74FF0286	MDX L ECKSW,-1 LEVEL ERROR CHECKED	88304550
023F 0 7027	MDX RT106 NO	88304560
0240 00 74FF0285	MDX L PSSW,-1 SKIP IF 500 PASSES	88304570
0242 0 70CF	MDX RT100	88304580
	*	88304590
	*	88304600
	1ST PASS COMPLETE CHECK IF	88304610
	MODE IS RUN WITHOUT STOPS	88304620
0243 00 C4000199	LD L RUNSW GET RUN SWITCH	88304630
0245 00 4C200250	BSC L RT11J,Z RUN WITH OUT STOPS	88304640
	*	88304650
	*	88304660
	RUN NORMAL PROGRAM MODE	88304670
0247 0 C039	LD CN102 GET 1ST PASS SWITCH	88304680
0248 0 4820	BSC Z	88304690
0249 0 702F	MDX RT107 1ST PASS	88304700
	*	88304710
	*	88304720
	ROUTINE ONE COMPLETE	88304730
024A 00 440004A0	BSI L NEST1 SET NEST ADDRESSES	88304740
	*****	88304750
024C 00 44000523	BSI L LOG PRINT TURN DISABLE SRC	88304760

INTERRUPT FUNCTION TEST

024E 0 095E	DC INM02 SWITCH OFF	88304770
	*****	88304780
	*	88304790
024F 0 3005	WT5 WAIT 5 TURN DISABLE SW OFF	88304800
	*	88304810
0250 00 C400018F	RT110 LD L RTNND PREPARE SEQUENCE CK	88304820
0252 0 902F	S CN103	88304830
0253 00 D4000191	STO L SEQCK	88304840
	*	88304850
0255 00 4400048E	BSI L INTST SET SPURIOUS INT ADR	88304860
	*	88304870
0257 00 4C000177	BSC L RTNRT RETURN TO CONTROL	88304880
	*	88304890
0259 0 C030	RT105 LD ISINT GET IOCC ADDRS WD	88304900
025A 0 4808	BSC + CK BIT 0 = 1	88304910
025B 0 7004	MDX RT108 BIT 0 = 1	88304920
025C 0 1001	SLA 1	88304930
025D 0 D02C	STO ISINT	88304940
025E 0 72FF	MDX 2 -1 SET FOR NEXT LVL. NO	88304950
025F 0 70C6	MDX RT101 CONTINUE	88304960
	*	88304970
0260 0 C02A	RT108 LD ISINT+1 CLEAR BIT 15 FROM	88304980
0261 0 901E	S CN101 IOCC CONTROL WORD	88304990
0262 0 D028	STO ISINT+1	88305000
0263 0 C01C	LD CN101 SET BIT 13 IN IOCC	88305010
0264 0 1002	SLA 2 ADDRESS WORD	88305020
0265 0 D024	STO ISINT	88305030
0266 0 70F7	MDX RT105+5	88305040
	*	88305050
0267 0 72FF	RT106 MDX 2 -1	88305060
0268 0 1000	NOP	88305070
0269 00 C600092E	LD L2 INLVT LEVEL ERROR	88305080
026B 00 D4000979	STO L INM03+11 SET IN ERROR	88305090
026D 00 D400098E	STO L INM04+11 *MESSAGES	88305100
026F 00 D40009A5	STO L INM05+11	88305110
0271 00 D4000A4F	STO L INM14+10	88305120
0273 00 67000277	LDX L3 RT106+16	88305130
0275 00 6F000521	STX L3 LPERR+1 LOOP ERROR RETURN	88305140
	*	88305150
0277 0 0100	DC /0100 ILLEGAL OP CODE	88305160
	*	88305170
	*	88305180
	*	88305190
	*	88305200
	ILLEGAL OP CODE DID	88305210
	NOT INTERRUPT	88305220
0278 0 708B	MDX RT109	88305230
	*	88305240
0279 0 1010	RT107 SLA 16	88305250
027A 0 D006	STO CN102 CLEAR 1ST PASS SW.	88305260
	*	88305270
	*****	88305280
027B 00 44000523	BSI L LOG PRINT TURN DISABLE SRC	88305290
027D 0 0949	DC INM01 SWITCH ON	88305300
	*****	88305310
027E 0 3004	WT4 WAIT 4 SET DISABLE SW ON	88305320
	*	88305330
027F 0 708E	MDX RT100-4 GO MAKE 2ND PASS	88305340
	*	88305350
	*	88305360
	ROUTINE TWO CONSTANTS	88305370
0280 0 0001	CN101 DC 1	88305380
0281 0 0000	CN102 DC 0 1ST PASS SWITCH	88305390
0282 0 0002	CN103 DC 2	88305400
0283 0 06D7	CN104 DC SERVC	88305410
0284 0 01F4	CN105 DC /01F4 PASS CONSTANT	88305420
0285 0 0000	PSSW DC 0 PASS SWITCH	88305430
	*	88305440

INTERRUPT FUNCTION TEST

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0286 0 0000      ECKSW DC      0      LEVEL ERR CHECK SW.      88305450
*
0288 00 00000000  DEC      0      88305460
028A 0 0000      ISINT DC      0      PROGRAMED INTERRUPT      88305470
028B 0 0000      DC      0      IOCC      88305480
*
028C 0 0010      XIOCC DC      /0010  12 LEVELS OF INTRPT      88305490
028D 0 04A0      DC      /04A0  88305500
028E 0 1000      DC      /1000  18 LEVELS OF INTRP      88305510
028F 0 04A1      DC      /04A1  88305520
0290 0 0040      DC      /0040  24 LEVELS OF INTRPT      88305530
0291 0 04A1      DC      /04A1  88305540
*
*
*****
*
ROUTINE NUMBER THREE
*****
*
0292 00 C4000932  INTO2 LD  L  INIVT+4  GET HEX 3      88305550
0294 00 D4000A5F  STO  L  INH15+7  SET IN LOG MESSAGE      88305560
*
0296 00 44000488  BSI  L  PRIST    GO SET TRAP ADDRESS SRC      88305570
*
0298 00 6580018E  LDX  I1 LVLIX   SET IOCC FOR LOWEST      88305580
029A 00 C000028C  LDD  L1 XIOCC  INTERRUPT LEVEL      88305590
029C 0 0815      STD  CN200      88305600
*
029D 00 67800198  LDX  I3 LVSVA  NUMBER OF INTERRUPTS      88305610
029F 0 7302      MDX  3 2      TO BE GENERATED      88305620
*
02A0 0 6100      LDX  1 0      PRINT TABLE INDEX      88305630
02A1 0 6200      LDX  2 0      88305640
*
02A2 0 080F      XIO  CN200    ISSUE INTERRUPT      88305650
02A3 0 1000      NOP      88305660
*
*
RETURN FROM TRAP ROUTINES
*
02A4 00 440004E2  BSI  L  PRIPT   GO OUTPUT PRIO. SEQ      88305670
*
02A6 00 4400048E  BSI  L  INTST   SET SPURIOUS INT ADR      88305680
*
02A8 00 C400018F  LD   L  RTI,NO  PREPARE SEQUENCE CK      88305690
02AA 0 9009      S     CN201      88305700
02AB 00 D4000191  STO  L  SEQCK   88305710
*
02AD 00 4C000177  BSC  L  RTNRT   RETURN TO CONTROL      88305720
*
*
ROUTINE THREE CONSTANTS
*
0280 00 00000000  DEC  0      88305730
0282 0 0000      CN200 DC  0      INTERRUPT IOCC      88305740
0283 0 0000      DC  0      88305750
*
0284 0 0003      CN201 DC  3      CONSTANT 3      88305760
*
*****
*
ROUTINE NUMBER FOUR
*****
*
0285 00 C4000933  INTO3 LD  L  INLVT+5  GET HEX 4      88305770
0287 00 D4000975  STO  L  INM03+7  SET RTN NO. IN ERROR      88305780
0289 00 D400098A  STO  L  INM04+7  *MESSAGES      88305790
*
0288 00 440004AA  BSI  L  LVLST   GO SET UP TRAP ADDR      88305800
0280 0 C0C6      LD   CN105     SET PASS SWITCH      88305810
028C 0 D0C6      STO  PSSW     88305820
*

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INTERRUPT FUNCTION TEST

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028F 00 C4000280  RT300 LD  L  CN101  88306130
02C1 0 0057      STO  CN300    SET 1ST PASS SW.      88306140
*
02C2 00 650002D0  LDX  L1 RT301  SET UP LOOP ON      88306150
02C4 00 6D000521  STX  L1 LPERR+1 ERROR RETURN      88306160
*
02C6 0 0859      XIO  MASK0    MASK UPPEP LEVELS      88306170
02C7 0 085A      XIO  MASK1    MASK LOWER LEVELS      88306180
*
02C8 00 6780018E  RT306 LDX  I3 LVLIX  SET UP INITIAL IOCC      88306190
02CA 00 CF00028C  LDD  L3 XIOCC  FOR LOWEST LEVEL      88306200
02CC 0 0851      STD  CN301    88306210
*
02CD 00 67800198  LDX  I3 LVSVA  SET IX FOR NO. OF      88306220
02CF 0 7301      MDX  3 1      INTERRUPTS      88306230
*
02D0 00 C700092E  RT301 LD  L3 INLVT  GET REQUEST NUMBER      88306240
02D2 00 D4000979  STO  L  INM03+11 SET IN ERROR      88306250
02D4 00 D400098E  STO  L  INM04+11 *MESSAGES      88306260
02D6 00 D4000A39  STO  L  INM13+11 88306270
*
02D8 0 0845      RT302 XIO  CN301  ISSUE INTERRUPT      88306280
02D9 0 1000      NOP      88306290
*
02DA 0 C03E      LD   CN300    GET 1ST PASS SWITCH      88306300
02DB 0 4808      BSC  +      88306310
02DC 0 7011      MDX  RT303    NOT 1ST PASS INTR ER 88306320
*
02DD 0 73FF      RT305 MDX  3 -1  CHECK IF ALL LVLS      88306330
02DE 0 701A      MDX  RT304    NO      88306340
02DF 0 C039      LD   CN300    YES      88306350
02E0 0 4820      BSC  Z      88306360
02E1 0 7012      MDX  RT311    1ST PASS COMPL.CNTNU 88306370
02E2 00 74FF0285  MDX  L  PSSW,-1 SKIP IF 500 PASSES      88306380
02E4 0 70DA      MDX  RT300    88306390
*
*
ROUTINE COMPLETED
*
02E5 00 4400048E  BSI  L  INTST   SET SPURIOUS INT ADR      88306400
*
02E7 00 C400018F  LD   L  RTNNO  PREPARE SEQUENCE CK      88306410
02E9 0 9031      S     CN303      88306420
02EA 00 D4000191  STO  L  SEQCK   88306430
*
02EC 00 4C000177  BSC  L  RTNRT   RETURN TO CONTROL      88306440
*
*
REQ DID NOT INTRP MASK OFF
*
02EE 00 440004F5  RT303 BSI  L  ERROR  LOG REQUEST FAILED SRC      88306450
02F0 0 096E      DC  INM03     TO INTERRUPT      88306460
*
*****
*
02F1 00 44000609  BSI  L  SVINT   88306470
02F3 0 70E9      MDX  RT305    88306480
*
*
SET UP FOR 2ND PASS
*
02F4 0 1010      RT311 SLA  16  88306490
02F5 0 D023      STO  CN300    CLEAR 1ST PASS SWITC      88306500
*
02F6 0 082D      XIO  UMSKO    UNMASK UPPER LEVELS      88306510
02F7 0 082E      XIO  UMSK1    UNMASK LOWER LEVELS      88306520
*
02F8 0 70CF      MDX  RT306    GO MAKE SECOND PASS      88306530
*
02F9 0 C024      RT304 LD  CN301  MODIFY IOCC FOR      88306540

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INTERRUPT FUNCTION TEST

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02FA 0 4808      BSC  +      NEXT INTERRUPT      88306810
02FB 0 7003      MDX  RT307      88306820
02FC 0 1001      SLA  1      88306830
02FD 0 D020      STO  CN301     88306840
02FE 0 70D1      MDX  RT301     GO ISSUE NEXT INTRPT 88306850
*
02FF 0 C01F      RT307 LD  CN301+1 CLEAR BIT 1K FROM 88306860
0300 00 94000280 S  L  CN101     CCKMAND WORD      88306870
0302 0 D01C      STO  CN301+1   88306880
0303 0 C016      LD  CN302     SET BIT 13 IN IOCC 88306890
0304 0 D019      STO  CN301     ADDRESS WORD      88306900
0305 0 70CA      MDX  RT301     GO ISSUE NEXT INTRPT 88306920
*
*          RETURN FROM TRAP ROUTINES
*
0306 0 C012      RT308 LD  CN300     GET PASS SWITCH    88306960
0307 0 4820      BSC  Z      SKIP IF 2ND PASS 88306970
0308 0 7C06      MDX  RT309     88306980
0309 00 C40006CA LD  L  CNM00     88306990
030B 0 4820      BSC  Z      88307000
030C 0 7008      MDX  RT310     WRONG LEVEL SERVICED 88307010
030D 00 4C4002DD BOSC L RT305    OK GO ON          88307020
*
030F 00 440004F5 *****
0311 0 0A2E      RT309 BSI L ERROR  REQ INTRPD WHILE  SRC 88307030
          DC  INM13  MASKED          88307040
          *****
          88307050
          88307060
          88307070
          88307080
          88307090
          88307100
          88307110
          88307120
          88307130
          88307140
          88307150
          88307160
          88307170
          88307180
          88307190
          88307200
          88307210
          88307220
          88307230
          88307240
          88307250
          88307260
          88307270
          88307280
          88307290
          88307300
          88307310
          88307320
          88307330
          88307340
          88307350
          88307360
          88307370
          88307380
          88307390
          88307400
          88307410
          88307420
          88307430
          88307440
          88307450
          88307460
          88307470
          88307480
          *****
          *          ROUTINE FOUR CONSTANTS
          *
0319 0 0000      CN300 DC  0      1ST PASS SWITCH
031A 0 0004      CN302 DC /0004
031B 0 0004      CN303 DC 4
*
031C 00 00000J00 DEC  0
031E 0 0000      CN301 DC  0      INTERRUPT IOCC
031F 0 0000      DC  0
*
0320 0 FFFF      MASK0 DC /FFFF   MASK UPPER IOCC
0321 0 0480      DC /0480
0322 0 FFFF      MASK1 DC /FFFF   MASK LOWER IOCC
0323 0 0481      DC /0481
*
0324 0 0000      UMSK0 DC /0000   UNMASK UPPER IOCC
0325 0 0480      DC /0480
0326 0 0000      UMSK1 DC /0000   UNMASK LOWER IOCC
0327 0 0481      DC /0481
*
*****
*          ROUTINE NUMBER FIVE
*****
0328 00 C4000199 INTO4 LD  L  RUNSW  CK. IF RUN NO STOPS
032A 00 4C20040E BSC  L  RT414,Z  BRNCH IF RUN NO STOP
*
032C 00 C4000934 LD  L  INLVT+6  GET HEX 5

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INTERRUPT FUNCTION TEST

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032E 00 D4000975 STO  L  INM03+7  SET IN ERROR MESSAGE 88307490
0330 00 D400098A STO  L  INM04+7  88307500
0332 00 D40009A1 STO  L  INM05+7  88307510
0334 00 D4000A5F STO  L  INM15+7  88307520
0336 0J D400098B STO  L  INM06+7  88307530
*
0338 00 440004AA BSI  L  LVLST    GO SET UP INT. ADDRSS 88307540
*
033A 00 66000438 LDX  L2 RT401    SET TRAP RTNS RETURN 88307550
033C 00 6700034A LOX  L3 RT400    SET LOOP ON ERROR    88307560
033E 0C 6F000521 STX  L3 LPERR+1  RETURN              88307570
0340 00 67000350 LDX  L3 RT402    SET COMN RTN RETURN  88307580
0342 00 C400097E LD  L  INLVT    SET REQUEST NO IN    88307590
0344 00 D4000979 STO  L  INM03+11 *ERROR MESSAGES     88307600
0346 00 D400098E STO  L  INM04+11 88307610
*
0348 0 2C41      DC /2C41     WRITE STORAGE PROTCT 88307620
0349 0 0429      DC  CN401     88307630
*
034A 00 D4000429 RT400 STO  L  CN401 VIOLATE PROT. STORAGE 88307640
*
034C 00 440004F5 *****
034E 0 096E      BSI  L  ERROR  REQUEST FAILED TO  SRC 88307650
          DC  INM03  INTERRUPT          88307660
          *****
          88307670
          88307680
          88307690
          88307700
          88307710
          88307720
          88307730
          88307740
          88307750
          88307760
          88307770
          88307780
          88307790
          88307800
          88307810
          88307820
          88307830
          88307840
          88307850
          88307860
          88307870
          88307880
          88307890
          88307900
          88307910
          88307920
          88307930
          88307940
          88307950
          88307960
          88307970
          88307980
          88307990
          88308000
          88308010
          88308020
          88308030
          88308040
          88308050
          88308060
          88308070
          88308080
          88308090
          88308100
          88308110
          88308120
          88308130
          88308140
          88308150
          88308160
          *****
          *
0356 00 44000523 BSI  L  LOG     WRONG ILSW BIT 88308000
0358 0 0984      DC  INM06     *****
          *****
          *
0359 00 C40006D6 LD  L  ILSAV    ILSW TO A      88308010
*
035B 0 3006      WT6  WAIT  6      ILSW ERROR      88308020
*
035C 00 C4000948 RT413 LD  L  INLVT+26 SET CE REQ IN ERROR 88308030
035E 00 D400098E STO  L  INM04+11  MESSAGES          88308040
0360 00 D4000979 STO  L  INM03+11  88308050
0362 00 6700036C LDX  L3 RT404    SET LOOP ERROR 88308060
0364 00 6F000521 STX  L3 LPERR+1  RETURN          88308070
*
0366 00 67000374 LDX  L3 RT403    SET COMN RTN RETURN 88308080
0368 00 C400042C LD  L  CN404    CE INTERRUPT BRANCH 88308090
036A 00 D4000002 STO  L /0002     *TO ADDRESS      88308100
          *****
          *
036C 00 44000523 RT404 BSI L LOG  PUSH CE INTERRUPT  SRC 88308110
036E 0 09D7      DC  INM08  BUTTON          88308120
          *****
          *
036F 00 440005F3 BSI  L  DELAY   GO WAIT FOR INTRPT 88308130
*
0371 00 440004F5 BSI  L  ERROR  LOG CE REQ FAILED  SRC 88308140
0373 0 096E      DC  INM03  *****
          *****
          *
0374 00 C4000947 RT403 LD  L  INLVT+25 SET TRACE REQUEST 88308150
0376 00 D400098E STO  L  INM04+11  IN ERROR MESSAGES 88308160
0378 00 D4000979 STO  L  INM03+11

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INTERRUPT FUNCTION TEST

037A 00 67000382 LDX L3 RT412 SET LOOP ERROR RETRN 88308170
037C 00 6F000521 STX L3 LPERR+1 88308180
* 88308190
037E 00 6700038A LDX L3 RT405 SET COMN RTN RETURN 88308200
0380 00 74010428 MDX L CN400,1 SET TRACE INDICATOR 88308210
* 88308220
***** 88308230
0382 00 44000523 RT412 BSI L LOG LOG SET TRACE 88308240
0384 0 09F8 DC INM10 88308250
* 88308260
***** 88308270
0385 0 3007 WT7 WAIT 7 SET TRACE MODE 88308280
* 88308290
***** 88308300
0386 0 1000 NOP INT AFTER THIS INSTR 88308310
* 88308320
***** 88308330
0387 00 440004F5 BSI L ERROR TRACE FAILED TO SRC 88308340
0389 0 096E DC INM03 *INTERRUPT 88308350
* 88308360
***** 88308370
038A 00 44000523 RT405 BSI L LOG LOG SET RUN SRC 88308380
038C 0 0A0B DC INM11 88308390
* 88308400
***** 88308410
038D 0 3008 MT8 WAIT 8 SET RUN MODE 88308420
* 88308430
***** 88308440
038E 00 44000609 BSI L SVINT RESET TRACE INTRPT 88308450
0390 0 1010 SLA 16 CLEAR TRACE INDICATR 88308460
0391 00 D4000428 STO L CN400 88308470
0393 00 6600038E LDX L2 WT8+1 SETUP LOOP ON ERRCR 88308480
0395 00 6E000521 STX L2 LPERR+1 *RETURN 88308490
0397 00 660003A4 LDX L2 RT406 SET TRAP RTNS RETURN 88308500
* 88308510
***** 88308520
0399 00 44000523 BSI L LOG LGG PUSH CONSOLE SRC 88308530
039B 0 09E7 DC INM09 INTERRUPT BUTTON 88308540
* 88308550
***** 88308560
039C 00 440005F3 BSI L DELAY GO WAIT FOR INTRPT. 88308570
* 88308580
***** 88308590
039E 00 0C000436 XIO L CNSNS RESET CONSOLE CNTRLS 88308600
* 88308610
***** 88308620
03A0 00 440004F5 BSI L ERROR LOG CONSOLE BUTTON SRC 88308630
03A2 0 0B22 DC INM20 FAILED 88308640
* 88308650
***** 88308660
03A3 0 7020 MDX RT415 BYPASS CONSOLE MESAG 88308670
* 88308680
***** 88308690
03A4 00 44000609 RT406 BSI L SVINT RESET CONSOLE CNTRL 88308700
03A6 00 C4800688 LD I CMTRP GET INTERRUPTING LVL 88308710
03A8 00 D4000B44 STO L INM21+17 SET IN MESSAGE 88308720
03AA 00 C40006D6 LD L ILSAV SET ILSW BIT IN MSSG 88308730
03AC 0 4820 BSC Z SKIP IF NO BIT ON 88308740
03AD 0 7005 MDX *+5 88308750
03AE 00 67000025 LDX L3 /0025 NO ILSW SET MESSAGE 88308760
03B0 00 6F000B4A STX L3 INM21+23 *FOR BIT N 88308770
03B2 0 700E MDX *+14 88308780
03B3 0 630F LDX 3 15 DETERMINE WHICH ILSW 88308790
03B4 0 1340 SLCA 3 *BIT IS ON 88308800
03B5 0 687C STX 3 HOLD COMPLEMENT BITS 12 88308810
03B6 0 C07B LD HOLD *THROUGH 15 88308820
03B7 0 F07B EOR COMP 88308830
03B8 0 D079 STO HOLD 88308840
03B9 00 67800432 LDX I3 HOLD GET CODED EQUIVELANT
03BB 00 C700092F LD L3 INLVT+1 *OF ILSW BIT AND SET
03BD 00 E4000431 AND L CN409 *IN MESSAGE

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03BF 00 D4000B4A STO L INM21+23 88308850
***** 88308860
03C1 00 44000523 BSI L LOG LOG CONSOLE BUTTON SRC 88308870
03C3 0 0B33 DC INM21 INTRPT LEVEL 88308880
* 88308890
***** 88308900
03C4 00 44000488 RT415 BSI L PRIST GO SET XFER VECTORS SRC 88308910
* 88308920
***** 88308930
03C6 00 67800198 LDX I3 LVSAV IX 3 = NO.LVLS.+ 3 88308940
03C8 0 7303 MDX 3 3 88308950
* 88308960
***** 88308970
03C9 0 6100 LDX 1 0 PRINT INDEX 88308980
03CA 0 62C0 LDX 2 0 88308990
* 88309000
***** 88309010
03CB 00 44000523 BSI L LOG LOG SET TRACE MODE SRC 88309020
03CD 0 09F8 DC INM10 88309030
* 88309040
***** 88309050
03CE 0 3009 WT9 WAIT 9 SET TRACE MODE 88309060
03CF 0 1000 NOP INT AFTER THIS INSTR 88309070
* 88309080
***** 88309090
03D0 00 440004E2 RT407 BSI L PRIPT GO LOG PRIORITY SEQ SRC 88309100
* 88309110
***** 88309120
03D2 00 44000523 BSI L LOG LOG SET RUN MODE SRC 88309130
03D4 0 0A0B DC INM11 88309140
* 88309150
***** 88309160
03D5 0 300A WTA WAIT 10 SET RUN MODE 88309170
* 88309180
***** 88309190
03D6 00 44000609 BSI L SVINT RESET TRACE INTRPT 88309200
* 88309210
***** 88309220
03D8 00 44000488 BSI L PRIST GO SET TRAP ADDR 88309230
03DA 0 C052 LD CN405 CE INTERRUPT BRANCH
03DB 00 D4000002 STO L /0002 *TO ADDRESS 88309240
* 88309250
***** 88309260
03DD 00 67800198 LDX I3 LVSAV IX 3 = NO.LEVELS +3 88309270
03DF 0 7303 MDX 3 3 88309280
03E0 0 6100 LDX 1 0 PRINT INDEX 88309290
03E1 0 6200 LDX 2 0 88309300
* 88309310
***** 88309320
03E2 00 44000523 RT409 BSI L LOG LOG PUSH CE BUTTON SRC 88309330
03E4 0 09D7 DC INM08 88309340
* 88309350
***** 88309360
03E5 0 300B MTB WAIT 11 PUSH CE INTRP BUTTON 88309370
* 88309380
***** 88309390
03E6 00 440004E2 RT408 BSI L PRIPT GO LOG PRIORITY SEQ SRC 88309400
* 88309410
***** 88309420
03E8 00 440004AA BSI L LVLST GO SET UP TRAP ADRSS 88309430
* 88309440
***** 88309450
03EA 0 C041 LD CN404 SET CE INTERRUPT 88309460
03EB 00 D4000002 STO L /0002 *BRANCH ADDRESS 88309470
03ED 00 66000417 LDX L2 RT410 SET TRAP RTNS RETURN 88309480
* 88309490
***** 88309500
03EF 00 67000420 LDX L3 RT411 SET LOOP ERR RETURN 88309510
03F1 00 6F000521 STX L3 LPERR+1 88309520

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```

*****
03F3 00 4400523      BSI L LOG      LOG SET DISABLE  SRC
03F5 0 0B4C          DC      INM22    CHECK BUTTONS
*****
03F6 0 300C          WTC  WAIT    12      SET DISABLE CK BTNS
*****
03F7 00 4C4003F9    B0SC L *      RESET IF TRACE ERROR
03F9 00 440004A0    BSI L NEST1   SET NST ADDRESSES
*****
03FB 0  C032          LD      CN406   SET CE INTERRUPT
03FC 00 D4000002    STO L /0002   *BRANCH ADDRESS
03FE 0  C030          LD      CN407   SET CE INTERRUPT
03FF 00 D4000003    STO L /0003   *RETURN ADDRESS
0401 0  C02E          LD      CN408
0402 0C D4000004    STO L /0004
0404 0  2C40          DC      /2C40   CLEAR STORAGE PROTCT
0405 0  0429          DC      CN401
*****
0406 00 44000523    BSI L LOG      SRC
0408 0  0A0B          DC      INM11
*****
0409 0  300D          WTD  WAIT    13      SET RUN MODE
*****
040A 00 44000523    BSI L LOG      TURN DISABLE SW OFF
040C 0  095E          DC      INM02
*****
040D 0  300E          WTE  WAIT    14      TURN DISABLE SW OFF
*****
040E 00 L400018F    RT414 LD L RTNMD  PREPARE SEQUENCE CK
0410 0  F01A          EOR  CN403
0411 00 D4000191    STO L SEQCK
*****
0413 00 4400048E    BSI L INTST   SET SPURIOUS INT ADR
*****
0415 00 4C000177    BSC L RTMRT
*****
                                RETURN IF INTERRUPT
*****
0417 00 0C000436    RT410 XIO L CNSNS  CONSOLE CNTRL RESET
0419 00 L480068B    LD  I CMTRP
041B 00 D40009A5    STO L INM05+11
*****
041D 00 440004F9    BSI L ERALT   LOG DISABLED INTRPT SRC
041F 0  099A          DC      INM05
*****
0420 00 C40009A5    RT411 LD L INM05+11 PICK UP FAILING LVL
0422 00 F40006FE    EOR L REQTR   CHECK IF LEVEL TRACE
0424 00 4C1803F6    BSC L WTC,+-- NONRESET BR IF TRACE
0426 00 4C4003F6    B0SC L WTC    RESET BRANCH
*****
                                ROUTINE FIVE CONSTANTS
*****
0428 0  0000          CN400 DC      0      TRACE INDICATOR
0429 0  0000          CN401 DC      0
042A 0  2000          CN402 DC      /2000  SPV ILSW CK WORD
042B 0  0005          CN403 DC      5
042C 0  0640          CN404 DC      LVL27
042D 0  0702          CN405 DC      PRI27
042E 0  0607          CN406 DC      SERVC  *CE INTERRUPT
042F 0  4C80          CN407 DC      /4C80  *SERVICE CONSTANTS

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```

0430 0 000A          CN408 DC      /000A  *
0431 0 01FF          CN409 DC      /01FF
0432 0 0000          HOLD DC      0      TEMP STORAGE
0433 0 000F          COMP DC      /000F  COMPLEMENT CONSTANT
*****
0434 00 00000000    DEC      0
0436 0 0000          CNSNS DC      /0000  SENSE AREA 0
0437 0 07C1          DC      /07C1  IOCC
*****
                                RTN 4 COMMON INTRPT RTN
*****
0438 0  COEF          RT401 LD      CN400  GET TRACF INDICATOR
0439 0  4820          BSC  Z        SKIP IF OFF
043A 0  7002          MDX  *+2
043B 00 44000609    BSI L SVINT   RESET INTRPT CONTROL
043D 00 C40006CA    LD  L CNM00   GET SERVICED COMPARE
043F 00 4F180000    BSC L3 0,+-- RETURN TO USER IF OK
*****
0441 00 440004F5    BSI L ERROR   WRONG LEVEL SERVICED SRC
0443 0  0983          DC      INM04
*****
0444 0  1010          SLA  16
0445 0  70F9          MDX  RT401+7
*****
0446 00 C4000199    INT05 LD L RUNSW  CK IF RUN NO STOPS
0448 00 4C200481    BSC L RT504,Z RUN BYPASS ROUTINE 5
044A 00 670008F6    LDX  L3 TRACE
044C 00 6F000009    STX  L3 /0009  SET TRACE TRAP ADDR
*****
044E 00 67000930    LDX  L3 INLVT+2
0450 0  6827          STX  3 TRAER+1  HEX PASS NO.
*****
0451 00 6700047F    LDX  L3 RT502
0453 00 6F000521    STX  L3 LPERR+1 SET LOOP ON ERROR
*****
0455 0  630A          LDX  3 10      PASS CONTROL
*****
0456 0  1010          SLA  16
0457 0  0035          STO  TRIND     CLEAR TRACED INDICTR
*****
0458 0  6200          LDX  2 0       EXPECTED INSTRN.INDX
*****
0459 00 44000523    BSI L LCG     LOG SET TRACE  SRC
045B 0  09F8          DC      INM10
*****
045C 0  300F          WTF  WAIT    15      SET TRACE MODE
*****
                                THE 1ST 10 INSTRUCTION
                                ARE CHECKED FOR PROPER
                                SEQUENCE OF INTERRUPTS
*****
045D 0  C02C          RT500 LD      CN500
045E 0  18DF          RFE  31
045F 0  D02B          STO  CN501
0460 0  9029          S    CN500
0461 0  F029          EOR  CN501
0462 0  A028          M    CN501
0463 0  8026          A    CN500
0464 0  4820          BSC  Z

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INTERRUPT FUNCTION TEST

```

0465 0 1000      NOP
0466 0 7000      MDX   RT501
*
0467 0 C025      RT501 LD   TRIND   GET TRACED INDICATOR
0468 00 4C180477 BSC  L  TRAER,+  BRANCH NO INSTR TRCD
046A 00 74010478 MDX  L  TRAER+1,1
046C 0 6200      LDX   2 0
046D 0 1010      SLA   16
046E 0 D01E      STO   TRIND   CLEAR TRACED INDICTR
*
046F 0 73FF      MDX   3 -1    CHECK IF 10 PASSES
0470 0 700E      MDX   RT502  NO
*
*****
0471 00 44000523 BSI  L  LOG      LOG SET RUN MODE   SRC
0473 0 0A0B      DC    INM11
*****
0474 0 3010      WT10 WAIT  16    SET RUN MODE
*
0475 00 4C400481 BOSC L  RT504    RESET BRANCH
*
0477 00 4C000000 TRAER LD  L  0    SET PASS NUMBER IN
0479 00 04000803 STO  L  INM18+23 *ERROR MESSAGE
*
*****
047B 00 440004F5 BSI  L  ERROR    LOG TRACE DID NOT  SRC
047D 0 0AEC      DC    INM18    INTERRUPT
*****
047E 0 70EB      MDX   RT501+3
*
047F 00 4C40045D RT502 BOSC L  RT500    MAKE ANOTHER PASS
*
0481 00 4C00018F RT504 LD  L  RTNND    PREPARE SEQUENCE CK
0483 0 F008      EOR   CN502
0484 00 04000191 STO  L  SEQCK
*
0486 00 4400048E BSI  L  INTST    SET SPURIOUS INT ADR
*
0488 00 4C000177 BSC  L  RTNRT
*
*
ROUTINE SIX CONSTANTS
*
048A 0 0001      CN500 DC  1
048B 0 000U      CN501 DC  0
048C 0 0006      CN502 DC  6
048D 0 0000      TRIND DC  0
*
*
ROUTINE TO LOAD SPURIOUS
INTERRUPT TRAP ADDRESSES
*
048E 0 0000      INTST DC  0
048F 00 0C000320 XIO  L  MASKO    MASK INTERRUPTS   SE
0491 00 0C000322 XIO  L  MASK1
0493 0 631B      LDX   3 27
0494 0 C00A      LD    INCN      ADDRESS SVINT
0495 00 07000007 STO  L3 /0007   SET ADDRESS SVINT
0497 0 73FF      MDX   3 -1    *INTO ALL INTERRUPT
0498 0 70FC      MDX   *-4    *LOCATIONS
0499 00 0C000324 XIO  L  UMSK0    UNMASK INTERRUPTS
049B 00 0C000326 XIO  L  UMSK1
049D 00 4C80048E BSC  I  INTST    RETURN TO USER   SX
*
049F 0 0609      INCN DC  SVINT   TRAP ROUTINE ADDRES
*
*
SET TRAP ADDRESSES TO

```

INTERRUPT FUNCTION TEST

```

*
*
*
SERVICE NESTED INTERRUPTS 88311570
WHILE DISABLED            88311580
*                           88311590
*
*
04A0 0 0000      NEST1 DC  0
04A1 0 621B      LDX   2 27    SET INDEX
04A2 0 C006      LD    NSTCN   ADDRESS SERVC
04A3 00 D6000007 STO  L2 7    SET ADDR IN XFER LC
04A5 0 72FF      MDX   2 -1    SKIP IF DONE
04A6 0 70FC      MDX   *-4    BR TO DO NEXT VECTOR
04A7 00 4C8004A0 BSC  I  NEST1   EXIT SUBROUTINE   SX 88311660
*
*
04A9 0 06D7      NSTCN DC  SERVC
*
*
*
TRAP ADDRESS SETUP
*
*
04AA 0 0000      LVLST DC  0
04AB 0 6300      LDX   3 0
04AC 0 621B      LDX   2 27
04AD 0 C008      LD    LVL51    = LVL01
04AE 00 07000008 LVST1 STO L3 /0008
04B0 0 8006      A    LVL52    ADD 3 FOR NEXT ADDR
04B1 0 7301      MDX   3 1
04B2 0 72FF      MDX   2 -1
04B3 0 70FA      MDX   LVST1
04B4 00 4C8004AA BSC  I  LVLST   RETURN           SX 88311820
*
*
04B6 0 063A      LVL51 DC  LVL01  1ST TRAP RTN.ADDRESS
04B7 0 0003      LVL52 DC  3
*
*
*
PRIORITY TRAP ADDRESS AND 88311870
PRINT TABLE SETUP        88311880
*
*
*
PRIST DC  0
04B8 0 0000      LD    PRIST
04B9 0 6300      LDX   3 0    SET INDEX
04BA 0 621A      LDX   2 26    SET INDEX
04BB 0 C020      LD    CNSTO   PICKUP STARTING ADRS
04BC 00 07000009 SET01 STO L3 9    SET IN XFER VECTOR
04BE 0 7301      MDX   3 1    ADD 1 TO STORE IX
04BF 0 801D      A    CNST1    ADD 20 FOR NEXT ADRS
04C0 0 72FF      MDX   2 -1    SKIP WHEN DONE
04C1 0 70FA      MDX   SET01   GO LOAD NEXT VECTOR
04C2 00 670006E0 LD   L3 PRI01  LOAD INTERNAL INTRPT
04C4 00 6F000008 STX  L3 8    *XFER VECTOR
*
*
*
SET UP PRIORITY SEQUENCE 88312030
PRINT TABLE              88312040
*
*
*
04C6 0 6334      LDX   3 52
04C7 0 C818      LDD   CNST2
04C8 00 DF000A76 SET02 STD L3 IN16V-2  REQUEST SEQUENCE MSG
04CA 00 DF000A86 STD L3 IN17V-2  SERVICD SEQUENCE MSG
04CC 0 73FE      MDX   3 -2
04CD 0 70FA      MDX   SET02
04CE 00 04000AAB STO  L  IN16V+51 SET TERMINATOR AT
04D0 00 04000AEB STO  L  IN17V+51 END OF MESSAGE TABLE
*
*
*
SET IOCC FOR LOWEST LEVEL 88312150
*
*
*
04D2 00 6580018E LDX  I1 LVLIX
04D4 00 CD00028C LDD  L1 XIOCC  GET COMMAND FROM TBL
04D6 00 DC000700 STD  L  PR262   SET IN TR AND CE
04D8 00 DC000714 STD  L  PR272   TRAP ROUTINES
04DA 00 4C800488 BSC  I  PRIST   EXIT           SX 88312210
*
*
*
SETUP CONSTANTS          88312220
*
*

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INTERRUPT FUNCTION TEST

```

04DC 0 06EE      CNST0 DC    PRI26   LOWEST LEVEL ADDRESS  88312250
04DD 0 0014      CNST1 DC    20                               88312260
*                                                         88312270
04DE 00 00000000  *          DEC    0                               88312280
04E0 0 FFFF      CNST2 DC    /FFFF   TERMINATOR           88312290
04E1 0 0000      DC        /0000   BLANK                 88312300
*                                                         88312310
*                                                         88312320
*          PRIORITY SEQUENCE LOG                          88312330
*                                                         88312340
04E2 0 0000      PRIPT DC    0          SE                88312350
*                                                         88312360
04E3 00 0C00018A *          XIO L BSWO   CHECK IF BYPASS           88312370
04E5 00 C4000192 *          LD  L BSWOO  *PRIORITY PRINTOUT          88312380
04E7 0 1007      SLA        7                               88312390
04E8 0 4828      BSC        +Z                              88312400
04E9 0 7009      MDX        PRIXT                             88312410
*                                                         88312420
*          LOG PRIORITY HEADING                            88312430
*          *****                                       88312440
04EA 00 44000523 *          BSI L LOG          SRC                88312450
04EC 0 0A58      DC        INM15                          88312460
*                                                         88312470
*          OUTPUT REQUEST SEQUENCE                        88312480
*          *****                                       88312490
04ED 00 44000523 *          BSI L LOG          SRC                88312500
04EF 0 0A6C      DC        INM16                          88312510
*                                                         88312520
*          OUTPUT SERVICED SEQUENCE                       88312530
*          *****                                       88312540
04F0 00 44000523 *          BSI L LOG          SRC                88312550
04F2 0 0AAC      DC        INM17                          88312560
*                                                         88312570
*          *****                                       88312580
04F3 00 4C8004E2 *          PRIXT BSC I PRIPT  EXIT                88312590
*          *****                                       88312600
*          *****                                       88312610
*          *****                                       88312620
*          *****                                       88312630
*          *****                                       88312640
*          *****                                       88312650
04F5 0 0000      ERROR DC    0          SE                88312660
04F6 00 44000609 *          BSI L SVINT   RESET POSSIBLE DSW  SRC      88312670
04F8 0 7003      MDX        ERALT+3  SKIP ALTERNATE ENTRY  SRC      88312680
04F9 0 0000      ERALT DC    0          ALTERNATE TRACE ENTR  88312690
04FA 0 COFE      LD        ERALT   STORE ALTERNATE ENTR  88312700
04FB 0 D0F9      STO        ERROR   *I CTR IN NORMAL ENT  88312710
04FC 00 C48004F5 *          LD  I ERROR   SET MESSAGE ADDRESS  88312720
04FE 0 D00A      STO        ERRO1+1  IN LOG CALL          88312730
*                                                         88312740
04FF 00 74010522 *          MDX L ERRID,1  SET ERR CALL INDOCTOR  88312750
*                                                         88312760
0501 00 0C00018A *          XIO L BSWO   CHECK IF BYPASS           88312770
0503 00 C4000192 *          LD  L BSWOO  *ERROR PRINT REQSTD          88312780
0505 0 1802      SRA        2                               88312790
0506 0 4804      BSC        E                               88312800
0507 0 7002      MDX        ERRO2                             88312810
*                                                         88312820
*          *****                                       88312830
0508 0 401A      ERRO1 BSI LOG          GO PRINT ERROR  SRC      88312840
0509 0 0000      DC        0                               88312850
*          *****                                       88312860
050A 0 1010      ERRO2 SLA 16          CLEAR ERROR CALL  88312870
050B 0 D016      STO        ERRID   INDICATOR          88312880
*                                                         88312890
050C 00 0C00018A *          XIO L BSWO   CHECK IF HALT ON          88312900
050E 00 C4000192 *          LD  L BSWOO  *ERROR REQUESTED          88312910
*                                                         88312920

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INTERRUPT FUNCTION TEST

```

0510 0 1801      SRA        1                               88312930
0511 0 4804      BSC        E                               88312940
0512 0 700B      MDX        WT11   HALT ON ERROR BRANCH  88312950
*                                                         88312960
0513 00 0C00018A *          ERR03 XIO L BSWO   HECK IF LOOP ON          88312970
0515 00 C4000192 *          LD  L BSWOO  *ERROR REQUESTED          88312980
0517 0 1803      SRA        3                               88312990
0518 0 4804      BSC        E                               88313000
0519 0 7006      MDX        LPERR   LOOP ERROR          88313010
*                                                         88313020
051A 00 740104F5 *          MDX L ERROR,1  ADD 1 TO RETURN          88313030
051C 00 4C8004F5 *          BSC I ERROR   RETURN TO USER          SX      88313040
*                                                         88313050
*          *****                                       88313060
*          *****                                       88313070
*          *****                                       88313080
051E 0 3011      WT11 WAIT 17          HALT ON ERROR REQ.  88313090
051F 0 70F3      MDX        ERRO3                             88313100
*                                                         88313110
*          *****                                       88313120
*          *****                                       88313130
0520 00 4C000000 *          LPERR BSC L 0                               88313140
*                                                         88313150
0522 0 0000      *          ERR ID DC    0          ERROR CALL INDICATOR  88313160
*          *****                                       88313170
*          *****                                       88313180
*          *****                                       88313190
*          *****                                       88313200
0523 0 0000      *          LOG DC    0          SE                88313210
*                                                         88313220
0524 0 681D      *          LOG01 STX 3 LOG06+1  SAVE Ix 3          88313230
0525 00 0C000320 *          XIO L MASKO   MASK INTERRUPTS          88313240
0527 00 0C000322 *          XIO L MASK1                             88313250
*                                                         88313260
0529 00 C400019A *          LD  L OPIND   CK OUTPUT DEVICE          88313270
052B 00 4C180548 *          BSC L TWRTR,+  BRANCH IF TYPEWRITER  88313280
*                                                         88313290
052D 00 C4800523 *          LD  I LOG      GET MESSAGE ADDRESS          88313300
052F 0 D058      STO        PRWRT   SET IN IOCC          88313310
*                                                         88313320
0530 0 0853      *          LOG02 XIO PRSNS  CHECK PRINTER READY  88313330
0531 00 4C040537 *          BSC L WT12,E  BRANCH IF NOT READY  88313340
0533 0 1801      SRA        1                               88313350
0534 00 4C040539 *          BSC L WT13,E  BRANCH IF BUSY          88313360
0536 0 7004      MDX        LOG05  READY AND NOT BUSY  88313370
*                                                         88313380
0537 0 3012      *          WT12 WAIT 18          1443 NOT READY  88313390
0538 0 70F7      MDX        LOG02  CHECK AGAIN          88313400
*                                                         88313410
0539 0 3013      *          WT13 WAIT 19          1443 BUSY          88313420
053A 0 70F5      MDX        LOG02  CHECK AGAIN          88313430
*                                                         88313440
053B 0 084C      *          LOG05 XIO PRWRT   OUTPUT MESSAGE  88313450
*                                                         88313460
053C 0 0849      *          XIO PRSN   CHECK FOR OP COMPLT  88313470
053D 0 1002      SLA        2                               88313480
053E 0 4810      BSC        -                               88313490
053F 0 70FC      MDX        *-4          RESET DSW          88313500
0540 0 0843      *          XIO PRSNS  88313510
*                                                         88313520
*          *****                                       88313530
*          *****                                       88313540
0541 00 67000300 *          LOG06 LDX L3 0          RESTORE Ix 3          88313550
0543 00 0C000324 *          XIO L UMASKO  UNMASK INTERRUPTS          88313560
0545 00 0C000326 *          XIO L UMASK1                             88313570
0547 00 74010523 *          ADX L LOG,1   BUMP RETURN          88313580
*                                                         88313590
0549 00 4C800523 *          BSC I LOG      RETURN TO USER          SX      88313600
*                                                         88313600

```

INTERRUPT FUNCTION TEST

```

054B 0 1010      TWRTR SLA 16      88313610
054C 0 0032      STO      WRDSM      88313620
054D 0 083C      XIO      TWSNS      CHECK IF TYPEWRITER
054E 0 1005      SLA      5        READY      88313630
054F 0 180F      SRA      15        88313640
0550 00 4C180554 BSC L TWR01,+ 88313650
*                88313660
*                88313670
0552 0 3014      WT14  WAIT 20      1816/1053 NOT READY 88313680
0553 0 70F9      MDX      TWRTR+2 88313690
*                88313700
0554 0 C028      TWR01 LD  TWRTO  CARRAIGE RETURN AND
0555 0 D02A      STO      IOARA  LINE SPACE TO IO ARA 88313710
*                88313720
*                88313730
0556 0 0835      XIO      TWRTR  CARG RETURN/LINE SP 88313740
*                88313750
*                88313760
0557 0 0332      XIO      TWSNS  HANG TILL NOT BUSY 88313770
0558 0 180B      SRA      11      88313780
0559 0 4804      BSC      E        88313790
055A 0 70FC      MDX      *-4     88313800
*                88313810
055B 0 6301      LDX      3 1     BYPASS 1443 WORD COUNT 88313820
055C 00 C480052D LD I LOG  GET MESSAGE ADDRESS 88313830
055E 0 D001      STO      TWR02+1 88313840
*                88313850
055F 00 C7000000 TWR02 LD L3 0     GET WORD TO PRINT 88313860
0561 00 D40005BE STO L CODWD  SFT IN CONVERSION RT 88313870
0563 0 F01A      EOR      TWR1   CHECK IF TERMINATOR 88313880
0564 00 4C180541 BSC L LOG06,+  BRANCH IF TERMINATOR 88313890
*                88313900
*****
0566 00 4400058E BSI L CODCV  GO CONVERT 43 TO TW SRC 88313910
*****
*                88313920
*                88313930
0568 00 C40005BE LD L CODWD  88313940
056A 0 D015      STO      IOARA  88313950
*                88313960
*                88313970
*                88313980
056B 0 0820      XIOWR XIO  TWRTR  WRITE CHARACTER 88313990
*                88314000
*                88314010
056C 0 081D      XIOSN XIO  TWSNS  HANG ON BUSY 88314020
056D 0 180B      SRA      11      88314030
056E 0 4804      BSC      E        88314040
056F 0 70FC      MDX      XIOSN  BUSY 88314050
*                88314060
*                88314070
*                88314080
0570 0 C00E      LD      WRDSW  GET 1/2 WORD SWITCH 88314090
0571 0 4804      BSC      E        88314100
0572 0 7006      MDX      TWR03  GO SET UP NEXT WORD 88314110
*                88314120
*                88314130
*                88314140
0573 0 C00C      LD      IOARA  88314150
0574 0 1008      SLA      8        POSITION 2ND 1/2 WD 88314160
0575 0 D00A      STO      IOARA  88314170
0576 00 7401057F MDX L WRDSW,1  BUMP WORD SWITCH 88314180
0578 0 70F2      MDX      XIOWR  GO WRITC 2ND 1/2 WD 88314190
*                88314200
*                88314210
*                88314220
0579 0 7301      TWR03 MDX 3 1     NEXT WORD INDEX 88314230
057A 00 7401057F MDX L WRDSW,1  BUMP WORD SWITCH 88314240
057C 0 70E2      MDX      TWR02  GO GET NEXT WORD 88314250
*                88314260
*                88314270
*                88314280
057D 0 8103      TWRTO DC  /8103  LINE SP/CARRAIGE RTN 88314280

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057E 0 FFFF      TWR1 DC  /FFFF  TERMINATOR 88314290
057F 0 0000      WRDSW DC  0      1/2 WORD SWITCH 88314300
0580 0 0000      IOARA DC  0      OUTPUT AREA 88314310
*                88314320
*                88314330
0582 00 00000000 * DEC 0 88314340
*                88314350
0584 0 0000      PRSNS DC  /0000  PRINTER SENSE IOCC 88314360
0585 0 3701      DC      /3701  88314370
0586 0 0000      PRSN DC  0      NON RESET SENSE 88314380
0587 0 3700      DC      /3700  88314390
0588 0 0000      PRWRT DC /0000  PRINTER WRITE IOCC 88314400
0589 0 3500      DC      /3500  88314410
058A 0 0000      TWSNS DC /0000  TYPEWTR SENSE IOCC 88314420
058B 0 0F03      DC      /0F03  88314430
058C 0 0580      TWRTR DC IOARA  TYPEWTR WRITE IOCC 88314440
058D 0 0902      DC      /0902  88314450
*                88314460
*****
*                1443 CODE TO 1816/1053 * 88314470
*                CODE CONVERSION ROUTINE * 88314480
*****
*                88314490
*                88314500
058E 0 0000      CODCV DC  0      SAVE INDEX REGS SE 88314510
058F 0 6927      STX      1 CODC4+1 88314520
0590 0 6A28      STX      2 CODC4+3 88314530
0591 0 6829      STX      3 CODC4+5 88314540
*                88314550
0592 0 1010      SLA      16     CLEAR LEFT HALF WORD 88314560
0593 0 D02B      STO      LHIND  *INDICATOR 88314570
0594 0 6300      LDX      3 0     88314580
*                88314590
*                88314600
0595 0 C028      CODC1 LD  CODWD  GFT WORD TO CONVERT 88314610
0596 0 1890      SRT      16     SET IN Q 88314620
0597 0 C027      LD      LHIND  88314630
0598 0 4820      BSC      Z      SKIP IF LEFT HALF 88314640
0599 0 1088      SLT      8      POSITION RIGHT HALF 88314650
*                88314660
*                88314670
059A 0 1010      SLA      16     ZONE TO ACCUM 88314680
059B 0 1084      SLT      4      88314690
059C 0 D023      STO      COD00  IX 1 = ZONE 88314700
059D 00 658005C0 LDX 11 COD00  88314710
*                88314720
059F 0 1010      SLA      16     DIGIT TO ACCUM 88314730
05A0 0 1084      SLT      4      88314740
05A1 0 D01E      STO      COD00  IX 2 = DIGIT 88314750
05A2 00 668005C0 LDX 12 COD00  88314760
*                88314770
05A4 00 C50005C3 LD L1 ZONE  GET ZONE TABLE ADDR 88314780
05A6 0 D001      STO      CODC2+1 SET IN CONVERSION WU 88314790
*                88314800
05A7 00 C6000000 CODC2 LD L2 0  GET CONVERTED CODE 88314810
05A9 00 D70005C1 STO L3 COD01  88314820
*                88314830
05AB 0 C013      LD      LHIND  BRNCH IF RIGHT HALF 88314840
05AC 00 4C200582 BSC L CODC3,Z  88314850
05AE 00 7401058F MDX L LHIND,1  88314860
05B0 0 7301      MDX      3 1     GO CONVERT RIGHT HLF 88314870
05B1 0 70E3      MDX      CODC1  88314880
*                88314890
05B2 0 C00E      CODC3 LD  COD01  PACK CONVERTED CODES 88314900
05B3 0 1008      SLA      8      88314910
05B4 0 E80D      OR      COD02  88314920
05B5 0 D008      STO      CODWD  88314930
*                88314940
05B6 00 65000000 CODC4 LDX L1 0  RESTORE INDEX REGS 88314950
05B8 00 66000000 LDX L2 0  88314960
05BA 00 67000000 LDX L3 0  88314960

```

INTERRUPT FUNCTION TEST

```

058C 00 4C80058E      BSC  I  CODCV      RETURN TO USER      SX  88314970
*
*
*
058E 0  0000      CODWD DC      0      WORD LOCATION      88314980
058F 0  0000      LHIND DC      0      LEFT HALF INDICATOR  88314990
05C0 0  0000      COD00 DC      0      WORK AREA           88315000
05C1 0  0000      COD01 DC      0      CONVERTED LH CHARACT 88315010
05C2 0  0000      COD02 DC      0      CONVERTED RH CHARACT 88315020
*
*
*
                                1443 TO 1816/1053 CODE
                                CONVERSION TABLES
*
05C3 0  05C7      ZONE DC      ZONEN  NO ZONE      88315120
05C4 0  05D2      DC      ZONE1  0 ZONE      88315130
05C5 0  05DD      DC      ZONE2  11 ZONE     88315140
05C6 0  05E7      DC      ZONE3  12 ZONE     88315150
*
05C7 0  0021      ZONEN DC      /0021  SPACE      88315170
05C8 0  00FC      DC      /00FC  1           88315180
05C9 0  00D8      DC      /00D8  2           88315190
05CA 0  00DC      DC      /00DC  3           88315200
05CB 0  00F0      DC      /00F0  4           88315210
05CC 0  00F4      DC      /00F4  5           88315220
05CD 0  00D0      DC      /00D0  6           88315230
05CE 0  00D4      DC      /00D4  7           88315240
05CF 0  00E4      DC      /00E4  8           88315250
05D0 0  00E0      DC      /00E0  9           88315260
05D1 0  00C4      UC      /00C4  0           88315270
05D2 0  0000      ZONE1 DC      0           88315280
05D3 0  0000      DC      0           88315290
05D4 0  009A      DC      /009A  S           88315300
05D5 0  009E      DC      /009E  T           88315310
05D6 0  0082      DC      /0082  U           88315320
05D7 0  0086      DC      /0086  V           88315330
05D8 0  0092      DC      /0092  W           88315340
05D9 0  0096      DC      /0096  X           88315350
05DA 0  00A6      DC      /00A6  Y           88315360
05DB 0  00A2      DC      /00A2  Z           88315370
05DC 0  0021      DC      /0021  SPACE      88315380
05DD 0  0000      ZONE2 DC      0           88315390
05DE 0  007E      DC      /007E  J           88315400
05DF 0  005A      DC      /005A  K           88315410
05E0 0  005E      DC      /005E  L           88315420
05E1 0  0072      DC      /0072  M           88315430
05E2 0  0076      DC      /0076  N           88315440
05E3 0  0052      DC      /0052  O           88315450
05E4 0  0056      DC      /0056  P           88315460
05E5 0  0066      DC      /0066  Q           88315470
05E6 0  0062      DC      /0062  R           88315480
05E7 0  0000      ZONE3 DC      0           88315490
05E8 0  003E      DC      /003E  A           88315500
05E9 0  001A      DC      /001A  B           88315510
05EA 0  001E      DC      /001E  C           88315520
05EB 0  0032      DC      /0032  D           88315530
05EC 0  0036      DC      /0036  E           88315540
05ED 0  0012      DC      /0012  F           88315550
05EE 0  0016      DC      /0016  G           88315560
05EF 0  0026      DC      /0026  H           88315570
05F0 0  0022      DC      /0022  I           88315580
05F1 0  0086      DC      /0086  O ERROR    88315590
05F2 0  0000      DC      /0000  PERIOD     88315600
*
*
*
                                1 MINUTE DELAY
*

```

INTERRUPT FUNCTION TEST

```

05F3 0  0000      DELAY DC      0      SE      88315650
05F4 00 65000300    LDX L1 /0300    SET DELAY INDEX      88315660
05F6 0  C008      LD      DELY1    PICKUP DELAY CONSTAT 88315670
05F7 0  D008      STO      DELY2    SET IN DELAY SWITCH  88315680
05F8 00 74FF0600    MDX L DELY2,-1  SKIP WHEN SW = 0     88315690
05FA 0  70FD      MDX      DELAY+5  BRN TO DECREMENT SW  88315700
05FB 0  71FF      MDX      1 -1    SKIP WHEN DLY IX = 0  88315710
05FC 0  70F9      MDX      DELAY+3  BR TO RELOAD DLY SW  88315720
05FD 00 4C8005F3    BSC I DELAY     EXIT SUBROUTINE     SX  88315730
*
*
05FF 0  8D3F      DELY1 DC      /8D3F  SE      88315740
0600 0  0000      DELY2 DC      0      SE      88315750
*
*
*
                                ROUTINE 1 TRAP ROUTINE
*
0601 0  0000      POLL DC      0      ENTRY      IE      88315800
0602 00 0C0006D4    XIO L ILSW     INSURE NO ILSW ON    88315810
0604 0  C0FC      LD      POLL    88315820
0605 00 D40001F6    STO L ICTR     SAVE I COUNT AT INTP 88315830
0607 00 4C400000    PLEXT BOSC L  0    RETURN TO MAIN LINE  IX  88315840
*
*
*
                                ROUTINE TO SERVICE NON
                                PROGRAM GENERATED INTERPT
*
0609 0  0000      SVINT DC      0      SE      88315900
060A 0  D02D      STO      SVIO    SAVE ACCUMULATOR    88315910
060B 00 0C0006D4    XIO L ILSW     RESET ILSW           88315920
060D 00 74020637    MDX L SV7,2    SET PASS SWITCH      88315930
060F 0  1010      SLA      16     88315940
0610 0  D023      STO      SV4    CLEAR AREA CODE CNTR 88315950
0611 0  C020      LD      SV2     SET IOCC IN USE SW   88315960
0612 0  D023      STO      SV6    88315970
0613 0  C01D      SVINO LD      SV1  SET MODIFIER COUNTER 88315980
0614 0  D020      STO      SV5    88315990
0615 0  C01E      SVINI LD      SV4 *      88316000
0616 0  1008      SLA      11    *      88316010
0617 0  E81D      OR      SV5    *BUILD IOCC         88316020
0618 0  E81D      OR      SV6    *      88316030
0619 0  D01F      STO      SVIO+1 *      CB316040
061A 0  081D      XIO      SVIO   SENSE DSW AND RESET  88316050
061B 00 74FF0635    MDX L SV5,-1   BRANCH IF NOT ALL MD 88316060
061D 0  70F7      MDX      SVINI  INCREMENT AREA CODE  88316070
061E 00 74010634    MDX L SV4,+1   88316080
0620 0  C013      LD      SV4     88316090
0621 0  900E      S      SVO     CHECK IF ALL AC USED  88316100
0622 0  4808      BSC      +      SKIP IF ALL AC USED  88316110
0623 0  70FF      MDX      SVINO  GO SENSE WITH NXT AC 88316120
0624 00 74FF0637    MDX L SV7,-1   SKIP IF SECOND PASS  88316130
0626 0  7001      MDX      +1    88316140
0627 0  7005      MDX      SVEXT-1  88316150
0628 0  C00A      LD      SV3    SET IOCC FOR PI      88316160
0629 0  D00C      STO      SV6    88316170
062A 0  1010      SLA      16     88316180
062B 0  D008      STO      SV4    SET AC FOR NEXT      88316190
062C 0  70E6      MDX      SVINO *PASS           88316200
062D 0  C00A      LD      SVIO   RESTORE ACCUMULATOR  88316210
062E 00 4CC00609    SVEXT BOSC I  SVINT EXIT      IX      88316220
*
*
*
** CONSTANTS **
*
0630 0  001F      SVO DC      /001F  NUMBER OF AREA CODES 88316230
0631 0  00FF      SV1 DC      /00FF  NUMBER OF MODIFIERS  88316240
0632 0  0701      SV2 DC      /0701  SENSE/RESET DSW     88316250
0633 0  0700      SV3 DC      /0700  SENSE/RESET PISW    88316260
0634 0  0000      SV4 DC      0    AREA CODE INDICATOR 88316270
0635 0  0000      SV5 DC      0    MODIFIER INDICATOR  88316280
0636 0  0000      SV6 DC      0    IOCC IN USE         88316300

```

INTERRUPT FUNCTION TEST

```

0637 0 0000 SV7 DC 0 PASS SWITCH 88316330
0638 0 0000 BSS E 0 88316340
0638 0 0000 SV10 DC 0 SENSE DSW IOCC 88316350
0639 0 0000 DC 0 88316360
* 88316370
* INTERRUPT TRAP ROUTINES 88316380
* 88316390
* 88316400
* INTERRUPT LEVEL ERROR 88316410
* PRIORITY 1 88316420
* 88316430
* 88316440
063A 0 0000 LVLO1 DC 0 GO TO COMN TRAP RTN. SRC 88316450
063B 0 404F BSI CMTRP ER 88316460
063C 0 3529 DC /3529 88316470
* 88316480
* INTERRUPT LEVEL TRACE 88316490
* PRIORITY 26 88316500
* 88316510
* 88316520
063D 0 0000 LVL26 DC 0 GO TO COMN TRAP RTN. SRC 88316530
063E 0 404C BSI CMTRP TR 88316540
063F 0 1329 DC /1329 88316550
* 88316560
* INTERRUPT LEVEL CE 88316570
* PRIORITY 27 88316580
* 88316590
* 88316600
064J 0 0000 LVL27 DC 0 GO TO COMN TRAP RTN. SRC 88316610
064I 0 4049 BSI CMTRP CE 88316620
0642 0 3335 DC /3335 88316630
* 88316640
* INTERRUPT LEVEL 0 88316650
* PRIORITY 2 88316660
* 88316670
* 88316680
0643 0 0000 LVL02 DC 0 GO TO COMN TRAP RTN. SRC 88316690
0644 0 4046 BSI CMTRP 00 88316700
0645 0 0A0A DC /0A0A 88316710
* 88316720
* INTERRUPT LEVEL 1 88316730
* PRIORITY 3 88316740
* 88316750
* 88316760
0646 0 0000 LVL03 DC 0 GO TO COMN TRAP RTN. SRC 88316770
0647 0 4043 BSI CMTRP 01 88316780
0648 0 0A01 DC /0A01 88316790
* 88316800
* INTERRUPT LEVEL 2 88316810
* PRIORITY 4 88316820
* 88316830
* 88316840
0649 0 0000 LVL04 DC 0 GO TO COMN TRAP RTN. SRC 88316850
064A 0 4040 BSI CMTRP 02 88316860
064B 0 0A02 DC /0A02 88316870
* 88316880
* INTERRUPT LEVEL 3 88316890
* PRIORITY 5 88316900
* 88316910
* 88316920
064C 0 0000 LVL05 DC 0 GO TO COMN TRAP RTN. SRC 88316930
064D 0 403D BSI CMTRP 03 88316940
064E 0 0A03 DC /0A03 88316950
* 88316960
* INTERRUPT LEVEL 4 88316970
* PRIORITY 6 88316980
* 88316990
* 88317000
064F 0 0000 LVL06 DC 0

```

INTERRUPT FUNCTION TEST

```

0650 0 403A BSI CMTRP GO TO COMN TRAP RTN. SRC 88317010
0651 0 0A04 DC /0A04 04 88317020
* 88317030
* INTERRUPT LEVEL 5 88317040
* PRIORITY 7 88317050
* 88317060
* 88317070
* 88317080
0652 0 0000 LVL07 DC 0 GO TO COMN TRAP RTN. SRC 88317090
0653 0 4037 BSI CMTRP 05 88317100
0654 0 0A05 DC /0A05 88317110
* 88317120
* INTERRUPT LEVEL 6 88317130
* PRIORITY 8 88317140
* 88317150
* 88317160
0655 0 0000 LVL08 DC 0 GO TO COMN TRAP RTN. SRC 88317170
0656 0 4034 BSI CMTRP 06 88317180
0657 0 0A06 DC /0A06 88317190
* 88317200
* INTERRUPT LEVEL 7 88317210
* PRIORITY 9 88317220
* 88317230
* 88317240
0658 0 0000 LVL09 DC 0 GO TO COMN TRAP RTN. SRC 88317250
0659 0 4031 BSI CMTRP 07 88317260
065A 0 0A07 DC /0A07 88317270
* 88317280
* INTERRUPT LEVEL 8 88317290
* PRIORITY 10 88317300
* 88317310
* 88317320
065B 0 0000 LVL10 DC 0 GO TO COMN TRAP RTN. SRC 88317330
065C 0 402E BSI CMTRP 08 88317340
065D 0 0A08 DC /0A08 88317350
* 88317360
* INTERRUPT LEVEL 9 88317370
* PRIORITY 11 88317380
* 88317390
* 88317400
065E 0 0000 LVL11 DC 0 GO TO COMN TRAP RTN. SRC 88317410
065F 0 4028 BSI CMTRP 09 88317420
0660 0 0A09 DC /0A09 88317430
* 88317440
* INTERRUPT LEVEL 10 88317450
* PRIORITY 12 88317460
* 88317470
* 88317480
0661 0 0000 LVL12 DC 0 GO TO COMN TRAP RTN. SRC 88317490
0662 0 4028 BSI CMTRP 10 88317500
0663 0 010A DC /010A 88317510
* 88317520
* INTERRUPT LEVEL 11 88317530
* PRIORITY 13 88317540
* 88317550
* 88317560
0664 0 0000 LVL13 DC 0 GO TO COMN TRAP RTN. SRC 88317570
0665 0 4025 BSI CMTRP 11 88317580
0666 0 0101 DC /0101 88317590
* 88317600
* INTERRUPT LEVEL 12 88317610
* PRIORITY 14 88317620
* 88317630
* 88317640
0667 0 0000 LVL14 DC 0 GO TO COMN TRAP RTN. SRC 88317650
0668 0 4022 BSI CMTRP 12 88317660
0669 0 0102 DC /0102 88317670
* 88317680

```


INTERRUPT FUNCTION TEST

```

*          INTERRUPT LEVEL 13
*          PRIORITY 15
066A 0 0000  LVL15 DC 0
066B 0 401F  BSI CMTRP GO TO COMN TRAP RTN. SRC
066C 0 0103  DC /0103 13
*
*          INTERRUPT LEVEL 14
*          PRIORITY 16
066D 0 0000  LVL16 DC 0
066E 0 401C  BSI CMTRP GO TO COMN TRAP RTN. SRC
066F 0 0104  DC /0104 14
*
*          INTERRUPT LEVEL 15
*          PRIORITY 17
0670 0 0000  LVL17 DC 0
0671 0 4019  BSI CMTRP GO TO COMN TRAP RTN. SRC
0672 0 0105  DC /0105 15
*
*          INTERRUPT LEVEL 16
*          PRIORITY 18
0673 0 0000  LVL18 DC 0
0674 0 4016  BSI CMTRP GO TO COMN TRAP RTN. SRC
0675 0 0106  DC /0106 16
*
*          INTERRUPT LEVEL 17
*          PRIORITY 19
0676 0 0000  LVL19 DC 0
0677 0 4013  BSI CMTRP GO TO COMN TRAP RTN. SRC
0678 0 0107  DC /0107 17
*
*          INTERRUPT LEVEL 18
*          PRIORITY 20
0679 0 0000  LVL20 DC 0
067A 0 4010  BSI CMTRP GO TO COMN TRAP RTN. SRC
067B 0 0108  DC /0108 18
*
*          INTERRUPT LEVEL 19
*          PRIORITY 21
067C 0 0000  LVL21 DC 0
067D 0 400D  BSI CMTRP GO TO COMN TRAP RTN. SRC
067E 0 0109  DC /0109 19
*
*          INTERRUPT LEVEL 20
*          PRIORITY 22
067F 0 0000  LVL22 DC 0
0680 0 400A  BSI CMTRP GO TO COMN TRAP RTN. SRC
0681 0 020A  DC /020A 20
*
*          INTERRUPT LEVEL 21
*          PRIORITY 23
0682 0 0000  LVL23 DC 0

```

```

88317690
88317700
88317710
88317720
88317730
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88317800
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88317990
88318000
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88318020
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88318090
88318100
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INTERRUPT FUNCTION TEST

```

0683 0 4007  BSI CMTRP GO TO COMN TPAP RTN SRC 88318370
0684 0 0201  DC /0201 21 8831838C
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INTERRUPT FUNCTION TEST

```

06BA 0 C01B          LD    ILSAV    CHECK ILSW
06BB 00 4C6006BF     BOSC L  CMT05,Z  BRANCH IF ILSW NOT 0
*
06BD 00 4C40023B     BOSC L  RT104    RETURN TO ROUTINE 1
*
*****
06BF 00 440004F5     CMT05 BSI L  ERROR    PRINT ILSW NOT ZERO SRC
06C1 0 0A45          DC    INM14
*****
*
06C2 00 C4000199     CMT04 LD  L  RUNSW    BYPASS WAIT IF RUN
06C4 00 4C2006C8     BSC  L  CNM00-2,Z  MODE WITH OUT STOPS
06C6 0  COOF          LD    ILSAV      ILSW TO A
*
06C7 0 3015          WT15 WAIT    21    ILSW NOT 0  PROG INT
*
06C8 00 4C00023B     BSC  L  RT104    ERROR ILSW
06CA 0 0000          CNM00 DC     0    CONTINUE
*
06CB 0  C00A          CMT06 LD  ILSAV
06CC 00 4C2806C8     BSC  L  CNM00-2,+Z
*****
06CE 00 440004F5     BSI  L  ERROR    WRONG ILSW DN OP SRC
06D0 0 0984          DC    INM06      CODE VIOLATE
*****
06D1 0 70F0          MDX   CMT04    CONTINUE
06D2 00 00000000     DEC    0
06D4 0 0000          ILSW DC     /0000  SENSE ILSW IOCC
06D5 0 0300          DC     /0300
*
06D6 0 0000          ILSAV DC     0    SAVE FOR ILSW
*
06D7 0 0000          SERVC DC     0
06D8 00 0C000436     XIO  L  CNSNS
06DA 0 08F9          XIO  ILSW     SENSE ILSW
06DB 00 4CC006D7     BOSC I  SERVC   EXIT     ESET   SX
*
*****
PRIORITY TRAP ROUTINES
*****
INTERRUPT ROUTINE LEVEL ER
*****
06DE 00 00000000     DEC    0
06E0 J 0000          PRI01 DC     0
06E1 0  C00B          LD    REGER     SET LEVEL ER REQUEST
06E2 00 D5000A78     STO  L1 IN16V  NUMBER IN REQ SEQ MG
06E4 00 D6000A88     STO  L2 IN17V  SET ER IN SER SEQ MG
06E6 0 7202          MDX   2 2
06E7 00 0C000436     XIO  L  CNSNS  WRONG BTM PROTECT
06E9 00 0C0006D4     XIO  L  ILSW   SENSE RESET
06EB 00 4CC006E0     BOSC I  PRI01
06ED 0 3529          REGER DC     /3529 ER
*
*****
INTERRUPT ROUTINE LEVEL TR
*****
06EE 0 0000          PRI26 DC     0
06EF 0  C00E          LD    REQTR    LEVEL TR REQUEST TO
06F0 00 D5000A78     STO  L1 IN16V  REQUEST SEQUENCE MSG
06F2 0 7102          MDX   1 2
06F3 0 73FF          MDX   3 -1
06F4 0 7006          MDX   PR260   NOT LAST INTERRUPT
*
*****
SERVICE THIS LEVEL
*****
06F5 0  C008          PR261 LD  REQTR    LEVEL TR TO LEVEL
06F6 00 D6000A88     STO  L2 IN17V  SRVCD SEQUENCE MSG
06F8 0 7202          MDX   2 2

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INTERRUPT FUNCTION TEST

```

06F9 00 4C8006EE     BSC  I  PRI26   NON RESET BRANCH
*
06FB 0 0804          PR260 XIO     PR262   ISSUE INTERRUPT
06FC 0 1000          NOP
06FD 0 70F7          MDX   PR261
*
06FE C 1329          REQTR DC     /1329   TR
06FF 0 0000          DC     0
*
0700 0 0000          PR262 DC     0       LOWEST LEVEL IOCC
0701 0 0000          DC     0
*
*****
INTERRUPT ROUTINE LEVEL CE
*****
0702 0 0000          PRI27 DC     0
0703 0  C00E          LD    REQCE    LEVEL CE REQUEST TO
0704 00 D5000A78     STO  L1 IN16V  REQUEST SEQUENCE MSG
0706 0 7102          MDX   1 2
0707 0 73FF          MDX   3 -1
0708 0 7006          MDX   PR270
*
*****
SERVICE THIS LEVEL
*****
0709 0  C008          PR271 LD  REQCE    LEVEL CE TO LEVEL
070A 00 D6000A88     STO  L2 IN17V  SRVCD SEQUENCE MSG
070C 0 7202          MDX   2 2
070D 00 4CC0C00A     BOSC I  /000A   BRANCH RESET
*
070F 0 0804          PR270 XIO     PR272   ISSUE INTERRUPT
0710 0 1000          NOP
0711 0 70F7          MDX   PR271
*
0712 0 3335          REQCE DC     /3335   CE
0713 0 0000          DC     0
*
0714 0 0000          PR272 DC     0       LOWEST LEVEL IOCC
0715 0 0000          DC     0
*
*****
INTERRUPT ROUTINE LEVEL 00
*****
0716 0 0000          PRI02 DC     0
0717 0  C00E          LD    REQ00    LEVEL 00 REQUEST TO
0718 00 D5000A78     STO  L1 IN16V  REQUEST SEQUENCE MSG
071A 0 7102          MDX   1 2
071B 0 73FF          MDX   3 -1
071C 0 7006          MDX   PR020   NOT LAST INTERRUPT
*
*****
SERVICE THIS INTERRUPT
*****
071D 0  C008          PRO21 LD  REQ00    LEVEL 00 TO LEVEL
071E 00 D6000A88     STO  L2 IN17V  SRVCD SEQUENCE MSG
0720 0 7202          MDX   2 2
0721 00 4CC00716     BOSC I  PRI02   BRANCH RESET
*
0723 0 0100          PRO20 DC     /0100   ILLEGAL OP INTRP ER
0724 0 1000          NOP
0725 0 70F7          MDX   PRO21
*
0726 0 0A0A          REQ00 DC     /0A0A   00
0727 0 0000          DC     0
*
0728 0 0000          DC     0
0729 0 0000          DC     0
*
*****
INTERRUPT ROUTINE LEVEL 01
*****
072A 0 0000          PRI03 DC     0
072B 0  C00E          LD    REQ01    LEVEL 01 REQUEST TO

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INTERRUPT FUNCTION TEST

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072C 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
072E 0 7102           MDX 1 2
072F 0 73FF           MDX 3 -1
0730 0 7006           MDX      PRO30      NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0731 0 C008           PRO31 LD      REQ01      LEVEL 01 TO LEVEL
0732 00 D6000A88      STO L2 IN17V      SRVCD SEQUENCE MSG
0734 0 7202           MDX 2 2
0735 00 4CC0072A      BOSC I PRI03      BRANCH RESET
*
0737 0 0804           PRO30 XIO     PRO32      INTERRUPT FOR LVL
0738 0 1000           NOP
0739 0 70F7           MDX      PRO31
*
073A 0 0A01           REQ01 DC     /0A01      01
073B 0 0000           DC 0
*
073C 0 8000           PRO32 DC     /8000      INTRP 00 IOCC
073D 0 04A0           DC /04A0
*
*           INTERRUPT ROUTINE LEVEL 02
*
073E 0 0000           PRI04 DC     0
073F 0 C00E           LD      REQ02      LEVEL 01 REQUEST TO
0740 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0742 0 7102           MDX 1 2
0743 0 73FF           MDX 3 -1
0744 0 7006           MDX      PRO40      NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0745 0 C008           PRO41 LD     REQ02      LEVEL 02 TO LEVEL
0746 00 D6000A88      STO L2 IN17V      SRVCD SEQUENCE MSG
0748 0 7202           MDX 2 2
0749 00 4CC0073E      BOSC I PRI04      BRANCH RESET
*
074B 0 0804           PRO4G XIO     PRO47      INTERRUPT FOR LVL 02
074C 0 1000           NOP
074D 0 70F7           MDX      PRO41
*
074E 0 0A02           REQ02 DC     /0A02      02
074F 0 0000           DC 0
*
0750 0 4000           PRO42 DC     /4000      INTRP 01 IOCC
0751 0 04A0           DC /04A0
*
*           INTERRUPT ROUTINE LEVEL 03
*
0752 0 0000           PRI05 DC     0
0753 0 C00E           LD      REQ03      LEVEL 03 REQUEST TO
0754 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0756 0 7102           MDX 1 2
0757 0 73FF           MDX 3 -1
0758 0 7006           MDX      PRO50      NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0759 0 C008           PRO51 LD     REQ03      LEVEL 03 TO LEVEL
075A 00 D6000A88      STO L2 IN17V      SRVCD SEQUENCE MSG
075C 0 7202           MDX 2 2
075D 00 4CC00752      BOSC I PRI05      BRANCH RESET
*
075F 0 0804           PRO50 XIO     PRC52      INTERRUPT FOR LVL 01
0760 0 1000           NOP
0761 0 70F7           MDX      PRO51
*

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INTERRUPT FUNCTION TEST

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0762 0 0A03          REQ03 DC     /0A03      03
0763 0 0000          DC 0
*
0764 0 2000          * PRO52 DC     /2000      INTRP 01 IOCC
0765 0 04A0          DC /04A0
*
*           INTERRUPT ROUTINE LEVEL 04
*
0766 0 0000          PRI06 DC     0
0767 0 C00E          LD      REQ04      LEVEL 04 REQUEST TO
0768 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
076A 0 7102          MDX 1 2
076B 0 73FF          MDX 3 -1
076C 0 7006          MDX      PRO60      NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
076D 0 C008          PRO61 LD     REQ04      LEVEL 04 TO LEVEL
076E 00 D6000A88      STO L2 IN17V      SRVCD SEQUENCE MSG
0770 0 7202          MDX 2 2
0771 00 4CC00766      BOSC I PRI06      BRANCH RESET
*
0773 0 0804          PRO60 XIO     PRO62      INTERRUPT FOR LVL 03
0774 0 1000          NOP
0775 0 70F7          MDX      PRO61
*
0776 0 0A04          REQ04 DC     /0A04      04
0777 0 0000          DC 0
0778 0 1000          PRO62 DC     /1000      INTRP 03 IOCC
0779 0 04A0          DC /04A0
*
*           INTERRUPT ROUTINE LEVEL 05
*
077A 0 0000          PRI07 DC     0
077B 0 C00E          LD      REQ05      LEVEL 05 REQUEST TO
077C 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
077E 0 7102          MDX 1 2
077F 0 73FF          MDX 3 -1
0780 0 7006          MDX      PRO70      NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0781 0 C008          PRO71 LD     REQ05      LEVEL 05 TO LEVEL
0782 00 D6000A88      STO L2 IN17V      SRVCD SEQUENCE MSG
0784 0 7202          MDX 2 2
0785 00 4CC0077A      BOSC I PRI07      BRANCH RESET
*
0787 0 0804          PRO70 XIO     PRO72      INTERRUPT FOR LVL 04
0788 0 1000          NOP
0789 0 70F7          MDX      PRO71
*
078A 0 0A05          REQ05 DC     /0A05      05
078B 0 0000          DC 0
*
078C 0 0800          PRO72 DC     /0800      INTRP 04 IOCC
078D 0 04A0          DC /04A0
*
*           INTERRUPT ROUTINE LEVEL 06
*
078E 0 0000          PRI08 DC     0
078F 0 C00E          LD      REQ06      LEVEL 06 REQUEST TO
0790 00 D5000A78      STO L1 IN16V      REQUEST SEQUENCE MSG
0792 0 7102          MDX 1 2
0793 0 73FF          MDX 3 -1
0794 0 7006          MDX      PRO80
*
*           SERVICE THIS INTERRUPT
*

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INTERRUPT FUNCTION TEST

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0795 0 C008    PR081 LD    REQ06    LEVEL 06 TO LEVEL
0796 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
0798 0 7202    MDX 2 2
0799 00 4CC0078E  BOSC I PRI08    BRANCH RESET
*
0798 0 0804    PR080 XIO    PR082    INTERRUPT FOR LVL 05
079C 0 1000    NOP
079D 0 70F7    MDX    PR081
*
079E 0 0A06    REQ06 DC    /0A06    06
079F 0 0000    DC    0
*
07A0 0 0400    PR082 DC    /0400    INTRP 05 IOCC
07A1 0 04A0    DC    /04A0
*
*           INTERRUPT ROUTINE LEVEL 07
*
07A2 0 0000    PRI09 DC    0
07A3 0 C00E    LD    REQ07    LEVEL 07 REQUEST TO
07A4 00 D5000A78  ST0 L1 IN16V  REQUEST SEQUENCE MSG
07A6 0 7102    MDX 1 2
07A7 0 73FF    MDX 3 -1
07A8 0 7006    MDX    PR090    NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
07A9 0 C008    PR091 LD    REQ07    LEVEL 07 TO LEVEL
07AA 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
07AC 0 7202    MDX 2 2
07AD 00 4CC007A2  BOSC I PRI09    BRANCH RESET
*
07AF 0 0804    PR090 XIO    PR092    INTERRUPT FOR LVL 06
07B0 0 1000    NOP
07B1 0 70F7    MDX    PR091
*
07B2 0 0A07    REQ07 DC    /0A07    07
07B3 0 0000    DC    0
*
07B4 0 0200    PR092 DC    /0200    INTRP 06 IOCC
07B5 0 04A0    DC    /04A0
*
*           INTERRUPT ROUTINE LEVEL 08
*
07B6 0 0000    PRI10 DC    0
07B7 0 C00E    LD    REQ08    LEVEL 08 REQUEST TO
07B8 00 D5000A78  ST0 L1 IN16V  REQUEST SEQUENCE MSG
07BA 0 7102    MDX 1 2
07BB 0 73FF    MDX 3 -1
07BC 0 7006    MDX    PRI100   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
07BD 0 C008    PRI01 LD    REQ08    LEVEL 08 TO LEVEL
07BE 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
07C0 0 7202    MDX 2 2
07C1 00 4CC0078E  BOSC I PRI10    BRANCH RESET
*
07C3 0 0804    PRI00 XIO    PRI02    INTERRUPT FOR LVL 07
07C4 0 1000    NOP
07C5 0 70F7    MDX    PRI01
*
07C6 0 0A08    REQ08 DC    /0A08    08
07C7 0 0000    DC    0
*
07C8 0 0100    PRI02 DC    /C100    INTRP 07 IOCC
07C9 0 04A0    DC    /04A0
*
*           INTERRUPT ROUTINE LEVEL 09

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INTERRUPT FUNCTION TEST

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07CA 0 0000    PPI11 DC    0
07CB 0 C00E    LD    REQ09    LEVEL 09 REQUEST TO
07CC 00 D5000A78  ST0 L1 IN16V  REQUEST SEQUENCE MSG
07CE 0 7102    MDX 1 2
07CF 0 73FF    MDX 3 -1
07D0 0 7006    MDX    PRI10    NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
07D1 0 C008    PRI11 LD    REQ09    LEVEL 09 TO LEVEL
07D2 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
07D4 0 7202    MDX 2 2
07D5 00 4CC007CA  BOSC I PRI11    BRANCH RESET
*
07D7 0 0804    PRI10 XIO    PRI12    INTERRUPT FOR LVL 08
07D8 0 1000    NOP
07D9 0 70F7    MDX    PRI11
*
07DA 0 0A09    REQ09 DC    /0A09    09
07DB 0 0000    DC    0
*
07DC 0 0080    PRI12 DC    /0080    INTRP 08 IOCC
07DD 0 04A0    DC    /04A0
*
*           INTERRUPT ROUTINE LEVEL 10
*
07DE 0 0000    PRI12 DC    0
07DF 0 C00E    LD    REQ10    LEVEL 10 REQUEST TO
07E0 00 D5000A78  ST0 L1 IN16V  REQUEST SEQUENCE MSG
07E2 0 7102    MDX 1 2
07E3 0 73FF    MDX 3 -1
07E4 0 7006    MDX    PRI120   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
07E5 0 C008    PRI21 LD    REQ10    LEVEL TO LEVEL
07E6 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
07E8 0 7202    MDX 2 2
07E9 00 4CC007DE  BOSC I PRI12    BRANCH RESET
*
07EB 0 0804    PRI20 XIO    PRI22    INTERRUPT FOR LVL 09
07EC 0 1000    NOP
07ED 0 70F7    MDX    PRI21
*
07EE 0 010A    REQ10 DC    /010A    10
07EF 0 0000    DC    0
*
07F0 0 0040    PRI22 DC    /0040    INTRP 09 IOCC
07F1 0 04A0    DC    /04A0
*
*           INTERRUPT ROUTINE LEVEL 11
*
07F2 0 0000    PRI13 DC    0
07F3 0 C00E    LD    REQ11    LEVEL 11 REQUEST TO
07F4 00 D5000A78  ST0 L1 IN16V  REQUEST SEQUENCE MSG
07F6 0 7102    MDX 1 2
07F7 0 73FF    MDX 3 -1
07F8 0 7006    MDX    PRI130   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
07F9 0 C008    PRI31 LD    REQ11    LEVEL 11 TO LEVEL
07FA 00 D6000A88  ST0 L2 IN17V  SRVCD SEQUENCE MSG
07FC 0 7202    MDX 2 2
07FD 00 4CC007F2  BOSC I PRI13    BRANCH RESET
*
07FF 0 0804    PRI30 XIO    PRI32    INTERRUPT FOR LVL 10

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INTERRUPT FUNCTION TEST

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0800 0 1000      NOP
0801 0 70F7      MDX      PR131
*
0802 0 0101      REQ11 DC    /0101    11
0803 0 0000      DC      0
*
0804 0 0020      PR132 DC    /0020    INTRP 10 IOCC
0805 0 04A0      DC      /04A0
*
*
*
*
0805 0 0000      PR114 DC    0
0807 0 C00E      LD      REQ12    LEVEL 12 REQUEST TO
0808 00 D5000A78 STO L1 IN16V  REQUEST SEQUENCE MSG
080A 0 7102      MDX    1 2
080B 0 73FF      MDX    3 -1
080C 0 7J06      MDX    PR140    NOT LAST INTERRUPT
*
*
*
*
080D 0 C008      PR141 LD      REQ12    LEVEL 12 TO LEVEL
080E 00 D6000AB8 STO L2 IN17V  SRVCD SEQUENCE MSG
0810 0 7202      MDX    2 2
0811 00 4CC00806 BOSC I  PR114    BRANCH RESET
*
0813 0 0804      PR140 XIO   PR142    INTERRUPT FOR LVL 11
0814 0 1000      NOP
0815 0 70F7      MDX    PR141
*
0816 0 0102      REQ12 DC    /0102    12
0817 0 0000      DC      0
*
0818 0 0010      PR142 DC    /0010    INTRP 11 IOCC
0819 0 04A0      DC      /04A0
*
*
*
*
081A 0 0000      PR115 DC    0
081B 0 C00E      LD      REQ13    LEVEL 13 REQUEST TO
081C 00 D5000A78 STO L1 IN16V  REQUEST SEQUENCE MSG
081E 0 7102      MDX    1 2
081F 0 73FF      MDX    3 -1
0820 0 7006      MDX    PR150    NOT LAST INTERRUPT
*
*
*
*
0821 0 C008      PR151 LD      REQ13    LEVEL 13 TO LEVEL
0822 00 D6000AB8 STO L2 IN17V  SRVCD SEQUENCE MSG
0824 0 7202      MDX    2 2
0825 00 4CC0081A BOSC I  PR115    BRANCH RESET
*
0827 0 0804      PR150 XIO   PR152    INTERRUPT FOR LVL 12
0828 0 1000      NOP
0829 0 70F7      MDX    PR151
*
082A 0 0103      REQ13 DC    /0103    13
082B 0 0000      DC      0
*
082C 0 C008      PR152 DC    /0008    INTRP 12 IOCC
082D 0 04A0      DC      /04A0
*
*
*
*
082E 0 0000      PR116 DC    0
082F 0 C00E      LD      REQ14    LEVEL 14 REQUEST TO
0830 00 D5000A78 STO L1 IN16V  REQUEST SEQUENCE MSG
0832 0 7102      MDX    1 2
0833 0 73FF      MDX    3 -1

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INTERRUPT FUNCTION TEST

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0834 0 7006      MDX      PR160    NOT LAST INTERRUPT
*
*
*
0835 0 C008      PR161 LD      REQ14    LEVEL 14 TO LEVEL
0836 00 D6000AB8 STO L2 IN17V  SRVCD SEQUENCE MSG
0838 0 7202      MDX    2 2
0839 00 4CC0082E BOSC I  PR116    BRANCH RESET
*
0838 0 0804      PR160 XIO   PR162    INTERRUPT FOR LVL 13
083C 0 1000      NOP
083D 0 70F7      MDX    PR161
*
083E 0 0104      REQ14 DC    /0104    14
083F 0 0000      DC      0
*
0840 0 0004      PR162 DC    /0004    INTRP 13 IOCC
0841 0 04A0      DC      /04A0
*
*
*
*
0842 0 0000      PR117 DC    0
0843 0 C00E      LD      REQ15    LEVEL 15 REQUEST TO
0844 00 D5000A78 STO L1 IN16V  REQUEST SEQUENCE MSG
0846 0 7102      MDX    1 2
0847 0 73FF      MDX    3 -1
0848 0 7006      MDX    PR170    NOT LAST INTERRUPT
*
*
*
*
0849 0 C008      PR171 LD      REQ15    LEVEL 15 TO LEVEL
084A 00 D6000AB8 STO L2 IN17V  SRVCD SEQUENCE MSG
084C 0 7202      MDX    2 2
084D 00 4CC00842 BOSC I  PR117    BRANCH RESET
*
084F 0 0804      PR170 XIO   PR172    INTERRUPT FOR LVL 14
0850 0 1000      NOP
0851 0 70F7      MDX    PR171
*
0852 0 0105      REQ15 DC    /0105    15
0853 0 0000      DC      0
*
0854 0 8000      PR172 DC    /8000    INTERP 14 IOCC
0855 0 04A1      DC      /04A1
*
*
*
*
0856 0 0000      PR118 DC    0
0857 0 C00E      LD      REQ16    LEVEL 16 REQUEST TO
0858 00 D5000A78 STO L1 IN16V  REQUEST SEQUENCE MSG
085A 0 7102      MDX    1 2
085B 0 73FF      MDX    3 -1
085C 0 7006      MDX    PR180    NOT LAST INTERRUPT
*
*
*
*
085D 0 C008      PR181 LD      REQ16    LEVEL 16 TO LEVEL
085E 00 D6000AB8 STO L2 IN17V  SRVCD SEQUENCE MSG
0860 0 7202      MDX    2 2
0861 00 4CC00856 BOSC I  PR118    BRANCH RESET
*
0863 0 0804      PR180 XIO   PR182    INTERRUPT FOR LVL 15
0864 0 1000      NOP
0865 0 70F7      MDX    PR181
*
0866 0 0106      REQ16 DC    /0106    16
0867 0 0000      DC      0
*

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INTERRUPT FUNCTION TEST

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0868 0 4002    PRI18 DC    /4000    INTRP 15 IOCC
0869 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 17
*
086A 0 0000    PRI19 DC    0
086B 0 C00E    LD        REQ17    LEVEL 17 REQUEST TO
086C 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
086E 0 7102    MDX      1 2
086F 0 73FF    MDX      3 -1
0870 0 7006    MDX      PRI190   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0871 0 C008    PRI19 LD      REQ17    LEVEL 17 TO LEVEL
0872 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
0874 0 7202    MDX      2 2
0875 00 4CC0086A  BOSC I  PRI19   BRANCH RESET
*
0877 0 0804    PRI19 XIO    PRI192   INTERRUPT FOR LVL 16
0878 0 1000    NOP
0879 0 70F7    MDX      PRI191
*
087A 0 0107    REQ17 DC    /0107    17
087B 0 0000    DC        0
*
087C 0 2000    PRI19 DC    /2000    INTRP 16 IOCC
087D 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 18
*
087E 0 0000    PRI20 DC    0
087F 0 C00E    LD        REQ18    LEVEL 18 REQUEST TO
0880 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
0882 0 7102    MDX      1 2
0883 0 73FF    MDX      3 -1
0884 0 7006    MDX      PRI200   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0885 0 C008    PRI20 LD      REQ18    LEVEL 18 TO LEVEL
0886 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
0888 0 7202    MDX      2 2
0889 00 4CC0087E  BOSC I  PRI20   BRANCH RESET
*
088B 0 0804    PRI20 XIO    PP202    INTERRUPT FOR LVL 17
088C 0 1000    NOP
088D 0 70F7    MDX      PRI201
*
088E 0 0108    REQ18 DC    /0108    18
088F 0 0000    DC        0
*
0890 0 1000    PRI20 DC    /1000    INTRP 17 IOCC
0891 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 19
*
0892 0 0000    PRI21 DC    0
0893 0 C00E    LD        REQ19    LEVEL 19 REQUEST TO
0894 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
0896 0 7102    MDX      1 2
0897 0 73FF    MDX      3 -1
0898 0 7006    MDX      PRI210   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
0899 0 C008    PRI21 LD      REQ19    LEVEL 19 TO LEVEL
089A 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG

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INTERRUPT FUNCTION TEST

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089C 0 7202    MDX      2 2
089D 00 4CC00892  BOSC I  PRI21   BRANCH RESET
*
089F 0 0804    PRI21 XIO    PRI212   INTERRUPT FOR LVL 18
08A0 0 1000    NOP
08A1 0 70F7    MDX      PRI211
*
08A2 0 0109    REQ19 DC    /0109    19
08A3 0 0000    DC        0
*
08A4 0 0800    PRI21 DC    /0800    INTRP 18 IOCC
08A5 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 20
*
08A6 0 0000    PRI22 DC    0
08A7 0 C00E    LD        REQ20    LEVEL 20 REQUEST TO
08A8 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
08AA 0 7102    MDX      1 2
08AB 0 73FF    MDX      3 -1
08AC 0 7006    MDX      PRI220   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
08AD 0 C008    PRI22 LD      REQ20    LEVEL 20 TO LEVEL
08AE 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
08B0 0 7202    MDX      2 2
08B1 00 4CC008A6  BOSC I  PRI22   BRANCH RESET
*
08B3 0 0804    PRI22 XIO    PRI222   INTERRUPT FOR LVL 19
08B4 0 1000    NOP
08B5 0 70F7    MDX      PRI221
*
08B6 0 020A    REQ20 DC    /020A    20
08B7 0 0000    DC        0
*
08B8 0 0400    PRI22 DC    /0400    INTRP 19 IOCC
08B9 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 21
*
08BA 0 0000    PRI23 DC    0
08BB 0 C00E    LD        REQ21    LEVEL 21 REQUEST TO
08BC 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
08BE 0 7102    MDX      1 2
08BF 0 73FF    MDX      3 -1
08C0 0 7006    MDX      PRI230   NOT LAST INTERRUPT
*
*           SERVICE THIS INTERRUPT
*
08C1 0 C008    PRI23 LD      REQ21    LEVEL 21 TO LEVEL
08C2 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
08C4 0 7202    MDX      2 2
08C5 00 4CC008BA  BOSC I  PRI23   BRANCH RESET
*
08C7 0 0804    PRI23 XIO    PRI232   INTERRUPT FOR LVL 20
08C8 0 1000    NOP
08C9 0 70F7    MDX      PRI231
*
08CA 0 0201    REQ21 DC    /0201    21
08CB 0 0000    DC        0
*
08CC 0 0200    PRI23 DC    /0200    INTRP 20 IOCC
08CD 0 04A1    DC        /04A1
*
*           INTERRUPT ROUTINE LEVEL 22
*
08CE 0 0000    PRI24 DC    0

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INTERRUPT FUNCTION TEST

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08CF 0 C00E      LD      REQ22    LEVEL 22 REQUEST TO
08D0 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
08D2 0 7102      MDX 1 2
08D3 0 73FF      MDX 3 -1
08D4 0 7006      MDX PR240   NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
08D5 0 C008      PR241 LD      REQ22    LEVEL 22 TO LEVEL
08D6 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
08D8 0 7202      MDX 2 2
08D9 00 4CC008CE  BOSC I PR124  BRANCH RESET
*
08DB 0 0804      PR240 XIO PR242  INTERRUPT FOR LVL 21
08DC 0 1000      NOP
08DD 0 70F7      MDX PR241
*
08DE 0 0202      REQ22 DC /0202  22
08DF 0 0000      DC 0
*
08E0 0 0100      PR242 DC /0100  INTRP 21 IOCC
08E1 0 04A1      DC /04A1
*
*          INTERRUPT ROUTINE LEVEL 23
*
08E2 0 0000      PRI25 DC 0
08E3 0 C00E      LD      REQ23    LEVEL 23 REQUEST TO
08E4 00 D5000A78  STO L1 IN16V  REQUEST SEQUENCE MSG
08E6 0 7102      MDX 1 2
08E7 0 73FF      MDX 3 -1
08E8 0 7006      MDX PR250   NOT LAST INTERRUPT
*
*          SERVICE THIS INTERRUPT
*
08E9 0 C008      PR251 LD      REQ23    LEVEL 23 TO LEVEL
08EA 00 D6000A88  STO L2 IN17V  SRVCD SEQUENCE MSG
08EC 0 7202      MDX 2 2
08ED 00 4CC008E2  BOSC I PR125  BRANCH RESET
*
08EF 0 0804      PR250 XIO PR252  INTERRUPT FOR LVL 22
08F0 0 1000      NOP
08F1 0 70F7      MDX PR251
*
08F2 0 0203      REQ23 DC /0203  23
08F3 0 0000      DC 0
*
08F4 0 0080      PR252 DC /0080  INTRP 22 IOCC
08F5 0 04A1      DC /04A1
*
*          ROUTINE SIX TRACE INTRPT
*
08F6 0 0000      TRACE DC 0
08F7 0 0028      STO TRCN2    SAVE ACCUMULATOR
08F8 00 74FF08F6  MDX L TRACE,-1
08FA 0 C0FB      LD TRACE    GET INTERP I COUNT
08FB 0 9025      S TRCNO    CK IF INSTR TO TRACE
08FC 00 4C280903  BSC L TRACO,+2 BRANCH IF TRACING
08FE 00 740108F6  MDX L TRACE,1  RESTORE RETURN
0900 0 C022      LD TRCN2    RESTORE ACCUMULATOR
0901 00 4C8008F6  BSC I TRACE  GO COMPLETE CNTRL OP
*
0903 00 C6000930  TRACO LD L2 INLVT+2 SET EXPECTED INSTRN
0905 00 D4000B1C  STO L INM19+23 NO. IN ERROR MESSAGE
*
0907 0 C0EE      LD TRACE    GET INTERP I COUNT
0908 00 96000924  S L2 *NSAD  COMPR WITH EXPECTED
090A 00 4C200914  BSC L TRAC1,Z BRANCH IF WRONG ISTR

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INTERRUPT FUNCTION TEST

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090C 0 7201      * MDX 2 1
090D 00 7401048D  MDX L TRIND,1 STEP TRACF INDICATOR
090F 00 740108F6  MDX L TRACE,1  RESTORE RETURN
0911 0 C011      LD TRCN2    RESTORE ACCUMULATOR
0912 00 4CC008F6  TRAC2 BOSC I TRACE EXIT SX
*
0914 0 6A0D      TRAC1 STX 2 TRCN1
0915 0 800C      A TRCN1
0916 0 D00B      STO TRCN1
0917 00 65800922  LDX I1 TRCN1 SET INDEX TO DIFRNC
0919 00 C5000930  LD L1 INLVT+2 SET INTERRUPTING
0916 00 D4000B20  STO L INM19+27 INSTR TO ERROR MSG
*
091D 00 440004F9  * BSI L ERALT LOG FAILING TRACE SRC
091F 0 0B05      DC INM19 INSTRUCTION
*****
*
0920 0 70EB      MDX TPAC1-8
*
0921 0 0467      TRCNO DC RT501
0922 0 0000      TRCN1 DC 0
0923 0 0000      TRCN2 DC 0
*
*          TRACE INSTRUCTION ADDRESS
*
0924 0 0450      INSAD DC RT500
0925 0 045E      DC RT500+1
0926 0 045F      DC RT500+2
0927 0 0460      DC RT500+3
0928 0 0461      DC RT500+4
0929 0 0462      DC RT500+5
092A 0 0463      DC RT500+6
092B 0 0464      DC RT500+7
092C 0 0465      DC RT500+8
092D 0 0466      DC RT500+9
*
*          HEX INTERRUPT LEVEL TABLE
*
092E 0 3529      INLVT DC /3529 ER
092F 0 0A0A      DC /0A0A 00
0930 0 0A01      DC /0A01 01
0931 0 0A02      DC /0A02 02
0932 0 0A03      DC /0A03 03
0933 0 0A04      DC /0A04 04
0934 0 0A05      DC /0A05 05
0935 0 0A06      DC /0A06 06
0936 0 0A07      DC /0A07 07
0937 0 0A08      DC /0A08 08
0938 0 0A09      DC /0A09 09
0939 0 010A      DC /010A 10
093A 0 0101      DC /0101 11
093B 0 0102      DC /0102 12
093C 0 0103      DC /0103 13
093D 0 0104      DC /0104 14
093E 0 0105      DC /0105 15
093F 0 0106      DC /0106 16
0940 0 0107      DC /0107 17
0941 0 0108      DC /0108 18
0942 0 0109      DC /0109 19
0943 0 020A      DC /020A 20
0944 0 0201      DC /0201 21
0945 0 0202      DC /0202 22
0946 0 0203      DC /0203 23
0947 0 1329      DC /1329 TR
0948 0 3335      DC /3335 CE
*
*          PRINT MESSAGES 1443 CODED

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INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

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0949 0 0013      * INM01 DC /0013  WORD COUNT
094A 0 330A      DC /330A  CO
094B 0 0A01      DC /0A01  OI
094C 0 0000      DC /0000  SPACE
094D 0 0000      DC /0000  SPACE
094E 0 1314      DC /1314  TU
094F 0 2925      DC /2925  RN
0950 0 0034      DC /0034  D
0951 0 3912      DC /3912  IS
0952 0 3132      DC /3132  AB
0953 0 2335      DC /2335  LE
0954 0 0012      DC /0012  S
0955 0 1600      DC /1600  W
0956 0 2625      DC /2625  ON
0957 0 0027      DC /0027  P
0958 0 1412      DC /1412  US
0959 0 3800      DC /3800  H
095A 0 1213      DC /1213  ST
095B 0 3129      DC /3129  AR
095C 0 1300      DC /1300  T
095D 0 FFFF      DC /FFFF  TERM

095E 0 000E      * INM02 DC /000E  WORD COUNT
095F 0 330A      DC /330A  CO
0960 0 0A02      DC /0A02  OZ
0961 0 0000      DC /0000  SPACE
0962 0 0000      DC /0000  SPACE
0963 0 1314      DC /1314  TJ
0964 0 2925      DC /2925  RN
0965 0 0034      DC /0034  D
0966 0 3912      DC /3912  IS
0967 0 3132      DC /3132  AB
0968 0 2335      DC /2335  LE
0969 0 0012      DC /0012  S
096A 0 1600      DC /1600  W
096B 0 2636      DC /2636  OF
096C 0 3600      DC /3600  F
096D 0 FFFF      DC /FFFF  TERM

096E 0 0013      * INM03 DC /0013  WORD COUNT
096F 0 350A      DC /350A  EO
0970 0 0A01      DC /0A01  OI
0971 0 0000      DC /0000  SPACE
0972 0 0000      DC /0000  SPACE
0973 0 2913      DC /2913  RT
0974 0 2500      DC /2500  N
0975 0 0000      DC /0000  ROUTINE NUMBER
0976 0 0023      DC /0023  L
0977 0 3515      DC /3515  EV
0978 0 2300      DC /2300  L
0979 0 0000      DC /0000  LEVEL NUMBER
097A 0 0036      DC /0036  F
097B 0 3139      DC /3139  AI
097C 0 2335      DC /2335  LE
097D 0 3400      DC /3400  D
097E 0 1326      DC /1326  TO
097F 0 0039      DC /0039  I
0980 0 2513      DC /2513  NI
0981 0 2927      DC /2927  RP
0982 0 FFFF      DC /FFFF  TERM

0983 0 0015      * INM04 DC /0015  WORD COUNT
0984 0 350A      DC /350A  EO
0985 0 0A02      DC /0A02  OZ
0986 0 0000      DC /0000  SPACE
0987 0 0000      DC /0000  SPACE
0988 0 2913      DC /2913  RT

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0989 0 2500      DC /2500  N
098A 0 0000      DC /0000  ROUTINE NUMBER
098B 0 0029      DC /0029  R
098C 0 3528      DC /3528  EQ
098D 0 0000      DC /0000  BLANK
098E 0 0000      DC /0000  REQUEST NUMBER
098F 0 0037      DC /0037  G
0990 0 3915      DC /3915  IV
0991 0 3525      DC /3525  EN
0992 0 0023      DC /0023  L
0993 0 3515      DC /3515  EV
0994 0 2300      DC /2300  L
0995 0 0000      DC /0000  LEVEL NUMBER
0996 0 0012      DC /0012  S
0997 0 2915      DC /2915  RV
0998 0 3334      DC /3334  CD
0999 0 FFFF      DC /FFFF  TERM

* INM05 DC /0018  WORD COUNT
099A 0 0018      DC /0018  WORD COUNT
099B 0 350A      DC /350A  EO
099C 0 0A03      DC /0A03  O3
099D 0 0000      DC /0000  SPACE
099E 0 0000      DC /0000  SPACE
099F 0 2913      DC /2913  RT
09A0 0 2500      DC /2500  N
09A1 0 0000      DC /0000  ROUTINE NUMBER
09A2 0 0029      DC /0029  R
09A3 0 3528      DC /3528  EQ
09A4 0 0000      DC /0000  BLANK
09A5 0 0000      DC /0000  REQUEST NUMBER
09A6 0 0039      DC /0039  I
09A7 0 2513      DC /2513  NT
09A8 0 2927      DC /2927  RP
09A9 0 3400      DC /3400  D
09AA 0 1639      DC /1639  WI
09AB 0 1338      DC /1338  TH
09AC 0 0034      DC /0034  D
09AD 0 3912      DC /3912  IS
09AE 0 3132      DC /3132  AB
09AF 0 2335      DC /2335  LE
09B0 0 0012      DC /0012  S
09B1 0 1600      DC /1600  W
09B2 0 2625      DC /2625  ON
09B3 0 FFFF      DC /FFFF  TERM

* INM06 DC /0013  WORD COUNT
09B4 0 0013      DC /0013  WORD COUNT
09B5 0 350A      DC /350A  EO
09B6 0 0A04      DC /0A04  O4
09B7 0 0000      DC /0000  SPACE
09B8 0 0000      DC /0000  SPACE
09B9 0 2913      DC /2913  RT
09BA 0 2500      DC /2500  N
09BB 0 0000      DC /0000  ROUTINE NUMBER
09BC 0 0016      DC /0016  W
09BD 0 2926      DC /2926  RO
09BE 0 2537      DC /2537  NG
09BF 0 0039      DC /0039  I
09C0 0 2312      DC /2312  LS
09C1 0 1600      DC /1600  W
09C2 0 2625      DC /2625  ON
09C3 0 0035      DC /0035  E
09C4 0 2929      DC /2929  RR
09C5 0 0039      DC /0039  I
09C6 0 2513      DC /2513  NT
09C7 0 2927      DC /2927  RP
09C8 0 FFFF      DC /FFFF  TERM

* INM07 DC /000C  WORD COUNT
09C9 0 000C      DC /000C  WORD COUNT

```

```

88327890
88327900
88327910
88327920
88327930
88327940
88327950
88327960
88327970
88327980
88327990
88328000
88328010
88328020
88328030
88328040
88328050
88328060
88328070
88328080
88328090
88328100
88328110
88328120
88328130
88328140
88328150
88328160
88328170
88328180
88328190
88328200
88328210
88328220
88328230
88328240
88328250
88328260
88328270
88328280
88328290
88328300
88328310
88328320
88328330
88328340
88328350
88328360
88328370
88328380
88328390
88328400
88328410
88328420
88328430
88328440
88328450
88328460
88328470
88328480
88328490
88328500
88328510
88328520
88328530
88328540
88328550
88328560

```

INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

```

09CA 0 310A      DC /310A  AO
09CB 0 0A01      DC /0A01  O1
09CC 0 0000      DC /0000  SPACE
09CD 0 0000      DC /0000  SPACE
09CE 0 2729      DC /2729  PR
09CF 0 2637      DC /2637  OG
09D0 0 2931      DC /2931  RA
09D1 0 2400      DC /2400  M
09D2 0 3326      DC /3326  CO
09D3 0 2427      DC /2427  MP
09D4 0 2335      DC /2335  LE
09D5 0 1335      DC /1335  TE
09D6 0 FFFF      DC /FFFF  TERM

* INM08 DC /000E  WORD COUNT
09D7 0 000E      DC /330A  CO
09D8 0 330A      DC /0A03  O3
09D9 0 0A03      DC /0000  SPACE
09DA 0 0000      DC /0000  SPACE
09DB 0 0000      DC /2714  PU
09DC 0 2714      DC /1238  SH
09DD 0 1238      DC /0033  C
09DE 0 0033      DC /3500  E
09DF 0 3500      DC /3925  IN
09E0 0 3925      DC /1329  TR
09E1 0 1329      DC /2700  P
09E2 0 2700      DC /3214  BU
09E3 0 3214      DC /1313  TT
09E4 0 1313      DC /2625  ON
09E5 0 2625      DC /FFFF  TERM
09E6 0 FFFF      DC /000F  WORD COUNT
* INM09 DC /330A  CO
09E7 0 000F      DC /0A04  O4
09E8 0 330A      DC /0000  SPACE
09E9 0 0A04      DC /0000  SPACE
09EA 0 0000      DC /2714  PU
09EB 0 0000      DC /1238  SH
09EC 0 2714      DC /0033  C
09ED 0 1238      DC /2625  ON
09EE 0 0033      DC /1200  S
09EF 0 2625      DC /3925  IN
09F0 0 1200      DC /1329  TR
09F1 0 3925      DC /2700  P
09F2 0 1329      DC /3214  BU
09F3 0 2700      DC /1313  TT
09F4 0 3214      DC /2625  ON
09F5 0 1313      DC /FFFF  TERM
09F6 0 2625      DC /0011  WORD COUNT
* INM10 DC /330A  CO
09F7 0 FFFF      DC /0A05  O5
09F8 0 0011      DC /0000  SPACE
09F9 0 330A      DC /0000  SPACE
09FA 0 0A05      DC /1235  SE
09FB 0 0000      DC /1300  T
09FC 0 0000      DC /1329  TR
09FD 0 1235      DC /3133  AC
09FE 0 1300      DC /3500  E
09FF 0 1329      DC /2426  MO
0A00 0 3133      DC /3435  DE
0A01 0 3500      DC /0000  BLANK
0A02 0 2426      DC /2714  PU
0A03 0 3435      DC /1238  SH
0A04 0 0000      DC /0012  S
0A05 0 2714      DC /1331  TA
0A06 0 1238      DC /2913  RT
0A07 0 0012      DC /0012  TERM
0A08 0 1331
0A09 0 2913
0A0A 0 FFFF

```

```

88328570
88328580
88328590
88328600
88328610
88328620
88328630
88328640
88328650
88328660
88328670
88328680
88328690
88328700
88328710
88328720
88328730
88328740
88328750
88328760
88328770
88328780
88328790
88328800
88328810
88328820
88328830
88328840
88328850
88328860
88328870
88328880
88328890
88328900
88328910
88328920
88328930
88328940
88328950
88328960
88328970
88328980
88328990
88329000
88329010
88329020
88329030
88329040
88329050
88329060
88329070
88329080
88329090
88329100
88329110
88329120
88329130
88329140
88329150
88329160
88329170
88329180
88329190
88329200
88329210
88329220
88329230
88329240

```

```

0A0B 0 0010
0A0C 0 330A
0A0D 0 0A06
0A0E 0 0000
0A0F 0 0000
0A10 0 1235
0A11 0 1300
0A12 0 2914
0A13 0 2500
0A14 0 2426
0A15 0 3435
0A16 0 0000
0A17 0 2714
0A18 0 1238
0A19 0 0012
0A1A 0 1331
0A1B 0 2913
0A1C 0 FFFF

0A1D 0 000F
0A1E 0 350A
0A1F 0 0A05
0A20 0 0000
0A21 0 0000
0A22 0 1235
0A23 0 2814
0A24 0 3525
0A25 0 3335
0A26 0 0035
0A27 0 2929
0A28 0 2629
0A29 0 0029
0A2A 0 1325
0A2B 0 0000
0A2C 0 0000
0A2D 0 FFFF

0A2E 0 0015
0A2F 0 350A
0A30 0 0A06
0A31 0 0000
0A32 0 0000
0A33 0 2913
0A34 0 2500
0A35 0 0400
0A36 0 2335
0A37 0 1535
0A38 0 2300
0A39 0 0000
0A3A 0 0039
0A3B 0 2513
0A3C 0 2927
0A3D 0 3400
0A3E 0 1638
0A3F 0 3923
0A40 0 3500
0A41 0 2431
0A42 0 1222
0A43 0 3534
0A44 0 FFFF

0A45 0 0011
0A46 0 350A
0A47 0 0A07
0A48 0 0000
0A49 0 0000
0A4A 0 2913

```

```

* INM11 DC /0010  WORD COUNT
DC /330A  CO
DC /0A06  O6
DC /0000  SPACE
DC /0000  SPACE
DC /1235  SE
DC /1300  T
DC /2914  RU
DC /2500  N
DC /2426  MO
DC /3435  DE
DC /0000  BLANK
DC /2714  PU
DC /1238  SH
DC /0012  S
DC /1331  TA
DC /2913  RT
DC /FFFF  TERM

* INM12 DC /000F  WORD COUNT
DC /350A  EO
DC /0A05  O5
DC /0000  SPACE
DC /0000  SPACE
DC /1235  SE
DC /2814  QU
DC /3525  EN
DC /3335  CE
DC /0035  E
DC /2929  RR
DC /2629  OR
DC /0029  R
DC /1325  TN
DC /0000  BLANK
DC /0000  ROUTINE NUMBER
DC /FFFF  TERM

* INM13 DC /0015  WORD COUNT
DC /350A  EO
DC /0A06  O6
DC /0000  SPACE
DC /0000  SPACE
DC /2913  RT
DC /2500  N
DC /0400  4
DC /2335  LE
DC /1535  VE
DC /2300  L
DC /0000  REQUEST NUMBER
DC /0039  I
DC /2513  NT
DC /2927  RP
DC /3400  D
DC /1638  WH
DC /3923  IL
DC /3500  E
DC /2431  MA
DC /1222  SK
DC /3534  ED
DC /FFFF  TERM

* INM14 DC /0011  WORD COUNT
DC /350A  EO
DC /0A07  O7
DC /0000  SPACE
DC /0000  SPACE
DC /2913  RT

```

```

88329250
88329260
88329270
88329280
88329290
88329300
88329310
88329320
88329330
88329340
88329350
88329360
88329370
88329380
88329390
88329400
88329410
88329420
88329430
88329440
88329450
88329460
88329470
88329480
88329490
88329500
88329510
88329520
88329530
88329540
88329550
88329560
88329570
88329580
88329590
88329600
88329610
88329620
88329630
88329640
88329650
88329660
88329670
88329680
88329690
88329700
88329710
88329720
88329730
88329740
88329750
88329760
88329770
88329780
88329790
88329800
88329810
88329820
88329830
88329840
88329850
88329860
88329870
88329880
88329890
88329900
88329910
88329920

```

INTERRUPT FUNCTION TEST

0A4B 0	2500	DC	/2500	N
0A4C 0	0200	DC	/0200	2
0A4D 0	3925	DC	/3925	IN
0A4E 0	1300	DC	/1300	T
0A4F 0	0000	DC	/0000	LEVEL NUMBER
0A50 0	0039	DC	/0039	I
0A51 0	2312	DC	/2312	LS
0A52 0	1600	DC	/1600	M
0A53 0	2526	DC	/2526	NO
0A54 0	1300	DC	/1300	T
0A55 0	1935	DC	/1935	ZE
0A56 0	2926	DC	/2926	RO
0A57 0	FFFF	DC	/FFFF	TERM
* INM15				
0A58 0	000F	DC	/000F	WORD COUNT
0A59 0	340A	DC	/340A	DO
0A5A 0	0A01	DC	/0A01	01
0A5B 0	0000	DC	/0000	SPACE
0A5C 0	0000	DC	/0000	SPACE
0A5D 0	2913	DC	/2913	RT
0A5E 0	2500	DC	/2500	N
0A5F 0	0000	DC	/0000	ROUTINE NUMBER
0A60 0	0000	DC	/0000	BLANK
0A61 0	2729	DC	/2729	PR
0A62 0	3926	DC	/3926	IO
0A63 0	2939	DC	/2939	RI
0A64 0	1318	DC	/1318	TY
0A65 0	0033	DC	/0033	C
0A66 0	3835	DC	/3835	ME
0A67 0	3322	DC	/3322	CK
0A68 0	FFFF	DC	/FFFF	TERM
* INM16				
0A6A 00	00000000	DEC	0	WORD COUNT
0A6C 0	003E	DC	/003E	SPACE
0A6D 0	0000	DC	/0000	SPACE
0A6E 0	0000	DC	/0000	SPACE
0A6F 0	0000	DC	/0000	SPACE
0A70 0	0000	DC	/0000	SPACE
0A71 0	2935	DC	/2935	RE
0A72 0	2800	DC	/2800	Q
0A73 0	1235	DC	/1235	SE
0A74 0	2814	DC	/2814	QU
0A75 0	3525	DC	/3525	EM
0A76 0	3335	DC	/3335	CE
0A77 0	0000	DC	/0000	BLANK
0A78	0034	IN16V BSS	52	
* INM17				
0A8C 0	003E	DC	/003E	WORD COUNT
0A8D 0	0000	DC	/0000	SPACE
0A8E 0	0000	DC	/0000	SPACE
0A8F 0	0000	DC	/0000	SPACE
0A90 0	0000	DC	/0000	SPACE
0A91 0	1229	DC	/1229	SR
0A92 0	1533	DC	/1533	VC
0A93 0	0012	DC	/0012	S
0A94 0	3528	DC	/3528	EQ
0A95 0	1435	DC	/1435	UE
0A96 0	2533	DC	/2533	NC
0A97 0	3500	DC	/3500	r
0A98	0034	IN17V BSS	52	
* INM18				
0AEC 0	0017	DC	/0017	WORD COUNT
0AED 0	350A	DC	/350A	EO
0AEE 0	0A08	DC	/0A08	08
0AEF 0	0000	DC	/0000	SPACE
0AF0 0	0000	DC	/0000	SPACE
0AF1 0	2913	DC	/2913	RT
0AF2 0	2500	DC	/2500	N

INTERRUPT FUNCTION TEST

0AF3 0	0600	DC	/0600	6
0AF4 0	1329	DC	/1329	TR
0AF5 0	3133	DC	/3133	AC
0AF6 0	3500	DC	/3500	E
0AF7 0	3439	DC	/3439	DI
0AF8 0	3400	DC	/3400	D
0AF9 0	2526	DC	/2526	NO
0AFA 0	1300	DC	/1300	T
0AFB 0	3925	DC	/3925	IN
0AFC 0	1329	DC	/1329	TR
0AFD 0	2713	DC	/2713	PT
0AFE 0	0026	DC	/0026	O
0AFF 0	2500	DC	/2500	N
0B00 0	2731	DC	/2731	PA
0B01 0	1212	DC	/1212	SS
0B02 0	0000	DC	/0000	BLANK
0B03 0	000C	DC	/0000	PASS NUMBER
0B04 0	FFFF	DC	/FFFF	TERM
* INM19				
0B05 0	001B	DC	/001B	WORD COUNT
0B06 0	350A	DC	/350A	EO
0B07 0	0A09	DC	/0A09	09
0B08 0	0000	DC	/0000	SPACE
0B09 0	0000	DC	/0000	SPACE
0B0A 0	2913	DC	/2913	RT
0B0B 0	2500	DC	/2500	N
0B0C 0	0600	DC	/0600	6
0B0D 0	3517	DC	/3517	EX
0B0E 0	2735	DC	/2735	PE
0B0F 0	3313	DC	/3313	CT
0B10 0	3534	DC	/3534	ED
0B11 0	0039	DC	/0039	I
0B12 0	2513	DC	/2513	NT
0B13 0	2927	DC	/2927	RP
0B14 0	1300	DC	/1300	T
0B15 0	3629	DC	/3629	FR
0B16 0	2624	DC	/2624	OM
0B17 0	0039	DC	/0039	I
0B18 0	2512	DC	/2512	NS
0B19 0	1329	DC	/1329	TR
0B1A 0	2500	DC	/2500	N
0B1B 0	0000	DC	/0000	BLANK
0B1C 0	0000	DC	/0000	EXPECTED NUMBER
0B1D 0	0000	DC	/0000	BLANK
0B1E 0	3726	DC	/3726	JO
0B1F 0	1300	DC	/1300	T
0B20 0	0000	DC	/0000	ACTUAL NUMBER
0B21 0	FFFF	DC	/FFFF	TERM
* INM20				
0B22 0	000F	DC	/000F	WORD COUNT
0B23 0	350A	DC	/350A	EO
0B24 0	0A31	DC	/0A31	0A
0B25 0	0000	DC	/0000	SPACE
0B26 0	0000	DC	/0000	SPACE
0B27 0	3326	DC	/3326	CO
0B28 0	2512	DC	/2512	NS
0B29 0	2623	DC	/2623	OL
0B2A 0	3500	DC	/3500	E
0B2B 0	3214	DC	/3214	BU
0B2C 0	1313	DC	/1313	TT
0B2D 0	2625	DC	/2625	ON
0B2E 0	0036	DC	/0036	F
0B2F 0	3139	DC	/3139	AI
0B30 0	2335	DC	/2335	LE
0B31 0	3400	DC	/3400	D
0B32 0	FFFF	DC	/FFFF	TERM
* INM21				
0B33 0	0017	DC	/0017	WORD COUNT

88330610
88330620
88330630
88330640
88330650
88330660
88330670
88330680
88330690
88330700
88330710
88330720
88330730
88330740
88330750
88330760
88330770
88330780
88330790
88330800
88330810
88330820
88330830
88330840
88330850
88330860
88330870
88330880
88330890
88330900
88330910
88330920
88330930
88330940
88330950
88330960
88330970
88330980
88330990
88331000
88331010
88331020
88331030
88331040
88331050
88331060
88331070
88331080
88331090
88331100
88331110
88331120
88331130
88331140
88331150
88331160
88331170
88331180
88331190
88331200
88331210
88331220
88331230
88331240
88331250
88331260
88331270
88331280

INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

```

0834 0 340A      DC  /340A  DO
0835 0 0A02      DC  /0A02  OZ
0836 0 0000      DC  /0000  SPACE
0837 0 0000      DC  /0000  SPACE
0838 0 3326      DC  /3326  CO
0839 0 2512      DC  /2512  NS
083A 0 2623      DC  /2623  OL
083B 0 3500      DC  /3500  E
083C 0 3214      DC  /3214  BU
083D 0 1313      DC  /1313  TT
083E 0 2625      DC  /2625  ON
083F 0 0026      DC  /0026  O
0840 0 2500      DC  /2500  N
0841 0 2335      DC  /2335  LE
0842 0 1535      DC  /1535  VE
0843 0 2300      DC  /2300  L
0844 0 0000      DC  /0000  LEVEL NUMBER
0845 0 0039      DC  /0039  I
0846 0 2312      DC  /2312  LS
0847 0 1600      DC  /1600  W
0848 0 3239      DC  /3239  BI
0849 0 1300      DC  /1300  T
084A 0 0000      DC  /0000  ILSW BIT
084B 0 FFFF      DC  /FFFF  TERM

084C 0 0021      *INM22 DC /0021  WORD COUNT
084D 0 330A      DC /330A  CO
084E 0 0A07      DC /0A07  OZ
084F 0 0000      DC /0000  SPACE
0850 0 0000      DC /0000  SPACE
0851 0 1235      DC /1235  SE
0852 0 1300      DC /1300  T
0853 0 3439      DC /3439  DI
0854 0 1231      DC /1231  SA
0855 0 3223      DC /3223  BL
0856 0 3500      DC /3500  E
0857 0 2625      DC /2625  ON
0858 0 0038      DC /0038  H
0859 0 3913      DC /3913  IT
085A 0 0033      DC /0033  C
085B 0 3500      DC /3500  E
085C 0 3125      DC /3125  AN
085D 0 3400      DC /3400  D
085E 0 3326      DC /3326  CO
085F 0 2512      DC /2512  NS
0860 0 0032      DC /0032  B
0861 0 1325      DC /1325  TN
0862 0 1200      DC /1200  S
0863 0 0000      DC /0000  BLANK
0864 0 1235      DC /1235  SE
0865 0 1300      DC /1300  T
0866 0 1329      DC /1329  TR
0867 0 3133      DC /3133  AC
0868 0 3500      DC /3500  E
0869 0 3125      DC /3125  AN
086A 0 3400      DC /3400  D
086B 0 1213      DC /1213  ST
086C 0 3129      DC /3129  AR
086D 0 1300      DC /1300  T
086E 0 FFFF      DC /FFFF  TERM

0870 0 0000      *BSS E 0
0870 0 0017      *INM23 DC /0017  WORD COUNT
0871 0 350A      DC /350A  EO
0872 0 CA32      DC /0A32  OB
0873 0 0000      DC /0000  SPACE
0874 0 0000      DC /0000  SPACE
0875 0 2913      DC /2913  RT

```

```

88331290
88331300
88331310
88331320
88331330
88331340
88331350
88331360
88331370
88331380
88331390
88331400
88331410
88331420
88331430
88331440
88331450
88331460
88331470
88331480
88331490
88331500
88331510
88331520
88331530
88331540
88331550
88331560
88331570
88331580
88331590
88331600
88331610
88331620
88331630
88331640
88331650
88331660
88331670
88331680
88331690
88331700
88331710
88331720
88331730
88331740
88331750
88331760
88331770
88331780
88331790
88331800
88331810
88331820
88331830
88331840
88331850
88331860
88331870
88331880
88331890
88331900
88331910
88331920
88331930
88331940
88331950
88331960

```

```

0876 0 2500      DC /2500  N
0877 0 0100      DC /0100  I
0878 0 3925      DC /3925  IN
0879 0 1329      DC /1329  TR
087A 0 2713      DC /2713  PT
087B 0 0025      DC /0025  N
087C 0 2613      DC /2613  OT
087D 0 0039      DC /0039  I
087E 0 2538      DC /2538  NH
087F 0 3932      DC /3932  IB
0880 0 3913      DC /3913  IT
0881 0 3534      DC /3534  ED
0882 0 0031      DC /0031  A
0883 0 3613      DC /3613  FT
0884 0 3529      DC /3529  ER
0885 0 0000      DC 0      SPACE
0886 0 0000      DC /0000
0887 0 0000      DC /0000
0888 0 FFFF      DC /FFFF  TERM

0889 0 0014      *INM24 DC /0014  WORD COUNT
088A 0 330A      DC /330A  CO
088B 0 0A08      DC /0A08  OC
088C 0 0000      DC 0      SPACE
088D 0 0000      DC 0      SPACE
088E 0 2935      DC /2935  RE
088F 0 2731      DC /2731  PA
0890 0 3929      DC /3929  IR
0891 0 0036      DC /0036  F
0892 0 3139      DC /3139  AI
0893 0 2314      DC /2314  LV
0894 0 2935      DC /2935  RE
0895 0 0032      DC /0032  B
0896 0 3536      DC /3536  EF
0897 0 2629      DC /2629  OR
0898 0 3500      DC /3500  E
0899 0 3326      DC /3326  CO
089A 0 2513      DC /2513  NT
089B 0 3925      DC /3925  IN
089C 0 1439      DC /1439  UI
089D 0 2537      DC /2537  NG
089E 0 FFFF      DC /FFFF  TERM

089F 0 0000      *INM25 DC /0000  WORD COUNT
08A0 0 350A      DC /350A  EO
08A1 0 0A33      DC /0A33  OC
08A2 0 0000      DC 0      SPACE
08A3 0 0000      DC 0      SPACE
08A4 0 3923      DC /3923  IL
08A5 0 2335      DC /2335  LE
08A6 0 3731      DC /3731  GA
08A7 0 2300      DC /2300  L
08A8 0 2913      DC /2913  RT
08A9 0 2500      DC /2500  N
08AA 0 3525      DC /3525  EN
08AB 0 1329      DC /1329  TR
08AC 0 1800      DC /1800  Y
08AD 0 FFFF      DC /FFFF  TERM
08AE 0 0120      END  START

```

```

88331970
88331980
88331990
88332000
88332010
88332020
88332030
88332040
88332050
88332060
88332070
88332080
88332090
88332100
88332110
88332120
88332130
88332140
88332150
88332160
88332170
88332180
88332190
88332200
88332210
88332220
88332230
88332240
88332250
88332260
88332270
88332280
88332290
88332300
88332310
88332320
88332330
88332340
88332350
88332360
88332370
88332380
88332390
88332400
88332410
88332420
88332430
88332440
88332450
88332460
88332470
88332480
88332490
88332500
88332510
88332520
88332530
88332540
88332550

```

INTERRUPT FUNCTION TEST

INTERRUPT FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
BSI	01FC	01C9
BSICK	01F8	01D7
BSWO	018A	013D, 016C, 04E3, 0501, 050C, 0513
BSW00	0192	0136, 013F, 0147, 016D, 018A, 04E5, 0503, 050E, 0515
BSW01	0193	018C
BSW1	018C	
CMTRP	068B	03A6, 0419, 063B, 063E, 0641, 0644, 0647, 064A, 064D, 0650, 0653, 0656, 0659, 065C, 065F, 0662, 0665, 0668, 066B, 066E, 0671, 0674, 0677, 067A, 067D, 0680, 0683, 0686, 0689, 068C
CMT00	06AC	06A7
CMT01	06B1	06AB
CMT02	06A8	06A3
CMT03	06B4	06A9
CMT04	06C2	06D1
CMT05	06BF	06BB
CMT06	06CB	06BB
CNC02	0194	012F
CNC03	0196	0132
CNM00	06CA	0309, 043D, 0692, 06A8, 06C4, 06CC
CNSNS	0436	039E, 0417, 06D8, 06E7
CNST0	04DC	048B
CNST1	04DD	048F
CNST2	04E0	04C7
CNTRL	015B	0179
CN001	01F9	01DB
CN101	0280	020A, 0261, 0263, 02BF, 0300
CN102	0281	020C, 0234, 0247, 027A, 06A1
CN103	0282	0212, 0252
CN104	0283	
CN105	0284	G20E, 02BD
CN200	02B2	029C, 02A2
CN201	02B4	02AA
CN300	0319	02C1, 02DA, 02DF, 02F5, 0306
CN301	031E	02CC, 02D8, 02F9, 02FD, 02FF, 0302, 0304
CN302	031A	0303
CN303	031B	02E9, 069D
CN400	0428	0138, 0380, 0391, 0438
CN401	0429	0138, 0349, 034A, 0405
CN402	042A	0352
CN403	042B	0410, 0697
CN404	042C	0368, 03EA
CN405	042D	03DA
CN406	042E	03FB
CN407	042F	03FE
CN408	0430	0401
CN409	0431	03BD
CN500	048A	045D, 0460, 0463
CN501	048B	045F, 0461, 0462
CN502	048C	0483
CODCV	058E	0566, 058C
CODC1	0595	05B1
CODC2	05A7	05A6
CODC3	05B2	05AC
CODC4	05B6	058F, 0590, 0591
CODWD	058E	0561, 0568, 0595, 05B5
COD00	05C0	059C, 059D, 05A1, 05A2
COD01	05C1	05A9, 05B2
COD02	05C2	05B4
COMP	0433	03B7
CON01	013E	
CON03	016C	0165
CON05	0166	0161
CON06	0159	0170
CTRL1	014E	0149
CTRL2	0155	0150

CTRL3	0158	014D, 0154
DELAY	05F3	036F, 039C, 05FA, 05FC, 05FD
DCLY1	05FF	05F6
DELY2	0600	05F7, 05F8
ECKSW	0286	0214, 023D
ERALT	04F9	041D, 04F8, 04FA, 091D
ERRID	0522	04FF, 050B
ERROR	04F5	01ED, 0238, 02EE, 03CF, 0315, 034C, 0371, 0387, 03A0, 0441, 047B, 04FB, 04FC, 051A, 051C, 06AC, 0681, 06BF, 06CE
ERR01	0508	04FE
ERR02	050A	0507
ERR03	0513	051F
FAIL	01E1	01C4
HOLD	0432	03B5, 03B6, 03B8, 03B9
ICTR	01F6	01C5, 01D6, 0605
ILSAV	06D6	0350, 0359, 03AA, 0594, 06BA, 06C6, 06CB
ILSW	06D4	0602, 060B, 0693, 06DA, 06E9
INCN	049F	0494
INLVT	052E	017C, 01FE, 0226, 0269, 0292, 02B5, 02D0, 032C, 0342, 035C, 0374, 03BB, 044E, 0686, 0903, 0919
INM01	0949	027D
INM02	095E	024E, 040C
INM03	096E	0200, 0228, 023A, 026B, 02B7, 02D2, 02F0, 032E, 0344, 034E, 0360, 0373, 0378, 0389, 06B4
INM04	0983	0202, 022A, 026D, 02B9, 02D4, 0317, 0330, 0346, 035E, 0376, 0443, 068E, 0690, 06B3
INM05	099A	0204, 022C, 026F, 0332, 041B, 041F, 0420, 06AE
INM06	09B4	0206, 0336, 0358, 06D0
INM07	09C9	0173
INM08	09D7	036E, 03E4
INM09	09E7	039B
INM10	09F8	0384, 03CD, 045B
INM11	0A0B	038C, 03D4, 0408, 0473
INM12	0A1D	017E, 0182
INM13	0A2E	02D6, 0311
INM14	0A45	022E, 0271, 06C1
INM15	0A58	0294, 0334, 04EC
INM16	0A6C	04EF
INM17	0AAC	04F2
INM18	0AEC	0479, 047D
INM19	0B05	0905, 091B, 091F
INM20	0B22	03A2
INM21	0B33	03A8, 03B0, 03BF, 03C3
INM22	0B4C	03F5
INM23	0B70	01B2, 01CA, 01EF
INM24	0B89	01F2
INM25	0B9F	01A4
INSAD	0924	0908
INTER	01A2	01A1
INTST	048E	012D, 01DD, 0255, 02A6, 02E5, 0413, 0486, 049D
INT00	01A6	019B, 01E9
INT01	01FE	019C
INT02	0292	019D
INT03	0285	019E
INT04	0328	019F
INT05	0446	01A0
IN001	01C1	01E7
IN002	01DA	01E8
IN16V	0A78	04C8, 04CE, 06E2, 06F0, 0704, 0718, 072C, 0740, 0754, 0768, 077C, 0790, 07A4, 07B8, 07CC, 07E0, 07F4, 0808, 081C, 0830, 0844, 0858, 086C, 0880, 0894, 08A8, 08BC, 08D0, 08E4
IN17	0AB8	04CA, 04D0, 06E4, 06F6, 070A, 071E, 0732, 0746, 075A, 076E, 0782, 0796, 07AA, 07BE, 07D2, 07E6, 07FA, 080E, 0822, 0836, 084A, 085E, 0872, 0886, 089A, 08AE, 08C2, 08D6, 08EA
IOARA	0580	0555, 056A, 0573, 0575, 058C
ISINT	028A	01B6, 01C1, 01DC, 01E1, 01E4, 0224, 0230, 0259, 025D, 0260, 0262, 0265
LHIND	05BF	0593, 0597, 05AB, 05AE

INTERRUPT FUNCTION TEST

LOG	0523	0171, 0180, 01A2, 01F0, 024C, 027B, 0356, 036C, 0382, 038A, 0399, 03C1, 03CB, 03D2, 03E2, 03F3, 0406, 040A, 0459, 0471, 04EA, 04ED, 04F0, 0508, 052D, 0547, 0549, 055C
LOG01	0524	
LOG02	0530	0538, 053A
LOG05	053B	0536
LOG06	0541	0524, 0564
LPERR	0520	01EB, 021E, 0275, 02C4, 033E, 0364, 037C, 0395, 03F1, 0453, 0519
LVLIX	018E	0158, 0220, 0298, 02C8, 04D2
LVLST	04AA	0208, 028B, 0338, 03E8, 0484
LVLS1	0486	04AD
LVLS2	0487	0480
LVL01	063A	0486
LVL02	0643	
LVL03	0646	
LVL04	0649	
LVL05	064C	
LVL06	064F	
LVL07	0652	
LVL08	0655	
LVL09	0658	
LVL10	065B	
LVL11	065E	
LVL12	0661	
LVL13	0664	
LVL14	0667	
LVL15	066A	
LVL16	066D	
LVL17	0670	
LVL18	0673	
LVL19	0676	
LVL20	0679	
LVL21	067C	
LVL22	067F	
LVL23	0682	
LVL24	0685	
LVL25	0688	
LVL26	063D	
LVL27	0640	042C
LVSAY	0198	0148, 0152, 0156, 0216, 0219, 029D, 02CD, 03C6, 03DD
LVST1	04AE	04B3
MASK0	0320	01A6, 02C6, 0312, 048F, 0525
MASK1	0322	01A8, 02C7, 0313, 0491, 0527
NEST1	04A0	024A, 03F9, 04A7
NSTCN	04A9	04A2
OPI ND	019A	0146, 0529
PLEXT	0607	018B, 01CE
PL1	01C4	0189, 01F7
PL2	01D5	01CC, 01F8
POLEP	01E9	01C7, 01D8
POLL	0601	01F5, 0604
PRIP1	04E2	02A4, 03D0, 03E6, 04F3
PRIST	04B8	0296, 03C4, 03D8, 04DA
PRIXT	04F3	04E9
PRI01	06E0	04C2, 06EB
PRI02	0716	0721
PRI03	072A	0735
PRI04	073E	0749
PRI05	0752	075D
PRI06	0766	0771
PRI07	077A	0785
PRI08	078E	0799
PRI09	07A2	07AD
PRI10	07B6	07C1
PRI11	07CA	07D5
PRI12	07DE	07E9
PRI13	07F2	07FD

INTERRUPT FUNCTION TEST

PRI14	0806	0811
PRI15	081A	0825
PRI16	082E	0839
PRI17	0842	084D
PRI18	0856	0861
PRI19	086A	0875
PRI20	087E	0889
PRI21	0892	089D
PRI22	08A6	08B1
PRI23	08BA	08C5
PRI24	08CE	08D9
PRI25	08E2	08ED
PRI26	06EE	04DC, 06F9
PRI27	0702	042D
PRSN	0586	053C
PRSNS	0584	0530, 0540
PRWRT	0588	052F, 053B
PRO20	0723	071C
PRO21	071D	0725
PRO30	0737	0730
PRO31	0731	0739
PRO32	073C	0737
PRO40	074B	0744
PRO41	0745	074D
PRO42	0750	074B
PRO50	075F	0758
PRO51	0759	0761
PRO52	0764	075F
PRO60	0773	076C
PRO61	076D	0775
PRO62	0778	0773
PRO70	0787	0780
PRO71	0781	0789
PRO72	078C	0787
PRO80	079B	0794
PRO81	0795	079D
PRO82	07A0	079B
PRO90	07AF	07A8
PRO91	07A9	07B1
PRO92	07B4	07AF
PR100	07C3	07BC
PR101	07BD	07C5
PR102	07C8	07C3
PR110	07D7	07D0
PR111	07D1	07D9
PR112	07DC	07D7
PR120	07EB	07E4
PR121	07E5	07ED
PR122	07F0	07EB
PR130	07FF	07F8
PR131	07F9	0801
PR132	0804	07FF
PR140	0813	080C
PR141	080D	0815
PR142	0818	0813
PR150	0827	0820
PR151	0821	0829
PR152	082C	0827
PR160	083B	0834
PR161	0835	083D
PR162	0840	083B
PR170	084F	0848
PR171	0849	0851
PR172	0854	084F
PR180	0863	085C
PR181	085D	0865
PR182	0868	0863
PR190	0877	0870

INTERRUPT FUNCTION TEST

PR191	0871	0879
PR192	087C	0877
PR200	088B	0884
PR201	0885	088D
PR202	0890	088B
PR210	089F	0898
PR211	0899	08A1
PR212	08A4	089F
PR220	08B3	08AC
PR221	08AD	08B5
PR222	08B8	08B3
PR230	08C7	08C0
PR231	08C1	08C9
PR232	08CC	08C7
PR240	08DB	08D4
PR241	08D5	08DD
PR242	08E0	08DB
PR250	08EF	08E8
PR251	08E9	08F1
PR252	08F4	08EF
PR260	06FB	06F4
PR261	06F5	06FD
PR262	0700	04D6, 06FB
PR270	070F	0708
PR271	0709	0711
PR272	0714	04D8, 070F
PSSW	0285	0210, 0240, 02BE, 02E2
REQCE	0712	0703, 0709
REQER	06ED	06E1
REQTR	06FE	0422, 06EF, 06F5
REQ00	0726	0717, 071D
REQ01	073A	072B, 0731
REQ02	074E	073F, 0745
REQ03	0762	0753, 0759
REQ04	0776	0767, 076D
REQ05	078A	077B, 0781
REQ06	079E	078F, 0795
REQ07	0782	07A3, 07A9
REQ08	07C6	07B7, 07BD
REQ09	07DA	07CB, 07D1
REQ10	07EE	07DF, 07E5
REQ11	0802	07F3, 07F9
REQ12	0816	0807, 080D
REQ13	082A	081B, 0821
REQ14	083E	082F, 0835
REQ15	0852	0843, 0849
REQ16	0866	0857, 085D
REQ17	087A	086B, 0871
REQ18	088E	087F, 0885
REQ19	08A2	0893, 0899
REQ20	08B6	08A7, 08AD
REQ21	08CA	08B8, 08C1
REQ22	08DE	08CF, 08D5
REQ23	08F2	08E3, 08E9
RTN	019B	016A
RTNNO	018F	015A, 0160, 0162, 0166, 0168, 017A, 01DA, 0250, 02A8, 02E7,
		040E, 0481, 0695, 069B
		01DF, 0257, 02AD, 02EC, 0415, 0488
RTNRT	0177	0242, 027F
RT100	0212	021C, 025F
RT101	0226	0237, 06AF, 06BD, 06C8
RT104	023B	023C, 0266
RT105	0259	023F, 0273
RT106	0267	0249
RT107	0279	025B
RT108	0260	0258
RT109	0234	0278
RT110	0250	0245
RT300	02BF	02E4

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RT301	C2D0	02C2, 02FE, 0305
RT302	02D8	
RT303	02EE	02DC
RT304	02F9	02DE
RT305	02D0	02F3, 030D, 0314, 0318
RT306	02C8	02F8
RT307	02FF	02FB
RT308	0306	069F
RT309	030F	0308
RT310	0315	030C
RT311	02F4	02E1
RT400	034A	033C
RT401	0438	033A, 0445
RT402	0350	0340
RT403	0374	0366
RT404	036C	0362
RT405	038A	037E
RT406	03A4	0397
RT407	03D0	
RT408	03E6	
RT409	03E2	
RT410	0417	03ED
RT411	0420	03EF
RT412	0382	037A
RT413	035C	034F, 0355
RT414	040E	032A
RT415	03C4	03A3
RT500	045D	047F, 0924, 0925, 0926, 0927, 0928, 0929, 092A, 092B, 092C,
		092D
RT501	0467	0466, 047E, 0921
RT502	047F	0451, 0470
RT504	0481	0448, 0475
RUNSW	0199	0137, 0143, 0243, 0328, 0446, 06C2
SEQCK	0191	0177, 01DC, 0253, 02AB, 02EA, 0411, 0484
SERVIC	06D7	0232, 0283, 042E, 04A9, 06A5, 06DB
SET01	048C	04C1
SET02	04C8	04CD
SIX	0190	0163
SNSWS	0188	0158
START	012D	0175, 0184, 0195, 08AE
SVEXT	062E	0627
SVINT	0609	02F1, 038E, 03A4, 03D6, 0438, 049F, 04F6, 062E
SVINO	0613	0623, 062C
SVIN1	0615	061D
SVIO	0638	060A, 0619, 061A, 062D
SVO	0630	0621
SV1	0631	0613
SV2	0632	0611
SV3	0633	0628
SV4	0634	0610, 0615, 061E, 0620, 0628
SV5	0635	0614, 0617, 0618
SV6	0636	0612, 0618, 0629
SV7	0637	060D, 0624
TRACE	08F6	044A, 08F8, 08FA, 08FE, 0901, 0907, 090F, 0912
TRACO	0903	08FC
TRAC1	0914	090A, 0920
TRAC2	0912	
TRAER	0477	0450, 0468, 046A
TRCNO	0921	08FB
TRCN1	0922	0914, 0915, 0916, 0917
TRCN2	0923	08F7, 0900, 0911
TRIND	048D	0457, 0467, 046E, 090D
TWRTR	0548	052B, 0553
TWRTO	057D	0554
TWRT1	057E	0563
TWR01	0554	0550
TWR02	055F	055E, 057C
TWR03	0579	0572

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INTERRUPT FUNCTION TEST

TWSNS	058A	054D, 0557, 056C
TWVRT	058C	0556, 0568
UMSK0	0324	018D, 02F6, 0499, 0543
UMSK1	0326	01BF, 02F7, 049B, 0545
VCTOR	01F5	01AB
WRDSR	057F	054C, 0570, 0576, 057A
WTA	03D5	300A
WTB	03E5	300B
WTC	03F6	300C, 0424, 0426
WTD	0409	300D
WTE	0400	300E
WTF	045C	300F
WT1	013C	3001, 01A5, 01F3
WT10	0474	3010
WT11	051E	3011, 0512
WT12	0537	3012, 0531
WT13	0539	3013, 0534
WT14	0552	3014
WT15	06C7	3015
WT2	0174	3002
WT3	0183	3003
WT4	027E	3004
WT5	024F	3005
WT6	0358	3006
WT7	0385	3007
WT8	038D	3008, 0393
WT9	03CE	3009
XIO	01FA	01B1
XIOCC	028C	0184, 0222, 029A, 02CA, 0404
XIOCK	01F7	01C6
XIOSN	056C	056F
XIOWR	056B	0578
ZONE	05C3	05A4
ZONEN	05C7	05C3
ZONE1	05D2	05C4
ZONE2	05D0	05C5
ZONE3	05E7	05C6

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1. PURPOSE

THE PURPOSE OF THE 1800 PROCESSOR CONTROL (PC) FUNCTION TEST IS TO LOCATE FAILING INSTRUCTIONS. EACH SEPARATE PC INSTRUCTION IS TESTED AND CHECKED FOR COMPLIANCE WITH THE PRODUCT SPECIFICATIONS. FEATURES THAT ARE NOT UNIQUE TO AN OPERATION CODE (INDEXING, INDIRECT ADDRESSING, ETC.) ARE ALSO TESTED. I/O RELATED FEATURES (INTERRUPT, CYCLE STEAL, ETC.) ARE NOT TESTED.

PROGRAM RUNNING TIME
2 USEC MACHINE - APPROXIMATELY 1 MINUTE
4 USEC MACHINE - APPROXIMATELY 2 MINUTES

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE PROGRAM CAN BE OPERATED BY ITSELF BUT MUST BE LOADED BY THE 1800 BASIC DIAGNOSTIC LOADER.

2.2 EQUIPMENT PREREQUISITES

- A. 1800 PC HAVING 4096-WORD STORAGE.
- B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

THE 1800 P C FUNCTION TEST (08B4) IS LOADED BY THE 1800 BASIC LOADER. SEE THE 1800 BASIC LOADER DOCUMENTATION FOR THE DESCRIPTION OF THE LOADING PROCEDURE.

3.2 PROGRAM OPERATION

AFTER THE PROGRAM IS LOADED THE FOLLOWING NORMAL WAITS OCCUR,

LOCATION B REG SYMBOLIC	DESCRIPTION AND ACTION
3000 (X000)	START OF PROGRAM. SET ALL BIT SWITCHES ON. PRESS START.
3001 (X001)	TESTING OF BIT SWITCHES ON COMPLETE, TURN OFF, PRESS START.
3002 (X003)	TESTING OF BIT SWITCHES OFF COMPLETE SET IN OPTION, PRESS START.
3003 (X007)	PROGRAM COMPLETED. PUSH START TO RERUN PROGRAM. IF OTHER WAITS OCCUR, REFER TO SECTION 3.5 FOR ERROR ISOLATION.

ANY WAITS OTHER THAN THOSE ABOVE ARE ERROR WAITS.

WHEN AN ERROR WAIT IS OBTAINED,

1. SEE THE PROGRAM LISTING TO DETERMINE THE PROBLEM. ERROR WAITS ARE DOCUMENTED AT THE FRONT OF THE PROGRAM LISTING BY THE CONTENTS OF THE B REGISTER.
2. IF THE ERROR WAIT HAS B REGISTER LESS THAN 3069, THE OPERATOR CANNOT LOOP ON THAT ERROR. INSTEAD, THE OPERATOR SHOULD SINGLE INSTRUCTION STARTING AT THE BEGINNING OF THE FAILING ROUTINE TO DETERMINE THE EXACT FAILURE. (SECTION 3.5)
3. IF THE ERROR WAIT HAS B REGISTER GREATER THAN 3068, THE OPERATOR SHOULD, (SECTION 3.5)
 - A. LOOP INSTRUCTION BEING TESTED (BIT SW 8 ON)
OR IF A LARGER LOOP IS DESIRED
LOCK ON ERROR (BIT SW 12 ON)
OR
LOOP ON ROUTINE (BIT SW 10 ON)
 - B. SINGLE STEP TO LOCATE THE EXACT FAILURE.
 - C. IF NO ERROR OCCURS, BYPASS THE ERROR WAIT (BIT SW 14 ON) AND USE A SCOPE TO DETERMINE THE FAILURE.

TABLE 1

DATA ENTRY SWITCHES															DESCRIPTION	
0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	
														1		BYPASS ERROR WAIT (SEE NOTE)
														1		LOCK ON ERROR
														1		LOOP PROGRAM
														1		LOOP ON ROUTINE
														1		LOOP ON INSTRUCTION BEING TESTED
														1		BYPASS MPL/DIV TEST

NOTE- IF ERROR OCCURS, BITS 12 OR 8 MUST BE ON TO MAKE BIT 14 EFFECTIVE.

3.3 TERMINATION

NORMAL TERMINATION OCCURS WITH PROGRAM STOPPING AT WAIT WITH B REG = 3003.

3.4 RESTART PROCEDURE

PRESS STOP, RESET, AND START BUTTONS.

3.5 ERROR WAITS

THERE ARE TWO TYPES OF ERROR CONDITIONS WHICH CAUSE ERROR WAITS.

1. ERRORS WHICH USE THE COMMON ERROR CONTROL ROUTINE (F000).
2. ERRORS WHICH OCCUR BEFORE SUFFICIENT PORTIONS OF THE HARDWARE HAVE BEEN CHECKED OUT TO ALLOW USE OF THE COMMON ERROR CONTROL ROUTINE.

ERRORS WHICH USE THE COMMON ERROR CONTROL ROUTINE HAVE B REG NUMBERS FROM /3069 AND UP. WHEN A NUMBERED WAIT OCCURS, BITS 5-15 OF THE STORAGE (B REG = 3XXX). WHEN A NUMBERED WAIT OCCURS, BITS 5-15 OF THE STORAGE BUFFER REGISTER GIVE THE ERROR IDENTIFICATION NUMBER. TO FIND THE FAILING ROUTINE, LOOK IN THE ERROR IDENTIFICATION TABLE (IN FRONT OF THE LISTING). THIS WILL GIVE YOU THE SYMBOLIC AND ACTUAL STARTING ADDRESS OF THE ROUTINE THAT FAILED.

ERRORS WHICH DO NOT USE THE COMMON ERROR CONTROL ROUTINE HAVE B REG NUMBERS FROM /3003 THRU /3068. THE INSTRUCTION REG WILL POINT DIRECTLY TO THE FAILING ROUTINE. TO FACILITATE FINDING THE START OF A TEST ROUTINE EACH TEST ROUTINE BEGINS WITH A LABEL HAVING AN A OR B AS ITS FIRST LETTER. IN THE LISTING EACH ROUTINE IS FURTHER BRACKETED BY A SOLID LINE OF ASTRISKS. TO FIND THE FAILING ROUTINE OF ERRORS WHICH DO NOT USE THE COMMON ERROR CONTROL START AT THE LOCATION SPECIFIED BY THE ERROR WAIT AND WORK UP THE LISTING (BACKWARDS) UNTIL THE FIRST SYMBOLIC LOCATION WHICH HAS A LABEL BEGINNING WITH A AND B. THIS IS THE START OF THE FAILING ROUTINE.

TWO WAYS OF LOCATING A FAILURE ARE AS FOLLOWS-

- A. DETERMINE WHAT FAILURE CAUSED THE ERROR WAIT. TO LOCATE THE FAILURE, IT IS RECOMMENDED THAT THE PROGRAM BE MANUALLY ENTERED AT THE START OF THE FAILING ROUTINE AND SINGLE INSTRUCTION, FOLLOWING THE LISTING TO DETERMINE THE EXACT FAILURE.
- B. USE AN OSCILLOSCOPE TO HELP LOCATE THE FAILURE. IF THE FAILURE IS IN THE COMMON-ERROR ROUTINE, SIMPLY TURN ON CONSOLE ENTRY SWITCH 8 AND DEPRESS START PUSHBUTTON TO LOOP ON THE INSTRUCTION BEING TESTED. IF THE FAILURE IS IN THE FIRST PART OF THE PROGRAM (BEFORE THE COMMON ERROR ROUTINE INSTRUCTIONS HAVE BEEN CHECKED OUT), A BRANCH (PDX) TO THE BEGINNING OF THE ROUTINE MAY BE MANUALLY INSERTED IN PLACE OF THE WAIT INSTRUCTION. THEN, THE ROUTINE MAY BE LOOPED.

4. PRINTOUTS (NONE)

5. COMMENTS

THE 1800 P-C FUNCTION TEST STARTS WITH VERY SIMPLE INSTRUCTIONS AND DETERMINES IF EACH INSTRUCTION PERFORMS TO SPECIFICATIONS. EACH SUCCESSIVE ROUTINE ATTEMPTS TO UTILIZE ONLY AN INSTRUCTION THAT HAS NOT BEEN PREVIOUSLY TESTED. THE PROGRAM OPTIONS PROVIDE A MEANS FOR CONTINUOUSLY LOOPING THE ENTIRE PROGRAM AND ALSO ALLOW FAILING ROUTINES TO BE LOOPED.

AN ATTEMPT IS MADE DURING THE EARLY STAGES OF THE PROGRAM TO DEVELOP THOSE INSTRUCTIONS WHICH ALLOW THE USAGE OF THE COMMON CONTROL (F00E AND F005) AND ERROR (F000) ROUTINES. AFTER THESE INSTRUCTIONS HAVE BEEN TESTED THE USER THEN HAS THE ABILITY TO REQUEST VARIOUS CONTROL OPTIONS BY MEANS OF THE DATA ENTRY SWITCHES.

5.1 OPERATING MODES

THE NORMAL MODE OF OPERATION IS WITH THE DATA ENTRY SWITCHES SET TO /0000. THIS CAUSES A SINGLE PASS THROUGH THE PROGRAM WITH AN ERROR WAIT OCCURRING IF AN ERROR IS DETECTED.

IF AN ERROR IS DETECTED AND THE COMMON ERROR WAIT OCCURS, THE USER SHOULD TURN ON THE " LOOP ON ROUTINE " (DATA ENTRY SWITCHES SET TO /0020) AND SINGLE INSTRUCTION THROUGH THE FAILING ROUTINE TO ISOLATE THE FAILING INSTRUCTION.

IF THE FAILING ROUTINE DOES NOT FAIL WHEN EXECUTED IN SINGLE INSTRUCTION MODE, THE USER CAN TURN ON THE " BYPASS ERROR WAIT " SWITCH AND THE " LOOP ROUTINE " SWITCH (DATA ENTRY SWITCHES SET TO /0022) AND PROCEED TO USE SCOPING TECHNIQUES TO ISOLATE THE FAILURE.

5.2 PROGRAM LABELS

LABELS OCCURRING IN THE PROGRAM LISTING CAN BE QUICKLY IDENTIFIED AS FOLLOWS-

- A. LABELS STARTING WITH A OR B INDICATE THE BEGINNING OF A TEST ROUTINE.
- B. LABELS STARTING WITH G, H, J, OR K INDICATE COMMUNICATION LABELS WITH A ROUTINE.
- C. LABELS STARTING WITH V OR X ARE RESERVED FOR WAITS.
- D. LABELS STARTING WITH N, R, OR S INDICATE A CONSTANT OR WORK AREA.
- E. LABELS STARTING WITH F, W, Z OR U ARE USED IN COMMON OR SPECIAL ROUTINES THAT ARE NOT A REGULAR TEST ROUTINE.

6. APPENDIX (NONE)

PROCESSOR-CONTROLLER FUNCTION TEST

```

02BC      ABS      88400030
          ORG      /3000      88400040
          *****      88400050
          * THIS ENGINEERING CHANGE REFLECTS MINOR BUT      88400060
          * SIGNIFICANT MODIFICATIONS TO THE PROGRAM AND      88400070
          * PROGRAM DESCRIPTION.      88400080
          *      88400090
          * THE CHANGES ARE--      88400100
          *      88400110
          * 1. THE PROGRAM IDENTIFICATION HAS BEEN ADDED      88400120
          * JUST AFTER THE ORG 300 INSTRUCTION TO ENABLE      88400130
          * THIS PROGRAM TO BE CALLED OFF THE DISK.      88400140
          *      88400150
          * 2. WAIT /3000 AT LABEL X000 HAS BEEN INSERTED      88400160
          * AT THE FRONT OF THE PROGRAM SO THAT THE      88400170
          * INITIAL BIT SWITCH SETTINGS CAN BE MADE.      88400180
          *      88400190
          *      88400200
          *      88400210
          *      88400220
          *      88400230
          *      88400240
          *      88400250
          *      88400260
          *      88400270
          *      88400280
          *      88400290
          *      88400300
          *      88400310
          *      88400320
          *      88400330
          *      88400340
          *      88400350
          *      88400360
          *      88400370
          *      88400380
          *      88400390
          *      88400400
          *      88400410
          *      88400420
          *      88400430
          *      88400440
          *      88400450
          *      88400460
          *      88400470
          *      88400480
          *      88400490
          *      88400500
          *      88400510
          *      88400520
          *      88400530
          *      88400540
          *      88400550
          *      88400560
          *      88400570
          *      88400580
          *      88400590
          *      88400600
          *      88400610
          *      88400620
          *      88400630
          *      88400640
          *      88400650
          *      88400660
          *      88400670
          *      88400680
          *      88400690
          *      88400700

          OPERATING INSTRUCTIONS
          BIT SWITCH SETTINGS
          BIT 14 ON BYPASS ERROR WAIT
          BIT 12 OR 8 MUST BE ON TO
          MAKE BIT 14 EFFECTIVE.
          BIT 13 NOT USED
          BIT 12 LOCK ON ERROR
          BIT 11 ON LOOP PROGRAM
          BIT 10 ON LOOP ROUTINE
          BIT 9 NOT USED
          BIT 8 LOOP ON INSTRUCTION BEING
          TESTED
          BIT 7 ON BYPASS MPL/DIV TEST

          *****
          B-REG  I-REG      A HALT AT      88400560
          *****      88400570
          3000 0 012E      DC      X000+1      1130 AND 1800      88400580
          *      SET DATA ENTRY SWITCHES      88400600
          *      TO /FFFF + PRESS START      88400610
          *      88400620
          *      (1800) ALSO SET S/P SMS TO      88400630
          *      /FF00 AND PRESS START      88400640
          *      88400650
          3001 0 02B7      DC      X001+1      SET SENSE/PRG AND      88400660
          *      DATA ENTRY SWITCHES      88400670
          *      TO ZERDS AND PRESS      88400680
          *      START      88400690
          *      88400700

```

PROCESSOR-CONTROLLER FUNCTION TEST

```

3002 0 02CB      DC      X003+1      SET SWITCHES FOR OPTIONS      88400710
          *      AND PRESS START      88400720
          *      88400730
          *      88400740
          *      88400750
          *      88400760
          *      88400770
          *      88400780
          *      88400790
          *      88400800
          *      88400810
          *      88400820
          *      88400830
          *      88400840
          *      88400850
          *      88400860
          *      88400870
          *      88400880
          *      88400890
          *      88400900
          *      88400910
          *      88400920
          *      88400930
          *      88400940
          *      88400950
          *      88400960
          *      88400970
          *      88400980
          *      88400990
          *      88401000
          *      88401010
          *      88401020
          *      88401030
          *      88401040
          *      88401050
          *      88401060
          *      88401070
          *      88401080
          *      88401090
          *      88401100
          *      88401110
          *      88401120
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          *****
          ADDRESS      *
          OF      *
          B-REG ROUTINE * A REG Q REG XR-1 XR-2 XR-3 STATUS
          *****
          3004 0 012E      DC      A080      MDX      88400880
          *      SHORT FORM MDX FAILED TO MODIFY I CTR +1      88400890
          *      88400900
          *      88400910
          *      88400920
          *      88400930
          *      88400940
          *      88400950
          *      88400960
          *      88400970
          *      88400980
          *      88400990
          *      88401000
          *      88401010
          *      88401020
          *      88401030
          *      88401040
          *      88401050
          *      88401060
          *      88401070
          *      88401080
          *      88401090
          *      88401100
          *      88401110
          *      88401120
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          3005 0 012E      DC      A080      MDX      MOD+0
          3006 0 012E      DC      A080      MDX      MOD+0
          *      +1
          *      SHORT FORM MDX-SHOULD HAVE MODIFIED I CTR
          *      +2 BUT MODIFIED BY 0 OR +1
          *      88400950
          *      88400960
          *      88400970
          *      88400980
          *      88400990
          *      88401000
          *      88401010
          *      88401020
          *      88401030
          *      88401040
          *      88401050
          *      88401060
          *      88401070
          *      88401080
          *      88401090
          *      88401100
          *      88401110
          *      88401120
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          3007 0 012E      DC      A080      MDX      MOD+0
          3008 0 012E      DC      A080      MDX      MOD+0
          3009 0 012E      DC      A080      MDX      MOD+0
          300A 0 012E      DC      A080      MDX      MOD+0
          *      +1
          *      +2
          *      +3
          *      SHORT FORM MDX SHOULD HAVE MODIFIED I CTR
          *      +4 BUT MODIFIED BY 0, +1, +2 OR +3
          *      88401030
          *      88401040
          *      88401050
          *      88401060
          *      88401070
          *      88401080
          *      88401090
          *      88401100
          *      88401110
          *      88401120
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          300B 0 012E      DC      A080      MDX      MOD+0
          *      MDX SHORT FORM FAILED TO MODIFY I CTR
          *      88401070
          *      88401080
          *      88401090
          *      88401100
          *      88401110
          *      88401120
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          300C 0 012E      DC      A080      MDX      MOD+0
          300D 0 012E      DC      A080      MDX      MOD+0
          300E 0 012E      DC      A080      MDX      MOD+0
          *      +1
          *      +2
          *      MDX SHORT FORM-SHOULD HAVE MODIFIED I CTR
          *      -2, DID MODIFY BY 0, +1 OR +2
          *      88401130
          *      88401140
          *      88401150
          *      88401160
          *      88401170
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          300F 0 013F      DC      A0C0      BSC,C
          *      N/A      N/A      N/A      N/A      N/A      C+0
          *      BSC SKIPPED-SHOULD NOT HAVE
          *      88401180
          *      88401190
          *      88401200
          *      88401210
          *      88401220
          *      88401230
          *      88401240
          *      88401250
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          3010 0 013F      DC      A0C0      BSC,0
          3011 0 013F      DC      A0C0      BSC,0
          *      N/A      N/A      N/A      N/A      N/A      C+0 AFTER LDS
          *      N/A      N/A      N/A      N/A      N/A      C AFTER 1ST RSC
          *      FIRST BSC SKIPPED-SHOULD NOT HAVE
          *      SECOND BSC FAILED TO SKIP-INDICATING 1ST BSC
          *      FAILED TO TURN OFF OVERFLOW
          *      88401260
          *      88401270
          *      88401280
          *      88401290
          *      88401300
          *      88401310
          *      88401320
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          3012 0 013F      DC      A0C0      BSC,C
          *      N/A      N/A      N/A      N/A      N/A      OFF
          *      BSC DID NOT SKIP WITH OVERFLOW OFF
          *      88401330
          *      88401340
          *      88401350
          *      88401360
          *      88401370
          *      88401380

          3013 0 014C      DC      A100      LD
          *      0000      N/A      N/A      N/A      N/A      N/A
          *      ACCUM NOT EQUAL TO 0000

```

PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS
OF
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3014 0 014C      DC      A100      LD
* 0000 N/A N/A N/A N/A N/A 1ST LD
* 0000 N/A N/A N/A N/A N/A 2ND LD
* A LOAD 0000 FOLLOWED BY LOAD 0000 DID NOT
* LEAVE ACCUM EQUAL TO 0000
*
*
3015 0 014C      DC      A100      BSC,E
* 0000 N/A N/A N/A N/A N/A
* BSC FAILED TO SKIP
*
*
3016 0 0154      DC      A140      LD
* 0000 N/A N/A N/A N/A N/A 1ST VALVE
* FFFF N/A N/A N/A N/A N/A AFTER LD
* LOAD FFFF ON TDP OF 0000 DID NOT LEAVE ACC
* NEGATIVE
*
*
3017 0 0154      DC      A140      BSC,+
* FFFF N/A N/A N/A N/A N/A
*
*
3018 0 0154      DC      A140      BSC,E
* FFFF N/A N/A N/A N/A N/A
* BSC SKIPPED SHOULD NOT HAVE
*
*
3019 0 0154      DC      A140      ACCUM NOT EQUAL 7FFF
301A 0 0154      DC      A140      ACCUM NOT EQUAL 3FFF
301B 0 0154      DC      A140      ACCUM NOT EQUAL 1FFF
301C 0 0154      DC      A140      ACCUM NOT EQUAL 0FFF
301D 0 0154      DC      A140      ACCUM NOT EQUAL 07FF
301E 0 0154      DC      A140      ACCUM NOT EQUAL 03FF
301F 0 0154      DC      A140      ACCUM NOT EQUAL 01FF
3020 0 0154      DC      A140      ACCUM NOT EQUAL 00FF
3021 0 0154      DC      A140      ACCUM NOT EQUAL 007F
3022 0 0154      DC      A140      ACCUM NOT EQUAL 003F
3023 0 0154      DC      A140      ACCUM NOT EQUAL 001F
3024 0 0154      DC      A140      ACCUM NOT EQUAL 000F
3025 0 0154      DC      A140      ACCUM NOT EQUAL 0007
3026 0 0154      DC      A140      ACCUM NOT EQUAL 0003
3027 0 0154      DC      A140      ACCUM NOT EQUAL 0001
3028 0 0154      DC      A140      ACCUM NOT EQUAL 0000
3029 0 0154      DC      A140      ACCUM NOT EQUAL 0000
* FFFF N/A N/A N/A N/A N/A LOADED
* 0000 N/A N/A N/A N/A N/A AFTER SRA'S
* THE ABOVE WAITS OCCUR AS A RESULT OF A
* FAILURE ON A ROUTINE THAT LOADS FFFF ON
* 0000 AND CHECKS USING SRA 1 AND BSC E.
*
*
302A 0 01A0      DC      A180      ACCUM NOT EQUAL FFFF
302B 0 01A0      DC      A180      ACCUM NOT EQUAL FFFF
302C 0 01A0      DC      A180      ACCUM NOT EQUAL 7FFF
302D 0 01A0      DC      A180      ACCUM NOT EQUAL 3FFF
302E 0 0154      DC      A140      ACCUM NOT EQUAL 1FFF
302F 0 01A0      DC      A180      ACCUM NOT EQUAL 0FFF
3030 0 01A0      DC      A180      ACCUM NOT EQUAL 07FF
3031 0 01A0      DC      A180      ACCUM NOT EQUAL 03FF

```

```

88401390
88401400
88401410
88401420
88401430
88401440
88401450
88401460
88401470
88401480
88401490
88401500
88401510
88401520
88401530
88401540
88401550
88401560
88401570
88401580
88401590
88401600
88401610
88401620
88401630
88401640
88401650
88401660
88401670
88401680
88401690
88401700
88401710
88401720
88401730
88401740
88401750
88401760
88401770
88401780
88401790
88401800
88401810
88401820
88401830
88401840
88401850
88401860
88401870
88401880
88401890
88401900
88401910
88401920
88401930
88401940
88401950
88401960
88401970
88401980
88401990
88402000
88402010
88402020
88402030
88402040
88402050
88402060

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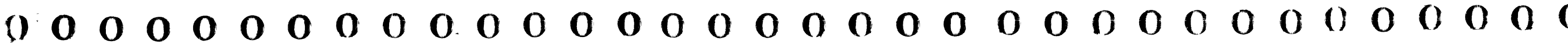
*****
ADDRESS
OF
B-REG ROUTINE * A-REG Q-REG Xk-1 Xk-2 XR-3 STATUS
*****
3032 0 01A0      DC      A180      ACCUM NOT EQUAL 01FF
3033 0 01A0      DC      A180      ACCUM NOT EQUAL 00FF
3034 0 01A0      DC      A180      ACCUM NOT EQUAL 007F
3035 0 01A0      DC      A180      ACCUM NOT EQUAL 003F
3036 0 01A0      DC      A180      ACCUM NOT EQUAL 001F
3037 0 01A0      DC      A180      ACCUM NOT EQUAL 000F
3038 0 01A0      DC      A180      ACCUM NOT EQUAL 0007
3039 0 01A0      DC      A180      ACCUM NOT EQUAL 0003
303A 0 01A0      DC      A180      ACCUM NOT EQUAL 0001
303B 0 01A0      DC      A180      ACCUM NOT EQUAL 0000
303C 0 01A0      DC      A180      ACCUM NOT EQUAL 0000
* FFFF N/A N/A N/A N/A N/A LOADED
* 0000 N/A N/A N/A N/A N/A AFTER SRA'S
* THE ABOVE WAITS OCCUR AS A RESULT OF A
* FAILURE ON A ROUTINE THAT LOADS FFFF ON
* FFFF AND CHECKS USING SRA 1 AND BSC E.
*
*
303D 0 01EB      DC      A1C0      LD 0000 DN 0000
* 0000 N/A N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
*
303E 0 01EB      DC      A1C0      LD FFFF DN 0000
* 0000 N/A N/A N/A N/A N/A BEFORE LD
* FFFF N/A N/A N/A N/A N/A AFTER LD
* ACCUM NOT EQUAL FFFF
*
*
303F 0 01F5      DC      A1D0      LD
* 0000 N/A N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
*
3040 0 01F5      DC      A1D0      EOR
* 0000 N/A N/A N/A N/A N/A
* 0000 N/A N/A N/A N/A N/A
* WITH ACCUM EQUAL 0000 AN EOR USING 0000 DID
* NOT RESULT IN ACCUM EQUAL 0000
*
*
3041 0 01F5      DC      A1D0      EOR
* FFFF N/A N/A N/A N/A N/A LOADED + EOR
* 0000 N/A N/A N/A N/A N/A SHOULD BE
* WITH ACCUM EQUAL FFFF AN EOR USING FFFF DID
* NOT RESULT IN ACCUM EQUAL 0000
*
*
3042 0 01F5      DC      A1D0      EOR
3043 0 01F5      DC      A1D0
* 0000 N/A N/A N/A N/A N/A BEFORE
* FFFF N/A N/A N/A N/A N/A S/B AFTER
* WITH ACCUM EQUAL 0000 AN EOR USING FFFF DID
* NOT RESULT IN ACCUM EQUAL FFFF
*
*
3044 0 01F5      DC      A1D0      EOR
* FFFF N/A N/A N/A N/A N/A BEFORE EOR
* FFFF N/A N/A N/A N/A N/A S/B AFTER
* WITH ACCUM EQUAL FFFF AN EOR USING 0000 DID
* NOT RESULT IN ACCUM EQUAL FFFF
*
*

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88402070
88402080
88402090
88402100
88402110
88402120
88402130
88402140
88402150
88402160
88402170
88402180
88402190
88402200
88402210
88402220
88402230
88402240
88402250
88402260
88402270
88402280
88402290
88402300
88402310
88402320
88402330
88402340
88402350
88402360
88402370
88402380
88402390
88402400
88402410
88402420
88402430
88402440
88402450
88402460
88402470
88402480
88402490
88402500
88402510
88402520
88402530
88402540
88402550
88402560
88402570
88402580
88402590
88402600
88402610
88402620
88402630
88402640
88402650
88402660
88402670
88402680
88402690
88402700
88402710
88402720
88402730
88402740

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 8B402750
OF * 8B402760
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 8B402770
***** 8B402780
3045 0 01F5 DC A1D0 SRA + EOR 8B402790
* 7FFF N/A N/A N/A N/A N/A S/B AFTER SRA 8B402800
* 0J00 N/A N/A N/A N/A N/A S/B AFTER EOR 8B402810
* WITH ACCM EQUAL 7FFF AN EOR USING 7FFF DID NOT 8B402820
* RESULT IN ACCM EQUAL TO 0000 8B402830
* RESULT IN ACCM EQUAL TO 0000 8B402840
* 8B402850
* 8B402860
3046 0 0214 DC A1E0 LD LONG FORM 8B402870
* 0000 N/A N/A N/A N/A N/A S/B AFTER LD 8B402880
* ACCM NOT EQUAL 0000-INDICATING WRONG 8B402890
* LOCATION WAS LOADED 8B402900
* 8B402910
* 8B402920
3047 0 0214 DC A1E0 LD LONG FORM 8B402930
* C,N1E0 N/A N/A N/A N/A N/A S/B AFTER LD 8B402940
* 0000 N/A N/A N/A N/A N/A S/B AFTER EOR 8B402950
* ACCM NET EQUAL 0000 INDICATING WRONG LOCATION 8B402960
* WAS LOADED 8B402970
* 8B402980
* 8B402990
3048 0 0220 DC A1F0 LD IND 8B403000
3049 0 0220 DC A1F0 LD IND 8B403010
* 0000 N/A N/A N/A N/A N/A S/B FOR BSC 8B403020
* ACCM NOT EQUAL 0000 INDICATING WRONG 8B403030
* LOCATION WAS LOADED 8B403040
* 8B403050
* 8B403060
304A 0 022D DC A200 BSC LONG FORM 8B403070
* UNCONDITIONAL BSC DID NOT BRANCH 8B403080
* 8B403090
* 8B403100
304B 0 022D DC A200 BSC LONG FORM 8B403110
* UNCONDITIONAL BSC SKIPPED-SHOULD BRANCH 8B403120
* 8B403130
* 8B403140
304C 0 022D DC A200 BSC,E LONG FORM 8B403150
304D 0 022D DC A200 8B403160
* FFFF N/A N/A N/A N/A N/A 8B403170
* BSC FELL THRU OR SKIPPED-SHOULD BRANCH 8B403180
* DID NOT SKIP OR SKIPPED - SHOULD BR. 8B403190
* 8B403200
304E 0 022D DC A200 BSC,+ LONG FORM 8B403210
304F 0 022D DC A200 8B403220
* FFFF N/A N/A N/A N/A N/A S/B AT TEST 8B403230
* DID NOT SKIP OR SKIPPED - SHOULD BR. 8B403240
* 8B403250
* 8B403260
3050 0 022D DC A200 BSC,Z LONG FORM 8B403270
3051 0 022D DC A200 8B403280
* FFFF N/A N/A N/A N/A N/A S/B AT TEST 8B403290
* BSC DID NOT SKIP OR SKIPPED - SHOULD BR. 8B403300
* 8B403310
* 8B403320
3052 0 022D DC A200 BSC,- LONG FORM 8B403330
* FFFF N/A N/A N/A N/A N/A S/B AT TEST 8B403340
* BSC BRANCHED-SHOULD NOT 8B403350
* 8B403360
* 8B403370
3053 0 022D DC A200 BSC,C LONG FORM 8B403380
3054 0 022D DC A200 8B403390
* N/A N/A N/A N/A N/A C+D S/B AT TEST 8B403400
* BSC DID NOT SKIP OR SKIPPED-SHOULD BRANCH 8B403410
* 8B403420

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 8B403430
OF * 8B403440
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 8B403450
***** 8B403460
3055 0 022D DC A200 BSC,D LONG FORM 8B403470
3056 0 022D DC A200 8B403480
* N/A N/A N/A N/A N/A C+D S/B AT TEST 8B403490
* BSC DID NOT SKIP OR SKIPPED-SHOULD BRANCH 8B403500
* 8B403510
* 8B403520
* 8B403530
3057 0 022D DC A200 BSC,D LONG FORM 8B403540
* N/A N/A N/A N/A N/A C S/B AT TEST 8B403550
* BSC FAILED TO TURN OFF OVERFLOW 8B403560
* 8B403570
* 8B403580
* 8B403590
3058 0 022D DC A200 BSC,C LONG FORM 8B403600
* N/A N/A N/A N/A N/A OFF S/B AT TEST 8B403610
* BSC BRANCHED-SHOULD NOT 8B403620
* 8B403630
3059 0 022D DC A200 BSC,D LONG FORM 8B403640
* N/A N/A N/A N/A N/A OFF S/B AT TEST 8B403650
* BSC BRANCHED-SHOULD NOT 8B403660
* 8B403670
* 8B403680
305A 0 022D DC A200 BSC,+ LONG FORM 8B403690
305B 0 022D DC A200 8B403700
* 0000 N/A N/A N/A N/A N/A 8B403710
* BSC DID NOT SKIP OR SKIPPED-SHOULD BRANCH 8B403720
* 8B403730
* 8B403740
305C 0 022D DC A200 BSC,+ LONG FORM 8B403750
* FFFF N/A N/A N/A N/A N/A S/B AT TEST 8B403760
* BSC BRANCHED-SHOULD NOT 8B403770
* 8B403780
* 8B403790
305D 0 022D DC A200 BSC,+ LONG FORM 8B403800
* 0001 N/A N/A N/A N/A N/A S/B AT TEST 8B403810
* BSC BRANCHED SHOULD NOT 8B403820
* 8B403830
* 8B403840
305E 0 022D DC A200 BSC INDIRECT 8B403850
305F 0 022D DC A200 8B403860
* BSC DID NOT SKIP OR SKIPPED-SHOULD BRANCH 8B403870
* 8B403880
* 8B403890
3060 0 0270 DC A240 BSI 8B403900
* UNCONDITIONAL BSI DID NOT BRANCH 8B403910
* 8B403920
* 8B403930
3061 0 0270 DC A240 BSI 8B403940
* UNCONDITIONAL BSI DID NOT STORE I CTR 8B403950
* CORRECTLY 8B403960
* 8B403970
* 8B403980
3062 0 0270 DC A240 BSI,+ LONG FORM 8B403990
3063 0 0270 DC A240 8B404000
* 0000 N/A N/A N/A N/A N/A S/B AT TEST 8B404010
* BSI DID NOT SKIP OR SKIPPED-SHOULD BRANCH 8B404020
* 8B404030
* 8B404040
3064 0 0270 DC A240 BSI,+ LONG FORM 8B404050
* BSI DID NOT STORE THE I CTR CORRECTLY 8B404060
* 8B404070
* 8B404080
3065 0 0282 DC A900 STORE 8B404090
* STORE INSTRUCTION FAILED 8B404100

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3066 0 0282 DC A900 XIO SENSE/PROG SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO FFOO-- SENSE/PROG SWS
* WERE INCORRECTLY SENSED
*
3067 0 0282 DC A900 XIO DATA ENTRY SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO FFFF-- DATA ENTRY SWS
* WERE INCORRECTLY READ
*
3068 0 0282 DC A900 XIO SENSE/PROG SWS
* FFOO N/A N/A N/A N/A S/B AT TEST
* ACCUM NOT EQUAL TO 0000-- SENSE/PROG SWS
* WERE INCORRECTLY SENSED
*
3069 0 0282 DC A900 XIO
* 0000 N/A N/A N/A N/A NT/B AT TEST
* ACCUM NOT EQUAL TO 0000--DATA ENTRY SWS
* WERE INCORRECTLY READ
*
*****
* THE FOLLOWING ERRORS ARE HANDLED BY THE
* COMMON ERROR CONTROL ROUTINE. THE ID NUMBER
* SHOWN FOR EACH ERROR WILL APPEAR IN BITS
* 5 THRU 15 OF THE WAIT INSTRUCTION.
*****
306A 0 02D9 DC A280 SRA 16
* FFFF N/A N/A N/A N/A S/B AFTER LD
* 0000 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT ZERO
*
306B 0 02E3 DC A281 SRA 15
* 8000 N/A N/A N/A N/A S/B AFTER LD
* 0001 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 0001
*
306C 0 02EE DC A282 SRA 1
* AAAA N/A N/A N/A N/A S/B AFTER LD
* 5555 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 5555
*
306D 0 02F9 DC A283 SRA 1
* 5555 N/A N/A N/A N/A S/B AFTER LD
* 2AAA N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 2AAA
*
306E 0 0304 DC A284 SERIES OF SRAS-15
*TOTAL SHIFTS
* 8000 N/A N/A N/A N/A S/B AFTER LD
* 0001 N/A N/A N/A N/A S/B AFTER SRA
* ACCUM NOT EQUAL 0001

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PROCESSOR-CONTROLLER FUNCTION TEST

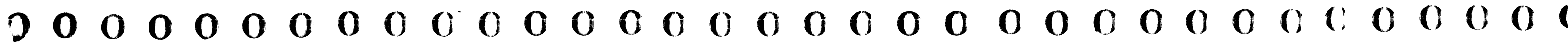
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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
306F 0 0319 DC A2C0 AND-MEMORY=0000
* 0000 N/A N/A N/A N/A S/B AFTER LD
* 0000 N/A N/A N/A N/A AFTER AND
* ACCUM NOT EQUAL 0000
*
3070 0 0323 DC A2C4 AND-MEMORY=FFFF
* 0000 N/A N/A N/A N/A
* 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
3071 0 032D DC A2C8 AND-MEMORY=0000
* FFFF N/A N/A N/A N/A
* 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
3072 0 0337 DC A2CC AND-MEMORY=FFFF
* FFFF N/A N/A N/A N/A
* FFFF N/A N/A N/A N/A
* ACCUM NOT EQUAL FFFF
*
3073 0 0345 DC A300 OR-MEMORY = 0000
* 0000 N/A N/A N/A N/A AFTER LD+OR
* 0000 N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL 0000
*
3074 0 034F DC A302 OR-MEMORY=FFFF
* 0000 N/A N/A N/A N/A AFTER LD + OR
* FFFF N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL FFFF
*
3075 0 035A DC A304 OR-MEMORY=FFFF
* FFFF N/A N/A N/A N/A AFTER LD+OR
* FFFF N/A N/A N/A N/A AFTER EOR
* ACCUM NOT EQUAL FFFF
*
3076 0 0368 DC A340 RTE 16
* FFFF 0000 N/A N/A N/A BEFORE RTE
* 0000 FFFF N/A N/A N/A AFTER RTE
* ACCUM NOT EQUAL 0000
*
3077 0 0368 DC A340 RTE 16
* 0000 FFFF N/A N/A N/A BEFORE RTE
* FFFF 0000 N/A N/A N/A AFTER RTE
* ACCUM NOT EQUAL FFFF
*
3078 0 0381 DC A380 SRT 32
* 8000 N/A N/A N/A N/A BEFORE SRT
* FFFF FFFF N/A N/A N/A AFTER SRT
* ACCUM NOT EQUAL FFFF
*

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88405470
OF * 88405480
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88405490
***** 88405500
* 88405510
* 88405520
3079 0 0381 * DC A380 SRT 32 + RTE 16 88405530
* 8000 N/A N/A N/A N/A N/A BEFORE SRT 88405540
* FFFF FFFF N/A N/A N/A N/A AFTER SRT+RTE 88405550
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED 88405560
* 88405570
* 88405580
307A 0 0396 * DC A384 SRT 32 88405590
* 4000 N/A N/A N/A N/A N/A AFTER LD 88405600
* 0000 0000 N/A N/A N/A N/A AFTER SRT 88405610
* ACCUM NOT EQUAL 0000 88405620
* 88405630
* 88405640
307B 0 0396 * DC A384 SRT 32 + RTE 16 88405650
* 4000 N/A N/A N/A N/A N/A AFTER LD 88405660
* 0000 0000 N/A N/A N/A N/A AFTER SRT 88405670
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88405680
* 88405690
* 88405700
307C 0 03A9 * DC A388 SRT 15 88405710
* 5555 N/A N/A N/A N/A N/A AFTER LD 88405720
* 0000 AAAA N/A N/A N/A N/A AFTER SRT 88405730
* ACCUM NOT EQUAL 0000 88405740
* 88405750
* 88405760
307D 0 03A9 * DC A388 SRT 15 + RTE 16 88405770
* 5555 N/A N/A N/A N/A N/A AFTER LD 88405780
* 0000 AAAA N/A N/A N/A N/A AFTER SRT 15 88405790
* AAAA 0000 N/A N/A N/A N/A AFTER RTE 16 88405800
* ACCUM NOT EQUAL AAAA-INDICATING Q REG FAILED 88405810
* 88405820
* 88405830
307E 0 038D * DC A38C SERIES OF SRTS-30 88405840
* *TOTAL SHIFTS 88405850
* 5555 N/A N/A N/A N/A N/A AFTER LD 88405860
* 0000 0001 N/A N/A N/A N/A AFTER SRT'S 88405870
* ACCUM NOT EQUAL 0000 88405880
* 88405890
* 88405900
307F 0 038D * DC A38C SERIES OF SRTS-30 88405910
* *TOTAL SHIFTS + 88405920
* *RTE 16 88405930
* 5555 N/A N/A N/A N/A N/A AFTER LD 88405940
* 0000 0001 N/A N/A N/A N/A AFTER SRT'S 88405950
* 0001 0000 N/A N/A N/A N/A AFTER RTE 16 88405960
* ACCUM NOT EQUAL 0001-INDICATING Q REG FAILED 88405970
* 88405980
* 88405990
3080 0 03DD * DC A3C0 RTE 15 88406000
* 5555 AAAA N/A N/A N/A N/A AFTER LD'S 88406010
* 5554 AAAB N/A N/A N/A N/A AFTER RTE 15 88406020
* ACCUM NOT EQUAL 5554 - RTE 15 Q TO A FAILED 88406030
* 88406040
* 88406050
3081 0 03DD * DC A3C0 RTE 15 + RTE 16 88406060
* 5555 AAAA N/A N/A N/A N/A AFTER LD'S 88406070
* 5554 AAAB N/A N/A N/A N/A AFTER RTE 15 88406080
* AAAB 5554 N/A N/A N/A N/A AFTER RTE 16 88406090
* ACCUM NOT EQUAL AAAB-INDICATING Q REG FAILED 88406100
* 88406110
* 88406120
* 88406130
* 88406140

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88406150
OF * 88406160
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88406170
***** 88406180
* 88406190
3082 0 03F4 * DC A3C4 SERIES OF RTES-31 88406200
* *TOTAL SHIFTS 88406210
* 0000 8000 N/A N/A N/A N/A AFTER LD 88406220
* 0001 0000 N/A N/A N/A N/A AFTER RTE'S 88406230
* ACCUM NOT EQUAL 0001 88406240
* 88406250
* 88406260
3083 0 03F4 * DC A3C4 SERIES OF RTES-31 88406270
* *TOTAL SHIFTS 88406280
* *FOLLOWED BY RTE 16 88406290
* 0000 8000 N/A N/A N/A N/A AFTER LD 88406300
* 0001 0000 N/A N/A N/A N/A AFTER RTE'S 88406310
* 0000 0001 N/A N/A N/A N/A AFTER RTE 16 88406320
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88406330
* 88406340
* 88406350
3084 0 0419 * DC A400 SLA 16 88406360
* FFFF FFFF N/A N/A N/A N/A AFTER LD 88406370
* 0000 FFFF N/A N/A N/A N/A AFTER SLA 88406380
* ACCUM NOT EQUAL 0000 88406390
* 88406400
* 88406410
3085 0 0419 * DC A400 SLA 16 88406420
* FFFF FFFF N/A N/A N/A OFF AFTER LD 88406430
* 0000 FFFF N/A N/A N/A C AFTER SLA 88406440
* CARRY NOT SET 88406450
* 88406460
* 88406470
3086 0 0419 * DC A400 SLA 16 + RTE 16 88406480
* FFFF FFFF N/A N/A N/A N/A AFTER LD 88406490
* 0000 FFFF N/A N/A N/A N/A AFTER SLA 88406500
* FFFF 0000 N/A N/A N/A N/A AFTER RTE 16 88406510
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED 88406520
* 88406530
* 88406540
3087 0 043A * DC A408 SLA 16 88406550
* 0001 0000 N/A N/A N/A N/A AFTER LD 88406560
* 0000 0000 N/A N/A N/A N/A AFTERSLA 88406570
* ACCUM NOT EQUAL 0000 88406580
* 88406590
* 88406600
3088 0 043A * DC A408 SLA 16 88406610
* 0001 0000 N/A N/A N/A C AFTER LD 88406620
* 0000 0000 N/A N/A N/A C AFTER SLA 88406630
* CARRY NOT SET 88406640
* 88406650
* 88406660
3089 0 043A * DC A408 SLA 16 + RTE 16 88406670
* 0001 0000 N/A N/A N/A N/A AFTER LD 88406680
* 0000 0000 N/A N/A N/A N/A AFTER SLA 88406690
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88406700
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88406710
* 88406720
* 88406730
308A 0 045A * DC B400 SLA 1 88406740
* AAAA 0000 N/A N/A N/A N/A AFTER LD 88406750
* 5554 0000 N/A N/A N/A N/A AFTER SLA 88406760
* ACCUM NOT EQUAL 5554 88406770
* 88406780
* 88406790
* 88406800
* 88406810
* 88406820

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 8B406830
OF * 8B406840
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 8B406850
***** 8B406860
308B 0 045A DC B400 SLA 1 8B406870
* AAAA 0000 N/A N/A N/A C 8B406880
* 5554 0000 N/A N/A N/A C 8B406890
* CARRY NOT SET 8B406900
* 8B406910
* 8B406920
* 8B406930
308C 0 045A DC B400 SLA 1 + RTE 16 8B406940
* AAAA 0000 N/A N/A N/A N/A 8B406950
* 5554 0000 N/A N/A N/A N/A 8B406960
* 0000 5554 N/A N/A N/A N/A AFTER RTE 8B406970
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 8B406980
* 8B406990
* 8B407000
308D 0 0478 DC B406 SLA 1 8B407010
* 5555 0000 N/A N/A N/A N/A AFTER LD 8B407020
* AAAA 0000 N/A N/A N/A N/A AFTER SLA 8B407030
* ACCUM NOT EQUAL AAAA 8B407040
* 8B407050
* 8B407060
308E 0 0478 DC B406 SLA 1 8B407070
* 5555 0000 N/A N/A N/A C AFTER LD 8B407080
* AAAA 0000 N/A N/A N/A OFF AFTER SLA 8B407090
* CARRY SET-SHOULD BE CLEAR 8B407100
* 8B407110
* 8B407120
308F 0 0478 DC B406 SLA 1 + RTE 16 8B407130
* 5555 0000 N/A N/A N/A N/A AFTER LD 8B407140
* AAAA 0000 N/A N/A N/A N/A AFTER SLA 8B407150
* 0000 AAAA N/A N/A N/A N/A AFTER RTE 8B407160
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 8B407170
* 8B407180
* 8B407190
3090 0 0497 DC B40A SERIES OF SLAS-16 8B407200
* *TOTAL SHIFTS 8B407210
* 0001 0000 N/A N/A N/A N/A AFTER SLA 0 8B407220
* 0000 0000 N/A N/A N/A N/A AFTER SLA'S 8B407230
* ACCUM NOT EQUAL 0000 8B407240
* 8B407250
* 8B407260
3091 0 0497 DC B40A SERIES OF SLAS-16 8B407270
* *TOTAL SHIFTS 8B407280
* 0001 0000 N/A N/A N/A C AFTER SLA 0 8B407290
* 0000 0000 N/A N/A N/A C AFTER SLA'S 8B407300
* CARRY NOT SET 8B407310
* 8B407320
* 8B407330
3092 0 0497 DC B40A SERIES OF SLAS-16 8B407340
* *TOTAL SHIFTS + 8B407350
* *RTE 16 8B407360
* 0001 0000 N/A N/A N/A N/A AFTER SLA 0 8B407370
* 0000 0000 N/A N/A N/A N/A AFTER SLA'S 8B407380
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 8B407390
* ACC NOT EQUAL 0000-INDICATING Q REG FAILED 8B407400
* 8B407410
* 8B407420
3093 0 04C4 DC A440 SLT 32 8B407430
* 0000 0001 N/A N/A N/A N/A AFTER LD 8B407440
* 0000 0000 N/A N/A N/A N/A AFTER SLT 32 8B407450
* ACCUM NOT EQUAL 0000 8B407460
* 8B407470
* 8B407480
* 8B407490
* 8B407500

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PROCESSOR-CONTROLLER FUNCTION TEST

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***** 8B407510
ADDRESS * 8B407520
OF * 8B407530
B-REG ROUTINE * A-REG Q-REG YR-1 XR-2 YR-3 STATUS 8B407540
***** 8B407550
3094 0 04C4 DC A440 SLT 32 8B407560
* 0000 0001 N/A N/A N/A N/A AFTER LD 8B407570
* 0000 0000 N/A N/A N/A C AFTER SLT 32 8B407580
* CARRY NOT SET 8B407590
* 8B407600
* 8B407610
3095 0 04C4 DC A440 SLT 32 + RTE 16 8B407620
* 0000 0001 N/A N/A N/A N/A AFTER LD 8B407630
* 0000 0000 N/A N/A N/A N/A AFTER SLT 32 8B407640
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 8B407650
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 8B407660
* 8B407670
* 8B407680
3096 0 04E1 DC A444 SLT 16 8B407690
* 0000 FFFF N/A N/A N/A N/A AFTER LD 8B407700
* FFFF 0000 N/A N/A N/A N/A AFTER SLT 16 8B407710
* ACCUM NOT EQUAL FFFF 8B407720
* 8B407730
* 8B407740
3097 0 04E1 DC A444 SLT 16 8B407750
* 0000 FFFF N/A N/A N/A N/A AFTER LD 8B407760
* FFFF 0000 N/A N/A N/A N/A OFF AFTER SLT 16 8B407770
* CARRY ON SHOULD NOT BE 8B407780
* 8B407790
* 8B407800
3098 0 04E1 DC A444 SLT 16 + RTE 16 8B407810
* 0000 FFFF N/A N/A N/A N/A AFTER LD 8B407820
* FFFF 0000 N/A N/A N/A N/A AFTER SLT 16 8B407830
* 0000 FFFF N/A N/A N/A N/A AFTER RTE 16 8B407840
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 8B407850
* 8B407860
* 8B407870
3099 0 0500 DC A44A SLT 15 8B407880
* 0000 5555 N/A N/A N/A N/A AFTER LD 8B407890
* 2AAA 8000 N/A N/A N/A N/A AFTER SLT 15 8B407900
* ACCUM NOT EQUAL 2AAA 8B407910
* 8B407920
* 8B407930
309A 0 0500 DC A44A SLT 15 8B407940
* 0000 5555 N/A N/A N/A N/A AFTER LD 8B407950
* 2AAA 8000 N/A N/A N/A N/A OFF AFTER SLT 15 8B407960
* CARRY SET-SHOULD NOT BE 8B407970
* 8B407980
* 8B407990
309B 0 0500 DC A44A SLT 15 + RTE 16 8B408000
* 0000 5555 N/A N/A N/A N/A AFTER LD 8B408010
* 2AAA 8000 N/A N/A N/A N/A AFTER SLT 15 8B408020
* 8000 2AAA N/A N/A N/A N/A AFTER RTE 16 8B408030
* ACCUM NOT EQUAL 8000-INDICATING Q REG FAILED 8B408040
* 8B408050
* 8B408060
309C 0 0520 DC B440 SERIES OF SLTS-32 8B408070
* *TOTAL SHIFTS 8B408080
* 0000 0001 N/A N/A N/A N/A AFTER LD 8B408090
* 0000 0000 N/A N/A N/A N/A AFTER SLT'S 8B408100
* ACCUM NOT EQUAL 0000 8B408110
* 8B408120
* 8B408130
309D 0 0520 DC B440 SERIES OF SLTS-32 8B408140
* *TOTAL SHIFTS 8B408150
* 0000 0001 N/A N/A N/A N/A AFTER LD 8B408160
* 0000 0000 N/A N/A N/A N/A C AFTER SLT'S 8B408170
* CARRY NOT ON 8B408180

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*****
ADDRESS * 88408190
OF * 88408200
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88408210
*****
309E 0 0520 DC B440 SERIES OF SLTS-32 88408220
* 88408230
* TOTAL SHIFTS + 88408240
* RTE 16 88408250
* 0000 0001 N/A N/A N/A N/A AFTER LD 88408260
* 0000 0000 N/A N/A N/A N/A AFTER SLT'S 88408270
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88408280
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88408290
* 88408300
* 88408310
* 88408320
309F 0 0549 DC A480 STD 88408330
* 0000 N/A N/A N/A N/A N/A 88408340
* STORING 0000 INTO A STORAGE LOCATION 88408350
* CONTAINING FFFF DID NOT RETURN 0000 WHEN 88408360
* RELOADED IN THE ACCUM 88408370
* 88408380
* 88408390
30A0 0 0555 DC A482 STD 88408400
* FFFF N/A N/A N/A N/A N/A 88408410
* STORING FFFF INTO A STORAGE LOCATION 88408420
* CONTAINING 0000 DID NOT RETURN FFFF WHEN 88408430
* RELOADED IN THE ACCUM 88408440
* 88408450
* 88408460
30A1 0 0566 DC A4C0 STS 88408470
* N/A N/A N/A N/A N/A OFF BEFORE STS 88408480
* 0000 N/A N/A N/A N/A N/A OFF AFTER LD 88408490
* STS OF 0000 INTO STORAGE LOCATION 88408500
* CONTAINING 0003 DID NOT RETURN 0000 WHEN 88408510
* RELOADED IN THE ACCUM 88408520
* 88408530
* 88408540
30A2 0 0571 DC A4C2 STS 88408550
* N/A N/A N/A N/A N/A C+O AFTER LDS 88408560
* N/A N/A N/A N/A N/A OFF AFTER STS 88408570
* STS DID NOT CLEAR CARRY 88408580
* 88408590
* 88408600
30A3 0 0571 DC A4C2 STS CK ACC 88408610
* INITIALLY ACC HAS CORE LOCATION OF 88408620
* SYMBOLIC LABEL A4C2 88408630
* ACC DESTROYED AFTER STS 88408640
* 88408650
* 88408660
30A4 0 0571 DC A4C2 STS 88408670
* N/A N/A N/A N/A N/A C+O AFTER LDS 88408680
* N/A N/A N/A N/A N/A OFF AFTER STS 88408690
* STS DID NOT CLEAR OVERFLOW AFTER STS 88408700
* 88408710
* 88408720
30A5 0 0571 DC A4C2 STS 88408730
* N/A N/A N/A N/A N/A BEFORE LD 88408740
* 0003 AFTER LD 88408750
* STS OF 0003 INTO A STORAGE LOCATION 88408760
* CONTAINING 0000 DID NOT RETURN 0003 WHEN 88408770
* RELOADED IN THE ACCUM 88408780
* 88408790
* 88408800
30A6 0 0596 DC A4C8 STS 88408810
* N/A N/A N/A N/A N/A C AFTER LDS 88408820
* 0002 N/A N/A N/A N/A OFF AFTER LD 88408830
* STS OF 0002 INTO A STORAGE LOCATION 88408840
* CONTAINING 0003 DID NOT RETURN 0002 WHEN 88408850
* RELOADED IN THE ACCUM 88408860

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88408870
OF * 88408880
B-REC ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88408890
*****
30A7 0 0596 DC A4C8 STS 88408900
* N/A N/A N/A N/A N/A C AFTER LDS 88408910
* 0000 N/A N/A N/A N/A OFF AFTER STS 88408920
* STS DID NOT CLEAR CARRY 88408930
* 88408940
* 88408950
* 88408960
* 88408970
30A8 0 05AC DC A4CC STS 88408980
* N/A N/A N/A N/A N/A O AFTER LDS 88408990
* 0001 N/A N/A N/A N/A OFF AFTER STS 88409000
* STS OF 0001 INTO A STORAGE LOCATION 88409010
* CONTAINING 0002 DID NOT RETURN 0001 WHEN 88409020
* RELOADED IN THE ACCUM 88409030
* 88409040
* 88409050
30A9 0 05AC DC A4CC STS 88409060
* N/A N/A N/A N/A N/A O AFTER LDS 88409070
* 0000 N/A N/A N/A N/A OFF AFTER LDA 88409080
* STS DID NOT CLEAR OVERFLOW 88409090
* 88409100
* 88409110
30AA 0 05C8 DC A500 BSC,D+EZC 88409120
* 8001 N/A N/A N/A N/A C+O 88409130
* BSC SKIPPED-SHOULD NOT HAVE 88409140
* 88409150
* 88409160
30AB 0 05D3 DC A502 BSC,-DC+ 88409170
* 0000 N/A N/A N/A N/A C+O 88409180
* BSC SKIPPED-SHOULD NOT HAVE 88409190
* 88409200
* 88409210
30AC 0 05DE DC A504 BSC,D-E 88409220
* 8000 N/A N/A N/A N/A C+O 88409230
* BSC FAILED TO SKIP 88409240
* 88409250
* 88409260
30AD 0 05DE DC A504 BSC,D 88409270
* 8000 N/A N/A N/A N/A C 88409280
* BSC FAILED TO CLEAR OVERFLOW 88409290
* 88409300
* 88409310
30AE 0 05F5 DC A508 BSC,C+Z 88409320
* 0001 N/A N/A N/A N/A OFF 88409330
* BSC FAILED TO SKIP 88409340
* 88409350
* 88409360
30AF 0 0600 DC A50A BSC,+DCE LONG FORM 88409370
* 8001 N/A N/A N/A N/A C+O 88409380
* BSC DID NOT BRANCH - SHOULD HAVE 88409390
* 88409400
* 88409410
30B0 0 0600 DC A50A BSC,+DCE LONG FORM 88409420
* 8001 N/A N/A N/A N/A C+O 88409430
* BSC SKIPPED-SHOULD BRANCH 88409440
* 88409450
* 88409460
30B1 0 061D DC A50C BSC,-Z LONG FORM 88409470
* 0004 N/A N/A N/A N/A C+O 88409480
* BSC DID NOT BRANCH - SHOULD HAVE 88409490
* 88409500
* 88409510
30B2 0 061D DC A50C BSC,-Z LONG FORM 88409520
* 0004 N/A N/A N/A N/A C+O 88409530
* BSC SKIPPED-SHOULD BRANCH 88409540

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88409550
OF * 88409560
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88409570
***** / ***** 88409580
***** 88409590
30B3 0 0631 * DC A50E BSC,+E0CZ LONG 88409600
* *FORM 88409610
* 8001 N/A N/A N/A N/A C+D 88409620
* BSC BRANCHED-SHOULD NOT 88409630
* 88409640
* 88409650
* 88409660
30B4 0 0631 * DC A50E BSC,+E0CZ LONG 88409670
* *FORM 88409680
* 8001 N/A N/A N/A N/A C+D 88409690
* BSC SKIPPED-SHOULD NOT 88409700
* 88409710
* 88409720
* 88409730
30B5 0 0645 * DC B500 BSC,+ 88409740
* 0001 N/A N/A N/A N/A C+D 88409750
* BSC ON PLUS CLEARED THE OVERFLOW F-F 88409760
* 88409770
* 88409780
30B6 0 0645 * DC B500 BSC,+ 88409790
* 0001 N/A N/A N/A N/A N/A 88409800
* BSC FAILED TO SKIP 88409810
* 88409820
* 88409830
30B7 0 0663 * DC A540 BSI,E0C+Z LONG 88409840
* *FORM 88409850
* 8001 N/A N/A N/A N/A C+D 88409860
* BSI DID NOT BRANCH - SHOULD HAVE 88409870
* 88409880
* 88409890
30B8 0 0663 * DC A540 BSI,E0C+Z LONG 88409900
* *FORM 88409910
* 8001 N/A N/A N/A N/A C+D 88409920
* BSI SKIPPED-SHOULD BRANCH 88409930
* 88409940
* 88409950
30B9 0 0663 * DC A540 BSI,E0C+Z LONG 88409960
* *FORM 88409970
* 8001 N/A N/A N/A N/A C+D AFTER LDS 88409980
* 8001 N/A N/A N/A N/A C AFTER BSI 88409990
* BSI DID NOT CLEAR OVERFLOW 88410000
* 88410010
* 88410020
30BA 0 0687 * DC A544 BSI,Z- LONG FORM 88410030
* 0002 N/A N/A N/A N/A N/A 88410040
* BSI DID NOT BRANCH - SHOULD HAVE 88410050
* 88410060
* 88410070
30BB 0 0687 * DC A544 BSI,Z- LONG FORM 88410080
* 0002 N/A N/A N/A N/A N/A 88410090
* BSI SKIPPED-SHOULD BRANCH 88410100
* 88410110
* 88410120
30BC 0 069C * DC A546 BSI,Z LONG FORM 88410130
* 0000 N/A N/A N/A N/A N/A 88410140
* BSI BRANCHED-SHOULD NOT 88410150
* 88410160
* 88410170
30BD 0 069C * DC A546 BSI,Z LONG FORM 88410180
* 0000 N/A N/A N/A N/A N/A 88410190
* BSI SKIPPED-SHOULD NOT U 88410200
* 88410210
* 88410220

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*****
ADDRESS * 88410230
OF * 88410240
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88410250
***** / ***** 88410260
***** 88410270
30BE 0 06AF * DC A548 BSI,- LONG FORM 88410280
* 8001 N/A N/A N/A N/A N/A 88410290
* BSI SKIPPED-SHOULD NOT 88410300
* 88410310
* 88410320
30BF 0 06AF * DC A548 BSI,- LONG FORM 88410330
* 8001 N/A N/A N/A N/A N/A 88410340
* BSI BRANCHED-SHOULD NOT 88410350
* 88410360
30C0 0 06C1 * DC A54A BSI,+ LONG FORM 88410370
* 0002 N/A N/A N/A N/A N/A 88410380
* BSI SKIPPED-SHOULD NOT 88410390
* 88410400
* 88410410
30C1 0 06C1 * DC A54A BSI,+ LONG FORM 88410420
* 0002 N/A N/A N/A N/A N/A 88410430
* BSI BRANCHED-SHOULD NOT 88410440
* 88410450
* 88410460
30C2 0 06D3 * DC A54C BSI,E LONG FORM 88410470
* 0002 N/A N/A N/A N/A N/A 88410480
* BSI SKIPPED-SHOULD NOT 88410490
* 88410500
* 88410510
30C3 0 06D3 * DC A54C BSI,E LONG FORM 88410520
* 0002 N/A N/A N/A N/A N/A 88410530
* BSI BRANCHED-SHOULD NOT 88410540
* 88410550
* 88410560
30C4 0 06E5 * DC A54E BSI,C LONG FORM 88410570
* N/A N/A N/A N/A N/A C 88410580
* BSI SKIPPED-SHOULD NOT 88410590
* 88410600
* 88410610
30C5 0 06E5 * DC A54E BSI,C LONG FORM 88410620
* N/A N/A N/A N/A N/A C 88410630
* BSI BRANCHED SHOULD NOT 88410640
* 88410650
* 88410660
30C6 0 06F7 * DC A54F BSI,D LONG FORM 88410670
* N/A N/A N/A N/A N/A D 88410680
* BSI SKIPPED-SHOULD NOT 88410690
* 88410700
* 88410710
30C7 0 06F7 * DC A54F BSI,D LONG FORM 88410720
* N/A N/A N/A N/A N/A D 88410730
* BSI BRANCHED-SHOULD NOT 88410740
* 88410750
* 88410760
30C8 0 070F * DC A580 LDD 88410770
* 0000 0000 N/A N/A N/A N/A 88410780
* ACCUM NOT EQUAL 0000 88410790
* 88410800
* 88410810
30C9 0 070F * DC A580 LDD + RTE 16 88410820
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88410830
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16 88410840
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED 88410850
* 88410860
* 88410870
30CA 0 0721 * DC A584 LDD 88410880
* FFFF FFFF N/A N/A N/A N/A 88410890
* ACCUM NOT EQUAL FFFF 88410900

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30CB 0 0721 DC A584 LDD + RTE 16
* FFFF FFFF N/A N/A N/A N/A AFTER LDD
* FFFF FFFF N/A N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL FFFF-INDICATING Q REG FAILED
*
30CC 0 0735 DC A588 LDD ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* ACCUM NOT EQUAL 0000
*
30CD 0 0735 DC A588 LDD-ODD ADDRESS
* + RTE 16
* 0000 0000 N/A N/A N/A N/A AFTER LDD
* 0000 0000 N/A N/A N/A N/A AFTER RTE 16
* ACCUM NOT EQUAL 0000-INDICATING Q REG FAILED
*
30CE 0 074C DC A5C0 STD
* 0000 0000 N/A N/A N/A N/A
* USING STD-ACCUM NOT STORED IN LOCATION EA
*
30CF 0 074C DC A5C0 STD
* 0000 0000 N/A N/A N/A N/A
* USING STD-Q REG NOT STORED IN LOCATION EA+1
*
30D0 0 0760 DC A5C4 STD
* FFFF FFFF N/A N/A N/A N/A
* USING STD-ACCUM NOT STORED IN LOCATION EA
*
30D1 0 0760 DC A5C4 STD
* FFFF FFFF N/A N/A N/A N/A
* USING STD-Q REG NOT STORED IN LOCATION EA+1
*
30D2 0 0779 DC A5C8 STD ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* STD USING ODD ADDRESS-ACCUM NOT STORED IN EA
*
30D3 0 0779 DC A5C8 STD-ODD ADDRESS
* 0000 0000 N/A N/A N/A N/A
* STD USING ODD ADDRESS-ACCUM NOT STORED
* IN EA+1
*
30D4 0 079F DC A600 LDX 1
* N/A N/A N/A N/A N/A N/A
* TAG REG BIT 7 WILL NOT SET
*
30D5 0 07A8 DC A602 LDX 2
* N/A N/A N/A N/A N/A N/A
* TAG REG BIT 6 WILL NOT SET
*
30D6 0 07B1 DC A604 LDX 1
* N/A N/A 0000 N/A N/A N/A
* INDEX REG 1 NOT EQUAL 0000
*
88410910
88410920
88410930
88410940
88410950
88410960
88410970
88410980
88410990
88411000
88411010
88411020
88411030
88411040
88411050
88411060
88411070
88411080
88411090
88411100
88411110
88411120
88411130
88411140
88411150
88411160
88411170
88411180
88411190
88411200
88411210
88411220
88411230
88411240
88411250
88411260
88411270
88411280
88411290
88411300
88411310
88411320
88411330
88411340
88411350
88411360
88411370
88411380
88411390
88411400
88411410
88411420
88411430
88411440
88411450
88411460
88411470
88411480
88411490
88411500
88411510
88411520
88411530
88411540
88411550
88411560
88411570
88411580

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30D7 0 078D DC A606 LDX 2
* N/A N/A N/A 0000 N/A N/A
* INDEX REG 2 NOT EQUAL 0000
*
30D8 0 07CA DC A608 LDX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT EQUAL 0000
*
30D9 0 07D7 DC A60A LDX 1
* N/A N/A FFFF N/A N/A N/A
* INDEX REG 1 NOT EQUAL FFFF
*
30DA 0 07E4 DC A60C LDX 2
* N/A N/A N/A FFFF N/A N/A
* INDEX REG 2 NOT EQUAL FFFF
*
30DB 0 07F1 DC A60E LDX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30DC 0 07FE DC B600 LDX 1 LONG FORM
* N/A N/A 0001 N/A N/A N/A
* INDEX REG 1 NOT EQUAL 0001
*
30DD 0 080C DC B602 LDX 3 INDIRECT
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30DE 0 0820 DC A640 STX
* N/A N/A N/A N/A N/A N/A
* STX WITH NO TAG DID NOT STORE I-CTR CORRECT
*
30DF 0 0837 DC A642 STX 1
* N/A N/A 0000 N/A N/A N/A
* INDEX REG 1 WAS NOT STORED BY STX
*
30E0 0 0844 DC A644 STX 2
* N/A N/A N/A 0000 N/A N/A
* INDEX REG 2 NOT STORED BY STX
*
30E1 0 0851 DC A646 STX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT STORED BY STX
*
30E2 0 085E DC A648 STX 1
* N/A N/A FFFF N/A N/A N/A
* INDEX REG 1 NOT STORED BY STX
*
30E3 0 086C DC A64A STX 2
* N/A N/A N/A FFFF N/A N/A
* INDEX REG 2 NOT STORED BY STX
*
88411590
88411600
88411610
88411620
88411630
88411640
88411650
88411660
88411670
88411680
88411690
88411700
88411710
88411720
88411730
88411740
88411750
88411760
88411770
88411780
88411790
88411800
88411810
88411820
88411830
88411840
88411850
88411860
88411870
88411880
88411890
88411900
88411910
88411920
88411930
88411940
88411950
88411960
88411970
88411980
88411990
88420000
88420010
88420020
88420030
88420040
88420050
88420060
88420070
88420080
88420090
88420100
88420110
88420120
88420130
88420140
88420150
88420160
88420170
88420180
88420190
88420200
88420210
88420220
88420230
88420240
88420250
88420260

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG G-REG XR-1 XR-2 XR-3 STATUS
*****
30E4 0 087A      DC      A64C      STX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT STORED BY STX
*
30E5 0 08EC      DC      A680      ADD
* FFFF N/A N/A N/A N/A C AFTER LD+LDS
* FFFF N/A N/A N/A N/A C AFTER A
* ADD FFFF + 0000 TURNED ON OVERFLOW
*
30E6 0 08EC      DC      A680      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD
* FFFF N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + 0000 FAILED TO EQUAL FFFF
*
30E7 0 0901      DC      A684      ADD
* FFFF N/A N/A N/A N/A OFF AFTER LD+LDS
* 0000 N/A N/A N/A N/A C AFTER A
* ADD FFFF + 0001 DID NOT TURN ON CARRY
*
30E8 0 0901      DC      A684      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD+LDS
* 0000 N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + 0001 DID NOT EQUAL 0000
*
30E9 0 0914      DC      A688      ADD
* FFFF N/A N/A N/A N/A OFF AFTER LD+LDS
* FFFF N/A N/A N/A N/A C AFTER A
* ADD FFFF + FFFF DID NOT TURN ON CARRY
*
30EA 0 0914      DC      A688      ADD
* FFFF N/A N/A N/A N/A N/A AFTER LD+LDS
* FFFF N/A N/A N/A N/A N/A AFTER A
* ADD FFFF + FFFF DID NOT EQUAL FFFE
*
30EB 0 0928      DC      A68C      ADD
* 4000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A D AFTER A
* ADD 4000 + 4000 DID NOT TURN ON OVERFLOW
*
30EC 0 0928      DC      A68C      ADD
* 4000 N/A N/A N/A N/A N/A
* ADD 4000 + 4000 DID NOT EQUAL 8000
*
30ED 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A N/A AFTER LD
* 0000 N/A N/A N/A N/A N/A AFTER A
* ADD 8000 + 8000 NOT EQUAL 0000
*
30EE 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A OFF AFTER LD
* 0000 N/A N/A N/A N/A C+D AFTER A
* ADD 8000 + 8000 DID NOT TURN ON OVERFLOW

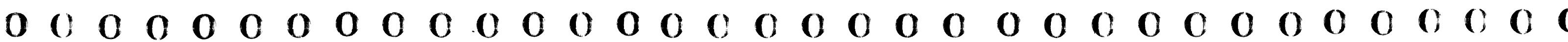
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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG G-REG XR-1 XR-2 XR-3 STATUS
*****
30EF 0 093C      DC      B680      ADD
* 8000 N/A N/A N/A N/A OFF AFTER LD
* 0000 N/A N/A N/A N/A C+D AFTER A
* ADD 8000 + 8000 DID NOT TURN ON CARRY
*
30F0 0 0964      DC      A6C0      LDX 1
* N/A N/A FFF4 N/A N/A N/A
* INDEX REG 1 WAS NOT LOADED EQUAL FFF4
*
30F1 0 0964      DC      A6C0      LD 1
* N/A N/A FFF4 N/A N/A N/A
* A LOAD INSTR INDEXED BY INDEX REG 1
* LOADED THE WRONG LOCATION
*
30F2 0 097C      DC      A6C2      LDX 2
* N/A N/A N/A 0004 N/A N/A
* INDEX REG 2 NOT LOADED EQUAL 0004
*
30F3 0 097C      DC      A6C2      LD 2
* N/A N/A N/A 0004 N/A N/A
* A LOAD INSTR INDEXED BY INDEX REG 2
* LOADED THE WRONG LOCATION
*
30F4 0 0994      DL      A6C4      LDX 3
* N/A N/A N/A N/A 0000 N/A
* INDEX REG 3 NOT LOADED EQUAL 0000
*
30F5 0 0994      DC      A6C4      LD 3
* N/A N/A N/A N/A 0000 N/A
* A LOAD INSTR INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*
30F6 0 09AB      DC      A6C6      LDX 3
* N/A N/A N/A N/A 0001 N/A
* INDEX REG 3 NOT EQUAL 0001
*
30F7 0 09AB      DC      A6C6      LD 3 LONG FORM
* N/A N/A N/A N/A 0001 N/A
* A LONG FORM LOAD INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*
30F8 0 09C3      DC      A6C8      LDX 3
* N/A N/A N/A N/A FFFF N/A
* INDEX REG 3 NOT EQUAL FFFF
*
30F9 0 09C3      DC      A6C8      LD 3 INDIRECT
* N/A N/A N/A N/A FFFF N/A
* AN INDIRECT LOAD INDEXED BY INDEX REG 3
* LOADED THE WRONG LOCATION
*

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
30FA 0 OA48 DC A700 SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* FFFF N/A N/A N/A N/A N/A AFTER S
* SUB 0001 FROM 0000 DID NOT EQUAL FFFF
*
30FB 0 OA48 DC A700 SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* FFFF N/A N/A N/A N/A C AFTER S
* SUB 0001 FROM 0000 DID NOT SET CARRY
*
30FC 0 OA5F DC A704 SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* 0001 N/A N/A N/A N/A N/A AFTER S
* SUB FFFF FROM 0000 DID NOT EQUAL 0001
*
30FD 0 OA5F DC A704 SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 0001 N/A N/A N/A N/A C AFTER S
* SUB FFFF FROM 0000 DID NOT SET CARRY
*
30FE 0 OA76 DC A708 SUB
* 8000 N/A N/A N/A N/A N/A AFTER LD
* 7FFF N/A N/A N/A N/A N/A AFTER S
* SUB 0001 FROM 8000 DID NOT EQUAL 7FFF
*
30FF 0 OA76 DC A708 SUB
* 8000 N/A N/A N/A N/A OFF AFTER LD
* 0001 N/A N/A N/A N/A C AFTER CARRY
* AND OVERFLOW CONDITION HAD BEEN LOADED INTO
* ACCUMULATOR AS A NUMBER
* SUB 0001 FROM 8000 DID NOT TURN ON OVERFLOW
*
3100 0 OA8D DC A70C SUB
* 0000 N/A N/A N/A N/A N/A AFTER LD
* 8000 N/A N/A N/A N/A N/A AFTER S
* SUB 8000 FROM 0000 DID NOT EQUAL 8000
*
3101 0 OA8D DC A70C SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A C+D AFTER S
* SUB 8000 FROM 0000 DID NOT TURN ON OVERFLOW
*
3102 0 OA8D DC A70C SUB
* 0000 N/A N/A N/A N/A OFF AFTER LD
* 8000 N/A N/A N/A N/A C+D AFTER S
* SUB 9000 FROM 0000 DID NOT TURN ON CARRY
*
3103 0 OA88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A N/A AFTER LDD
* FFFF FFFF N/A N/A N/A N/A AFTER AD
* ACCUM NOT EQUAL FFFF
*
*****
88413630
88413640
88413650
88413660
88413670
88413680
88413690
88413700
88413710
88413720
88413730
88413740
88413750
88413760
88413770
88413780
88413790
88413800
88413810
88413820
88413830
88413840
88413850
88413860
88413870
88413880
88413890
88413900
88413910
88413920
88413930
88413940
88413950
88413960
88413970
88413980
88413990
88414000
88414010
88414020
88414030
88414040
88414050
88414060
88414070
88414080
88414090
88414100
88414110
88414120
88414130
88414140
88414150
88414160
88414170
88414180
88414190
88414200
88414210
88414220
88414230
88414240
88414250
88414260
88414270
88414280
88414290
88414300

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3104 0 OA88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A N/A AFTER ADD
* FFFF FFFF N/A N/A N/A N/A AFTER RTE
* Q REG NOT EQUAL FFFF
*
3105 0 OA88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A N/A OFF AFTER LDD
* FFFF FFFF N/A N/A N/A N/A OFF AFTER RTE
* OVERFLOW SET SHOULD NOT BE
*
3106 0 OA88 DC A740 AD-0000 0000
* FFFF FFFF N/A N/A N/A N/A OFF AFTER LDD
* FFFF FFFF N/A N/A N/A N/A OFF AFTER RTE
* CARRY SET-SHOULD NOT BE
*
3107 0 OA8E DC A746 AD-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A AFTER LDD
* 0000 0000 N/A N/A N/A N/A AFTER AD
* ACCUM NOT EQUAL 0000
*
3108 0 OA8E DC A746 AD-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A AFTER LDD
* 0000 0000 N/A N/A N/A N/A AFTER AD
* Q REG NOT EQUAL 0000
*
3109 0 OA8E DC A746 AD-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A OFF AFTER LDD
* 0000 0000 N/A N/A N/A N/A C AFTER AD
* OVERFLOW SET-SHOULD NOT BE
*
310A 0 OA8E DC A746 AD-FFFF FFFF
* 0000 0001 N/A N/A N/A N/A OFF AFTER LDD
* 0000 0000 N/A N/A N/A N/A C AFTER AD
* CARRY NOT SET-SHOULD BE
*
310B 0 0B14 DC A74C AD-FFFF FFFF
* FFFF FFFF N/A N/A N/A N/A AFTER LDD
* FFFF FFFF N/A N/A N/A N/A AFTER AD
* ACCUM NOT EQUAL FFFF
*
310C 0 0B14 DC A74C AD-FFFF FFFF
* FFFF FFFF N/A N/A N/A N/A AFTER LDD
* FFFF FFFF N/A N/A N/A N/A AFTER AD
* Q REG NOT EQUAL FFFF
*
310D 0 0B14 DC A74C AD-FFFF FFFF
* FFFF FFFF N/A N/A N/A N/A OFF AFTER LDD
* FFFF FFFF N/A N/A N/A N/A C AFTER AD
* OVERFLOW ON-SHOULD NOT BE
*
*****
88414310
88414320
88414330
88414340
88414350
88414360
88414370
88414380
88414390
88414400
88414410
88414420
88414430
88414440
88414450
88414460
88414470
88414480
88414490
88414500
88414510
88414520
88414530
88414540
88414550
88414560
88414570
88414580
88414590
88414600
88414610
88414620
88414630
88414640
88414650
88414660
88414670
88414680
88414690
88414700
88414710
88414720
88414730
88414740
88414750
88414760
88414770
88414780
88414790
88414800
88414810
88414820
88414830
88414840
88414850
88414860
88414870
88414880
88414890
88414900
88414910
88414920
88414930
88414940
88414950
88414960
88414970
88414980

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88414990
OF * 88415000
B-REG ROUTINE * A-REG Q-RLG XR-1 XR-2 XR-3 STATUS 88415010
*****
310E 0 0B14 DC A74C AD-FFFF FFFF 88415020
* FFFF FFFF N/A N/A N/A OFF AFTER LDD 88415030
* FFFF 7FFE N/A N/A N/A C AFTER AD 88415040
* CARRY NOT ON-SHOULD BE 88415050
* 88415060
* 88415070
* 88415080
* 88415090
310F 0 0B3E DC B742 AD-FFFF FFFF 88415100
* FFFF 7FFF N/A N/A N/A N/A AFTER LDD 88415110
* FFFF 7FFE N/A N/A N/A N/A AFTER AD 88415120
* ACCUM NOT EQUAL FFFF 88415130
* 88415140
* 88415150
3110 0 0B3E DC B742 AD-FFFF FFFF 88415160
* FFFF 7FFF N/A N/A N/A N/A AFTER LDD 88415170
* FFFF 7FFE N/A N/A N/A N/A AFTER AD 88415180
* Q REG NOT EQUAL 7FFE 88415190
* 88415200
* 88415210
3111 0 0B3E DC B742 AD-FFFF FFFF 88415220
* FFFF 7FFF N/A N/A N/A OFF AFTER LDD 88415230
* FFFF 7FFE N/A N/A N/A C AFTER AD 88415240
* OVERFLOW SET-SHOULD NOT BE 88415250
* 88415260
* 88415270
3112 0 0B3E DC B742 AD-FFFF FFFF 88415280
* FFFF 7FFF N/A N/A N/A OFF AFTER LDD 88415290
* FFFF 7FFE N/A N/A N/A C AFTER AD 88415300
* CARRY NOT SET-SHOULD BE 88415310
* 88415320
* 88415330
3113 0 0B68 DC B747 AD-0001 ODD LDC 88415340
* 0000 0001 N/A N/A N/A N/A AFTER LDD 88415350
* 0001 0002 N/A N/A N/A N/A AFTER AD 88415360
* ACCUM NOT EQUAL 0001 88415370
* 88415380
* 88415390
3114 0 0B68 DC B747 AD-0001 ODD LDC 88415400
* 0000 0001 N/A N/A N/A N/A AFTER LDC 88415410
* 0001 0002 N/A N/A N/A N/A AFTER AD 88415420
* Q REG NOT EQUAL 0002 88415430
* 88415440
* 88415450
3115 0 0B8C DC A780 SD-0000 0001 88415460
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415470
* FFFF FFFF N/A N/A N/A N/A AFTER SD 88415480
* ACCUM NOT EQUAL FFFF 88415490
* 88415500
* 88415510
3116 0 0B8C DC A780 SD-0000 0001 88415520
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415530
* FFFF FFFF N/A N/A N/A N/A AFTER SD 88415540
* Q REG NOT EQUAL FFFF 88415550
* 88415560
* 88415570
3117 0 0B8C DC A780 SD-0000 0001 88415580
* 0000 0000 N/A N/A N/A OFF AFTER LDD 88415590
* FFFF FFFF N/A N/A N/A C AFTER SD 88415600
* OVERFLOW ON-SHOULD NOT BE 88415610
* 88415620
* 88415630
* 88415640
* 88415650
* 88415660

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88415670
OF * 88415680
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS 88415690
*****
3118 0 0B8C DC A780 SD-0000 0001 88415700
* 0000 0000 N/A N/A N/A OFF AFTER LDD 88415720
* FFFF FFFF N/A N/A N/A C AFTER SD 88415730
* CARRY NOT ON-SHOULD BE 88415740
* 88415750
* 88415760
* 88415770
3119 0 0B86 DC A786 SD-FFFF FFFF 88415780
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415790
* 0000 0001 N/A N/A N/A N/A AFTER SD 88415800
* ACCUM NOT EQUAL TC 0000 88415810
* 88415820
* 88415830
311A 0 0B86 DC A786 SD-FFFF FFFF 88415840
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415850
* 0000 0001 N/A N/A N/A N/A AFTER SD 88415860
* Q REG NOT EQUAL 0001 88415870
* 88415880
* 88415890
311B 0 0B8B DC A78A SD-FFFF FFFF 88415900
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415910
* 0000 0001 N/A N/A N/A N/A AFTER SD 88415920
* ACCUM NOT EQUAL 0000 88415930
* 88415940
* 88415950
311C 0 0B8B DC A78A SD-FFFF FFFF 88415960
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88415970
* 0000 0001 N/A N/A N/A N/A AFTER SD 88415980
* Q REG NOT EQUAL 0001 88415990
* 88416000
* 88416010
311D 0 0BDF DC A78E SD-FFFF ODD LDC 88416020
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88416030
* 0000 0001 N/A N/A N/A N/A AFTER SD 88416040
* ACCUM NOT EQUAL 0000 88416050
* 88416060
* 88416070
311E 0 0BDF DC A78E SD-FFFF ODD LDC 88416080
* 0000 0000 N/A N/A N/A N/A AFTER LDD 88416090
* 0000 0001 N/A N/A N/A N/A AFTER SD 88416100
* Q REG NOT EQUAL 0001 88416110
* 88416120
* 88416130
311F 0 0C01 DC A7C0 MULT-2AAA 88416140
* 5555 N/A N/A N/A N/A N/A AFTER LD 88416150
* 0E38 9C72 N/A N/A N/A N/A AFTER M 88416160
* ACCUM NOT EQUAL 0E38 88416170
* 88416180
* 88416190
3120 0 0C01 DC A7C0 MULT-2AAA 88416200
* 5555 N/A N/A N/A N/A N/A AFTER LD 88416210
* 0E38 9C72 N/A N/A N/A N/A AFTER M 88416220
* Q REG NOT EQUAL 9C72 88416230
* 88416240
* 88416250
3121 0 0C16 DC A7C4 MULT-FFFF 88416260
* FFFF N/A N/A N/A N/A N/A AFTER LD 88416270
* 0000 0001 N/A N/A N/A N/A AFTER M 88416280
* ACCUM NOT EQUAL 0000 88416290
* 88416300
* 88416310
* 88416320
* 88416330
* 88416340

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88416350
OF * 88416360
B-REG ROUTINE * A-REG Q-REG XK-1 XK-2 XR-3 STATUS 88416370
***** 88416380
3122 0 UC16 DC A7C4 MULT-F1FF 88416390
* FFFF N/A N/A N/A N/A N/A AFTER LD 88416400
* 0000 0001 N/A N/A N/A N/A AFTER M 88416410
* Q REG NOT EQUAL 0001 88416420
* 88416430
* 88416440
* 88416450
3123 0 UC2A DC A7C8 MULT-FFFF 88416460
* 0000 N/A N/A N/A N/A N/A AFTER LD 88416470
* 0000 0000 N/A N/A N/A N/A AFTER M 88416480
* ACCUM NOT EQUAL 0000 88416490
* 88416500
* 88416510
3124 0 UC2A DC A7C8 MULT-FFFF 88416520
* 0000 N/A N/A N/A N/A N/A AFTER LD 88416530
* 0000 0000 N/A N/A N/A N/A AFTER M 88416540
* Q REG NOT EQUAL 0000 88416550
* 88416560
* 88416570
3125 0 UC3D DC A7CC MULT-0000 88416580
* FFFF N/A N/A N/A N/A N/A AFTER LD 88416590
* 0000 0000 N/A N/A N/A N/A AFTER M 88416600
* ACCUM NOT EQUAL 0000 88416610
* 88416620
* 88416630
3126 0 UC3D DC A7CC MULT-0000 88416640
* FFFF N/A N/A N/A N/A N/A AFTER LD 88416650
* 0000 0000 N/A N/A N/A N/A AFTER M 88416660
* Q REG NOT EQUAL 0000 88416670
* 88416680
* 88416690
3127 0 UC58 DC A800 DVD-8000 88416700
* 4000 7FFF N/A N/A N/A N/A AFTER LDD 88416710
* 8000 7FFF N/A N/A N/A N/A AFTER D 88416720
* ACCUM NOT EQUAL 8000 88416730
* 88416740
* 88416750
3128 0 UC58 DC A800 DVD-8000 88416760
* 4000 7FFF N/A N/A N/A N/A AFTER LDD 88416770
* 8000 7FFF N/A N/A N/A N/A AFTER D 88416780
* Q REG NOT EQUAL 7FFF 88416790
* 88416800
* 88416810
3129 0 UC58 DC A800 DVD-8000 88416820
* 4000 7FFF N/A N/A N/A OFF AFTER LDD 88416830
* 8000 7FFF N/A N/A N/A N/A AFTER D 88416840
* OVERFLOW ON-SHOULD NOT BE 88416850
* 88416860
* 88416870
312A 0 UC58 DC A800 DVD-8000 88416880
* 4000 7FFF N/A N/A N/A OFF AFTER LDD 88416890
* 8000 7FFF N/A N/A N/A N/A AFTER D 88416900
* CARRY ON-SHOULD NOT BE 88416910
* 88416920
* 88416930
312B 0 UC87 DC A806 DVD-5555 88416940
* 1C71 BBE3 N/A N/A N/A N/A AFTER LDD 88416950
* 5555 2DAA N/A N/A N/A N/A AFTER D 88416960
* ACCUM NOT EQUAL 5555 88416970
* 88416980
* 88416990
* 88417000
* 88417010
* 88417020

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS * 88417030
OF * 88417040
B-REG ROUTINE * A-REG Q-REG XK-1 XK-2 XR-3 STATUS 88417050
***** 88417060
312C 0 OC87 DC A806 DVD-5555 88417070
* 1C71 BBE3 N/A N/A N/A N/A AFTER LDD 88417080
* 5555 2DAA N/A N/A N/A N/A AFTER D 88417090
* Q REG NOT EQUAL 2DAA 88417100
* 88417110
* 88417120
* 88417130
312D 0 OC87 DC A806 DVD-5555 88417140
* 1C71 BBE3 N/A N/A N/A OFF AFTER LDD 88417150
* 5555 2DAA N/A N/A N/A N/A AFTER D 88417160
* OVERFLOW ON-SHOULD NOT BE 88417170
* 88417180
* 88417190
312E 0 OC87 DC A806 DVD-5555 88417200
* 1C71 BBE3 N/A N/A N/A OFF AFTER LDD 88417210
* 5555 2DAA N/A N/A N/A N/A AFTER D 88417220
* CARRY ON-SHOULD NOT BE 88417230
* 88417240
* 88417250
312F 0 OCB2 DC A80C DVD-0000 88417260
* 0000 0001 N/A N/A N/A OFF AFTER LDD 88417270
* N/A N/A N/A N/A N/A C AFTER D 88417280
* OVERFLOW NOT ON-SHOULD BE 88417290
* 88417300
* 88417310
3130 0 OCB0 DC A80E DVD-0001 88417320
* 4000 0000 N/A N/A N/A OFF AFTER LDD 88417330
* N/A N/A N/A N/A N/A C AFTER D 88417340
* OVERFLOW NOT ON-SHOULD BE 88417350
* 88417360
* 88417370
3131 0 OCC8 DC B800 DVD-4000 88417380
* A000 0000 N/A N/A N/A OFF AFTER LDD 88417390
* N/A N/A N/A N/A N/A C AFTER D 88417400
* OVERFLOW NOT ON-SHOULD BE 88417410
* 88417420
* 88417430
3132 0 OCD3 DC B802 DVD-8000 88417440
* C000 0000 N/A N/A N/A OFF AFTER LDD 88417450
* N/A N/A N/A N/A N/A C AFTER D 88417460
* OVERFLOW OFF--SHOULD BE ON 88417470
* 88417480
* 88417490
3133 0 OCDE DC B804 DVD-0001 88417500
* 0000 FFFF N/A N/A N/A OFF AFTER LDD 88417510
* N/A N/A N/A N/A N/A C AFTER D 88417520
* OVERFLOW OFF--SHOULD BE ON 88417530
* 88417540
* 88417550
3134 0 OCE9 DC B806 DVD-0001 88417560
* FFFF 7FFF N/A N/A N/A OFF AFTER LDD 88417570
* N/A N/A N/A N/A N/A C AFTER D 88417580
* OVERFLOW OFF--SHOULD BE ON 88417590
* 88417600
* 88417610
3135 0 OD6A DC A840 MDX 1 88417620
* N/A N/A 0000 N/A N/A N/A AFTER LDD 88417630
* N/A N/A FFFF N/A N/A N/A AFTER MDX 1 88417640
* INDEX REG 1 NOT EQUAL FFFF WHEN MODIFIED 88417650
* BY MINUS 1 88417660
* 88417670
* 88417680
3136 0 OD78 DC A842 MDX LONG FORM 88417690
* ADD +1 TO MEMORY FAILED 88417700

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
ADDRESS          *
  (IF)           *
B-REG ROUTINE   * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3137 U 0D8D      *
                * DC      A844      MDX 2 LONG FORM
                * N/A    N/A    N/A    FFFF N/A    N/A AFTER LDX
                * N/A    N/A    N/A    FFFF N/A    N/A AFTER MDX 2
                * INDEX REG 2 NOT EQUAL TO FFFF AFTER MDX +1
                * TO INDEX REG 2
                *
3138 0 0D9C      *
                * DC      A846      MDX 3
                * N/A    N/A    N/A    N/A    FFFF N/A AFTER LDX
                * N/A    N/A    N/A    N/A    0000 N/A AFTER MDX 2
                * MDX DID NOT CAUSE A SKIP WHEN INDEX REG 3
                * WENT TO 0000
                *
3139 0 0DA6      *
                * DC      A848      MDX 1
                * N/A    N/A    FFFF N/A    N/A    N/A AFTER LDX
                * N/A    N/A    0003 N/A    N/A    N/A AFTER MDX 1
                * MDX DID NOT CAUSE A SKIP WHEN THE SIGN
                * CHANGED ON INDEX REG 1
                *
313A 0 0DB0      *
                * DC      A849      MDX 1 INDIRECT
                * N/A    N/A    FFFF N/A    N/A    N/A AFTER LDX
                * N/A    N/A    FFFF N/A    N/A    N/A AFTER LDX 11
                * INDIRECT MDX OF INDEX REG 1 BY +1 FAILED
                *
3139 0 0DE4      *
                * JC      A880      SLC-A-XR 1
                * 0000 N/A    0010 N/A    N/A    N/A AFTER LDX
                * 0000 N/A    0000 N/A    N/A    N/A AFTER SLCA
                * ACCUM NOT EQUAL 0000
                *
313C 0 0DE4      *
                * DC      A880      SLC-A-XR 1
                * 0000 N/A    0010 N/A    N/A    N/A AFTER LDX
                * 0000 N/A    0000 N/A    N/A    N/A AFTER SLAC
                * INDEX REG 1 NOT EQUAL 0000
                *
313D 0 0DEB      *
                * DC      A884      SLC-A-XR 1
                * 0001 N/A    FF00 N/A    N/A    N/A AFTER LDX
                * 0000 N/A    FFC1 N/A    N/A    N/A AFTER ASCL
                * ACCUM NOT EQUAL 8000
                *
313E 0 0E0B      *
                * DC      A884      SLC-A-XR 1
                * 0001 N/A    FF0C N/A    N/A    N/A AFTER LDX
                * 8000 N/A    FFC1 N/A    N/A    N/A AFTER LDX
                * INDEX REG 1 NOT EQUAL FFC1
                *
313F 0 0E33      *
                * DC      A888      SLC-A-XR 1
                * 8000 N/A    0010 N/A    N/A    N/A AFTER LDX
                * 8000 N/A    0010 N/A    N/A    N/A AFTER SLCA
                * ACCUM NOT EQUAL 8000
                *
3140 0 0E33      *
                * DC      A888      SLC-A-XR 1
                * 8000 N/A    0010 N/A    N/A    N/A AFTER LDX
                * 8000 N/A    0010 N/A    N/A    N/A AFTER SLCA
                * INDEX REG 1 NOT EQUAL 0010
                *
*****

```

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS          *
  (IF)           *
B-REG ROUTINE   * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3141 0 0E6E      *
                * DC      A88C      SLC-XR 1
                * 0000 0000 0020 N/A    N/A    N/A AFTER LDX
                * 0000 0000 0000 N/A    N/A    N/A AFTER SLC
                * ACCUM NOT EQUAL 0000
                *
3142 0 0E6E      *
                * DC      A88C      SLC-XR 1
                * 0000 0000 0020 N/A    N/A    N/A AFTER LDX
                * 0000 0000 0000 N/A    N/A    N/A AFTER SLC
                * Q REG NOT EQUAL 0000
                *
3143 0 0E6E      *
                * DC      A88C      SLC-XR 1
                * 0000 0000 0020 N/A    N/A    N/A AFTER LDX
                * 0000 0000 0000 N/A    N/A    N/A AFTER SLC
                * INDEX REG 1 NOT EQUAL 0000
                *
3144 0 0E8D      *
                * DC      B882      SLC-XR 1
                * 0000 0002 FFDF N/A    N/A    N/A AFTER LDX
                * 8000 0000 FFC1 N/A    N/A    N/A AFTER SLC
                * ACCUM NOT EQUAL 8000
                *
3145 0 0E8D      *
                * DC      B882      SLC-XR 1
                * 0000 0002 FFDF N/A    N/A    N/A AFTER LDX
                * 8000 0000 FFC1 N/A    N/A    N/A AFTER SLC
                * Q REG NOT EQUAL 0000
                *
3146 0 0E8D      *
                * DC      B882      SLC-XR 1
                * 0000 0002 FFDF N/A    N/A    N/A AFTER LDX
                * 8000 0000 FFC1 N/A    N/A    N/A AFTER SLC
                * INDEX REG 1 NOT EQUAL FFC1
                *
3147 0 0EAF      *
                * DC      B884      SLC-XR 1
                * 0000 0002 001F N/A    N/A    N/A AFTER LDD+LDX
                * 8000 0000 0001 N/A    N/A    C AFTER SLC
                * A SLC TERMINATED BY A ONE BIT IN ACCUM BIT
                * ZERO DID NOT TURN ON CARRY
                *
3148 0 0EAF      *
                * DC      B884      SLC-XR 1
                * 0000 0002 001F N/A    N/A    N/A AFTER LDD+LDX
                * 8000 0000 0001 N/A    N/A    C AFTER SLC
                * ACCUM WAS NOT EQUAL TO 8000
                *
3149 0 0EAF      *
                * DC      B884      SLC-XR 1
                * 0000 0002 001F N/A    N/A    N/A AFTER LDD+LDX
                * 8000 0002 0001 N/A    N/A    C AFTER SLC
                * A SLC TERMINATED BY A ONE IN ACCUM BIT
                * ZERO DID NOT LEAVE XR 1 EQUAL 0001
                *
314A 0 0ECF      *
                * DC      B885      SLC-IX 1
                * 0000 0002 001C N/A    N/A    N/A AFTER LDD+LDX
                * 2000 0000 0000 N/A    N/A    OFF AFTER SLC
                * A SLC TERMINATED BY XR 1 GOING TO ZERO LEFT
                * THE CARRY FF SET
                *
*****

```



PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS *
JF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
314B 0 0EEC          DC      B8A0      CMP  A GREATER M
* 4000 N/A  N/A  N/A  N/A  N/A
* A GREATER THAN M CMP FAILED
*
314C 0 0EEC          DC      B8A0      CMP  A GREATER M
* 4000 N/A  N/A  N/A  N/A  N/A AFTER LD
* 4000 N/A  N/A  N/A  N/A  N/A AFTER CMP
* ACC DESTROYED AFTER CMP
*
314D 0 0F07          DC      B8A1      CMP  A LESS M
* 0000 N/A  N/A  N/A  N/A  N/A
* ACC LESS THAN M FAILS
*
314E 0 0F11          DC      B8A2      CMP  A LESS M
* 0000 N/A  N/A  N/A  N/A  N/A
* ACC LESS THAN M FAILS
*
314F 0 0F18          DC      B8A3      CMP  A LESS M
* 0000 N/A  N/A  N/A  N/A  N/A
* ACC LESS THAN M FAILS
*
3150 0 0F25          DC      B8A4      CMP  A LESS M
* 8000 N/A  N/A  N/A  N/A  N/A
* ACC LESS THAN M FAILS
*
3151 0 0F2F          DC      B8A5      CMP  A EQ M
* 1000 N/A  N/A  N/A  N/A  N/A
* ACC EQ M FAILED
*
3152 0 0F3A          DC      B8C0      DCM  AQ GTR M,M+1
* 8000 0001 N/A  N/A  N/A  N/A
* DCM AQ GREATER THAN M, M+1 FAILED
*
3153 0 0F3A          DC      B8C0      DCM  AQ GTR M, M+1
* 8000 0001 N/A  N/A  N/A  N/A
* ACC DESTROYED AFTER DCM
*
3154 0 0F3A          DC      B8C0      DCM  AQ GTR M,M+1
* 8000 0001 N/A  N/A  N/A  N/A
* J REG DESTROYED AFTER DCM
*
3155 0 0F54          DC      B8C1      DCM  AQ LESS M,M+1
* 0000 8000 N/A  N/A  N/A  N/A
* DCM FAILED WHEN A,Q LESS THAN M, M+1
*
3156 0 0F5D          DC      B8C2      DCM  AQ EQ M,M+1
* 0000 8000 N/A  N/A  N/A  N/A
* DCM FAILED WHEN A,Q EQ M, M+1
*
*****

```

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3157 0 088F          DC      A660      LDX 1 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A FFFF 0000 0C00 N/A AFTER LDX 1
* INDEX 2 CHANGED
*
3158 0 088F          DC      A660      LDX 1 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A FFFF 0000 0000 N/A AFTER LDX 1
* INDEX 3 CHANGED
*
3159 0 08A7          DC      A662      LDX 2 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 FFFF 0000 N/A AFTER LDX 2
* INDEX 1 CHANGED
*
315A 0 08A7          DC      A662      LDX 2 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 FFFF 0000 N/A AFTER LDX 2
* INDEX 3 CHANGED
*
315B 0 08BF          DC      A664      LDX 3 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 0000 FFFF N/A AFTER LDX 3
* INDEX 1 CHANGED
*
315C 0 08BF          DC      A664      LDX 3 -1
* N/A N/A 0000 0000 0000 N/A AFTER LDX'S
* N/A N/A 0000 0000 FFFF N/A AFTER LDX 3
* INDEX 2 CHANGED
*
315D 0 09EC          DC      A6D0      INDEXED INST F=0
* INITIALLY XR 1 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C0
* SHORT FORM INDEXED INST FAILED (X=1)
*
315E 0 09F8          DC      A6D2      INDEXED INST F=0
* INITIALLY XR 2 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C2
* SHORT FORM INDEXED INST FAILED (X=2)
*
315F 0 0A04          DC      A6D3      INDEXED INST F=0
* INITIALLY XR 3 HAS CORE LOCATION OF
* SYMBOLIC LABEL N6C1
* AFTER THE TEST THE ACC SHOULD HAVE
* CORE LOCATION OF SYMBOLIC LABEL N6C1
* SHORT FORM INDEXED INST. FAILED (X=3)
*
*****

```

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS *
OF *
B-REG ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
3160 0 0DE4 DC A880 SLCA CK CARRY
* 0000 FFFF 000A N/A N/A C AFTER LDD+LDS
* 0000 FFFF 0000 N/A N/A OFF AFTER STS
* CARRY ON SHOULD BE OFF
*
3161 0 0E0B DC A884 SLCA CK CARRY
* 0001 0010 FF00 N/A N/A OFF AFTER LDD+LDS
* 8000 0010 FF01 N/A N/A C AFTER SLCA
* CARRY OFF, SHOULD BE ON
*
3162 0 0E50 DC A889 NON INDEXED SLCA
* 0001 N/A 0010 0010 0010 N/A AFTER LD
* 0002 N/A N/A N/A N/A N/A AFTER SLCA
* SLCA T=0 FAILED
*
3163 0 0A10 DC A6D5 INDEXED SLA
* 0001 N/A 0002 N/A N/A N/A AFTER LD+LDS
* 0004 N/A N/A N/A N/A N/A AFTER SLA
* INDEXED SLA FAILED
*
3164 0 0A1C DC A6D6 INDEXED SRA
* 0004 N/A N/A 0002 N/A N/A AFTER LDX+LD
* 0001 N/A N/A N/A N/A N/A AFTER SRA
* INDEXED SRA FAILED
*
3165 0 0A28 DC A6F0 INDEXED BSC
* INITIALLY ACC HAS COKE LOCATION OF
* SYMBOLIC LABEL N6F1
* ACC DESTROYED AFTER INDEXED BSC
*
3166 0 0A39 DC A6F1 INDIR, INDEX BSC
* N/A N/A 0001 N/A N/A N/A AFTER LDX
* N/A N/A N/A N/A N/A N/A AFTER BSC
* INDIRECT, INDEXED BSC FAILED
*
3167 0 0820 DC A640 STX CK ACC
* INITIALLY ACC HAS COKE LOCATION OF
* SYMBOLIC LABEL H640
* ACC DESTROYED AFTER STX
*
3168 0 0D80 DC A849 MDX CK ACC
* INITIALLY ACC HAS CORE LOCATION OF
* SYMBOLIC LABEL H849
* ACC DESTROYED AFTER MDX
*
3169 0 08D9 DC A670 ACC DECODE
* 0001 N/A 0010 N/A N/A N/A
* 0000 N/A 0000 N/A N/A N/A
* FALSE DECODE OF ACC BE ZERO
* * EACH BIT POSITION IS TESTED
*
*****

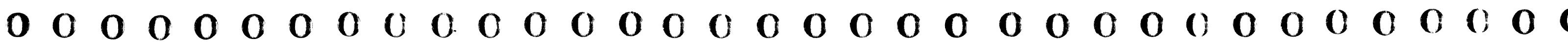
```

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
ADDRESS *
OF *
B-REC ROUTINE * A-REG Q-REG XR-1 XR-2 XR-3 STATUS
*****
316A 0 0D18 DC B807 DVD OVFL0
* 6100 0000 N/A N/A N/A OFF AFTER LDD
* N/A N/A N/A N/A N/A C AFTER D
* OVFL0 NOT ON
*
316B 0 0D23 DC B808 DVD OVFL0
* 8000 0000 N/A N/A N/A OFF AFTER LDD
* N/A N/A N/A N/A N/A D AFTER D
* OVFL0 NOT ON
*
316C 0 0D2E DC B809 DVD NO OVFL0
* FFFF FFFF N/A N/A N/A OFF AFTER LDD
* N/A N/A N/A N/A N/A OFF AFTER D
* OVFLD ON, SHOULD BE OFF
*
316D 0 0D3A DC B810 MPY-DIV ZEPO REM
* ACC WRONG AFTER MPY-DIV TEST
*
316E 0 0D3A DC B810 MPY-DIV ZERO REM
* Q REG WRONG AFTER MPY-DIV TEST
*
316F 0 0D78 DC A842 MDX CK ACC
* INITIALLY ACC HAS COKE LOCATION OF
* SYMBOLIC LABEL N844
* ACC DESTROYED AFTER ADD TO MEMCRY
*
3170 0 0600 DC A50A BSC CK ACC
* 8001 N/A N/A N/A N/A N/A AFTER LD
* 8001 N/A N/A N/A N/A N/A AFTER BSC
* ACC DESTROYED AFTER BSC CONDITIONS MET
*
3171 0 0DC6 DC A84A MDX MEM CK SKIP
* MEMORY LOC HAS ZERO
* MDX FAILED TO SKIP
*
3172 0 0DD0 DC A85A MDX MEM CK NO SKP
* MEMORY LOC IS NON ZERO
* MDX SKIPEU, SHOULD NOT HAVE
*
3173 0 0E60 DC A88A SW 15 NO INDEX
* 0000 FFFF 0010 0010 0010 NAFTER LDX'S
* 7FFF N/A N/A N/A N/A NAFTER SLC
* ACCUM NOT EQ TO 7FFF
*
3174 0 0F83 DC F000 IMPROPER CONTROL
* OPERATION SPECIFIED,
* BIT SW 14 ON WITHOUT
* BIT SW 8 OR 12 ON.
* CORRECT SWs AND PUSH
* START TO CONTINUE
*
*****

```



PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
3175          *****
012C 0 B400   DKG      300          88421790
                DC      /B400      PID          88421800
                *          88421810
                *          88421820
                *          88421830
                *          88421840
                *          88421850
                * 8000 N/A  N/A  N/A  N/A  D AFTER A 88421860
                * 0000 N/A  N/A  N/A  N/A  C+AFTER A 88421870
                *          TEST MDX OPERATION 88421880
                *          88421890
                *          88421900
                *          88421910
                *          88421920
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
012D 0 3000 X000 DC /3000 SET SWITCHES TO RUN 88421950
012E 0 7001 A080 MDX G080 88421960
012F 0 3004 DC /3004 ERR ID + ERR WAIT 88421970
                * MDX BY 1 FAILED 88421980
0130 0 7002 G080 MDX G081 88421990
0131 0 3005 DC /3005 ERR ID + ERR WAIT 88422000
                * MDX BY 2 FAILED 88422010
0132 0 3006 DC /3006 ERR ID + ERR WAIT 88422020
                * MDX BY 2 FAILED 88422030
0133 0 7004 G081 MDX G082 88422040
0134 0 3007 DC /3007 ERR ID + ERR WAIT 88422050
                * MDX BY 4 FAILED 88422060
0135 0 3008 DC /3008 ERR ID + ERR WAIT 88422070
                * MDX BY 4 FAILED 88422080
0136 0 3009 DC /3009 ERR ID + ERR WAIT 88422090
                * MDX BY 4 FAILED 88422100
0137 0 300A DC /300A ERR ID + ERR WAIT 88422110
                * MDX BY 4 FAILED 88422120
0138 0 7002 G082 MDX G084 88422130
0139 0 300B DC /300B ERR ID + ERR WAIT 88422140
                * MDX BY 2 FAILED 88422150
013A 0 7004 G083 MDX A0C0 88422160
013B 0 70FE G084 MDX G083 88422170
013C 0 300C DC /300C ERR ID + ERR WAIT 88422180
                * MDX BY -2 FAILED 88422190
013D 0 300D DC /300D ERR ID + ERR WAIT 88422200
                * MDX BY -2 FAILED 88422210
013E 0 300E DC /300E ERR ID + ERR WAIT 88422220
                * MDX BY -2 FAILED 88422230
                * 88422240
                * TEST OF BSC SKIP WHEN IT 88422250
                * SHOULD NOT 88422260
                * 88422270
                * 88422280
*****
013F 0 2003 AGL0 LDS 3 SET C AND OF ON 88422290
0140 0 4802 BSC C SK IF CARRY IS OFF 88422300
0141 0 7002 MDX G0C1 88422310
0142 0 300F DC /300F ERR ID + ERR WAIT 88422320
                * BSC-CARRY FAILED 88422330
0143 0 0000 N100 DC 0 88422340
0144 0 4801 G0C1 BSC 0 88422350
0145 0 7001 MDX G0C2 88422360
0146 0 3010 DC /3010 ERR ID + ERR WAIT 88422370
                * BSC-OVERFLOW FAILED 88422380
0147 0 4801 G0C2 BSC 0 CK IF OF WAS RESET 88422390
0148 0 3011 DC /3011 ERR ID + ERR WAIT 88422400
                * BSC-CVFLW SKPD-SHOULD 88422410
                * *NOT HAVE 88422420
0149 0 2000 LDS 0 RESET CARRY TO OFF 88422430
014A 0 4802 BSC C SK IF CARRY IS OFF 88422440
014B 0 3012 DC /3012 ERR ID + ERR WAIT 88422450
                * BSC-C DID NOT SKIP 88422460

```

PROCESSOR-CONTROLLER FUNCTION TEST

```

*****
*          TEST OF ACC ABILITY TO HOLD 88422470
*          ALL ZEROS 88422480
*          88422490
*          88422500
*          88422510
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
014C 0 C0F6 A100 LD N100 LD /0000 88422560
014D 0 4820 BSC Z SK IF ZERO 88422570
014E 0 3013 DC /3013 LNK ID + ERR WAIT 88422580
                * LD ACC TO 0 FAILED 88422590
014F 0 C0F3 LD N100 ACC=0, RFLD TO 0 88422600
0150 0 4820 BSC Z SK IF ZERO 88422610
0151 0 3014 DC /3014 ERR ID + ERR WAIT 88422620
                * LD ACC TO 0 FAILED 88422630
0152 0 4804 BSC E SK IF EVEN 88422640
0153 0 3015 DC /3015 EPR ID + ERR WAIT 88422650
                * BSC DN EVEN FAILED 88422660
                * 88422670
                * CONTAIN ALL CNES 88422680
                * 88422690
                * 88422700
*****
0154 0 C04A A140 LD N140 ACC.=0, RELOAD TO ONES 88422710
0155 0 4810 BSC - SK IF MINUS 88422720
0156 0 3016 DC /3016 ERR ID + ERR WAIT 88422730
                * LOAD ACC. FAILED OR 88422740
                * *BSC UN NEG. FAILED 88422750
0157 0 4803 BSC + 88422760
0158 0 7001 MDX G140 88422770
0159 0 3017 DC /3017 EPR ID + ERR WAIT 88422780
                * BSC DN + SKPD- 88422790
                * *SHOULD NOT HAVE 88422800
015A 0 4804 G140 BSC E 88422810
015B 0 7001 MDX G141 88422820
015C 0 301B DC /301B ERR ID + ERR WAIT 88422830
                * BSC DN E SKPD- 88422840
                * *SHOULD NOT HAVE 88422850
015D 0 1801 G141 SRA 1 88422860
015E 0 4804 BSC E 88422870
015F 0 7001 MDX G142 88422880
0160 0 3019 DC /3019 ERR ID + ERR WAIT 88422890
                * ACC NOT = 7FFF 88422900
0161 0 1801 G142 SRA 1 88422910
0162 0 4804 BSC E 88422920
0163 0 7001 MDX G143 88422930
0164 0 301A DC /301A ERR ID + ERR WAIT 88422940
                * ACC NOT = 3FFF 88422950
0165 0 1801 G143 SRA 1 88422960
0166 0 4804 BSC E 88422970
0167 0 7001 MDX G144 88422980
0168 0 301B DC /301B ERR ID + ERR WAIT 88422990
                * ACC NOT = 1FFF 88423000
0169 0 1801 G144 SRA 1 88423010
016A 0 4804 BSC E 88423020
016B 0 7001 MDX G145 88423030
016C 0 0000 DC /301C ACC NOT = 0FFF 88423040
016D 0 1801 G145 SRA 1 88423050
016E 0 4804 BSC E 88423060
016F 0 7001 MDX G146 88423070
0170 0 301D DC /301D ERR ID + ERR WAIT 88423080
                * ACC NOT = 07FF 88423090
0171 0 1801 G146 SRA 1 88423100
0172 0 4804 BSC E 88423110
0173 0 7001 MDX G147 88423120
0174 0 301E DC /301E ERR ID + ERR WAIT 88423130
                * ACC NOT = 03FF 88423140
0175 0 1801 G147 SRA 1

```

PROCESSOR-CONTROLLER FUNCTION TEST

0176 0 4804	BSC	E	88423150
0177 0 7001	MDX	G148	88423160
0178 0 301F	DC	/301F	88423170
		ERR ID + ERR WAIT	88423180
		ACC NOT = 01FF	88423190
0179 0 1801	* G148 SRA	1	88423200
017A 0 4804	BSC	E	88423210
017B 0 7001	MDX	G149	88423220
017C 0 3020	DC	/3020	88423230
		ERR ID + ERR WAIT	88423240
		ACC NOT = 00FF	88423250
017D 0 1801	* G149 SRA	1	88423260
017E 0 4804	BSC	E	88423270
017F 0 7001	MDX	G14A	88423280
0180 0 3021	DC	/3021	88423290
		ERR ID + ERR WAIT	88423300
		ACC NOT = 007F	88423310
0181 0 1801	* G14A SRA	1	88423320
0182 0 4804	BSC	E	88423330
0183 0 7001	MDX	G14B	88423340
0184 0 3022	DC	/3022	88423350
		ERR ID + ERR WAIT	88423360
		ACC NOT = 003F	88423370
0185 0 1801	* G14B SRA	1	88423380
0186 0 4804	BSC	F	88423390
0187 0 7001	MDX	G14C	88423400
0188 0 3023	DC	/3023	88423410
		ERR ID + ERR WAIT	88423420
		ACC NOT = 001F	88423430
0189 0 1801	* G14C SRA	1	88423440
018A 0 4804	BSC	E	88423450
018B 0 7001	MDX	G14D	88423460
018C 0 3024	DC	/3024	88423470
		ERR ID + ERR WAIT	88423480
		ACC NOT = 000F	88423490
018D 0 1801	* G14D SRA	1	88423500
018E 0 4804	BSC	E	88423510
018F 0 7001	MDX	G14E	88423520
0190 0 3025	DC	/3025	88423530
		ERR ID + ERR WAIT	88423540
		ACC NOT = 0007	88423550
0191 0 1801	* G14E SRA	1	88423560
0192 0 4804	BSC	E	88423570
0193 0 7001	MDX	G14F	88423580
0194 0 3026	DC	/3026	88423590
		ERR ID + ERR WAIT	88423600
		ACC NOT = 0003	88423610
0195 0 1801	* G14F SRA	1	88423620
0196 0 4804	BSC	E	88423630
0197 0 7001	MDX	G150	88423640
0198 0 3027	DC	/3027	88423650
		ERR ID + ERR WAIT	88423660
		ACC NOT = 0001	88423670
0199 0 1801	* G150 SRA	1	88423680
019A 0 4804	BSC	E	88423690
019B 0 3028	DC	/3028	88423700
		ERR ID + ERR WAIT	88423710
		ACC NOT = 0000	88423720
019C 0 4820	* BSC	Z	88423730
019D 0 3029	DC	/3029	88423740
		ERR ID + ERR WAIT	88423750
		ACC NOT = 0000	88423760
019E 0 7001	* MDX	A180	88423770
019F 0 FFFF	DC	/FFFF	88423780
		EXIT TO NEXT ROUTINE	88423790
			88423800
			88423810
			88423820

TEST LOADING OF ONES ON ONES

```

*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
01A0 0 C049 A180 LD N180 LD /FFFF
01A1 0 482C BSC +EZ SK ON +,EVEN OR ZERO
01A2 0 4810 BSC - SK IF MINUS
01A3 0 302A DC /302A ERR ID + ERR WAIT
ACC NOT = FFFF
01A4 0 C045 LD N180 LD /FFFF
01A5 0 482C BSC +EZ

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01A6 0 4810	BSC	-	88423830
01A7 0 302B	DC	/302B	88423840
		ERR ID + ERR WAIT	88423850
		ACC NOT = FFFF	88423860
01A8 0 1801	* SRA	1	88423870
		SHIFT MIGHT ONE	88423880
		TEST ABILITY OF ACC TO SHIFT	88423890
01A9 0 4804	* BSC	E	88423900
01AA 0 7001	MDX	G181	88423910
01AB 0 302C	DC	/302C	88423920
		ERR ID + ERR WAIT	88423930
		ACC NOT = 7FFF	88423940
01AC 0 1801	* G181 SRA	1	88423950
01AD 0 4804	BSC	E	88423960
01AE 0 7001	MDX	G182	88423970
01AF 0 302D	DC	/302D	88423980
		ERR ID + ERR WAIT	88423990
		ACC NOT = 3FFF	88424000
01B0 0 1801	* G182 SRA	1	88424010
01B1 0 4804	BSC	E	88424020
01B2 0 7001	MDX	G183	88424030
01B3 0 302E	DC	/302E	88424040
		ERR ID + ERR WAIT	88424050
		ACC NOT = 1FFF	88424060
01B4 0 1801	* G183 SRA	1	88424070
01B5 0 4804	BSC	E	88424080
01B6 0 7001	MDX	G184	88424090
01B7 0 302F	DC	/302F	88424100
		ERR ID + ERR WAIT	88424110
		ACC NOT = 00FF	88424120
01B8 0 1801	* G184 SRA	1	88424130
01B9 0 4804	BSC	E	88424140
01BA 0 7001	MDX	G185	88424150
01BB 0 3030	DC	/3030	88424160
		ERR ID + ERR WAIT	88424170
		ACC NOT = 07FF	88424180
01BC 0 1801	* G185 SRA	1	88424190
01BD 0 4804	BSC	E	88424200
01BE 0 7001	MDX	G186	88424210
01BF 0 3031	DC	/3031	88424220
		ERR ID + ERR WAIT	88424230
		ACC NOT = 03FF	88424240
01C0 0 1801	* G186 SRA	1	88424250
01C1 0 4804	BSC	E	88424260
01C2 0 7001	MDX	G187	88424270
01C3 0 3032	DC	/3032	88424280
		ERR ID + ERR WAIT	88424290
		ACC NOT = 01FF	88424300
01C4 0 1801	* G187 SRA	1	88424310
01C5 0 4804	BSC	E	88424320
01C6 0 7001	MDX	G188	88424330
01C7 0 3033	DC	/3033	88424340
		ERR ID + ERR WAIT	88424350
		ACC NOT = 00FF	88424360
01C8 0 1801	* G188 SRA	1	88424370
01C9 0 4804	BSC	E	88424380
01CA 0 7001	MDX	G189	88424390
01CB 0 3034	DC	/3034	88424400
		ERR ID + ERR WAIT	88424410
		ACC NOT = 007F	88424420
01CC 0 1801	* G189 SRA	1	88424430
01CD 0 4804	BSC	E	88424440
01CE 0 7001	MDX	G18A	88424450
01CF 0 3035	DC	/3035	88424460
		ERR ID + ERR WAIT	88424470
		ACC NOT = 003F	88424480
01D0 0 1801	* G18A SRA	1	88424490
01D1 0 4804	BSC	E	88424500
01D2 0 7001	MDX	G18B	
01D3 0 3036	DC	/3036	
		ERR ID + ERR WAIT	
		ACC NOT = 001F	
01D4 0 1801	* G18B SRA	1	
01D5 0 4804	BSC	E	
01D6 0 7001	MDX	G18C	
01D7 0 3037	DC	/3037	
		ERR ID + ERR WAIT	
		ACC NOT = 000F	
01D8 0 1801	* G18C SRA	1	
01D9 0 4804	BSC	E	
01DA 0 7001	MDX	G18D	
01DB 0 3038	DC	/3038	
		ERR ID + ERR WAIT	

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PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

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*          ACC NOT = 0007          8B424510
01DC 0 1801      G18D  SRA  1          8B424520
01DD 0 4804          BSC  E          8B424530
01DE 0 7001          MDX  G18E         8B424540
01DF 0 3039          DC   /3039        8B424550
*          ERR ID + ERR WAIT        8B424560
*          ACC NOT = 0003          8B424570
01E0 0 1801      G18E  SRA  1          8B424580
01E1 0 4804          BSC  E          8B424590
01E2 0 7001          MDX  G18F         8B424600
01E3 0 303A          DC   /303A        8B424610
*          ERR ID + ERR WAIT        8B424620
*          ACC NOT = 0001          8B424630
01E4 0 1801      G18F  SRA  1          8B424640
01E5 0 4804          BSC  E          8B424650
01E6 0 303B          DC   /303B        8B424660
*          ERR ID + ERR WAIT        8B424670
*          ACC NOT = 0000          8B424680
01E7 0 4820          BSC  Z          8B424690
01E8 0 303C          DC   /303C        8B424700
*          ERR ID + ERR WAIT        8B424710
*          ACC NOT = 0000          8B424720
01E9 0 7001          MDX  A1CO         8B424730
01EA 0 FFFF          N180 DC /FFFF        8B424740
*          EXIT TO NEXT ROUTINE      8B424750
*          TEST ABILITY TO LOAD ZEROS 8B424760
*          CN TOP OF ZEROS AND ONES ON 8B424770
*          TOP OF ZEROS              8B424780
*          *****
CORE   DATA OR *LA- OPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS  ID+SEQ= AT RIGHT
*****
01EB 0 C007      A1CO  LD   N1CO      LD /0000          8B424810
01EC 0 4820          BSC  Z          SK ON ZERO        8B424820
01ED 0 303D          DC   /303D        ERR ID + ERR WAIT 8B424830
*          ACC NOT = ZERO          8B424840
*          LD /FFFF                8B424850
01EE 0 C005          LD   N1C1          8B424860
01EF 0 482C          BSC  +EZ         8B424870
01FO 0 4810          BSC  -          SK ON MINUS      8B424880
01F1 0 303E          DC   /303E        ERR ID + ERR WAIT 8B424890
*          ACC NOT = FFFF          8B424900
*          EXIT TO NEXT ROUTINE      8B424910
01F2 0 7002          MDX  A1D0          8B424920
01F3 0 0000          N1CO DC /0000      8B424930
01F4 0 FFFF          N1C1 DC /FFFF        8B424940
*          TEST EOR OPERATION        8B424950
*          *****
01F5 0 C01C      A1D0  LD   N1D1      LD /0000          8B424970
01F6 0 4820          BSC  Z          SK ON ZERO        8B424980
01F7 0 303F          DC   /303F        ERR ID + ERR WAIT 8B424990
*          ACC NOT = ZERO          8B425000
*          ZERO WITH /0000          8B425010
01F8 0 F019          EOR  N1D1          8B425020
01F9 0 4820          BSC  Z          SK ON ZERO        8B425030
01FA 0 3040          DC   /3040        ERR ID + ERR WAIT 8B425040
*          EOR OF 0 AND 0 FAILED    8B425050
*          LD /FFFF                8B425060
01FB 0 C015          LD   N1D0          8B425070
01FC 0 F014          EOR  N1D0          8B425080
01FD 0 4820          BSC  Z          8B425090
01FE 0 3041          DC   /3041        ERR ID + ERR WAIT 8B425100
*          EOR OF 1 AND 1 FAILED    8B425110
01FF 0 F011          EOR  N1D0          8B425120
0200 0 482C          BSC  +EZ         8B425130
0201 0 4810          BSC  -          8B425140
0202 0 3042          DC   /3042        ERR ID + ERR WAIT 8B425150
*          EOR OF 1 AND 0 FAILED    8B425160
0203 0 1801          SRA  1          8B425170
0204 0 F00E          EOR  N1D2          8B425180
0205 0 4820          BSC  Z          8B425190
0206 0 3043          DC   /3043        ERR ID + ERR WAIT

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*          EOR OF 1 AND 0 FAILED    8B425190
0207 0 C009          LD   N1D0          8B425200
0208 0 F007          EOR  N1D1          8B425210
0209 0 482C          BSC  +EZ         8B425220
020A 0 4810          BSC  -          8B425230
020B 0 3044          DC   /3044        ERR ID + ERR WAIT 8B425240
*          EOR OF 0 AND 1 FAILED    8B425250
020C 0 1801          SRA  1          8B425260
020D 0 F005          FOR  N1D2          8B425270
020E 0 4820          BSC  Z          8B425280
020F 0 3045          DC   /3045        ERR ID + ERR WAIT 8B425290
*          EOR OF 0 AND 1 FAILED    8B425300
*          EXIT TO NEXT ROUTINE      8B425310
0210 0 7003          MDX  A1E0          8B425320
0211 0 FFFF          N1D0 DC /FFF+F      8B425330
0212 0 0000          N1D1 DC /0000      8B425340
0213 0 7FFF          N1D2 DC /7FFF        8B425350
*          TEST OF ABILITY TO SET    8B425360
*          F BIT TO ONE              8B425370
*          *****                  8B425380
0214 00 C400021F    A1E0 LD L N1E1      LD /0000          8B425400
0216 0 4820          BSC  Z          SK ON ZERO        8B425410
0217 0 3046          DC   /3046        ERR ID + ERR WAIT 8B425420
*          WRONG LOCATION LOADED     8B425430
0218 00 C400021E    LD L N1E0          LD C(N1E0)        8B425440
021A 0 F003          EOR  N1E0          8B425450
021B 0 4820          BSC  Z          SK ON ZERO        8B425460
021C 0 3047          DC   /3047        ERR ID + ERR WAIT 8B425470
*          WRONG LOCATION LOADED     8B425480
021D 0 7002          MDX  A1F0          8B425490
021E 0 021E          N1E0 DC N1E0        EXIT TO NEXT ROUTINE 8B425500
021F 0 0000          N1E1 DC /0000      8B425510
*          TEST OF INDIRECT ADDRESSING 8B425520
*          *****                  8B425530
0220 00 C480022C    A1F0 LD I N1F2      LD /0000          8B425550
0222 0 4820          BSC  Z          SK ON ZERO        8B425570
0223 0 3048          DC   /3048        ERR ID + ERR WAIT 8B425580
*          WRONG LOCATION LOADED     8B425590
0224 00 C480022B    LD I N1F1          LD C(N1F1)        8B425600
0226 0 F004          EOR  N1F1          ZERO WITH C(N1F1) 8B425610
0227 0 4820          BSC  Z          8B425620
0228 0 3049          DC   /3049        ERR ID + ERR WAIT 8B425630
*          WRONG LOCATION LOADED     8B425640
0229 0 7003          MDX  A200          8B425650
022A 0 0000          N1F0 DC /0000      8B425660
022B 0 022B          N1F1 DC N1F1        8B425670
022C 0 022A          N1F2 DC N1F0        8B425680
*          TEST OF BSC LONG FORM AND 8B425690
*          INDIRECT OPERATION        8B425700
*          *****                  8B425710
CORE   DATA OR *LA- OPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS  ID+SEQ= AT RIGHT
*****
022D 00 4C000231    A200 BSC L G200    8B425720
022F 0 304A          DC   /304A        ERR ID + ERR WAIT 8B425730
*          BSC DID NOT BRANCH        8B425740
0230 0 304B          DC   /304B        ERR ID + ERR WAIT 8B425750
*          BSC SKPD-SHOULD BRNCH     8B425760
0231 0 C03A          G200 LD N200        8B425770
0232 00 4C040236    BSC L G201,E      BR IF NOT EVEN    8B425780
0234 0 304C          DC   /304C        ERR ID + ERR WAIT 8B425790
*          BSC E DID NOT BRANCH     8B425800

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PROCESSOR-CONTROLLER FUNCTION TEST

0235 0 304D DC /304D ERR ID + ERR WAIT 88425870
0236 00 4C08023A G201 BSC L G202,+ 88425880
0238 0 304E DC /304E BR IF NOT PLUS 88425890
0239 0 304F DC /304F ERR ID + ERR WAIT 88425900
023A 00 4C20023E G202 BSC L G203,Z 88425910
023C 0 3050 DC /3050 BSC - DID NOT BRANCH 98425920
023D 0 3051 DC /3051 ERR ID + ERR WAIT 88425930
023E 00 4C100241 G203 BSC L V154,- 88425940
0240 0 7001 MDX G204 88425950
0241 0 3052 V154 DC /3052 88425960
0242 0 200J G204 LDS 3 88425970
0243 00 4C020247 BSC L G205,C 88425980
0245 0 3053 DC /3053 BR IF NOT MINUS 88425990
0246 0 3054 DC /3054 ERR ID + ERR WAIT 88426000
0247 00 4C010248 G205 BSC L G208,C 88426010
0249 0 3055 DC /3055 BR IF OF ON 88426020
024A 0 3056 DC /3056 ERR ID + ERR WAIT 88426030
024B 00 4C01024E G208 BSC L V168,0 88426040
024D 0 7001 MDX G206 88426050
024E 0 3057 V168 DC /3057 BR C DID NOT BRANCH 88426060
024F 0 2000 G206 LDS 0 88426070
0250 00 4C020253 BSC L V170,C 88426080
0252 0 7001 MDX G207 88426090
0253 0 3058 V170 DC /3058 BR IF CARRY IS ON 88426100
0254 00 4C010257 G207 BSC L V174,C 88426110
0256 0 7001 MDX G209 88426120
0257 0 3059 V174 DC /3059 BR IF OF ON 88426130
0258 0 C014 G209 LD N201 88426140
0259 00 4C180250 BSC L G20A,+ 88426150
025B 0 305A DC /305A BR IF CARRY IS OFF 88426160
025C 0 305B DC /305B ERR ID + ERR WAIT 88426170
025D 0 C00E G20A LD N200 88426180
025E 00 4C180261 BSC L V180,+ 88426190
0260 0 7001 MDX G200 88426200
0261 0 305C V180 DC /305C BR IF OF ON 88426210
0262 0 C00B G20D LD N202 88426220
0263 00 4C180266 BSC L V184,+ 88426230
0265 0 7001 MDX G208 88426240
0266 0 305D V184 DC /305D BR IF OF ON 88426250
0267 00 4C80026F G208 BSC I N203 88426260
0269 0 305E DC /305E BR IF OF ON 88426270
026A 0 305F DC /305F ERR ID + ERR WAIT 88426280
026B 0 7004 G20C MDX A240 88426290
026C 0 FFFF N200 DC /FFFF 88426300
026D 0 0000 N201 DC /0000 88426310
026E 0 0001 N202 DC /0001 88426320
026F 0 026B N203 DC G20C 88426330
TEST SHORT AND LONG FORM 88426340

PROCESSOR-CONTROLLER FUNCTION TEST

* BSI 88426550
* 88426560
***** 88426570
CORE DATA OR *LA- OPER- 88426590
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88426600
***** 88426610
0270 0 4002 A240 BSI N241 STORE ADDRESS CF I REG 88426620
0271 0 0271 N240 DC N240 STORE ADDRESS CF I REG 88426630
0272 0 3060 DC /3060 ERR ID + ERR WAIT 88426640
0273 0 0000 N241 DC /0000 BSI SKPD-SHOULD BRNCH 88426650
0274 0 COFE LD N241 RETURN ADDR FOR MAIN PROG 88426660
0275 0 FOFB EDR N240 LD RETURN ADDR 88426670
0276 0 4820 BSC Z ZERO IN RETURN ADDR 88426680
0277 0 3061 DC /3061 ERR ID + ERR WAIT 88426690
0278 00 4408027D * BSI L N243,+ 88426700
027A 0 3062 VIAC DC /3062 BSI NOT STORED I REG 88426710
027B 0 3063 * DC /3063 STORE ADDR OF I REG 88426720
027C 0 027A N242 DC VIAC ERR ID + ERR WAIT 88426730
027D 0 0000 N243 DC /0000 BSI + DID NOT BRANCH 88426740
027E 0 COFE LD N243 ERR ID + ERR WAIT 88426750
027F 0 FOFC EDR N242 BSI SKPD-SHOULD BNCH 88426760
0280 0 4820 BSC Z RETURN ADDR FOR MAIN PROG 88426770
0281 0 3064 DC /3064 ERR ID + ERR WAIT 88426780
* BSI NOT STORE I REG 88426790
* 88426800
* TEST OF INSTR REQUIRED FOR 88426810
* ERROR CONTROL 88426820
***** 88426830
0282 0 C04A A900 LD F911 LD A NUMBER 88426840
0283 0 D04A STO F912 88426850
0284 0 C04A LD F913 88426860
0285 0 CC48 LD F912 88426870
0286 0 F046 EDR F911 88426880
0287 0 4820 BSC Z 88426890
0288 0 3065 DC /3065 ERR ID + ERR WAIT 88426900
0289 0 C049 * LD F918 STORE FAILED 88426910
028A 0 4820 BSC Z CK FIRST PASS SW (/0002) 88426920
028B 0 704D MDX A280 IS SW ON 88426930
028C 0 C044 LD F916 YES GO TO NEXT ROUTINE 88426940
028D 0 D045 STO F918 GET 0002 88426950
028E 0 1810 SRA 16 STORE /0002 88426960
028F 00 D4000001 STO L /0001 CLEAR ACC 88426970
0291 0 61FF LDX I -1 ZERO WITH /0001 88426980
0292 00 C4000001 LD L /0001 LD XR I WITH -1 88426990
0294 0 4820 BSC Z ZERO IN 1800 -1 FOR 1130 88427000
0295 0 7010 MDX G901 ZERC FOR 1800 88427010
0296 0 C03D LD F919 1130 CPU 88427020
0297 0 D033 STO F903 1300 P-C LD /0240 88427030
0298 00 D4000F81 STO L N8C2 STO /0240 THIS IS AREA, 88427040
029A 00 D4000FED STO L F004 * FUNCTION AND MODIFIER 88427050
029C 00 D4000FF4 STO L F007 * FOR READING DATA ENTRY 88427060
029E 0 0837 G902 XID F922 * SWITCHES IN 1800 88427070
029F 0 E038 AND F923 SENSE SENSE/PROG SWS 88427080
02A0 0 F037 EDR F923 IGNORE CE SWS. (/FF00) 88427090
***** 88427100
CORE DATA OR *LA- OPER- 88427110
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88427120
***** 88427130
02A1 00 4C1802AE BSC L G900,+ BRANCH ON ZERO 88427140
02A3 0 F034 EDR F923 88427150
02A4 0 3066 DC /3066 ERROR ID + ERR WAIT 88427160

PROCESSOR-CONTROLLER FUNCTION TEST

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*
*
02A5 0 70F8 MDX G902 SENSE/PROG SWS NOT 88427230
02A6 0 C02E G901 LD F920 * EQUAL TO /FF00 88427240
02A7 0 D023 STO F903 REPEAT TEST 88427250
02A8 00 D4000F81 STO L N8C2 1130 CPU LD /3A00 88427260
02AA 00 D4000FED STO L F004 STO /3A00 THIS IS 88427270
02AC 00 D4000FF4 STO L F007 * AREA, FUNCTION + 88427280
02AE 0 081B G900 XIO F902 * MODIFIER FOR READING 88427290
02AF 0 C022 LD F917 * DATA ENTRY SWITCHES 88427300
02B0 0 F01F EDR F915 TEST DATA ENTRY SWS 88427310
02B1 00 4C1802B6 BSC L X001,+ BRANCH ON ZERO 88427320
02B3 0 F01C FOR F915 * FOR /FFFF 88427330
02B4 0 3067 DC /3067 ERR ID + ERR WAIT 88427340
* DATA ENTRY SWS NOT 88427350
* EQUAL TO /FFFF 88427360
02B5 0 70F8 MDX G900 88427370
02B6 0 3001 X001 DC /3001 SET SENSE/PROG AND 88427380
* DATA ENTRY SWS TO 88427390
* ZEROS AND PUSH START 88427400
* CK FOR 1130 (3A00-1130) 88427410
02B7 0 C013 LD F903 XFER IF 1130 88427420
02B8 0 F01C EDR F920 TEST SENSE/PROG SWS 88427430
02B9 00 4C1802C1 BSC L G904,+ IGNORE CE SWS. (/FF00) 88427440
02BA 0 081A G903 XIO F922 BRANCH IF CK 88427450
02BC 0 E01B AND F923 ERR ID + ERR WAIT 88427460
02BD 00 4C1802C1 BSC L G904,+ SENSE/PROG SWS NOT 88427470
02BF 0 3068 DC /3068 * EQUAL TO /0000 88427480
* REPEAT TEST 88427490
* 88427500
02C0 0 70FA MDX G903 TEST DATA ENTRY SWS 88427510
02C1 0 0808 G904 XIO F902 * FOR /0000 88427520
02C2 0 C00F LD F917 BRANCH ON ZERO 88427530
02C3 00 4C1802C7 BSC L X003,+ ERR ID + ERR WAIT 88427540
02C5 0 3069 DC /3069 DATA ENTRY SWITCHES 88427550
* * NCT EQ /0000 88427560
* 88427570
02C6 0 70FA MDX G904 SET BIT SWITCHES AS 88427580
02C7 0 3002 X003 DC /3002 * DESIRED FOR RUN 88427590
* * AND PUSH START 88427600
* EXIT TO NEXT ROUTINE 88427610
02C8 0 7010 MDX A280 88427620
02CA 0000 BSS E 88427630
02CA 0 0202 F902 DC F917 88427640
02CB 0 0240 F903 DC /0240 EQUAL /3A00 IN 1130 88427650
02CC 0 020C F904 DC F904 88427660
02CD 0 02CE F911 DC F912 88427670
02CE 0 0000 F912 DC /0000 88427680
02CF 0 0000 F913 DC /0000 88427690
02D0 0 1FFF F915 DC /FFFF 88427700
02D1 0 0002 F916 DC /0002 88427710
02D2 0 0000 F917 DC /0000 88427720
02D3 0 0000 F918 DC /0000 88427730
02D4 0 0240 F919 DC /0240 1800 READ BIT SWS CONSTANT 88427740
02D5 0 3A00 F920 DC /3A00 1130 READ BIT SWS CONSTANT 88427750
02D6 0 0000 F922 DC 0 SENSE SENSE/PRG CON 88427760
02D7 0 0760 DC /0760 88427770
02D9 0 FF00 F923 DC /FF00 88427780
***** 88427790
***** 88427800
***** 88427810
***** 88427820
* BEGINING OF SECTION OF 88427830
* PROGRAM USING COMMON ERROR 88427840
* CONTRCL ROUTINE 88427850
* 88427860
***** 88427870
***** 88427880
***** 88427890
* TEST OF SRA OPERATION 88427900

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PROCESSOR-CONTROLLER FUNCTION TEST

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*
*
***** 88427910
***** 88427920
CORE DATA OR =LA- OPER- 88427930
ADDR INSTRUCTION =BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88427940
***** 88427950
***** 88427960
02D9 0 C039 A280 LD N280 88427970
02DA 0 1810 SRA 16 88427980
02DB 00 4C1802E0 BSC L G280,+ BRANCH ON ZERO 88427990
02DD 00 44000F83 BSI L F000 SRA 16 FAILED 88428000
02DF 0 306A DC /306A ERR ID 88428010
02E0 00 44000FDE G280 BSI L F005 CK LCKK CN ERROR 88428020
02E2 0 70F6 MDX A280 LOOP 88428030
***** 88428040
02E3 0 C030 A281 LD N281 LD /8000 88428050
02E4 0 180F SRA 15 NOW A=/0001 88428060
02E5 0 F02F EDR N282 ZERO WITH /0001 88428070
02E6 00 4C1802EB BSC L G281,+ BRANCH ON ZERO 88428080
02E8 00 44000F83 BSI L F000 SRA 15 FAILED 88428090
02EA 0 306B DC /306B ERR ID 88428100
02EB 00 44000FDE G281 BSI L F005 CK LOCK ON ERROR 88428110
02ED 0 70F5 MDX A281 LOOP 88428120
***** 88428130
02EE 0 C027 A282 LD N283 LD /AAAA 88428140
02EF 0 1801 SRA 1 NOW A=/5555 88428150
02F0 0 F026 EDR N284 ZERO WITH /5555 88428160
02F1 00 4C1802F6 BSC L G282,+ BRANCH CN ZERO 88428170
02F3 00 44000F83 BSI L F000 SRA 1 FAILED 88428180
02F5 0 306C DC /306C ERR ID 88428190
02F6 00 44000FDE G282 BSI L F005 CK LCKK ON ERROR 88428200
02F8 0 70F5 MDX A282 LOOP 88428210
***** 88428220
02F9 0 C01D A283 LD N284 LD /5555 88428230
02FA 0 1801 SRA 1 NOW A=/2AAA 88428240
02FB 0 F01C EDR N285 ZERO WITH /2AAA 88428250
02FC 00 4C180301 BSC L G283,+ BRANCH CN ZERO 88428260
02FE 00 44000F83 BSI L F000 SRA 1 FAILED 88428270
0300 0 306D DC /306D ERR ID 88428280
0301 00 44000FDE G283 BSI L F005 CK LOCK ON ERROR 88428290
0303 0 70F5 MDX A283 LOOP 88428300
***** 88428310
0304 0 C00F A284 LD N281 LD /8000 88428320
0305 0 1801 SRA 1 NOW A- /4000 88428330
0306 0 1802 SRA 2 A= /1000 88428340
0307 0 1804 SRA 4 A=/0100 88428350
0308 0 1808 SRA 8 A = /0001 88428360
0309 0 F008 EDR N282 ZERO WITH /0001 88428370
030A 00 4C18030F BSC L G284,+ BRANCH ON ZERO 88428380
030C 00 44000F83 BSI L F000 COMB SRA FAILED 88428390
030E 0 306E DC /306E ERR ID 88428400
030F 00 44000FDE G284 BSI L F005 CK LOCK ON ERROR 88428410
0311 0 70F2 MDX A284 LOOP 88428420
0312 0 7006 MDX A2C0 EXIT TO NEXT ROUTINE 88428430
0313 0 FFFF N280 DC /FFFF 88428440
0314 0 8000 N281 DC /8000 88428450
0315 0 0001 N282 DC /0001 88428460
0316 0 AAAA N283 DC /AAAA 88428470
0317 0 5555 N284 DC /5555 88428480
0318 0 2AAA N285 DC /2AAA 88428490
* 88428500
* TEST OF AND FUNCTION 88428510
* 88428520
***** 88428530
***** 88428540
CORE DATA OR *LA- OPER- 88428550
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88428560
***** 88428570
0319 0 C029 A2C0 LD N2C0 LD /0000 88428580

```

PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

031A 0 E028 AND N2C0 AND /0000 88428590
031B 00 4C18032U BSC L G2C0,+ BRANCH ON ZERO 88428600
031D 00 44000F83 BSI L F000 AND OF 0 AND FAILED 88428610
031F 0 306F DC /306F ERR ID 88428620
0320 00 44000FDE G2C0 BSI L F005 CK LOCK ON ERROR 88428630
0322 0 70F6 MDX A2C0 LOOP 88428640

0323 0 CUIF A2C4 LD N2C0 LD /0000 88428650
0324 0 E01F AND N2C2 LD /FFFF 88428660
0325 00 4C18032A BSC L G2C4,+ BRANCH ON ZERO 88428670
0327 00 44000F83 BSI L F000 AND OF 0 AND 1 FAILED 88428680
0329 0 3070 DC /3070 ERR ID 88428690
032A 00 44000FDE G2C4 BSI L F005 CK LOCK ON ERROR 88428700
032C 0 70F6 MDX A2C4 LOOP 88428710

032D 0 C016 A2CB LD N2C2 LD /FFFF 88428720
032E 0 E014 AND N2C0 AND /0000 88428730
032F 00 4C180334 BSC L G2C8,+ BRANCH ON ZERO 88428740
0331 00 44000F83 BSI L F000 AND OF 1 AND 0 FAILED 88428750
0333 0 3071 DL /3071 ERR ID 88428760
0334 00 44000FDE G2CB BSI L F005 CK LOCK ON ERROR 88428770
0336 0 70F6 MDX A2CB LOOP 88428780

0337 0 C00C A2CC LD N2C2 LD /FFFF 88428790
0338 0 E00B AND N2C2 AND /FFFF 88428800
0339 0 F00A EOR N2C2 ZERO WITH /FFFF 88428810
033A 00 4C18033F BSC L G2CC,+ BRANCH ON ZERO 88428820
033C 00 44000F83 BSI L F000 AND OF 1 AND 1 FAILED 88428830
033E 0 3072 DC /3072 ERR ID 88428840
033F 00 44000FDE G2CC BSI L F005 CK LOCK ON ERROR 88428850
0341 0 70F5 MDX A2CC LOOP 88428860
0342 0 7002 MDX A300 EXIT TO NEXT ROUTINE 88428870
0343 0 0000 N2C0 DC /0000 88428880
0344 0 FFFF N2C2 DC /FFFF 88428890

TEST OF DR FUNCTION

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

0345 0 C020 A300 LD N300 LD /0000 88428900
0346 0 E81F OR N300 OR /0000 88428910
0347 00 4C18034C BSC L G300,+ BRANCH ON ZERO 88428920
0349 00 44000F83 BSI L F000 OR OF 0 AND 0 FAILED 88428930
034B 0 3073 DC /3073 ERR ID 88428940
034C 00 44000FDE G300 BSI L F005 CK LOCK ON ERROR 88428950
034E 0 70F6 MDX A300 LOOP 88428960

034F 0 C016 A302 LD N300 LD /0000 88428970
0350 0 E816 OR N302 OR /FFFF 88428980
0351 0 F015 EOR N302 ZERO WITH /FFFF 88428990
0352 00 4C180357 BSC L G302,+ BRANCH ON ZERO 88429000
0354 00 44000F83 BSI L F000 OR OF 0 AND 1 FAILED 88429010
0356 0 3074 DC /3074 ERR ID 88429020
0357 00 44000FDE G302 BSI L F005 CHECK LOOP SWITCH 88429030
0359 0 70F5 MDX A302 LOOP 88429040

035A 0 C00C A304 LD N302 LD /FFFF 88429050
035B 0 E80B JR N302 OR /FFFF 88429060
035C 0 F00A EOR N302 EOR IN /FFFF 88429070
035D 00 4C180362 BSC L G304,+ BRANCH ON ZERO 88429080
035F 00 44000F83 BSI L F000 OR OF 1 AND 1 FAILED 88429090
0361 0 3075 DC /3075 ERR ID 88429100
0362 00 44000FDE G304 BSI L F005 CK LOCK ON ERROR 88429110
0364 0 70F5 MDX A304 LOOP 88429120
0365 0 7002 MDX A240 EXIT TO NEXT ROUTINE 88429130

0366 0 0000 N300 DC /0000 88429270
0367 0 FFFF N302 DC /FFFF 88429280
* 88429290
* TEST OF RTE 16 OPERATION 88429300
* 88429310
***** 88429320
CORE DATA OR *LA- OPER- 88429330
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88429340
***** 88429350
0368 0 C016 A340 LD N340 LD /0000 88429360
0369 0 1800 RTE 16 PLACE /0000 IN Q REG 88429370
036A 0 C015 LD N341 LD /FFFF 88429380
036B 0 1800 RTE 16 NOW A=/0000 Q=/FFFF 88429390
036C 00 4C180271 BSC L G340,+ BRANCH ON ZERO 88429400
036E 00 44000F83 BSI L F000 ALL 0 THRU Q FAILED 88429410
0370 0 3076 DC /3076 ERR ID 88429420
0371 00 44000F82 G340 BSI L F00E CK LOCK ON ERROR 88429430
0373 0 70F4 MDX A340 LOOP 88429440
0374 0 1800 RTE 16 NOW A=/FFFF Q=/0000 88429450
0375 0 F00A EOR N341 ZERO WITH /FFFF 88429460
0376 00 4C18037B BSC L G342,+ BRANCH ON ZERO 88429470
0378 00 44000F83 BSI L F000 ALL 1 THRU Q FAILED 88429480
037A 0 3077 DC /3077 ERR ID 88429490
037B 00 44000FDE G342 BSI L F005 CK LOCK ON ERROR 88429500
037D 0 70EA MDX A340 LOOP 88429510
037E 0 7002 MDX A380 EXIT TO NEXT ROUTINE 88429520
037F 0 0000 N340 DC /0000 88429530
0380 0 FFFF N341 DC /FFFF 88429540
* 88429550
* TEST OF SRT OPERATION 88429560
* 88429570
***** 88429580
0381 0 C055 A380 LD N380 LD /8000 88429590
0382 0 18A0 SRT 32 NOW A=/FFFF Q=/FFFF 88429600
0383 0 F054 EOR N381 EOR IN /FFFF 88429610
0384 00 4C180389 BSC L G380,+ BRANCH ON ZERO 88429620
0386 00 44000F83 BSI L F000 SRT 32-A REG FAILED 88429630
0388 0 3078 DC /3078 ERR ID 88429640
0389 00 44000F82 G380 BSI L F00E CK LOCK ON ERROR 88429650
038B 0 70F5 MDX A380 LOOP 88429660
038C 0 1800 RTE 16 NOW A=/FFFF Q=/0000 88429670
038D 0 F04A EOR N381 EOR IN /FFFF 88429680
038E 00 4C180393 BSC L G382,+ BRANCH ON ZERO 88429690
0390 00 44000F83 BSI L F000 SRT 32-Q REG FAILED 88429700
0392 0 3079 DC /3079 ERR ID 88429710
0393 00 44000FDE G382 BSI L F005 CK LOCK ON ERROR 88429720
0395 0 70EB MDX A380 LOOP 88429730
***** 88429740
0396 0 C042 A384 LD N382 LD /4000 88429750
0397 0 18A0 SRT 32 NOW A=/0000 Q=/0000 88429760
0398 00 4C180390 BSC L G384,+ BRANCH ON ZERO 88429770
039A 00 44000F83 BSI L F000 SRT 32-A REG FAILED 88429780
039C 0 307A DC /307A ERR ID 88429790
039D 00 44000FDE G384 BSI L F005 CK LOCK ON ERROR 88429800
039F 0 70F6 MDX A384 LOOP 88429810
03A0 0 1800 RTE 16 NOW A=/0000 Q=/0000 88429820
03A1 00 4C1803A6 BSC L G386,+ BRANCH ON ZERO 88429830
03A3 00 44000F83 BSI L F000 SRT 32-Q REG FAILED 88429840
03A5 0 307B DC /307B ERR ID 88429850
03A6 00 44000FDE G386 BSI L F005 CK LOCK ON ERROR 88429860
03A8 0 70ED MDX A384 LOOP 88429870
***** 88429880
03A9 0 C030 A388 LD N383 LD /5555 88429890
03AA 0 18RF SRT 15 NOW A=/0000 Q=/0000 88429900
03AB 00 4C1803B0 BSC L G388,+ BRANCH ON ZERO 88429910
03AD 00 44000F83 BSI L F000 SRT 15-A REG FAILED 88429920
03AF 0 307C DC /307C ERR ID 88429930
88429940



PROCESSOR-CONTROLLER FUNCTION TEST

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03B0 00 44000FB2 G388 BSI L F00E CK LOCK ON ERROR 88429950
03B2 0 70F6 MDX A388 LOOP 88429960
03B3 0 18D0 RTE 16 NOW A=/AAAA Q=/0000 88429970
03B4 0 F02E EDR N384 ZERC WITH /AAAA 88429980
03B5 00 4C1803BA BSC L G36A,+ BRANCH ON ZERO 88429990
03B7 00 44000FB3 BSI L F000 SRT 15-Q REG FAILED 88430000
03B9 0 307D DC /307D ERR ID 88430010
03BA 00 44000FDE G38A BSI L F005 CK LOCK ON ERROR 88430020
03BC 0 70EC MDX A388 LOOP 88430030
*****
*****
CORE DATA OR *LA- OPER- 88430050
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88430060
*****
03BD 0 C01C A38C LD N383 LD /5555 88430090
03BE 0 1880 SRT 0 NOW A=/5555 Q=/0000 88430100
03BF 0 1882 SRT 2 NOW A=/1555 Q=/4000 88430110
03C0 0 1884 SRT 4 /0155 /5400 88430120
03C1 0 1886 SRT 6 /0005 /5550 88430130
03C2 0 1888 SRT 8 /0000 /0555 88430140
03C3 0 188A SRT 10 /0000 /0001 88430150
03C4 00 4C1803C9 BSC L G38C,+ BRANCH ON ZERO 88430160
03C6 00 44000FB3 BSI L F000 SERIES SRT FAILED 88430170
03C8 0 307E DC /307E ERR ID 88430180
03C9 00 44000FB2 G38C BSI L F00E CK LOCK ON ERROR 88430190
03CB 0 70F1 MDX A38C LOOP 88430200
03CC 0 18D0 RTE 16 NOW A=/0001 Q=/0000 88430210
03CD 0 F00E EDR N385 ZERC WITH /0001 88430220
03CE 00 4C1803D3 BSC L G38E,+ BRANCH ON ZERO 88430230
03DU 00 44000FB3 BSI L F000 SERIES SRT FAILED 88430240
03D2 0 307F DC /307F ERR ID 88430250
03D3 00 44000FDE G38E BSI L F005 CK LOCK ON ERROR 88430260
03D5 0 70E7 MDX A38C LOOP 88430270
03D6 0 7006 MDX A3C0 EXIT TO NEXT ROUTINE 88430280
03D7 0 8000 N380 DC /8000 88430290
03D8 0 FFFF N361 DC /FFFF 88430300
03D9 0 4000 N382 DC /4000 88430310
03DA 0 5555 N383 DC /5555 88430320
03DB 0 AAAA N384 DC /AAAA 88430330
03DC 0 0001 N385 DC /0001 88430340
*
* TEST OF RTE OPERATION
*
*****
*****
03DD 0 C035 A3C0 LD N3C1 LD /AAAA 88430380
03DE 0 18D0 RTE 16 NOW A=/0000 Q=/AAAA 88430390
03DF 0 C032 LD N3C0 NOW A=/5555 Q=/AAAA 88430400
03E0 0 18CF RTE 15 NOW A=/5554 Q=/AAAB 88430410
03E1 0 F034 EDR N3C4 ZERC WITH /5554 88430420
03E2 00 4C1803E7 BSC L G3C0,+ BRANCH ON ZERO 88430430
03E4 00 44000FB3 BSI L F000 RTE 15-Q TO A FAILED 88430440
03E6 0 3080 DC /3080 ERR ID 88430450
03E7 00 44000FB2 G3C0 BSI L F00E CK LOCK ON ERROR 88430460
03E9 0 70F3 MDX A3C0 LOOP 88430470
03EA 0 18D0 RTE 16 NOW A=/AAAB Q=/5554 88430480
03EB 0 F02B EDR N3C5 ZERC WITH /AAAB 88430490
03EC 00 4C1803F1 BSC L G3C2,+ BRANCH ON ZERO 88430500
03EE 00 44000FB3 BSI L F000 RTE 15-A TO Q FAILED 88430510
03FO 0 3081 DC /3081 ERR ID 88430520
03F1 00 44000FDE G3C2 BSI L F005 CK LOCK ON ERROR 88430530
03F3 0 70E9 MDX A3C0 LOOP 88430540
*****
*****
CORE DATA OR *LA- OPER- 88430580
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88430590
*****
03F4 0 C020 A3C4 LD N3C3 LD /8000 88430600
03F5 0 18D0 RTE 16 NOW A=/XXXX Q=/8000 88430610

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PROCESSOR-CONTROLLER FUNCTION TEST

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03F6 0 C01D LD N3C2 LD /0000 88430630
03F7 0 18C0 RTE 0 NOW A=/0000 Q=/8000 88430640
03F8 0 18C1 RTE 1 /0000 /4000 88430650
03F9 0 18C2 RTE 2 /0000 /1000 88430660
03FA 0 18C3 RTE 3 /0000 /0200 88430670
03FB 0 18C4 RTE 4 /0000 /0020 88430680
03FC 0 18C5 RTE 5 /0000 /0002 88430690
03FD 0 18C6 RTE 6 /0400 /0000 88430700
03FE 0 18CA RTE 10 /0001 /0000 88430710
03FF 0 F01B EDR N3C6 ZERC WITH /0001 88430720
0400 00 4C180405 BSC L G3C4,+ BRANCH ON ZERO 88430730
0402 00 44000FB3 BSI L F000 SERIES RTE FAILED 88430740
0404 0 3082 DC /3082 ERR ID 88430750
0405 00 44000FB2 G3C4 BSI L F00E CK LOCK ON ERROR 88430760
0407 0 70EC MDX A3C4 LOOP 88430770
0408 0 18D0 RTE 16 NOW A=/0000 Q=/0001 88430780
0409 00 4C18040E BSC L G3C6,+ BRANCH ON ZERO 88430790
040B 00 44000FB3 BSI L F000 SERIES RTE FAILED 88430800
040D 0 3083 DC /3083 ERR ID 88430810
040E 00 44000FDE G3C6 BSI L F00E CK LOCK ON ERROR 88430820
0410 0 70E3 MDX A3C4 LOOP 88430830
0411 0 7007 MDX A400 EXIT TO NEXT ROUTINE 88430840
0412 0 5555 N3C0 DC /5555 88430850
0413 0 AAAA N3C1 DC /AAAA 88430860
0414 0 0000 N3C2 DC /0000 88430870
0415 0 8000 N3C3 DC /8000 88430880
0416 0 5554 N3C4 DC /5554 88430890
0417 0 AAAB N3C5 DC /AAAB 88430900
0418 0 0001 N3C6 DC /0001 88430910
*
* TEST OF SLA OPERATION
*
*****
*****
0419 00 C40004BD A400 LD L N400 LD /FFFF 88430950
041B 0 18D0 RTE 16 NOW A=/XXXX Q=/FFFF 88430960
041C 00 C40004BD LD L N400 LD /FFFF 88430970
041E 0 1010 SLA 16 NOW A=/0000 Q=/FFFF 88430980
041F 00 4C020424 BSC L G404,+ BR ON CARRY 88430990
0421 00 44000FB3 BSI L F000 SLA 16-CARRY FAILED 88431000
0423 0 3085 DC /3085 ERR ID 88431010
0424 00 44000FB2 G404 BSI L F00E CK LOCK ON ERROR 88431020
0426 0 70F2 MDX A400 LOOP 88431030
0427 00 4C18042C BSC L G400,+ BRANCH ON ZERO 88431040
0429 00 44000FB3 BSI L F000 SLA 16-A REG FAILED 88431050
042B 0 3084 DC /3084 ERR ID 88431060
042C 00 44000FB2 G400 BSI L F00E CK LOCK ON ERROR 88431070
042E 0 70EA MDX A400 LOOP 88431080
042F 0 18D0 RTE 16 NOW A=/FFFF Q=/0000 88431090
0430 00 F40004BD EDR L N400 ZERC WITH /FFFF 88431100
0432 00 4C180437 BSC L G406,+ BRANCH ON ZERO 88431110
0434 00 44000FB3 BSI L F000 SLA 16-AFFECTED Q REG 88431120
0436 0 3086 DC /3086 ERR ID 88431130
0437 00 44000FDE G406 BSI L F005 CK LOCK ON ERROR 88431140
0439 0 70DF MDX A400 LOOP 88431150
*****
*****
CORE DATA OR *LA- OPER- 88431170
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88431180
*****
043A 00 C40004C2 A408 LD L N405 LD /0000 88431190
043C 0 18D0 RTE 16 NOW A=/XXXX Q=/0000 88431200
043D 00 C40004C3 LD L N406 /FFFE /0000 88431210
043F 0 1010 SLA 16 /0000 /0000 88431220
0440 00 4C020443 BSC L G407,+ BR ON CARRY 88431230
0442 0 7003 MDX G40C 88431240
0443 00 44000FB3 G407 BSI L F000 SLA 16- CARRY FAILED 88431250
0445 0 3088 DC /3088 ERR ID 88431260
0446 00 44000FB2 G40C BSI L F00E CK LOCK ON ERROR 88431270

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PROCESSOR-CONTROLLER FUNCTION TEST

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0448 0 70F1 MDX A408 LOOP 88431310
0449 00 4C18044E BSC L G408,+ BRANCH ON ZERO 88431320
044B 00 44000F83 BSI L F000 SLA 16-A REG FAILED 88431330
044D 0 3087 DC /3087 ERR ID 88431340
044E 00 44000FB2 G408 BSI L F00E CK LOCK ON ERROR 88431350
0450 0 70E9 MDX A408 LOOP 88431360
0451 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88431370
0452 00 4C180457 BSC L G40E,+ BRANCH ON ZERO 88431380
0454 00 44000F83 BSI L F000 SLA 16-AFFECTED Q REG 88431390
0456 0 3089 DC /3089 EPR ID 88431400
0457 00 44000FDE G40E BSI L F005 CK LOCK ON ERROR 88431410
0459 0 70E0 MDX A408 LOOP 88431420
*****
045A 0 C067 B400 LD N405 LD /0000 88431430
045B 0 18D0 RTE 15 NOW A=/XXXX Q=/0000 88431440
045C 0 C063 LD N403 LD /AAAA 88431450
045D 0 1001 SLA 1 NOW A=/5554 Q=/0000 88431460
045E 00 4C020463 BSC L H402,C BRANCH ON CARRY 88431470
0460 00 44000F83 BSI L F000 SLA 1-CARRY FAILED 88431480
0462 0 3088 DC /3088 ERR ID 88431490
0463 00 44000FB2 H402 BSI L F00E CK LOCK ON ERROR 88431500
0465 0 70F4 MDX B400 LOOP 88431510
0466 0 F05A EDR N404 ZERO WITH /5554 88431520
0467 00 4C18046C BSC L H400,+ BRANCH ON ZERO 88431530
0469 00 44000F83 BSI L F000 SLA 1-A REG FAILED 88431540
046B 0 308A DC /308A ERR ID 88431550
046C 00 44000FB2 H400 BSI L F00E CK LOCK ON ERROR 88431560
046E 0 70EB MDX B400 LOOP 88431570
046F 0 18D0 RTE 16 NOW A=/0000 Q=/5554 88431580
0470 00 4C180475 BSC L H404,+ BRANCH ON ZERO 88431590
0472 00 44000F83 BSI L F000 SRA 1-AFFECTED Q REG 88431600
0474 0 308C DC /308C ERR ID 88431610
0475 00 44000FDE H404 BSI L F005 CK LOCK ON ERROR 88431620
0477 0 70E2 MDX B400 LOOP 88431630
*****
0478 0 C049 B406 LD N405 LD /0000 88431640
0479 0 18D0 RTE 16 NOW A=/XXXX Q=/0000 88431650
047A 0 C044 LD N402 LD /5555 88431660
047B 0 1001 SLA 1 NOW A=/AAAA Q=/0000 88431670
047C 00 4C02047F BSC L H407,C BR ON CARRY 88431680
047E 0 7003 MDX H405 88431690
047F 00 44000F83 H407 BSI L F000 SLA 1-CARRY FAILED 88431700
0481 0 308E DC /308E ERR ID 88431710
0482 00 44000FB2 H405 BSI L F00E CK LOCK ON ERROR 88431720
0484 0 70F3 MDX B406 LOOP 88431730
0485 0 F03A EDR N403 ZERO WITH /AAAA 88431740
0486 00 4C18046B BSC L H406,+ BRANCH ON ZERO 88431750
0488 00 44000F83 BSI L F000 SLA 1-A REG FAILED 88431760
048A 0 308D DC /308D ERR ID 88431770
048B 00 44000FB2 H406 BSI L F00E CK LOCK ON ERROR 88431780
048D 0 70EA MDX B406 LOOP 88431790
048E 0 18D0 RTE 16 NOW A=/0000 Q=/AAAA 88431800
048F 00 4C180494 BSC L H408,+ BRANCH ON ZERO 88431810
0491 00 44000F83 BSI L F000 SLA 1-AFFECTED Q REG 88431820
0493 0 308F DC /308F ERR ID 88431830
0494 00 44000FDE H408 BSI L F005 CK LOCK ON ERROR 88431840
0496 0 70E1 MDX B406 LOOP 88431850
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0497 0 C02A B40A LD N405 LD /0000 88431860
0498 0 18D0 RTE 16 NOW A=/XXXX Q=/0G00 88431870
0499 0 C024 LD N401 LD /0001 88431880
049A 0 6101 LDX 1 1 88431890
049B 0 6204 LDX 2 4 88431900
049C 0 6303 LDX 3 3 88431910
88431920
88431930
88431940
88431950
88431960
88431970
88431980

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PROCESSOR-CONTRCLLER FUNCTION TEST

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049D 0 1000 SLA 0 NOW A=/0001 Q=/0000 88431990
049E 0 1100 SLA 1 0 /0002 /0000 88432000
049F 0 1002 SLA 2 /0008 /0000 88432010
04A0 0 1200 SLA 2 0 /0080 /0000 88432020
04A1 0 1006 SLA 6 /2000 /0000 88432030
04A2 0 1300 SLA 3 0 /0000 /0000 88432040
04A3 00 4C0204A8 BSC L H40D,C BRANCH ON CARRY 88432050
04A5 00 44000F83 BSI L F000 COMB SLA-CARRY FAILED 88432060
04A7 0 3091 DC /3091 ERR ID 88432070
04A8 00 44000FB2 H40D BSI L F00E CK LOCK ON ERROR 88432080
04AA 0 70EC MDX B40A LOOP 88432090
04AB 00 4C1804B0 BSC L H40A,+ BRANCH ON ZERO 88432100
04AD 00 44000F83 BSI L F000 COMB SLA-A REG FAILED 88432110
04AF 0 3090 DC /3090 ERR ID 88432120
04B0 00 44000FB2 H40A BSI L F00E CK LOCK ON ERROR 88432130
04B2 0 70E4 MDX B40A LOOP 88432140
04B3 0 18D0 RTE 16 88432150
04B4 00 4C1804B9 BSC L H40E,+ BRANCH ON ZERO 88432160
04B6 00 44000F83 BSI L F000 COMB SLA-AFFECTED Q 88432170
04B8 0 3092 DC /3092 ERR ID 88432180
04B9 00 44000FDE H40E BSI L F005 CK LOCK ON ERROR 88432190
04BB 0 70DB MDX B40A LOOP 88432200
04BC 0 7007 MDX A440 EXIT TO NEXT ROUTINE 88432210
04BD 0 FFFF N400 DC /FFFF 88432220
04BE 0 0001 N401 DC /0001 88432230
04BF 0 5555 N402 DC /5555 88432240
04C0 0 AAAA N403 DC /AAAA 88432250
04C1 0 5554 N404 DC /5554 88432260
04C2 0 0000 N405 DC /0000 88432270
04C3 0 FFFE N406 DC /FFFF 88432280
*
* TEST OF SLT OPERATION
*
*****
04C4 0 C07E A440 LD N440 LD /0001 88432320
04C5 0 18D0 RTE 16 NOW A=/XXXX Q=Q=/0001 88432330
04C6 0 C07D LD N441 LD /0000 88432340
04C7 0 10A0 SLT 32 /0000 Q=/0000 88432350
04C8 00 4C0204CD BSC L G442,C BRANCH ON CARRY 88432360
04CA 00 44000F83 BSI L F000 SLT 32-CARRY FAILED 88432370
04CC 0 3094 DC /3094 ERR ID 88432380
04CD 00 44000FB2 G442 BSI L F00E CK LOCK ON ERROR 88432390
04CF 0 70F4 MDX A440 LOOP 88432400
04D0 00 4C1804D5 BSC L G440,+ BRANCH ON ZERO 88432410
04D2 00 44000F83 BSI L F000 SLT 32-A REG FAILED 88432420
04D4 0 3093 DC /3093 ERR ID 88432430
04D5 00 44000FB2 G440 BSI L F00E CK LOCK ON ERROR 88432440
04D7 0 70EC MDX A440 LOOP 88432450
04D8 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88432460
04D9 00 4C1804DE BSC L G443,+ BRANCH ON ZERO 88432470
04DB 00 44000F83 BSI L F000 SLT 32-Q REG FAILED 88432480
04DD 0 3095 DC /3095 ERR ID 88432490
04DE 00 44000FDE G443 BSI L F005 CK LOCK ON ERROR 88432500
04E0 0 70E3 MDX A440 LOOP 88432510
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
04E1 0 C063 A444 LD N442 LD /FFFF 88432520
04E2 0 18D0 RTE 16 NOW A=/XXXX Q=/FFFF 88432530
04E3 0 C060 LD N441 LD /0000 88432540
04E4 0 1090 SLT 16 NOW A=/FFFF Q=/0000 88432550
04E5 00 4C0204E8 BSC L G446,C BR ON CARRY 88432560
04E7 0 7003 MDX G447 88432570
04E8 00 44000F83 G446 BSI L F000 SLT 16-CARRY FAILED 88432580
04EA 0 3097 DC /3097 ERR ID 88432590
04EB 00 44000FB2 G447 BSI L F00E CK LOCK ON ERROR 88432600

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PROCESSOR-CONTROLLER FUNCTION TEST

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04ED 0 70F3 MDX A444 LOOP 88432670
04EE 0 F056 EDR N442 ZERO WITH /FFFF 88432680
04EF 00 4C1804F4 BSC L G444,+ BRANCH ON ZERO 88432690
04F1 00 4400F83 BSI L F000 SLT 16-A REG FAILED 88432700
04F3 0 3096 DC /3096 ERR ID 88432710
04F4 00 4400FB2 G444 BSI L F00E CK LOCK ON ERROR 88432720
04F6 0 70FA MDX A444 LOOP 88432730
04F7 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88432740
04F8 00 4C1804FD BSC L G448,+ BRANCH ON ZERO 88432750
04FA 00 4400F83 BSI L F000 SLT 16-Q REG FAILED 88432760
04FC 0 3098 DC /3098 ERR ID 88432770
04FD 00 4400FDE G448 BSI L F005 CK LOCK ON ERROR 88432780
04FF 0 70E1 MDX A444 LOOP 88432790
***** 88432800
0500 0 C045 A44A LD N443 LD /5555 88432810
0501 0 18D0 RTE 16 NOW A=/XXXX Q=/5555 88432820
0502 0 C041 LD N441 /0000 /5555 88432830
0503 0 108F SLT 15 /2AAA /8000 88432840
0504 00 4C020507 BSC L G44C,C BR ON CARRY 88432850
0506 0 7003 MDX G44D 88432860
0507 00 4400FB3 G44C BSI L F000 SLT 15-CARRY FAILED 88432870
0509 0 309A DC /309A ERR ID 88432880
050A 00 4400FB2 G44D BSI L F00E CK LOCK ON ERROR 88432890
050C 0 70F3 MDX A44A LOOP 88432900
050D 0 F039 EDR N444 ZERO WITH /2AAA 88432910
050E 00 4C180513 BSC L G44A,+ BRANCH ON ZERO 88432920
0510 00 4400FB3 BSI L F000 SLT 15-A REG FAILED 88432930
0512 0 3099 DC /3099 ERR ID 88432940
0513 00 4400FB2 G44A BSI L F00E CK LOCK ON ERROR 88432950
0515 0 70EA MDX A44A LOOP 88432960
0516 0 18D0 RTE 16 NOW A=/8000 Q=/0000 88432970
0517 0 F030 EDR N445 ZERO WITH /8000 88432980
0518 00 4C18051D BSC L G44E,+ BRANCH ON ZERO 88432990
051A 00 4400FB3 BSI L F000 SLT 15-Q REG FAILED 88433000
051C 0 3098 DC /3098 ERR ID 88433010
051D 00 4400FDE G44E BSI L F005 CK LOCK ON ERROR 88433020
051F 0 70E1 MDX A44A LOOP 88433030
***** 88433040
***** 88433050
CORE DATA DR *LA- OPER- 88433060
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88433070
***** 88433080
0520 0 C022 B44C LD N440 LD /0001 88433090
0521 0 18D0 RTE 16 NOW A=/XXXX C=/0001 88433100
0522 0 C021 LD N441 LD /0000 88433110
0523 0 1080 SLT 0 NOW A=/0000 Q=/0001 88433120
0524 0 1081 SLT 1 /0000 /0002 88433130
0525 0 1085 SLT 5 /0000 /0040 88433140
0526 0 1087 SLT 7 /0000 /2000 88433150
0527 0 1089 SLT 9 /0040 /0000 88433160
0528 0 108A SLT 10 /0000 /0000 88433170
0529 00 4C02052E BSC L H443,C BR ON CARRY 88433180
052B 00 4400FB3 BSI L F000 COMB SLT-CARRY FAILED 88433190
052D 0 309D DC /309D ERR ID 88433200
052E 00 4400FB2 H443 BSI L F00E CK LOCK ON ERROR 88433210
0530 0 70EF MDX B440 LOOP 88433220
0531 00 4C180536 BSC L H440,+ BRANCH ON ZERO 88433230
0533 00 4400FB3 BSI L F000 COMB SLT-A REG FAILE 88433240
0535 0 309C DC /309C ERR ID 88433250
0536 00 4400FB2 H440 BSI L F00E CK LOCK ON ERROR 88433260
0538 0 70E7 MDX B440 LOOP 88433270
0539 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88433280
053A 00 4C18053F BSC L H444,+ BRANCH ON ZERO 88433290
053C 00 4400FB3 BSI L F000 COMB SLT-Q REG FAILE 88433300
053E 0 309E DC /309E ERR ID 88433310
053F 00 4400FDE H444 BSI L F005 CK LOCK ON ERROR 88433320
0541 0 70DE MDX B440 LOOP 88433330
0542 0 7006 MDX A480 EXIT TO NEXT ROUTINE 88433340

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PROCESSOR-CONTROLLER FUNCTION TEST

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0543 0 0001 N440 DC /0001 88433350
0544 0 0000 N441 DC /0000 88433360
0545 0 FFFF N442 DC /FFFF 88433370
0546 0 5555 N443 DC /5555 88433380
0547 0 2AAA N444 DC /2AAA 88433390
0548 0 8000 N445 DC /8000 88433400
* 88433410
* TEST OF STD OPERATION 88433420
* 88433430
***** 88433440
0549 0 C019 A480 LD N480 LD /0000 88433450
054A 0 D01A STO N482 STO /0000 88433460
054B 0 C018 LD N481 LD /FFFF 88433470
054C 0 C018 LD N482 LD /0000 88433480
054D 00 4C180552 BSC L G480,+ BRANCH ON ZERO 88433490
054F 00 4400FB3 BSI L F000 STO ZERCS FAILED 88433500
0551 0 309F DC /309F EPR ID 88433510
0552 00 4400FDE G480 BSI L F005 CK LOCK ON ERROR 88433520
0554 0 70F4 MDX A480 LOOP 88433530
***** 88433540
0555 0 C00E A482 LD N481 LD /FFFF 88433550
0556 0 D00E STO N482 88433560
0557 0 C00B LD N480 LD /0000 88433570
0558 0 C00C LD N482 LD /FFFF 88433580
0559 0 F00A EDR N481 ZERO WITH /FFFF 88433590
055A 00 4C18055F BSC L G482,+ BRANCH ON ZERO 88433600
055C 00 4400FB3 BSI L F000 STO ONES FAILED 88433610
055E 0 30A0 DC /30A0 ERR ID 88433620
055F 00 4400FDE G482 BSI L F005 CK LOCK ON ERROR 88433630
0561 0 70F3 MDX A482 LOOP 88433640
0562 0 7003 MDX A4C0 EXIT TO NEXT ROUTINE 88433650
0563 0 0000 N480 DC /0000 88433660
0564 0 FFFF N481 DC /FFFF 88433670
0565 0 FFFF N482 DC /FFFF 88433680
* 88433690
* TEST OF STS OPERATION 88433700
* 88433710
***** 88433720
***** 88433730
CORE DATA DR *LA- OPER- 88433740
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88433750
***** 88433760
0566 0 2000 A4C0 LDS 0 SET C AND OF OFF 88433770
0567 0 285B STS N4C0 88433780
0568 0 C05A LD N4C0 88433790
0569 00 4C18056E BSC L G4C0,+ BRANCH ON ZERO 88433800
056B 00 4400FB3 BSI L F000 STS FAILED TO STORE 88433810
056D 0 30A1 DC /30A1 ERR ID 88433820
056E 00 4400FDE G4C0 BSI L F005 CK LOCK ON ERROR 88433830
0570 0 70F5 MDX A4C0 LOOP 88433840
***** 88433850
0571 0 C0FF A4C2 LD A4C2 88433860
0572 0 2003 LDS 3 88433870
0573 0 284F STS N4C0 88433880
0574 0 F0FC EDR A4C2 88433890
0575 00 4C18057A BSC L H4C3,+ BRANCH ON ZERO 88433900
0577 00 4400FB3 BSI L F000 ACC GONE AFT LDS-STS 88433910
0579 0 30A3 DC /30A3 ERR ID 88433920
057A 00 4C02057D H4C3 BSC L H4C2,C BR IF CARRY IS NO 88433930
057C 0 7003 MDX G4C2 88433940
057D 00 4400FB3 H4C2 BSI L F000 STS NOT CLEAR CARRY 88433950
057F 0 30A2 DC /30A2 ERR ID 88433960
0580 00 4400FB2 G4C2 BSI L F00E CK LOCK ON ERROR 88433970
0582 0 70EE MDX A4C2 LOOP 88433980
0583 00 4C010586 BSC L H4C4,C BR IF CARRY IS ON 88433990
0585 0 7003 MDX G4C4 88434000
0586 00 4400FB3 H4C4 BSI L F000 STS NOT CLEAR OVERFLW 88434010
0588 0 30A4 DC /30A4 ERR ID 88434020

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PROCESSOR-CONTROLLER FUNCTION TEST

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0589 00 44000FB2 G4C4 BSI L F00E CK LOCK ON ERROR 88434030
058B 0 70E5 MDX A4C2 LOOP 88434040
058C 0 C036 LD N4C0 88434050
058D 0 F036 EOR N4C1 88434060
058E 00 4C180593 BSC L G4C6,+ BRANCH ON ZERO 88434070
0590 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434080
0592 0 30A5 DC /30A5 ERR ID 88434090
0593 00 44000FDE G4C6 BSI L F005 CK LOCK ON ERROR 88434100
0595 0 70DB MDX A4C2 LOOP 88434110
*****
0596 0 2002 A4C8 LDS 2 SET C ON OF OFF 88434120
0597 0 282B STS N4C0 SET /0002 IN N4C0 88434130
0598 0 282C STS N4C2 SET /0002 IN N4C2 88434140
0599 0 C029 LD N4C0 LD /0002 88434150
059A 0 F02B EOR N4C3 ZERO WITH /0002 88434160
059B 00 4C1805A0 BSC L G4C8,+ BRANCH ON ZERO 88434170
059D 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434180
059F 0 30A6 DC /30A6 ERR ID 88434190
05A0 00 44000FB2 G4C8 BSI L F00E CK LOCK ON ERROR 88434200
05A2 0 70F3 MDX A4C8 LOOP 88434210
05A3 0 C021 LD N4C2 LD /0002 88434220
05A4 00 4C1805A9 BSC L G4CA,+ BRANCH ON ZERO 88434230
05A6 00 44000FB3 BSI L F006 STS NOT CLEAR CARRY 88434240
05A8 0 30A7 DC /30A7 ERR ID 88434250
05A9 00 44000FDE G4CA BSI L F005 CK LOCK ON ERROR 88434260
05AB 0 70EA MDX A4C8 LOOP 88434270
*****
05AC 0 2001 A4CC LDS 1 SET C-OFF OF - ON 88434280
05AD 0 2815 STS N4C0 SET /0001 IN N4C0 88434290
05AE 0 2816 STS N4C2 SET /0001 IN N4C2 88434300
05AF 0 C013 LD N4C0 LD /0001 88434310
05B0 0 F016 EOR N4C4 ZERO WITH /0001 88434320
05B1 00 4C1805B6 BSC L G4CC,+ BRANCH ON ZERO 88434330
05B3 00 44000FB3 BSI L F000 STS FAILED TO STORE 88434340
05B5 0 30A8 DC /30A8 ERR ID 88434350
05B6 00 44000FB2 G4CC BSI L F00E CK LOCK ON ERROR 88434360
05B8 0 70F3 MDX A4CC LOOP 88434370
05B9 0 C00B LD N4C2 LD /0001 88434380
05BA 00 4C1805BF BSC L G4CD,+ BRANCH ON ZERO 88434390
05BC 00 44000FB3 BSI L F000 STS NOT CLEAR OVERFL 88434400
05BE 0 30A9 DC /30A9 ERR ID 88434410
05BF 00 44000FDE G4CD BSI L F005 CK LOCK ON ERROR 88434420
05C1 0 70EA MDX A4CC LOOP 88434430
05C2 0 7005 MDX A500 EXIT TO NEXT ROUTINE 88434440
05C3 0 0003 N4C0 DC /0003 88434450
05C4 0 0003 N4C1 DC /0003 88434460
05C5 0 0000 N4C2 DC /0000 88434470
05C6 0 0002 N4C3 DC /0002 88434480
05C7 0 0001 N4C4 DC /0001 88434490
*
* TEST OF BSC OPERATION
*
*****
CORE DATA DR *LA- OPER- 88434500
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88434510
*****
05C8 0 2003 A500 LDS 3 SET C AND OF ON 88434520
05C9 00 44000B5C LD L N500 LD /8001 88434530
05CB 0 482F BSC O+EZC SK IF OF OFF, PLUS, EVEN, 88434540
* * ZERO OR CARRY OFF. 88434550
05CC 0 7003 MDX G500 88434560
05CD 00 44000FB3 BSI L F000 BSC SKPD-SHOULD NOT 88434570
05CF 0 30AA DC /30AA ERR ID 88434580
05D0 00 44000FDE G500 BSI L F005 CK LOCK ON ERROR 88434590
05D2 0 70F5 MDX A500 LOOP 88434600
*****
05D3 0 2003 A502 LDS 3 SET C + OF ON 88434610

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PROCESSOR-CONTROLLER FUNCTION TEST

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05D4 00 4400065D LD L N501 LD /0000 88434710
05D6 0 481B BSC -OC+ SK IF MINUS, OF OFF, CARRY 88434720
* *OFF OR PLUS 88434730
05D7 0 7003 MDX G502 88434740
05D8 00 44000FB3 BSI L F000 BSC SKPD-SHOULD NOT 88434750
05DA 0 30AB DC /30AB ERR ID 88434760
05DB 00 44000FDE G502 BSI L F005 CK LOCK ON ERROR 88434770
05DD 0 70F5 MDX A502 LOOP 88434780
*****
05DE 0 2003 A504 LDS 3 SET C AND OF ON 88434790
05DF 0 C07E LD N502 LD /8000 88434800
05E0 0 2809 STS N507 SET /0003 IN N507 88434810
05E1 0 4815 BSC O-E SK IF OF OFF, MUNIS OR EVEN 88434820
05E2 0 7001 MDX G504 88434830
05E3 0 7003 MDX G505 88434840
05E4 00 44000FB3 G504 BSI L F000 BSC FAILED TO SKIP 88434850
05E6 0 30AC DC /30AC ERR ID 88434860
05E7 00 44000FB2 G505 BSI L F00E CK LOCK ON ERROR 88434870
05E9 0 70F4 MDX A504 LOOP 88434880
05EA 0 2000 N507 LDS 0 SET C + OF OFF 88434890
05EB 0 4801 BSC 0 SKIP IF OVERFLOW IS OFF 88434900
05EC 0 4801 BSC 0 88434910
05ED 0 7001 MDX G506 88434920
05EE 0 7003 MDX G507 88434930
05EF 00 44000FB3 G506 BSI L F000 BSC NOT CLEAR OVERFLW 88434940
05F1 0 30AD DC /30AD ERR ID 88434950
05F2 00 44000FDE G507 BSI L F005 CK LOCK ON ERROR 88434960
05F4 0 70E9 MDX A504 LOOP 88434970
*****
05F5 0 2000 A508 LDS 0 SET C AND OF OFF 88434980
05F6 0 C068 LD N503 LD /0001 88434990
05F7 0 482A BSC C+Z SK IF CARRY OFF, PLUS 88435000
* * OR ZERO 88435010
05F8 0 7001 MDX G508 88435020
05F9 0 7003 MDX H508 88435030
05FA 00 44000FB3 G508 BSI L F000 BSC FAILED TO SKIP 88435040
05FC 0 30AE DC /30AE ERR ID 88435050
05FD 00 44000FDE H508 BSI L F005 CK LOCK ON ERROR 88435060
05FF 0 70F5 MDX A508 LOOP 88435070
*****
0600 0 2003 A50A LDS 3 SET C AND OF ON 88435080
0601 0 C05A LD N500 LD /8001 88435090
0602 00 4C0F0613 BSC L G50A,+OCE BR ON NCT PLUS, OF ON, 88435100
* * CARRY ON OR NOT EVEN 88435110
0604 0 7001 MDX H50A 88435120
0605 0 7007 MDX J50A 88435130
0606 00 44000FB3 H50A BSI L F000 BSC FELL THRU 88435140
0608 0 30AF DC /30AF ERR ID 88435150
0609 00 44000FB2 BSI L F00E CK LOCK ON ERROR 88435160
060B 0 70F4 MDX A50A LOOP 88435170
060C 0 7006 MDX G50A 88435180
060D 00 44000FB3 J50A BSI L F000 BSC SKPD-SHOULD BRNCH 88435190
060F 0 3080 DC /3080 ERR ID 88435200
0610 00 44000FB2 BSI L F00E CK LOCK ON ERROR 88435210
0612 0 70ED MDX A50A LOOP 88435220
0613 0 F048 G501 EOR N500 ZERO WITH /8001 88435230
0614 0 4820 BSC Z SK ON ZERO 88435240
0615 0 7001 MDX H50B 88435250
0616 0 7003 MDX K50B 88435260
0617 00 44000FB3 H50B BSI L F000 ACC DISTROYED AFTER BSC 88435270
0619 0 3170 DC /3170 ERR ID 88435280
061A 00 44000FDE K50B BSI L F005 CK LOCK ON ERROR 88435290
061C 0 7000 MDX A50C EXIT TO NEXT ROUTINE 88435300
*****
CORE DATA DR *LA- OPER- 88435310
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88435320
*****
061C 0 7000 MDX A50C EXIT TO NEXT ROUTINE 88435330

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PROCESSOR-CONTROLLER FUNCTION TEST

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061D 0 2003      A50C  LDS    3      SET C + OF ON      88435390
061E 0 C041      LD      N504      LD /0004           88435400
061F 00 4C300623 BSC  L  G50C,-Z    BR NOT MINUS OR NOT ZERO 88435410
0621 0 7002      MDX    H50C           88435420
0622 0 7008      MDX    J50C           88435430
0623 0 700A      G50C  MDX    K50C           88435440
0624 00 44000F83 H50C  BSI  L  F000      BSC FELL THRU      88435450
0626 0 30B1      DC      /30B1      ERR ID             88435460
0627 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88435470
0629 0 70F3      MDX    A50C           88435480
062A 0 7006      MDX    A50E           88435490
062B 00 44000F83 J50C  BSI  L  F000      BSC SKPD-SHOULD BRNC 88435500
062D 0 30B2      DC      /30B2      ERR ID             88435510
062E 00 4400CFDE K50C  BSI  L  F005      CK LOCK ON ERROR  88435520
0630 0 70EC      MDX    A50C           88435530
0631 0 2000      A50E  LDS    0      SET C AND OF OFF  88435540
*****
0632 0 2003      LDS    3      SET C AND OF ON      88435560
0633 0 C028      LD      N500      LD /8001           88435570
0634 00 4C3F0638 BSC  L  G50E,+EOCZ- BR ON NOT PLUS, NOT EVEN, 88435580
*                *OF, CARRY, NOT ZERO OR
*                *NOT MINUS
0636 0 700B      MDX    H50E           88435610
0637 0 7007      MDX    J50E           88435620
0638 00 44000F83 G50E  BSI  L  F000      BSC BRNCHED-SHOULDNT 88435630
063A 0 30B3      DC      /30B3      ERR ID             88435640
063B 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88435650
063D 0 70F3      MDX    A50E           88435660
063E 0 7006      MDX    B500           88435670
063F 00 44000F83 J50E  BSI  L  F000      BSC SKPD-SHOULDNT  88435680
0641 0 30B4      DC      /30B4      ERR ID             88435690
0642 00 44000FDE H50E  BSI  L  F005      CK LOCK ON ERROR  88435700
0644 0 70EC      MDX    A50E           88435710
*****
0645 0 2003      B500  LDS    3      SET C AND OF ON      88435730
0646 0 C018      LD      N503      LD /0001           88435740
0647 0 4808      BSC    +          SK ON PLUS         88435750
0648 0 700C      MDX    S501           88435760
0649 0 2817      STS    N505      SET /0003 IN N505  88435770
064A 0 C016      LD      N505      LD /0003           88435780
064B 0 F016      EDR    N506      ZERO WITH /0003    88435790
064C 00 4C180658 BSC  L  S503,+--   BRANCH ON ZERO     88435800
064E 00 44000F83 BSI  L  F000      BSC + CLEARED OVFLW 88435810
0650 0 30B5      DC      /30B5      ERR ID             88435820
0651 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88435830
0653 0 70F1      MDX    B500           88435840
0654 0 700L      MDX    A540           88435850
0655 00 44000F83 S501  BSI  L  F000      BSC FAILED TO SKP   88435860
0657 0 30B6      DC      /30B6      ERR ID             88435870
0658 00 44000FDE S503  BSI  L  F005      CK LOCK ON ERROR  88435880
065A 0 70EA      MDX    B500           88435890
065B 0 7007      MDX    A540           88435900
065C 0 8001      N500  DC      /8001           88435910
065D 0 0000      N501  DC      /0000           88435920
065E 0 8000      N502  DC      /8000           88435930
065F 0 0001      N503  DC      /0001           88435940
0660 0 0004      N504  DC      /0004           88435950
0661 0 0000      N505  DC      /0000           88435960
0662 0 0003      N506  DC      /0003           88435970
*
*                TEST OF BSI OPERATION
*
*****
COKE  DATA OR  *LA- OPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0663 0 2003      A540  LDS    3      SET C AND OF ON      88436060

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PROCESSOR-CONTROLLER FUNCTION TEST

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0664 00 C400070B LD  L  N540      LD /8001           88436070
0666 00 442F0678 BSI  L  G540,ECD+Z BR CN NOT EVEN, CARRY, OF, 88436080
0668 0 7001      MUX    H540      * NCT PLUS OR NOT ZERO 88436090
0669 0 7007      MDX    J540           88436100
066A 00 44000F83 H540  BSI  L  F000      BSI FELL THRU      88436110
066C 0 30B7      DC      /30B7      ERR ID             88436120
066D 00 44000F82 BSI  L  F00E      CK LOCK ON ERROR  88436130
066F 0 70F3      MDX    A540           88436140
0670 0 7016      MDX    A5 4        EXIT TO NEXT ROUTINE 88436150
0671 00 44000F83 J540  BSI  L  F000      BSI SKPD-SHOULD BRNCH 88436160
0673 0 30B8      DC      /30B8      ERR ID             88436170
0674 00 44000F82 BSI  L  F00E      CK LOCK ON ERROR  88436180
0675 0 70EC      MDX    A540           88436190
0677 0 7001      MDX    G540+1     SK TO WORD AFTER G540 88436200
0678 0 0000      G540  DC      /0000           88436210
0679 00 2C00070C STS  L  N541      STORE /0002 IN N541 88436220
067B 00 C400070C LD  L  N541      LD /0002           88436230
067D 00 F400070D EUR  L  N542      ZERO WITH /0002    88436240
067F 00 4C180684 BSC  L  G542,+--   BRANCH ON ZERO     88436250
0681 00 44000F83 BSI  L  F000      BSI NOT CLEAR OVERFLOW 88436260
0683 0 30B9      DC      /30B9      ERR ID             88436270
0684 00 44000FDE G542  BSI  L  F005      CK LOCK ON ERROR  88436280
0686 0 70DC      MDX    A540           88436290
*****
0687 00 C400070D A544  LD  L  N542      LD /0002           88436310
0689 00 44300698 BSI  L  G544,+7-   SK CN NOT ZERO OR  88436320
068B 0 7001      MDX    H544      * NCT MINUS        88436330
068C 0 7007      MDX    J544           88436340
068D 00 44000F83 H544  BSI  L  F000      BSI DID NOT BRANCH 88436350
068F 0 30BA      DC      /30BA      ERR ID             88436360
0690 00 44000FDE BSI  L  F005      CK LOCK ON ERRCR   88436370
0692 0 70F4      MDX    A544           88436380
0693 0 7008      MDX    A546           88436390
0694 00 44000F83 J544  BSI  L  F000      BSI SKPD-SHOULD BRNC 88436400
0696 0 30BB      DC      /30BB      ERR ID             88436410
0697 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88436420
0699 0 70ED      MDX    A544           88436430
069A 0 7001      MDX    A546           88436440
069B 0 0000      G544  DC      /0000           88436450
*****
069C 0 C071      A546  LD  L  N543           88436460
069D 00 442006A1 BSI  L  G546,+Z    BR WHEN NOT ZERO   88436480
069F 0 700C      MDX    J546           88436490
06A0 0 7008      MDX    H546           88436500
06A1 0 0000      G546  DC      /0000           88436510
06A2 00 44000F83 BSI  L  F000      BSI BRNCHD-SHOULD NO 88436520
06A4 0 30BC      DC      /30BC      ERR ID             88436530
06A5 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88436540
06A7 0 70F4      MDX    A546           88436550
06A8 0 7006      MDX    A548           88436560
06A9 00 44000F83 H546  BSI  L  F000      BSI SKPD-SHOULD NOT 88436570
06AB 0 30BD      DC      /30BD      ERR ID             88436580
06AC 00 44000FDE J546  BSI  L  F005      CK LOCK ON ERROR  88436590
06AE 0 70ED      MDX    A546           88436600
*****
CORE  DATA OR  *LA- OPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
06AF 0 C05B      A548  LD  L  N540           88436660
06B0 00 441006BA BSI  L  G548,-     BR WHEN NOT MINUS  88436670
06B2 0 700B      MDX    H548           88436680
06B3 00 44000F83 BSI  L  F000      BSI SKP-ON COND TRUE 88436690
06B5 0 30BF      DC      /30BF      ERR ID             88436700
06B6 00 44000FDE BSI  L  F005      CK LOCK ON ERROR  88436710
06B8 0 70F6      MDX    A548           88436720
06B9 0 7007      MDX    A54A           88436730
06BA 0 0000      G548  DC      /0000           88436740

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PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

```

06bB 00 4400F83      BSI L F000      BSI BRNCHD-SHOULD NOT
06bD 0 30BF          DC /30BF        ERR ID
06BE 00 4400FDE      H54B BSI L F005      CK LOCK ON ERROR
06c0 0 70EE          MDX A548        LOOP
*****
06c1 0 C04B          A54A LD N542
06c2 00 440B06CC      BSI L G54A,+    BR WHEN NOT PLUS
06c4 0 700B          MDX H54A
06c5 00 4400F83      BSI L F000      BSI SKPD ON COND TRUE
06c7 0 30C0          DC /30C0        ERR ID
06c8 00 4400FDE      BSI L F005      CK LOCK ON ERROR
06ca 0 70F6          MDX A54A        LOOP
06cb 0 7007          MDX A54C        EXIT TO NEXT ROUTINE
06cc 0 0000          G54A DC /0000
06cd 00 4400F83      BSI L F000      BSI BRNCHD-SHOULD NOT
06cf 0 30C1          DC /30C1        ERR ID
06d0 00 4400FDE      H54A BSI L F005      CK LOCK ON ERROR
06d2 0 70EE          MDX A54A        LOOP
*****
06d3 0 C039          A54C LD N542
06d4 00 440406DE      BSI L G54C,E    BR WHEN NOT EVEN
06d6 0 700B          MDX H54A
06d7 00 4400F83      BSI L F000      BSI SKPD ON COND TRUE
06d9 0 30C2          DC /30C2        ERR ID
06da 00 4400FDE      BSI L F005      CK LOCK ON ERROR
06dc 0 70F6          MDX A54C        LOOP
06dd 0 7007          MDX A54E        EXIT TO NEXT ROUTINE
06de 0 0000          G54C DC /0000
06df 00 4400F83      BSI L F000      BSI BRNCHD-SHOULD NOT
06e1 0 30C3          DC /30C3        ERR ID
06e2 00 4400FDE      H54C BSI L F005      CK LOCK ON ERROR
06e4 0 70EE          MDX A54C        LOOP
*****
06e5 0 2000          A54E LDS 0        SET C AND OF OFF
06e6 00 440206F0      BSI L G54F,C    BR IF CARRY IS ON
06e8 0 700B          MDX H54E
06e9 00 4400F83      BSI L F000      BSI SKPD ON COND TRUE
06eb 0 30C4          DC /30C4        ERR ID
06ec 00 4400FDE      BSI L F005      CK LOCK ON ERROR
06ee 0 70F6          MDX A54E        LOOP
06ef 0 7007          MDX A54F        EXIT TO NEXT ROUTINE
06f0 0 0000          G54E DC /0000
06f1 00 4400F83      BSI L F000      BSI BRNCHD-SHOULD NOT
06f3 0 30C5          DC /30C5        ERR ID
06f4 00 4400FDE      H54E BSI L F005      CK LOCK ON ERROR
06f6 0 70EE          MDX A54E        LOOP
*****
06f7 0 2000          A54F LDS 0        SET C AND OF OFF
06f8 00 4401C703      BSI L G54F,C    BR ON OVERFLOW
06fa 0 700C          MDX H54F
06fb 00 4400F83      BSI L F000      BSI SKPD ON COND TRUE
06fd 0 30C6          DC /30C6        ERR ID
06fe 0 30C6          DC /30C6        ERR ID
06ff 00 4400FDE      BSI L F005      CK LOCK ON ERROR
0701 0 70F5          MDX A54F        LOOP
0702 0 700C          MDX A580        EXIT TO NEXT ROUTINE
0703 0 0000          G54F DC /0000
0704 00 4400F83      BSI L F000      BSI BRNCHD-SHOULD NOT
0706 0 30C7          DC /30C7        ERR ID
0707 00 4400FDE      H54F BSI L F005      CK LOCK ON ERROR
0709 0 70ED          MDX A54F        LOOP
070a 0 7004          MDX A580        EXIT TO NEXT ROUTINE
070b 0 8001          N540 DC /8001
070c 0 0000          N541 DC /0000
070d 0 0002          N542 DC /0002
070e 0 0000          N543 DC /0000
*
*

```

TEST OF LDD OPERATION

```

8B436750
8B436760
8B436770
8B436780
8B436790
8B436800
8B436810
8B436820
8B436830
8B436840
8B436850
8B436860
8B436870
8B436880
8B436890
8B436900
8B436910
8B436920
8B436930
8B436940
8B436950
8B436960
8B436970
8B436980
8B436990
8B437000
8B437010
8B437020
8B437030
8B437040
8B437050
8B437060
8B437070
8B437080
8B437090
8B437100
8B437110
8B437120
8B437130
8B437140
8B437150
8B437160
8B437170
8B437180
8B437190
8B437200
8B437210
8B437220
8B437230
8B437240
8B437250
8B437260
8B437270
8B437280
8B437290
8B437300
8B437310
8B437320
8B437330
8B437340
8B437350
8B437360
8B437370
8B437380
8B437390
8B437400
8B437410
8B437420

```

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*
*****
CORE DATA OR *LA- DPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
070F 0 C838          A580 LDD N581      LDD A=/0000 Q=/0000
0710 00 4C180715      BSC L G580,+    BRANCH ON ZERO
0712 00 4400F83      BSI L F000      LDD-A REG INCORRECT
0714 0 30C8          DC /30C8        ERR ID
0715 00 4400FB2      G580 BSI L F00E      CK LOCK ON ERROR
0717 0 70F7          MDX A580        LOOP
0718 0 1800          RTE 16
0719 00 4C18071E      BSC L G582,+    BRANCH ON ZERO
071B 00 4400F83      BSI L F000      LDD-Q REG INCORRECT
071D 0 30C9          DC /30C9        ERR ID
071E 00 4400FDE      G582 BSI L F005      CK LOCK ON ERROR
0720 0 70EE          MDX A580        LOOP
*****
0721 0 C828          A584 LDD I583      LD A=/FFFF Q=/FFFF
0722 0 F028          EOR N584        ZERC WITH /FFFF
0723 00 4C180728      BSC L G584,+    BRANCH ON ZERO
0725 00 4400F83      BSI L F000      LDD-A REG INCORRECT
0727 0 30CA          DC /30CA        ERR ID
0728 00 4400FB2      G584 BSI L F00E      CK LOCK ON ERROR
072A 0 70F6          MDX A584        LOOP
072B 0 1800          RTE 16
072C 0 F01E          EOR N584        ZERC WITH /FFFF
072D 00 4C180732      BSC L G586,+    BRANCH ON ZERO
072F 00 4400F83      BSI L F000      LDD-Q REG INCORRECT
0731 0 30CB          DC /30CB        ERR ID
0732 00 4400FDE      G586 BSI L F005      CK LOCK ON ERROR
0734 0 70EC          MDX A584        LOOP
*****
0735 0 C813          A588 LDD N582      LD A=/0000 Q=/FFFF
0736 00 4C180738      BSC L G588,+    BRANCH ON ZERO
0738 00 4400F83      BSI L F000      LDD-ODD-A REG FAILED
073A 0 30CC          DC /30CC        ERR ID
073B 00 4400FB2      G588 BSI L F00E      CK LOCK ON ERROR
073D 0 70F7          MDX A588        LOOP
073E 0 1800          RTE 16
073F 00 4C180744      BSC L G58A,+    BRANCH ON ZERO
0741 00 4400F83      BSI L F000      LDD-ODD-Q REG FAILED
0743 0 30CD          DC /30CD        ERR ID
0744 00 4400FDE      G58A BSI L F005      CK LOCK ON ERROR
0746 0 70EE          MDX A588        LOOP
0747 0 7004          MDX A5C0        EXIT TO NEXT ROUTINE
0748 0 0000          BSS E
0748 0 0000          N581 DC /0000
0749 0 0000          N582 DC /0000
074A 0 FFFF          N583 DC /FFFF
074B 0 FFFF          N584 DC /FFFF
*
*
TEST OF STD OPERATION
*
*****
CORE DATA OP *LA- DPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
074C 0 C84B          A5C0 LDD N5C1      LD A=/0000 Q=/0000
074D 0 D84E          STD N5C5
074E 0 C04D          LD N5C5        LD A=/0000 Q=/0000
074F 00 4C180754      BSC L G5C0,+    BRANCH ON ZERO
0751 00 4400F83      BSI L F000      STD-EA INCORRECT
0753 0 30CF          DC /30CF        ERR ID
0754 00 4400FB2      G5C0 BSI L F00E      CK LOCK ON ERROR
0756 0 70F5          MDX A5C0        LOOP

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8B437430
8B437440
8B437450
8B437460
8B437470
8B437480
8B437490
8B437500
8B437510
8B437520
8B437530
8B437540
8B437550
8B437560
8B437570
8B437580
8B437590
8B437600
8B437610
8B437620
8B437630
8B437640
8B437650
8B437660
8B437670
8B437680
8B437690
8B437700
8B437710
8B437720
8B437730
8B437740
8B437750
8B437760
8B437770
8B437780
8B437790
8B437800
8B437810
8B437820
8B437830
8B437840
8B437850
8B437860
8B437870
8B437880
8B437890
8B437900
8B437910
8B437920
8B437930
8B437940
8B437950
8B437960
8B437970
8B437980
8B437990
8B438000
8B438010
8B438020
8B438030
8B438040
8B438050
8B438060
8B438070
8B438080
8B438090
8B438100

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PROCESSOR-CONTROLLER FUNCTION TEST

```

0757 0 C045          LD      N5C6      LD /FFFF      88438110
0758 00 4018075D     BSC L  G5C2,+-  BRANCH ON ZERO 88438120
0759 00 44000F83     BSI L  F000     STD-EA+1 INCORRECT 88438130
075C 0 30CF          DC      /30CF   ERR ID          88438140
075D 00 44000FDE     G5C2 BSI L  F005 CK LOCK ON ERROR 88438150
075F 0 70EC          MDX     A5C0     LOOP           88438160
*****
0760 0 C037          A5C4 LD      N5C1      LD /0000      88438170
0761 0 D03A          STO     N5C5      STORE /0000   88438180
0762 0 D03A          STO     N5C6      STORE /0000   88438190
0763 0 C836          LDD     N5C3      LD A=/FFFF Q=/FFFF 88438200
0764 0 D837          STO     N5C5      STORE /FFFF AND /FFFF 88438210
0765 0 C036          LD      N5C5      LD /FFFF      88438220
0766 0 F033          EOR     N5C3      ZERO WITH /FFFF 88438230
0767 00 4C18076C     BSC L  G5C4,+-  BRANCH ON ZERO 88438240
0769 00 44000F83     BSI L  F000     STD-EA INCORRECT 88438250
076B 0 30D0          DC      /30D0   ERR ID          88438260
076C 00 44000F82     G5C4 BSI L  F00E CK LOCK ON ERROR 88438270
076E 0 70F1          MDX     A5C4     LOOP           88438280
076F 0 C02D          LD      N5C6      LD /1111      88438290
0770 0 F029          EOR     N5C3      ZERO WITH /FFFF 88438300
0771 00 4C180776     BSC L  G5C6,+-  BRANCH ON ZERO 88438310
0773 00 44000F83     BSI L  F000     STD-EA+1 INCORRECT 88438320
0775 0 30D1          DC      /30D1   ERR ID          88438330
0776 00 44000FDE     G5C6 BSI L  F005 CK LOCK ON ERROR 88438340
0778 0 70E7          MDX     A5C4     LOOP           88438350
*****
0779 0 C020          A5C8 LD      N5C3      LD /FFFF      88438360
077A 0 D021          STO     N5C5      STORE /FFFF   88438370
077B 0 D021          STO     N5C6      STORE /FFFF   88438380
077C 0 D021          STO     N5C7      STORE /FFFF   88438390
077D 0 C81A          LDD     N5C1      LD A=/0000 Q=/0000 88438400
077E 0 D81E          STO     N5C6      STORE IN N5C6 + N5C7 88438410
077F 0 C018          LD      N5C1      LD /0000      88438420
0780 0 C01C          LD      N5C6      LD /0000      88438430
0781 00 4C180786     BSC L  G5C8,+-  BRANCH ON ZERO 88438440
0783 00 44000F83     BSI L  F000     STD-0DD-EA INCORRECT 88438450
0785 0 30D2          DC      /30D2   ERR ID          88438460
0786 00 44000F82     G5C8 BSI L  F00E CK LOCK ON ERROR 88438470
0788 0 70F0          MDX     A5C8     LOOP           88438480
0789 0 C014          LD      N5C7      LD /FFFF      88438490
078A 0 F00F          EOR     N5C3      ZERO WITH /FFFF 88438500
078B 00 4C180790     BSC L  G5CA,+-  BRANCH ON ZERO 88438510
078D 00 44000F83     BSI L  F000     STD-0DD-EA+1 LOADED 88438520
078F 0 30D3          DC      /30D3   ERR ID          88438530
0790 00 44000FDE     G5CA BSI L  F005 CK LOCK ON ERROR 88438540
0792 0 70E6          MDX     A5C8     LOOP           88438550
0793 0 C006          LD      N5C3      LD /FFFF      88438560
0794 0 D008          STO     N5C6      STORE /FFFF   88438570
0795 0 D008          STO     N5C7      STORE /FFFF   88438580
0796 0 7008          MDX     A600     EXIT TO NEXT ROUTINE 88438590
0798 0000           BSS E
0798 0 0000           N5C1 DC      /0000 88438600
0799 0 0000           DC      /0000 88438610
079A 0 FFFF           N5C3 DC      /FFFF 88438620
079B 0 FFFF           DC      /FFFF 88438630
079C 0 FFFF           N5C5 DC      /FFFF 88438640
079D 0 FFFF           N5C6 DC      /FFFF 88438650
079E 0 FFFF           N5C7 DC      /FFFF 88438660

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TEST OF LDX OPERATION

```

*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
079F 00 650007A2     A600 LDX L1 G600 LD XR 1 WITH ADDR OF G600 88438700

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PROCESSOR-CONTROLLER FUNCTION TEST

```

07A1 0 7003          MDX     H600     TAG REG BIT 7 FAILED 88438790
07A2 00 44000F83     G600 BSI L  F000     ERR ID          88438800
07A4 0 30D4          DC      /30D4   CK L0CK ON ERROR 88438810
07A5 00 44000FDE     H600 BSI L  F005     LOOP           88438820
07A7 0 70F7          MDX     A600     LOOP           88438830
*****
07A8 00 660007AB     A602 LDX L2 G602 LD XR 2 WITH ADDR OF G602 88438840
07AA 0 7003          MDX     H602     TAG REG BIT 6 FAILED 88438850
07AB 00 44000F83     G602 BSI L  F009     ERR ID          88438860
07AD 0 30D5          DC      /30D5   CK LOCK ON ERROR 88438870
07AE 00 44000FDE     H602 BSI L  F005     LOOP           88438880
07B0 0 70F7          MDX     A602     LOOP           88438890
*****
07B1 0 6100          A604 LDX 1 0 LD DISP=0 TO XR 1 88438900
07B2 00 C500081C     LD L1 N601 LD ADDR OF N601 + XR 1 88438910
07B4 0 F067          EOR     N601     ZERO WITH ADDR OF N601 88438920
07B5 00 4C1807BA     BSC L  G604,+-  BRANCH ON ZERO 88438930
07B7 00 44000F83     BSI L  F000     IX 1 NOT LOADED 88438940
07B9 0 30D6          DC      /30D6   ERR ID          88438950
07BA 00 44000FDE     G604 BSI L  F005 CK LOCK ON ERROR 88438960
07BC 0 70F4          MDX     A604     LOOP           88438970
*****
07BD 0 6200          A606 LDX 2 0 LD DISP=0 TO XR 2 88438980
07BE 0 C05F          LD L2 N601 LD /FFFF 88438990
07BF 00 C600081C     LD L2 N601 LD ADDR OF N601 + XR 2 88439000
07C1 0 F05A          EOR     N601     ZERO WITH ADDR OF N601 88439010
07C2 00 4C1807C7     BSC L  G606,+-  BRANCH ON ZERO 88439020
07C4 00 44000F83     BSI L  F000     XR 2 NOT LOADED 88439030
07C6 0 30D7          DC      /30D7   ERR ID          88439040
07C7 00 44000FDE     G606 BSI L  F005 CK LOCK ON ERROR 88439050
07C9 0 70F3          MDX     A606     LOOP           88439060
*****
07CA 0 6300          A608 LDX 3 0 LD DISP=0 TO XR 3 88439070
07CB 0 C052          LD L3 N601 LD /FFFF 88439080
07CC 00 C700081C     LD L3 N601 LD ADDR OF N601 + XR 3 88439090
07CE 0 F04D          EOR     N601     ZERO WITH ADDR OF N601 88439100
07CF 00 4C1807D4     BSC L  G608,+-  BRANCH ON ZERO 88439110
07D1 00 44000F83     BSI L  F000     XR 3 NOT LOADED 88439120
07D3 0 30D8          DC      /30D8   ERR ID          88439130
07D4 00 44000FDE     G608 BSI L  F005 CK LOCK ON ERROR 88439140
07D6 0 70F3          MDX     A608     LOOP           88439150
*****
07D7 0 61FF          A60A LDX 1 -1 LD XR 1 WITH -1 88439160
07D8 0 C045          LD L1 N603 LD /1111 88439170
07D9 00 C500081C     LD L1 N601 LD ADDR OF N601 + XR 1 88439180
07DB 0 F03F          EOR     N600     ZERO WITH ADDR OF N600 88439190
07DC 00 4C1807E1     BSC L  G60A,+-  BRANCH ON ZERO 88439200
07DE 00 44000F83     BSI L  F000     XR 1 NOT LOADED 88439210
07E0 0 30D9          DC      /30D9   ERR ID          88439220
07E1 00 44000FDE     G60A BSI L  F005 CK LOCK ON ERROR 88439230
07E3 0 70F3          MDX     A60A     LOOP           88439240
*****
07E4 0 62FF          A60C LDX 2 -1 LD XR 2 WITH -1 88439250
07E5 0 C038          LD L2 N603 LD /FFFF 88439260
07E6 00 C600081C     LD L2 N601 LD ADDR OF N601 + XR 2 88439270
07E8 0 F032          EOR     N600     ZERO WITH ADDR OF N600 88439280
07E9 00 4C1807EE     BSC L  G60C,+-  BRANCH ON ZERO 88439290
07EB 00 44000F83     BSI L  F000     XR 2 NOT LOADED 88439300
07ED 0 30DA          DC      /30DA   ERR ID          88439310
07EE 00 44000FDE     G60C BSI L  F005 CK LOCK ON ERROR 88439320
07F0 0 70F3          MDX     A60C     LOOP           88439330
*****
07F1 0 63FF          A60E LDX 3 -1 LD XR 3 WITH -1 88439340
07F2 0 C02B          LD L3 N603 LD /FFFF 88439350
07F3 00 C700081C     LD L3 N601 LD ADDR OF N601 + XR 3 88439360
07F5 0 F025          EOR     N600     ZERO WITH ADDR OF N600 88439370
07F6 00 4C1807FB     BSC L  G60E,+-  BRANCH ON ZERO 88439380
07F8 00 44000F83     BSI L  F000     XR 3 NOT LOADED 88439390

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PROCESSOR-CONTROLLER FUNCTION TEST

```

07FA 0 30Db      DC      /300B      ERR ID      88439470
07FB 00 4400FDE G60E BSI L F005      CK LOCK ON ERROR 88439480
07FD 0 70F3      MDX      A60E      LOOP      88439490
*****
07FE 00 65000001 B600 LDX L1 1      LD XR 3 WITH +1 88439500
C800 0 C01D      LD      N603      LD /FFFF      88439510
0801 00 C500081C LD      L1 N601      LD ADDR OF N601 + XR 1 88439520
0803 0 F019      EUR      N602      ZERG WITH ADDR OF N602 88439530
0804 00 4C180809 BSC L J600,+--      BRANCH ON ZERO 88439540
0806 00 4400F83  BSI L F000      LONG FORM LDX-FAILED 88439550
0808 0 30DC      DC      /30DC      ERR ID      88439560
0809 00 4400FDE  J600 BSI L F005      CK LOCK ON ERROR 88439570
080B 0 70F2      MDX      B600      LOOP      88439580
*****
080C 00 6780081E B602 LDX L3 N603      LD XR 3 WITH /FFFF 88439590
080E 0 C010      LD      N604      LD /0001      88439600
080F 00 C700081C LD      L3 N601      LD ADDR OF N601 + XR 3 88439610
0811 0 F009      EOR      N600      ZERO WITH ADDR OF N600 88439620
0812 00 4C180817 BSC L J602,+--      BRANCH ON ZERO 88439630
0814 00 4400F83  BSI L F000      INDIRECT LDX FAILED 88439640
0816 0 30DD      DC      /30DD      ERR ID      88439650
0817 00 4400FDE  J602 BSI L F005      CK LOCK ON ERROR 88439660
0819 0 70F2      MDX      B602      LOOP      88439670
081A 0 7005      MDX      A640      EXIT TO NEXT ROUTINE 88439680
081B 0 081B      N600 DC      N600      88439690
081C 0 081C      N601 DC      N601      88439700
081D 0 081D      N602 DC      N602      88439710
081E 0 FFFF      N603 DC      /FFFF      88439720
081F 0 0001      N604 DC      /0001      88439730
*
* TEST OF STX OPERATION
*
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATIGN FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0820 0 C06D      A640 LD      N644      LD /FFFF      88439830
0821 0 D069      STD      N640      SAVE      88439840
0822 0 C0FF      N640 LD      H640      LD /COFF      88439850
0823 0 6867      STX      N640      STORE INST REG AT N640 88439860
0824 0 F0FD      K640 EOR      H640      CK THAT ACC WAS NOT 88439870
*
* RESET BY STX
*
0825 00 4C18082D BSC L G640,+--      BRANCH ON ZERO 88439880
0827 00 4400F83  BSI L F000      ACC GONE AFTER STX 88439890
0829 0 3167      DC      /3167      ERR ID      88439900
082A 00 4400CFB2 BSI L F00E      CK LOCK ON ERROR 88439910
082C 0 70F3      MDX      A640      88439920
082D 0 C05D      G640 LD      N640      CK THAT STX STORED CORECT 88439930
082E 0 F05D      EOR      N642      88439940
082F 00 4C180834 BSC L G641,+--      BRANCH ON ZERO 88439950
0831 00 4400F83  BSI L F000      I CTR NOT STORED 88439960
0833 0 30DE      DC      /30DE      ERR ID      88439970
0834 00 4400FDE  G641 BSI L F005      CK LOCK ON ERROR 88440000
0836 0 70E9      MDX      A640      LOOP      88440010
*****
0837 0 C056      A642 LD      N644      LD /FFFF      88440020
0838 0 D052      STD      N640      SAVE      88440030
0839 0 6100      LDX      1 0      LD XR 1 WITH /0000 88440040
083A 0 6950      STX      1 N640      STORE C(XR 1) AT N640 88440050
083B 0 C04F      LD      N640      LD C(N640) 88440060
083C 00 4C180841 BSC L G642,+--      BRANCH ON ZERO 88440070
083E 00 4400F83  BSI L F000      XR 1 NOT STORED 88440080
0840 0 30DF      DC      /30DF      ERR ID      88440090
0841 00 4400FDE  G642 BSI L F005      CK LOCK ON ERROR 88440100
0843 0 70F3      MDX      A642      LOOP      88440110
*****
0844 0 C049      A644 LD      N644      LD /FFFF      88440120
88440130
88440140

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PROCESSOR-CONTROLLER FUNCTION TEST

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0845 0 D045      STD      N640      SAVE      88440150
0846 0 6200      LDX      2 0      LD XR 2 WITH /0000 88440160
0847 0 6A43      STY      2 N640      STORE C(XR 2) AT N640 88440170
0848 0 C042      LD      N640      LD C(N640) 88440180
0849 00 4C18084E BSC L G644,+--      BRANCH ON ZERO 88440190
084B 00 4400F83  BSI L F000      XR 2 NOT STORED 88440200
084D 0 30E0      DC      /30E0      ERR ID      88440210
084E 00 4400FDE  G644 BSI L F005      CK LOCK ON ERROR 88440220
0850 0 70F3      MDX      A644      LOOP      88440230
*****
0851 0 C03C      A646 LD      N644      LD /FFFF      88440240
0852 0 D038      STO      N640      SAVE      88440250
0853 0 6300      LDX      3 0      LD XR 3 WITH /0000 88440260
0854 0 6836      STX      3 N640      STORE C(XR 3) AT N640 88440270
0855 0 C035      LD      N640      LD C(N640) 88440280
0856 00 4C18085B BSC L G646,+--      BRANCH ON ZERO 88440290
0858 00 4400F83  BSI L F000      XR 3 NOT STORED 88440300
085A 0 30E1      DC      /30E1      ERR ID      88440310
085B 00 4400FDE  G646 BSI L F005      CK LOCK ON ERROR 88440320
085D 0 70F3      MDX      A646      LOOP      88440330
*****
085E 0 C02E      A648 LD      N643      LD /0000      88440340
085F 0 D028      STO      N640      SAVE      88440350
0860 0 61FF      LDX      1 -1      LD XR 1 WITH /FFFF 88440360
0861 0 6929      STX      1 N640      STORE C(XR 1) AT N640 88440370
0862 0 C028      LD      N640      LD C(N640) 88440380
0863 0 F02A      EOR      N644      ZERO WITH /FFFF 88440390
0864 00 4C180869 BSC L G648,+--      BRANCH ON ZERO 88440400
0866 00 4400F83  BSI L F000      XR 1 NOT STORED 88440410
0868 0 30E2      DC      /30E2      ERR ID      88440420
0869 00 4400FDE  G648 BSI L F005      CK LOCK ON ERROR 88440430
086B 0 70F2      MDX      A648      LOOP      88440440
*****
086C 0 C020      A64A LD      N643      LD /0000      88440450
086D 0 D01D      STO      N640      SAVE      88440460
086E 0 62FF      LDX      2 -1      LD XR 2 WITH /FFFF 88440470
086F 0 6A1B      STX      2 N640      STORE C(XR 2) AT N640 88440480
0870 0 C01A      LD      N640      LD C(N640) 88440490
0871 0 F01C      EOR      N644      ZERO WITH /FFFF 88440500
0872 00 4C180877 BSC L G64A,+--      BRANCH ON ZERO 88440510
0874 00 4400F83  BSI L F000      XR 2 NOT STORED 88440520
0876 0 30E3      DC      /30E3      ERR ID      88440530
0877 00 4400FDE  G64A BSI L F005      CK LOCK ON ERROR 88440540
0879 0 70F2      MDX      A64A      LOOP      88440550
*****
087A 0 C012      A64C LD      N643      LD /0000      88440560
087B 0 D00F      STO      N640      SAVE      88440570
087C 0 63FF      LDX      3 -1      LD XR 3 WITH /FFFF 88440580
087D 0 680D      STX      3 N640      STORE C(XR 3) AT N640 88440590
087E 0 C00C      LD      N640      LD C(N640) 88440600
087F 0 F00E      EOR      N644      ZERO WITH /FFFF 88440610
0880 00 4C180885 BSC L G64C,+--      BRANCH ON ZERO 88440620
0882 00 4400F83  BSI L F000      XR 3 NOT STORED 88440630
0884 0 30E4      DC      /30E4      ERR ID      88440640
0885 00 4400FDE  G64C BSI L F005      CK LOCK ON ERROR 88440650
0887 0 70F2      MDX      A64C      LOOP      88440660
0888 0 C004      LD      N643      LD /0000      88440670
0889 0 D001      STO      N640      RESTORE N640 TO /0000 88440680
088A 0 7004      MDX      A660      EXIT TO NEXT ROUTINE 88440690
088B 0 0000      N640 DC      /0000      88440700
088C 0 0824      N642 DC      K640      88440710
088D 0 0000      N643 DC      /0000      88440720
088E 0 FFFF      N644 DC      /FFFF      88440730
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATIGN FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
88440780
88440790
88440800
88440810
88440820

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088F 0 6100	A660 LDX 1 0	LD XR 1 WITH /0000	88440830
0890 0 6200	LDX 2 0	LD XR 2 WITH /0000	88440840
0891 0 6300	LDX 3 0	LD XR 3 WITH /0000	88440850
0892 0 61FF	LDX 1 -1	LD XR 1 WITH /FFFF	88440860
0893 0 6A44	STX 2 N660	CK FOR DISTRUCTION OF	88440870
0894 0 C043	LD N660	OTHER INDEXES	88440880
0895 00 4C18089A	BSC L G660,+	BRANCH ON ZERO	88440890
0897 00 44000FB3	BSI L F000	XR 2 CHANGED	88440900
0899 0 3157	DC /3157	ERR ID	88440910
089A 00 44000FB2	G660 BSI L F00E	CK LOCK ON ERROR	88440920
089C 0 70F2	MDX A660	LOOP	88440930
089D 0 683A	STX 3 N660	STORE C(XR 3) AT N660	88440940
089E 0 C039	LD N660	LD C(N660)	88440950
089F 00 4C1808A4	BSC L G661,+	BRANCH ON ZERO	88440960
08A1 00 44000FB3	BSI L F000	XR 3 CHANGED	88440970
08A3 0 3158	DC /3158	ERR ID	88440980
08A4 00 44000FDE	G661 BSI L F005	CK LOCK ON ERROR	88440990
08A6 0 70E8	MDX A660	LOOP	88441000

08A7 0 6100	A662 LDX 1 0	LD XR 1 WITH /0000	88441010
08A8 0 6200	LDX 2 0	LD XR 2 WITH /0000	88441020
08A9 0 6300	LDX 3 0	LD XR 3 WITH /0000	88441030
08AA 0 62FF	LDX 2 -1	LD XR 2 WITH /FFFF	88441040
08AB 0 692C	STX 1 N660	STORE C(XR 1) AT N660	88441050
08AC 0 C02B	LD N660	LD C(N660)	88441060
08AD 00 4C1808B2	BSC L G662,+	BRANCH ON ZERO	88441070
08AF 00 44000FB3	BSI L F000	XR 1 CHANGED	88441080
08B1 0 3159	DC /3159	ERR ID	88441090
08B2 00 44000FB2	G662 BSI L F00E	CK LOCK ON ERROR	88441100
08B4 0 70F2	MDX A662	LOOP	88441110
08B5 0 6B22	STX 3 N660	STORE C(XR 3) AT N660	88441120
08B6 0 C021	LD N660	LD C(N660)	88441130
08B7 00 4C1808BC	BSC L G663,+	BRANCH ON ZERO	88441140
08B9 00 44000FB3	BSI L F000	CK LOCK ON ERROR	88441150
08BB 0 315A	DC /315A	ERR ID	88441160
08BC 00 44000FDE	G663 BSI L F005	CK LOCK ON ERROR	88441170
08BE 0 70E8	MDX A662	LOOP	88441180

08BF 0 6100	A664 LDX 1 0	CK DISTRUCTION OF	88441200
08C0 0 6200	LDX 2 0	OTHER INDEXES	88441210
08C1 0 6300	LDX 3 0	XR'S HAVE /0000	88441220
08C2 0 63FF	LDX 3 -1	LD XR 3 WITH /FFFF	88441230
08C3 0 6914	STX 1 N660	STORE C(XR 1) AT N660	88441240
08C4 0 C013	LD N660	LD C(N660)	88441250
08C5 00 4C1808CA	BSC L G664,+	BRANCH ON ZERO	88441260
08C7 00 44000FB3	BSI L F000	XR 1 CHANGED	88441270
08C9 0 315B	DC /315B	ERR ID	88441280
08CA 00 44000FB2	G664 BSI L F00E	CK LOCK ON ERROR	88441290
08CC 0 70F2	MDX A664	LOOP	88441300
08CD 0 6A0A	STX 2 N660	STORE C(XR 2) AT N660	88441310
08CE 0 C009	LD N660	LD C(N660)	88441320
08CF 00 4C1808D4	BSC L G665,+	BRANCH ON ZERO	88441330
08D1 00 44000FB3	BSI L F000	XR 2 CHANGED	88441340
08D3 0 315C	DC /315C	ERR ID	88441350
08D4 00 44000FDE	G665 BSI L F005	CK LOCK ON ERROR	88441360
08D6 0 70E8	MDX A664	LOOP	88441370
08D7 0 7001	MDX A670	EXIT TO NEXT ROUTINE	88441380
08D8 0 0000	N660 DC 0		88441390

08D9 0 6110	A670 LDX 1 16	LD XR 1 WITH /0010	88441400
08DA 0 C010	LD N670	LOAD CNE	88441410
08DB 00 4C1808E4	G671 BSC L G670,+	NOT BR FOR CORRECT OP	88441420
08DD 0 1001	G672 SLA 1		88441430
08DE 0 71FF	MDX 1 -1	-1 FROM C(XR 1)	88441440
08DF 0 70FB	MDX G671		88441450
08E0 00 44000FDE	BSI L F005	CK LOCK ON ERROR	88441460
08E2 0 70F6	MDX A670	LOOP	88441470
08E3 0 7008	MDX A680	EXIT TO NEXT ROUTINE	88441480

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08E4 00 44000FB3	G670 BSI L F000	WRONG DECODE OF ZERO ACC	88441510
08E6 0 3169	DC /3169	ERR ID	88441520
08E7 00 44000FB2	BSI L F00E	CK LOCK ON ERROR	88441530
08E9 0 70EF	MDX A670	LOOP	88441540
08EA 0 70F2	MDX G672		88441550
08EB 0 0001	N670 DC 1		88441560

TEST OF ADD OPERATION			

08EC 0 2002	A680 LDS 2	SET CARRY ON	88441600
08ED 0 C06E	LD N680	LD /FFFF	88441610
08EE 0 806E	A N681	A /0000	88441620
08EF 00 4C0108F2	BSC L G680,C	CK FOR OVERFLOW CN	88441630
08F1 0 7003	MDX H680	OVERFLOW IS OFF	88441640
08F2 0 44000FB3	G680 BSI L F000	OVERFLOW IS ON	88441650
08F4 0 30E5	DC /30E5	ERR ID	88441660
08F5 00 44000FB2	H680 BSI L F00E	CK LOCK ON ERROR	88441670
08F7 0 70F4	MDX A680	LOOP	88441680
08F8 0 F063	EOR N680	CK IF ADD ZERO	88441690
08F9 00 4C1808FE	BSC L G682,+	* CHANGED ACC	88441700
08FB 00 44000FB3	BSI L F000	ADD 1 AND 0 FAILED	88441710
08FD 0 30E6	DC /30E6	ERR ID	88441720
08FE 00 44000FDE	G682 BSI L F005	CK LOCK ON ERROR	88441730
0900 0 70EB	MDX A680	LOOP	88441740

0901 0 2000	A684 LDS 0	SET C AND OF OFF	88441750
0902 0 C059	LD N680	LD /FFFF	88441760
0903 0 805A	A N682	A /0001	88441770
0904 00 4C020909	BSC L G684,C	CK IF CARRY OCCURED	88441780
0906 00 44000FB3	BSI L F000	CARRY NOT ON	88441790
0908 0 30E7	DC /30E7	ERR ID	88441800
0909 00 44000FB2	G684 BSI L F00E	CK LOCK ON ERROR	88441810
090B 0 70F5	MDX A684	LOOP	88441820
090C 00 4C180911	BSC L G686,+	BRANCH ON ZERO	88441830
090E 00 44000FB3	BSI L F000	ADD FFFF+0001 FAILED	88441840
0910 0 30E8	DC /30E8	ERR ID	88441850
0911 00 44000FDE	G686 BSI L F005	CK LOCK ON ERROR	88441860
0913 0 70ED	MDX A684	LOOP	88441870

0914 0 2000	A688 LDS 0	SET C AND OF OFF	88441880
0915 0 C046	LD N680	LD /FFFF	88441890
0916 0 8045	A N680	A /FFFF	88441900
0917 00 4C02091C	BSC L G688,C	BR ON CARRY	88441910
0919 00 44000FB3	BSI L F000	CARRY NOT ON	88441920
091B 0 30E9	DC /30E9	ERR ID	88441930
091C 00 44000FB2	G688 BSI L F00E	CK LOCK ON ERROR	88441940
091E 0 70F5	MDX A688	LOOP	88441950
091F 0 F042	EOR N687	ZERC WITH /FFFF	88441960
0920 00 4C180925	BSC L G68A,+	BRANCH ON ZERO	88441970
0922 00 44000FB3	BSI L F000	ADD FFFF+FFFF FAILED	88441980
0924 0 30FA	DC /30FA	ERR ID	88441990
0925 00 44000FDE	G68A BSI L F005	CK LOCK ON ERROR	88442000
0927 0 70EC	MDX A688	LOOP	88442010

0928 0 2000	A68C LDS 0	SET C AND OF OFF	88442020
0929 0 C035	LD N683	LD /4000	88442030
092A 0 8034	A N683	A /4000	88442040
092B 00 4C010930	BSC L G68C,C	BR IF CF NOT ON	88442050
092D 00 44000FB3	BSI L F000	OVERFLOW NOT ON	88442060
092F 0 30EB	DC /30EB	ERR ID	88442070
0930 00 44000FB2	G68C BSI L F00E	CK LOCK ON ERROR	88442080
0932 0 70F5	MDX A68C	LOOP	88442090
0933 0 F02C	EOR N684	ZERO WITH /8000	88442100

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PROCESSOR-CONTROLLER FUNCTION TEST

0934 00 4C180939 BSC L G68E,+-- BRANCH ON ZERO 88442190
0936 00 44000F83 BSI L F000 ADD 4000+4000 FAILED 88442200
0938 0 30EC DC /30EC ERR ID 88442210
0939 00 44000FDE G68E BSI L F005 CK LOCK ON ERROR 88442220
093B 0 70EC MDX A68C LOOP 88442230

INDEXING TEST

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
0964 0 61FC A6C0 LDX 1 -4 LD XR 1 WITH -4 88442630
0965 00 C50009F0 LD L1 N6C4 LD C(N6C4+XR 1) 88442640
0967 0 F074 EOR N6C0 ZERO ACC IF CORRECT OP 88442650

PROCESSOR-CONTROLLER FUNCTION TEST

0983 0 C061 LD N6C9 GET XR 2 VALUE 88442870
0984 0 F062 EOR N6CB ZERO ACC IF CORRECT 88442880
0985 00 4C180991 BSC L G6C2,+-- BRANCH ON ZERO 88442890
0987 00 44000F83 BSI L F000 XR 2 LOADED WRONG 88442900
0989 0 30F2 DC /30F2 ERR ID 88442910
098A 00 44000FDE BSI L F005 CK LOCK ON ERROR 88442920
098C 0 70EF MDX A6C2 LOOP 88442930
098D 0 7006 MDX A6C4 EXIT TO NEXT ROUTINE 88442940
098E 00 44000F83 H6C2 BSI L F000 WRONG LOCATION 88442950
0990 0 30F3 DC /30F3 ERR ID 88442960
0991 00 44000FDE G6C2 BSI L F005 CK LOCK ON ERROR 88442970
0993 0 70E8 MDX A6C2 LOOP 88442980

PROCESSOR-CONTROLLER FUNCTION TEST

09DE 0 09DE N6C2 DC N6C2 88443550
09DF 0 09DF N6C3 DC N6C3 88443560
09E0 0 09E0 N6C4 DC N6C4 88443570
09E1 0 09E1 N6C5 DC N6C5 88443580
09E2 0 09E2 N6C6 DC N6C6 88443590
09E3 0 09E3 N6C7 DC N6C7 88443600
09E4 0 09E4 N6C8 DC N6C8 88443610
09E5 0 0000 N6C9 DC /0000 88443620
09E6 0 FFFC N6CA DC /FFFC 88443630
09E7 0 0004 N6CB DC /0004 88443640
09E8 0 0001 N6CD DC /0001 88443650
09E9 0 09E0 DC N6C4 88443660
09EA 0 FFFF N6CF DC /FFFF 88443670
09EB 0 70D7 MDX A6C8 LOOP 88443680
***** 88443690
CORE DATA OR *LA- OPER- 88443700
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88443710
***** 88443720
09EL 00 650009DD A6D0 LDX L1 N6C1 LD XR 1 WITH ADDRESS 88443740
* OF N6C1 88443750
09EE 0 C1FF LD 1 -1 SHORT FCRM INDEXING 88443760
09EF 0 FOEC EOR N6C0 ZERO IF CORRECT 88443770
09F0 00 4C1809F5 BSC L H6D0,+ BRANCH ON ZERO 88443780
09F2 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443790
09F4 0 315D DC /315D ERR ID 88443800
09F5 00 44000FDE H6D0 BSI L F005 CK LOCK ON ERROR 88443810
09F7 0 70F4 MDX A6D0 LOOP 88443820
***** 88443830
09F8 00 660009DD A6D2 LDX L2 N6C1 LD XR 2 WITH ADDRESS 88443840
* OF N6C1 88443850
09FA 0 C201 LD 2 1 LD C(OF ADDRESS IN XR 1+1) 88443860
09FB 0 FOE2 EOR N6C2 ZERO IF CORRECT 88443870
09FC 00 4C180A01 BSC L H6D2,+ BRANCH CN ZERO 88443880
09FE 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443890
0A00 0 315E DC /315E ERR ID 88443900
0A01 00 44000FDE H6D2 BSI L F005 CK LOCK ON ERROR 88443910
0A03 0 70F4 MDX A6D2 LOOP 88443920
***** 88443930
0A04 00 670009DD A6D3 LDX L3 N6C1 LD XR 3 WITH ADD OF N6C1 88443940
0A06 0 C300 LD 3 0 LD C(OF ADD IN XR 3 + 0) 88443950
0A07 0 F0D5 EOR N6C1 ZERO IF CORRECT 88443960
0A08 00 4C180A0D BSC L H6D3,+ BRANCH ON ZERO 88443970
0A0A 00 44000F83 BSI L F000 INDEXED LD INST. FAILED 88443980
0A0C 0 315F DC /315F ERR ID 88443990
0A0D 00 44000FDE H6D3 BSI L F005 CK LOCK ON ERROR 88444000
0A0F 0 70F4 MDX A6D3 LOOP 88444010
***** 88444020
0A10 0 6102 A6D5 LDX 1 2 LD XR 3 WITH +2 88444030
0A11 0 C0D6 LD N6C0 LD /0001 88444040
0A12 0 1101 SLA 1 1 NOW A=/0004 88444060
0A13 0 F0D3 EOR N6C8 NOW A=/0000 88444070
0A14 00 4C180A19 BSC L H6D5,+ BRANCH CN ZERO 88444080
0A16 00 44000F83 BSI L F000 INDEXED SLA FAILED 88444090
0A18 0 3163 DC /3163 ERR ID 88444100
0A19 00 44000FDE H6D5 BSI L F005 CK LOCK ON ERRCR 88444110
0A1B 0 70F4 MDX A6D5 LOOP 88444120
***** 88444130
0A1C 0 6202 A6D6 LDX 2 2 LD /00004 88444140
0A1D 0 CUC9 LD N6C8 NOW A=/0001 88444150
0A1E 0 1A01 SRA 2 1 ZERO ACC 88444160
0A1F 0 F0C8 EOR N6C0 ZERO WITH /0001 88444170
0A20 00 4C180A25 BSC L H6D6,+ BRANCH ON ZERO 88444180
0A22 00 44000F83 BSI L F000 INDEXED SRA FAILED 88444190
0A24 0 3164 DC /3164 ERR ID 88444200
0A25 00 44000FDE H6D6 BSI L F005 CK LOCK ON ERROR 88444210
0A27 0 70F4 MDX A6D6 LOOP 88444220

PROCESSOR-CONTROLLER FUNCTION TEST

***** 88444230
* 88444240
* TEST INDEXED BSC 88444250
* 88444260
***** 88444270
CORE DATA OR *LA- OPER- 88444280
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88444290
***** 88444300
0A28 0 6301 A6F0 LDX 3 1 LD XR 3 WITH +1 88444310
0A29 0 C00E LD N6F1 LD C(OF LABEL N6F1) 88444320
0A2A 00 4F000A2D BSC L3 N6F0 BR TO C(N6F0+XR 3) 88444330
0A2C 0 3000 WAIT INDEXED BSC FAILED 88444340
0A2D 0 3000 N6F0 WAIT INDEXED BSC FAILED 88444350
0A2E 0 F009 EOR N6F1 CK FOR DISTROYED ACC 88444360
0A2F 00 4C180A34 BSC L H6F0,+ BRANCH CN ZERO 88444370
0A31 00 44000F83 BSI L F000 ACC DISTROYED 88444380
0A33 0 3165 DC /3165 ERR ID 88444390
0A34 00 44000FDE H6F0 BSI L F005 CK LOCK ON ERROR 88444400
0A36 0 70F1 MDX A6F0 LOOP 88444410
0A37 0 7001 MDX A6F1 EXIT TO NEXT ROUTINE 88444420
0A38 0 0A38 N6F1 DC N6F1 88444430
***** 88444440
0A39 0 6201 A6F1 LDX 2 1 LD XR 2 WITH +1 88444450
0A3A 00 4E800A3D BSC 12 N6F2 BR TO N6F2+1 INDIRECT 88444460
0A3C 0 7005 MOX H6F1 BSC FAILED 88444470
0A3D 0 7004 N6F2 MDX H6F1 BSC FAILED 88444480
0A3E 0 0A41 DC N6F3 88444490
0A3F 0 7002 MOX H6F1 BSC FAILED 88444500
0A40 0 7001 MOX H6F1 BSC FAILED 88444510
0A41 0 7003 N6F3 MDX H6F2 88444520
0A42 00 44000F83 H6F1 BSI L F000 BSC DID NOT BRANCH 88444530
0A44 0 3166 DC /3166 ERR ID 88444540
0A45 00 44000FDE H6F2 BSI L F005 CK LOCK ON ERROR 88444550
0A47 0 70F1 MDX A6F1 LOOP 88444560
***** 88444570
* 88444580
* TEST OF SUBTRACT OPERATION 88444590
* 88444600
***** 88444610
0A48 0 2000 A700 LDS 0 SET C AND CF OFF 88444620
0A49 0 C066 LD N700 LD /0000 88444630
0A4A 0 9066 S N701 S /0001 A N6M /FFFF 88444640
0A4B 0 2866 STS N702 STORE CARRY IND. TO N702 88444650
0A4C 0 F066 EOR N703 ZERO ACC IF CORRECT 88444660
0A4D 00 4C180A52 BSC L G700,+ BRANCH ON ZERO 88444670
0A4F 00 44000F83 BSI L F000 0000 MINUS 0001 FAILED 88444680
0A51 0 30FA DC /30FA ERR ID 88444690
0A52 00 44000F82 G700 BSI L F000 CK LOCK ON ERROR 88444700
0A54 0 70F3 MDX A700 LOOP 88444710
0A55 0 C05C LD N702 LD CARRY INDICATION 88444720
0A56 0 F05D EOR N704 ZERO IF CORRECT 88444730
0A57 00 4C180A5C BSC L G702,+ BRANCH ON ZERO 88444740
0A59 00 44000F83 BSI L F000 CARRY NOT ON 88444750
0A5B 0 30FB DC /30FB ERR ID 88444760
0A5C 00 44000FDE G702 BSI L F005 CK LOCK ON ERROR 88444770
0A5E 0 70E9 MDX A700 LOOP 88444780
***** 88444790
0A5F 0 2000 A704 LDS 0 SET C AND CF OFF+ 88444800
0A60 0 C04F LD N700 LD /0000 88444810
0A61 0 9051 S N703 S /FFFF 88444820
0A62 0 284F STS N702 STORE CARRY ON CONDITION 88444830
0A63 0 F04D EOR N701 ZERO WITH /0001 88444840
0A64 00 4C180A69 BSC L G704,+ BRANCH CN ZERO 88444850
0A66 00 44000F83 BSI L F000 0000 MINUS FFFF FAILED 88444860
0A68 0 30FC DC /30FC ERR ID 88444870
0A69 00 44000F82 G704 BSI L F00E CK LOCK ON ERROR 88444880
0A6B 0 70F3 MDX A704 LOOP 88444890
0A6C 0 C045 LD N702 LD CARRY COND FROM N702 88444900

PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

0A6D 0 F046 EDR N704 ZERO ACC IF CORRECT 88444910
0A6E 00 4C180A73 BSC L G70C,+ BRANCH ON ZERO 88444920
0A70 00 44000F83 BSI L F000 CARRY NOT SET 88444930
0A72 0 30FD DC /30FD ERR ID 88444940
0A73 00 44000FDE G706 BSI L F005 CK LOCK ON ERROR 88444950
0A75 0 70E9 MDX A704 LOOP 88444960

CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88444970

0A76 0 2000 A70C LDS 0 SET C AND OF OFF 88445020
0A77 0 C03D LD N705 LD /8000 88445030
0A78 0 9028 S N701 S /0001 88445040
0A79 0 2838 STS N702 SAVE C + OF CONDITION 88445050
0A7A 0 F03C EDR N707 ZERO ACC IF CORRECT OP 88445060
0A7B 00 4C180A80 BSC L G70B,+ BRANCH ON ZERO 88445070
0A7D 00 44000F83 BSI L F000 8000 MINU 0001 FAILED 88445080
0A7E 0 30FE DC /30FE ERR ID 88445090
0A80 00 44000FB2 G708 BSI L F00E CK LOCK ON ERROR 88445100
0A82 0 70F3 MDX A708 LOOP 88445110
0A83 0 C02E LD N702 LD STORE CARRY CONDITION 88445120
0A84 0 F02C EDR N701 ZERO IF CORRECT 88445130
0A85 00 4C180A8A BSC L G70A,+ BRANCH ON ZERO 88445140
0A87 00 44000FB3 BSI L F000 OVERFLOW NOT SET 88445150
0A89 0 30FF DC /30FF ERR ID 88445160
0A8A 00 44000FDE G70A BSI L F005 CK LOCK ON ERROR 88445170
0A8C 0 70E9 MDX A708 LOOP 88445180

0A8D 0 2000 A70C LDS 0 SET C AND OF OFF 88445200
0A8E 0 C021 LD N700 LD /0000 88445210
0A8F 0 9025 S N705 S /8000 88445220
0A90 0 2821 STS N702 STORE C + OF CONDITION 88445230
0A91 0 F023 EDR N705 ZERO ACC IF CORRECT 88445240
0A92 00 4C180A97 BSC L G70C,+ BRANCH ON ZERO 88445250
0A94 00 44000FB3 BSI L F000 0000 MINUS 8000 FAILED 88445260
0A96 0 3100 DC /3100 ERR ID 88445270
0A97 00 44000FB2 G70C BSI L F00E CK LOCK ON ERROR 88445280
0A99 0 70F3 MDX A70C LOOP 88445290
0A9A 0 C017 LD N702 LD CON CF C+OF 88445300
0A9B 0 F01A EDR N706 ZERO ACC IF CORRECT 88445310
0A9C 00 4C180AAC BSC L G70E,+ BRANCH ON ZERO 88445320
0A9E 0 C013 LC N702 LD CON OF C + OF 88445330
0A9F 0 E011 AND N701 AND IN /0001 88445340
0AA0 00 4C200AA9 BSC L J70E,+ BR IF NOT ZERO 88445350
0AA2 00 44000FB3 BSI L F000 OVERFLOW NOT ON 88445360
0AA4 0 3101 DC /3101 ERR ID 88445370
0AA5 00 44000FDE BSI L F005 CK LOCK ON ERROR 88445380
0AA7 0 70E5 MDX A70C LOOP 88445390
0AA8 0 700F MDX A740 EXIT TO NEXT ROUTINE 88445400
0AA9 00 44000FB3 J70E BSI L F000 CARRY NOT ON 88445410
0AAB 0 3102 DC /3102 ERR ID 88445420
0AAC 00 44000FDE G70E BSI L F005 CK LOCK ON ERROR 88445430
0AAE 0 70DE MDX A70C LOOP 88445440
0AAF 0 7008 MDX A740 EXIT TO NEXT ROUTINE 88445450
0AB0 0 0000 N700 DC /0000 88445460
0AB1 0 0001 N701 DC /0001 88445470
0AB2 0 0000 N702 DC /0000 88445480
0AB3 0 FFFF N703 DC /FFFF 88445490
0AB4 0 0002 N704 DC /0002 88445500
0AB5 0 8000 N705 DC /8000 83445510
0AB6 0 0003 N706 DC /0003 88445520
0AB7 0 7FFF N707 DC /7FFF 88445530
*
* TEST OF ADD DOUBLE 88445540
* 88445550
* 88445560
* 88445570

88445580

CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88445590

0AB8 0 2000 A740 LDS 0 SET C AND OF OFF 88445620
0AB9 00 0C000B80 LDD L N742 LD A=/FFFF Q=/FFFF 88445630
0ABB 00 8C000B82 AD L N744 A /0000 /0000 88445640
0ABD 00 2C000B7E STS L N740 STORE CON. OF C + OF 88445650
0ABF 00 F4000B80 EDR L N742 88445660
0AC1 00 4C180AC6 BSC L G740,+ BRANCH ON ZERO 88445670
0AC3 00 44000FB3 BSI L F000 AD FFFF+0000 A FAILED 98445680
0AC5 0 3103 DC /3103 ERR ID 88445690
0AC6 00 44000FB2 G740 BSI L F00E CK LOCK ON ERROR 88445700
0AC8 0 70EF MDX A740 LOOP 88445710
0AC9 0 1800 RTE 16 88445720
0ACA 00 F4000B80 EDR L N742 88445730
0ACC 00 4C180AD1 BSC L G742,+ BR ON ZERO 88445740
0ACE 00 44000FB3 BSI L F000 AD FFFF+0000 Q FAILED 88445750
0AD0 0 3104 DC /3104 ERR ID 88445760
0AD1 00 44000FB2 G742 BSI L F00E CK LOCK ON ERROR 88445770
0AD3 0 70E4 MDX A740 LOOP 88445780
0AD4 00 C4000B7E LD L N740 CONDITION OF C + OF 88445790
0AD6 00 4C180AE4 BSC L G744,+ BRANCH ON ZERO 88445800
0AD8 00 4C040AE1 BSC L H744,E BR IF NOT EVEN 88445810
0ADA 00 44000FB3 BSI L F000 CARRY ON 88445820
0ADC 0 3105 DC /3105 ERR ID 88445830
0ADD 00 44000FDE BSI L F005 CK LOCK ON ERROR 88445840
0ADF 0 70D8 MDX A740 LOOP 88445850
0AE0 0 7003 MDX G744 88445860
0AE1 00 44000FB3 H744 BSI L F000 OVFLD ON 88445870
0AE3 0 3106 DC /3106 ERR ID 88445880
0AE4 00 44000FDE G744 BSI L F005 CK LOCK ON ERROR 88445890
0AE6 0 70D1 MDX A740 LOOP 88445900

0AE7 0 2000 A746 LDS 0 SET C AND OF OFF 88445920
0AE8 00 0C000B84 LDD L N746 LD A-/0000 Q=/0001 88445930
0AEA 00 8C000B80 AD L N742 A /FFFF /FFF 88445940
0AEC 00 2C000B7E STS L N740 STORE COND OF C AND OF 88445950
0AEE 00 4C180AF3 BSC L G746,+ BRANCH ON ZERO 88445960
0AEF 00 44000FB3 BSI L F000 AD 0000+FFFF A FAILED 88445970
0AF0 0 3107 DC /3107 ERR ID 88445980
0AF2 0 3107 DC /3107 ERR ID 88445990
0AF3 00 44000FB2 G746 BSI L F00E CK LOCK ON ERROR 88446000
0AF5 0 70F1 MDX A746 LOOP 88446010
0AF6 0 1800 RTE 16 INTERCHANGE A AND Q 88446020
0AF7 00 4C180AFC BSC L G748,+ BRANCH ON ZERO 88446030
0AF9 00 44000FB3 BSI L F000 AD 0001+FFFF Q FAILED 88446040
0AFB 0 3108 DC /3108 ERR ID 88446050
0AFC 00 44000FB2 G748 BSI L F00E CK LOCK ON ERROR 88446060
0AFE 0 70E8 MDX A746 LOOP 88446070
0AFF 00 C4000B7E LD L N740 LD COND OF C AND OF 88446080
0B01 00 F4000B86 EDR L N748 CHECK FOR CARRY 88446090
0B03 00 4C180B11 BSC L G74A,+ ZERO= C AND OF DK 88446100
0B05 00 4C040B0E BSC L H74A,E CHECK FOR CVERFLOW (B15) 88446110
0B07 00 44000FB3 BSI L F000 CARRY NOT ON 88446120
0B09 0 3109 DC /3109 ERR ID 88446130
0B0A 00 44000FDE BSI L F005 CK LOCK ON ERROR 88446140
0B0C 0 70DA MDX A746 LOOP 88446150
0B0D 0 7003 MDX G74A 88446160
0B0E 00 44000FB3 H74A BSI L F000 OVFLD ON 88446170
0B10 0 310A DC /310A ERR ID 88446180
0B11 00 44000FDE G74A BSI L F005 CK LOCK ON ERROR 88446190
0B13 0 70D3 MDX A746 LOOP 88446200

CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT 88446230

0B14 0 2000 A74C LDS 0 SET C AND OF OFF 88446250
0B15 0 C864 LDD N742 LD A=/FFFF Q=/FFFF 88446260



PROCESSOR-CONTROLLER FUNCTION TEST

0816 0 8865 AD N742 A /FFFF /FFFF 88446270
0817 0 2866 STS N740 STORE C AND OF COND 88446280
0818 0 F067 EOR N742 ZERG WITH /FFFF 88446290
0819 00 4C180B1E BSC L G74C,+ BRANCH ON ZERO 88446300
081B 00 44000FB3 BSI L F000 AD FFFF+FFFF ACC FAILED 88446310
081D 0 310B DC /310B ERR ID 88446320
081E 00 44000FB2 G74C BSI L F00E CK LOCK ON ERROR 88446330
0820 0 7JF3 MDX A74C LOOP 88446340
0821 0 180D RTE 16 INTERCHANGE A AND Q 88446350
0822 0 F065 EOR N74A ZERO WITH /FFFF 88446360
0823 00 4C180B28 BSC L G74E,+ BRANCH ON ZERO 88446370
0825 00 44000FB3 BSI L F000 AD FFFF+FFFF Q FAILED 88446380
0827 0 310C DC /310C ERR ID 88446390
0828 00 44000FB2 G74E BSI L F00E CK LOCK ON ERROR 88446400
082A 0 70E9 MDX A74C LOOP 88446410
082B 0 C052 LD N740 CONDITION OF C AND OF 88446420
082C 0 F059 EOR N748 CHECK FOR OVERFLOW 88446430
082D 00 4C180B3B BSC L J740,+ BRANCH ON ZERO 88446440
082F 00 4C040B38 BSC L K740,E CHECK FOR CARRY 88446450
0831 00 44000FB3 BSI L F000 CARRY NOT ON 88446460
0833 0 310E DC /310E ERR ID 88446470
0834 00 44000FDE BSI L F005 CK LOCK ON ERROR 88446480
0836 0 70DD MDX A74C LOOP 88446490
0837 0 7003 MDX J740 88446500
0838 00 44000FB3 K740 BSI L F000 DVFLD ON 88446510
083A 0 310D DC /310D ERR ID 88446520
083B 00 44000FDE J740 BSI L F005 CK LOCK ON ERROR 88446530
083D 0 70D6 MDX A74C LOOP 88446540

083E 0 2000 B742 LDS 0 SET C AND OF OFF 88446560
083F 0 C84A LDD N74C LD A=/FFFF Q=/7FFF 88446570
0840 0 883F AD N742 A /FFFF /FFFF 88446580
0841 0 283C STS N740 STORE CONDITION OF C + OF 88446590
0842 0 F03D EOR N742 88446600
0843 00 4C180B48 BSC L J742,+ BRANCH ON ZERO 88446610
0845 00 44000FB3 BSI L F000 AD FFFF+FFFF A FAILED 88446620
0847 0 310F DC /310F ERR ID 88446630
0848 00 44000FB2 J742 BSI L F00E CK LOCK ON ERROR 88446640
084A 0 70F3 MDX B742 LOOP 88446650
084B 0 180D RTE 16 INTERCHANGE A AND Q 88446660
084C 0 F03C EOR N748 88446670
084D 00 4C180B52 BSC L J744,+ BRANCH ON ZERO 88446680
084F 00 44000FB3 BSI L F000 AD /7FFF+FFFF Q /FAILED 88446690
0851 0 3110 DC /3110 ERR ID 88446700
0852 00 44000FB2 J742 BSI L F00E CK LOCK ON ERROR 88446710
0854 0 70E9 MDX B742 LOOP 88446720
0855 0 C02F LD N740 LD C AND OF CONDITION 88446730
0856 0 F02F EOR N748 ZERO IF CARRY WAS ON 88446740
0857 00 4C180B5D BSC L J746,+ BRANCH ON ZERO 88446750
0859 00 4C040B62 BSC L K746,E CHECK FOR CARRY 88446760
085B 00 44000FB3 BSI L F000 CARRY NOT ON 88446770
085D 0 3112 DC /3112 ERR ID 88446780
085E 00 44000FDE BSI L F005 CK LOCK ON ERROR 88446790
0860 0 70DD MDX B742 LOOP 88446800
0861 0 7003 MDX J746 88446810
0862 00 44000FB3 K746 BSI L F000 DVFLD ON 88446820
0864 0 3111 DC /3111 ERR ID 88446830
0865 00 44000FDE J746 BSI L F005 CK LOCK ON ERROR 88446840
0867 0 70D6 MDX B742 LOOP 88446850

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

0868 0 C81B B747 LDD N746 LD A=/0000 Q=/0001 88446910
0869 0 881B AD N747 A /0001 /0001 88446920
086A 0 F01A EOR N747 ZERO ACC IF CORRECT OP 88446930
086B 00 4C180B70 BSC L J748,+ BRANCH ON ZERO 88446940

PROCESSOR-CONTROLLER FUNCTION TEST

086D 00 44000FB3 BSI L F000 AD-ODD A REG FAILED 88446950
086F 0 3113 DC /3113 ERR ID 88446960
0870 00 44000FB2 J748 BSI L F00E CK LOCK ON ERROR 88446970
0872 0 70F5 MDX B747 LOOP 88446980
0873 0 180D RTE 16 NOW A=/0002 Q=/0000 88446990
0874 0 F011 EOR N748 ZERO ACC IF CORRECT OP 88447000
0875 00 4C180B7A BSC L J74A,+ BRANCH ON ZERO 88447010
0877 00 44000FB3 BSI L F000 AD-ODD Q REG FAILED 88447020
0879 0 3114 DC /3114 ERR ID 88447030
087A 00 44000FDE J74A BSI L F005 CK LOCK ON ERROR 88447040
087C 0 70EB MDX B747 LOOP 88447050
087D 0 700E MDX A780 EXIT TO NEXT ROUTINE 88447060
087E 0 0000 N740 DC /0000 88447070
0880 0000 BSS E 88447080
0880 0 FFFF N742 DC /FFFF 88447090
0881 0 FFFF DC /FFFF 88447100
0882 0 0000 N744 DC /0000 88447110
0883 0 0000 DC /0000 88447120
0884 0 0000 N746 DC /0000 88447130
0885 0 0001 N747 DC /0001 88447140
0886 0 0002 N748 DC /0002 88447150
0887 0 0000 DC /0000 88447160
0888 0 FFFF N74A DC /FFFF 88447170
0889 0 7FFE N74R DC /7FFE 88447180
088A 0 FFFF N74C DC /FFFF 88447190
088B 0 7FFF DC /7FFF 88447200

*
* TEST SUB DOUBLE 88447210
* 88447220
* 88447230

088C 0 2000 A780 LDS 0 SET C AND OF OFF 88447250
088D 0 C868 LDD N782 LD A=/0000 Q=/0000 88447260
088E 0 9869 SD N784 S /0000 /0001 88447270
088F 0 2864 STS N780 STORE C AND OF CONDITION 88447280
0890 0 F069 EOR N786 ZERO WITH /FFFF 88447290
0891 00 4C180B96 BSC L G780,+ BRANCH ON ZERO 88447300
0893 00 44000FB3 BSI L F000 SD 0000-0000 ACC FAILED 88447310
0895 0 3115 DC /3115 ERR ID 88447320
0896 00 44000FB2 G780 BSI L F00E CK LOCK ON ERROR 88447330
0898 0 70F3 MDX A780 LOOP 88447340
0899 0 180D RTE 16 NOW A=/FFFF Q=/0000 88447350
089A 0 F05F EOR N786 ZERO WITH /FFFF 88447360
089B 00 4C180BA0 BSC L G782,+ BR ON ZERO 88447370
089D 00 44000FB3 BSI L F000 SD 0000-0001 Q FAILED 88447380
089F 0 3116 DC /3116 ERR ID 88447390
08A0 00 44000FB2 G782 BSI L F00E CK LOCK ON ERROR 88447400
08A2 0 70E9 MDX A780 LOOP 88447410
08A3 0 C050 LD N780 LD C AND OF CONDITION 88447420
08A4 0 F057 EOR N788 ZERO IF CARRY WAS ON 88447430
08A5 00 4C180BB3 BSC L G784,+ BRANCH ON ZERO 88447440
08A7 00 4C040B80 BSC L H784,E CHECK FOR CARRY 88447450
08A9 00 44000FB3 BSI L F000 CARRY NOT ON 88447460
08AB 0 3117 DC /3117 ERR ID 88447470
08AC 00 44000FDE BSI L F005 CK LOCK ON ERROR 88447480
08AE 0 70DD MDX A780 LOOP 88447490
08AF 0 7003 MDX G784 88447500
08B0 00 44000FB3 H784 BSI L F000 DVFLD ON 88447510
08B2 0 3118 DC /3118 ERR ID 88447520
08B3 00 44000FDE G784 BSI L F005 CK LOCK ON ERROR 88447530
08B5 0 70D6 MDX A780 LOOP 88447540

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

08B6 0 2000 A786 LDS 0 SET C AND OF OFF 88447550
08B7 0 C83E LDD N782 LD A=/0000 Q=/0000 88447560
08B8 0 9841 SD N786 /FFFF /FFFF 88447570

08B8 0 2000 A786 LDS 0 SET C AND OF OFF 88447580
08B7 0 C83E LDD N782 LD A=/0000 Q=/0000 88447590
08B8 0 9841 SD N786 /FFFF /FFFF 88447600

PROCESSOR-CONTROLLER FUNCTION TEST

08B9 0J 4C1808BE BSC L G786,+-- BRANCH ON ZERO 88447630
08BB 00 44000F83 BSI L F000 SD 0000-FFFF A FAILED 88447640
08BD 0 3119 DC /3119 ERR ID 88447650
08BE 0J 44000F82 G786 BSI L F00E CK LOCK ON ERROR 88447660
08C0 0 70F5 MDX A786 LOOP 88447670
08C1 0 18D0 RTE 16 NOW A=/0001 Q=/0000 88447680
08C2 0 F036 EOR N785 ZERO WITH /0001 88447690
08C3 00 4C1808C8 BSC L G788,+-- BRANCH ON ZERO 88447700
08C5 00 44000F83 BSI L F000 SD 0000-FFFF Q FAILED 88447710
08C7 0 311A DC /311A ERR ID 88447720
08C8 0 44000FDE G788 BSI L F005 CK LOCK ON ERROR 88447730
08CA 0 70EB MDX A786 LOOP 88447740
88447750
08CB 0 C832 A78A LDD N78A LD A=/0000 Q=/C000 88447760
08CC 0 982D SD N786 S /FFFF /FFF 88447770
08CD 00 4C1808D2 BSC L G78A,+-- BRANCH ON ZERO 88447780
08CF 00 44000F83 BSI L F000 SD 0000-FFFF A FAILED 88447790
08D1 0 311B DC /311B ERR ID 88447800
08D2 00 44000F82 G78A BSI L F00E CK LOCK ON ERROR 88447810
08D4 0 70F6 MDX A78A LOOP 88447820
08D5 0 18D0 RTE 16 NOW A=/C001 Q=/0000 88447830
08D6 0 F029 EOR N78D ZERO WITH /C001 88447840
08D7 00 4C1808DC BSC L G78C,+-- BRANCH ON ZERO 88447850
08D9 00 44000F83 BSI L F000 SD C000-FFFF Q FAILED 88447860
08DB 0 311C DC /311C ERR ID 88447870
08DC 00 44000FDE G78C BSI L F005 CK LOCK ON ERROR 88447880
08DE 0 70EC MDX A78A LOOP 88447890
88447900
08DF 0 C816 A78E LDD N782 LD A=/0000 Q=/0000 88447910
08E0 0 981A SD N787 S /FFFF /FFFF 88447920
08E1 0 4C1808E6 BSC L G78E,+-- BRANCH ON ZERO 88447930
08E3 00 44000F83 BSI L F000 SD-0DD A FAILED 88447940
08E5 0 311D DC /311D ERR ID 88447950
08E6 00 44000F82 G78E BSI L F00E CK LOCK ON ERROR 88447960
08E8 0 70F6 MDX A78E LOOP 88447970
08E9 0 18D0 RTE 16 NOW A=/0001 Q=/0000 88447980
08EA 0 F00E EOP N785 ZERO WITH /0001 88447990
08EB 00 4C1808F0 BSC L H780,+-- BRANCH ON ZERO 88448000
08ED 00 44000F83 BSI L F000 SD-0DD Q FAILED 88448010
08EF 0 311E DC /311E ERR ID 88448020
08F0 00 44000FDE H780 BSI L F005 CK LOCK ON ERROR 88448030
08F2 0 70EC MDX A78E LOOP 88448040
08F3 0 70DD MDX A7C0 EXIT TO NEXT ROUTINE 88448050
08F4 0 0000 N780 DC /0000 88448060
08F6 0 0000 BSS E 88448070
08F6 0 0000 N782 DC /0000 88448080
08F7 0 0000 DC /0000 88448090
08F8 0 0000 N784 DC /0000 88448100
08F9 0 0001 N785 DC /0001 88448110
08FA 0 FFFF N786 DC /FFFF 88448120
08FB 0 FFFF N787 DC /FFFF 88448130
08FC 0 0002 N788 DC /0002 88448140
08FD 0 0000 DC /0000 88448150
08FE 0 0000 N78A DC /0000 88448160
08FF 0 C000 DC /C000 88448170
0C00 0 C001 N78D DC /C001 88448180
88448190
*
* TEST OF MULTIPLY OPERATION
*

CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *DEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

0C01 0 C04F A7C0 LD N7C0 LD /5555 88448200
0C02 0 A04F M N7C1 M /2AAA 88448210
0C03 0 F04F EOR N7C2 ZERO WITH /0E38 88448220
0C04 00 4C180C09 BSC L G7C0,+-- BRANCH ON ZERO 88448230
88448240
88448250
88448260
88448270
88448280
88448290
88448300

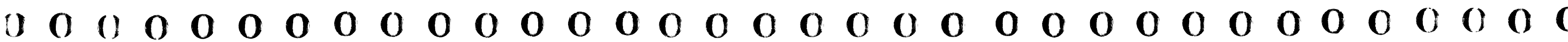
PROCESSOR-CONTROLLER FUNCTION TEST

0C06 00 44000F83 BSI L F000 M /5555X/2AAA ACC FAILED 88448310
0C08 0 311F DC /311F ERR ID 88448320
0C09 00 44000F82 G7C0 BSI L F00E CK LOCK ON ERROR 88448330
0C0B 0 70F5 MDX A7C0 LOOP 88448340
0C0C 0 18D0 RTE 16 NOW A=/9C72 Q-/0000 88448350
0C0D 0 F046 EOR N7C3 ZERO WITH /9C72 88448360
0C0E 00 4C180C13 BSC L G7C2,+-- BRANCH ON ZERO 88448370
0C10 00 44000F83 BSI L F000 MULT 5555X2AAA C FAILED 88448380
0C12 0 3120 DC /3120 ERR ID 88448390
0C13 00 44000FDE G7C2 BSI L F005 CK LOCK ON ERROR 88448400
0C15 0 70EB MDX A7C0 LOOP 88448410
88448420
0C16 0 C03E A7C4 LD N7C4 LD /FFFF 88448430
0C17 0 A03D M N7C4 M /FFFF 88448440
0C18 00 4C180C1D BSC L G7C4,+-- BRANCH ON ZERO 88448450
0C1A 00 44000F83 BSI L F000 M /FFFFX/FFFF ACC FAILED 88448460
0C1C 0 3121 DC /3121 ERR ID 88448470
0C1D 00 44000F82 G7C4 BSI L F00E CK LOCK ON ERROR 88448480
0C1F 0 70F6 MDX A7C4 LOOP 88448490
0C20 0 18D0 RTE 16 NOW A=/0001 Q=/0000 88448500
0C21 0 F034 EOR N7C5 ZERO WITH /0001 88448510
0C22 00 4C180C27 BSC L G7C6,+-- BRANCH ON ZERO 88448520
0C24 00 44000F83 BSI L F000 M /FFFFX/FFFF Q REG FAILED 88448530
0C26 0 3122 DC /3122 ERR ID 88448540
0C27 00 44000FDE G7C6 BSI L F005 CK LOCK ON ERROR 88448550
0C29 0 70EC MDX A7C4 LOOP 88448560
88448570
0C2A 0 C02C A7C8 LD N7C6 LD /0000 88448580
0C2B 0 A029 M N7C4 M /FFFF 88448590
0C2C 00 4C180C31 BSC L G7C8,+-- BRANCH ON ZERO 88448600
0C2E 00 44000F83 BSI L F000 M /FFFFX/0000 ACC FAILED 88448610
0C30 0 3123 DC /3123 ERR ID 88448620
0C31 00 44000F82 G7C8 BSI L F00E CK LOCK ON ERROR 88448630
0C33 0 70F6 MDX A7C8 LOOP 88448640
0C34 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88448650
0C35 00 4C180C3A BSC L G7CA,+-- BRANCH ON ZERO 88448660
0C37 00 44000F83 BSI L F000 M /FFFFX/0000 Q REG FAILED 88448670
0C39 0 3124 DC /3124 ERR ID 88448680
0C3A 00 44000FDE G7CA BSI L F005 CK LOCK ON ERROR 88448690
0C3C 0 70ED MDX A7C8 LOOP 88448700
88448710
0C3D 0 C017 A7C8 LD N7C4 LD /FFFF 88448720
0C3E 0 A018 M N7C6 M /0000 88448730
0C3F 00 4C180C44 BSC L G7CC,+-- BRANCH ON ZERO 88448740
0C41 00 44000F83 BSI L F000 M /0000X/FFFF ACC FAILED 88448750
0C43 0 3125 DC /3125 ERR ID 88448760
0C44 00 44000F82 G7CC BSI L F00E CK LOCK ON ERROR 88448770
0C46 0 70F6 MDX A7CC LOOP 88448780
0C47 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88448790
0C48 00 4C180C4D BSC L G7CE,+-- BRANCH ON ZERO 88448800
0C4A 00 44000F83 BSI L F000 M /0000X/FFFF Q REG FAILED 88448810
0C4C 0 3126 DC /3126 ERR ID 88448820
0C4D 00 44000FDE G7CE BSI L F005 CK LOCK ON ERROR 88448830
0C4F 0 70ED MDX A7CC LOOP 88448840
0C50 0 7007 MDX A800 EXIT TO NEXT ROUTINE 88448850
0C51 0 5555 N7C0 DC /5555 88448860
0C52 0 2AAA N7C1 DC /2AAA 88448870
0C53 0 0E38 N7C2 DC /0E38 88448880
0C54 0 9C72 N7C3 DC /9C72 88448890
0C55 0 FFFF N7C4 DC /FFFF 88448900
0C56 0 0001 N7C5 DC /0001 88448910
0C57 0 0000 N7C6 DC /0000 88448920
88448930
88448940
88448950
88448960
88448970
88448980

CORE DATA DR *LA- OPER-

TEST OF DIVIDE OPERATION

88448990



PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

```

ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS  ID+SEQ= AT RIGHT 88448990
*****
OC58 0 2000      A800  LDS      0          SET C AND OF OFF          88449010
OC59 00 CC000CF6  LOD      L N802      LD A=/400C Q=/7FFF      88449020
OC5B 00 AC000D06  D        L N812      D /8000                 88449030
OC5D 00 2C000CF5  STS      L N800      STORE C AND OF CONDITION 88449040
OC5F 00 F4000D06  EOR      L N812      ZERO WITH /8000        88449050
OC61 00 4C180C66  BSC      L G800,+--  BR ON ZERO              88449060
OC63 00 44000FB3  BSI      L F000      DVD-A-REG INCORRECT    88449070
OC65 0 3127      DC        /3127      ERR ID                   88449080
OC66 00 44000FB2  G800  BSI      L F00E      CK LOCK ON ERROR      88449090
OC68 0 70EF      MDX      A800      LOOP                     88449100
OC69 0 18D0      RTE      16         NOW A=/7FFF Q=/0000    88449110
OC6A 00 F4000D05  EOR      L N811      ZERO WITH /7FFF        88449120
OC6C 00 4C180C71  BSC      L G802,+--  BR ON ZERO              88449130
OC6E 00 44000FB3  BSI      L F000      DVD-Q REG INCORRECT    88449140
OC70 0 3128      DC        /3128      ERR ID                   88449150
OC71 00 44000FB2  G802  BSI      L F00E      CK LOCK ON ERROR      88449160
OC73 0 70E4      MDX      A800      LOOP                     88449170
OC74 00 C4000CF5  LD        L N800      LD /0000                88449180
OC76 00 4C180C84  BSC      L G804,+--  BR ON ZERO              88449190
OC78 00 4C040C81  BSC      L H804,E    BR ON NOT EVEN         88449200
OC7A 00 44000FB3  BSI      L F000      CARRY ON                88449210
OC7C 0 3129      DC        /3129      ERR ID                   88449220
OC7D 00 44000FDE  BSI      L F005      CK LOCK ON ERROR      88449230
OC7F 0 70D8      MDX      A800      LOOP                     88449240
OC80 0 7006      MDX      A806      EXIT TO NEXT ROUTINE   88449250
OC81 00 44000FB3  H804  BSI      L F000      OVFLD ON                88449260
OC83 0 312A      DC        /312A      ERR ID                   88449270
OC84 00 44000FDE  G804  BSI      L F005      CK LOCK ON ERROR      88449280
OC86 0 70D1      MDX      A800      LOOP                     88449290
*****
OC87 0 C870      A806  LDD      N804      LD A=/1C71 Q=/BBE3     88449310
OC88 00 AC000D07  D        L N813      D /5555                 88449320
OC8A 0 286A      STS      N800      STORE C AND OF CONDITION 88449330
OC8B 00 F4000D07  EOR      L N813      ZERO WITH /5555        88449340
OC8D 00 4C180C92  BSC      L G806,+--  BR ON ZERO              88449350
OC8F 00 44000FB3  BSI      L F000      DVD-A REG INCORRECT    88449360
OC91 0 3128      DC        /3128      ERR ID                   88449370
OC92 00 44000FB2  G806  BSI      L F00E      CK LOCK ON ERROR      88449380
OC94 0 70F2      MDX      A806      LOOP                     88449390
OC95 0 18D0      RTE      16         NOW A=/BBE3 Q=/0000    88449400
OC96 00 F4000D08  EOR      L N816      ZERO WITH /2DAA        88449410
OC98 00 4C180C9D  BSC      L G808,+--  BR ON ZERO              88449420
OC9A 00 44000FB3  BSI      L F000      DVD-Q REG INCORRECT    88449430
OC9C 0 312C      DC        /312C      ERR ID                   88449440
OC9D 00 44000FB2  G808  BSI      L F00E      CK LOCK ON ERROR      88449450
OC9F 0 70E7      MDX      A806      LOOP                     88449460
OCA0 0 C054      LD        N800      LD C AND OF CONDITION   88449470
OCA1 00 4C180CAF  BSC      L G80A,+--  BR ON ZERO              88449480
OCA3 00 4C040C83  BSC      L H80A,E    BR IF NOT EVEN         88449490
OCA5 00 44000FB3  BSI      L F000      CARRY ON                88449500
OCA7 0 312D      DC        /312D      ERR ID                   88449510
OCA8 00 44000FDE  BSI      L F005      CK LOCK ON ERROR      88449520
OCAA 0 70DC      MDX      A806      LOOP                     88449530
OCAB 0 7006      MDX      A80C      EXIT TO NEXT ROUTINE   88449540
OCAC 00 44000FB3  H80A  BSI      L F000      OVFLD CN                88449550
OCAE 0 312E      DC        /312E      ERR ID                   88449560
OCAF 00 44000FDE  G80A  BSI      L F005      CK LOCK ON ERROR      88449570
OCB1 0 70D5      MDX      A806      LOOP                     88449580
*****
CORE  DATA OR *LA- OPER- 88449600
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS  ID+SEQ= AT RIGHT 88449610
*****
OCB2 0 2000      A80C  LDS      0          SET C AND OF OFF          88449640
OCB3 0 C846      LOD      N806      SET A=/0000 Q=/0001    88449650
OCB4 0 A84D      D        N80E      D /0000                 88449660

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OCB5 00 4C010CBA  BSC      L G80C,D    BRANCH CN OVERFLOW     88449670
OCB7 00 44000FB3  BSI      L F000      DVD BY 0-OVRFLW CFF    88449680
OCB9 0 312F      DC        /312F      ERR ID                   88449690
OCBA 00 44000FDE  G80C  BSI      L F005      CK LOCK ON ERROR      88449700
OCBC 0 70F5      MDX      A80C      LOOP                     88449710
*****
OCBD 0 2000      A80E  LDS      0          SET C AND OF OFF          88449720
OCBE 0 C83D      LDD      N808      LD A=/4000 Q=/0000    88449730
OCBF 0 A83B      D        N807      D /0001                 88449740
OCC0 00 4C010CC5  BSC      L G80E,D    BRANCH ON OVERFLOW     88449760
OCC2 00 44000FB3  BSI      L F000      DVD-BY 1-OVRFLW CFF    88449770
OCC4 0 3130      DC        /3130      ERR ID                   88449780
OCC5 00 44000FDE  G80E  BSI      L F005      CK LCKK ON ERROR      88449790
OCC7 0 70F5      MDX      A80E      LOOP                     88449800
*****
OCC8 0 2000      B800  LDS      0          SET C AND OF OFF          88449810
OCC9 0 C834      LDD      N80A      LD A=/A000 Q=/0000    88449820
OCCA 0 A831      D        N808      D /4000                 88449830
OCCB 00 4C010CDB  BSC      L J800,D    BRANCH CN OVERFLOW     88449840
OCCD 00 44000FB3  BSI      L F000      DVD/4000-OVRFLW CFF    88449850
OCCF 0 3131      DC        /3131      ERR ID                   88449860
OCD0 00 44000FDE  J800  BSI      L F005      CK LCKK ON ERROR      88449870
OCD2 0 70F5      MDX      B800      LOOP                     88449880
*****
OCD3 0 2000      B802  LDS      0          SET C AND OF OFF          88449900
OCD4 0 C82B      LDD      N80C      LD A=/C000 Q=/0000    88449910
OCD5 0 A830      D        N812      D /8000                 88449920
OCD6 00 4C010CDB  BSC      L J802,D    BR ON OF                88449930
OCD8 00 44000FB3  BSI      L F000      DVD/8000-OVRFLW OFF    88449940
OCD8 0 3132      DC        /3132      ERR ID                   88449950
OCD8 0 3132      DC        /3132      ERR ID                   88449960
OCD8 00 44000FDE  J802  BSI      L F005      CHECK LCKP SWITCH      88449970
OCD0 0 70F5      MDX      B802      LOOP                     88449980
*****
OCDE 0 2000      B804  LDS      0          SET C AND OF OFF          88450000
OCDF 0 C822      LDD      N80E      LD A=/0000 Q=/FFFF    88450010
OCE0 0 A81A      D        N807      D /0001                 88450020
OCE1 00 4C010CE6  BSC      L J804,D    BR ON OF                88450030
OCE3 00 44000FB3  BSI      L F000      DVD/0001-OVRFLW OFF    88450040
OCE5 0 3133      DC        /3133      ERR ID                   88450050
OCE6 00 44000FDE  J804  BSI      L F005      CK LOCK ON ERROR      88450060
OCE8 0 70F5      MDX      B804      LOOP                     88450070
*****
OCE9 0 2000      B806  LDS      0          SET C AND OF OFF          88450080
OCEA 0 C819      LDD      N810      LD A=/FFFF Q=/7FFF    88450090
OCEB 0 A80F      D        N807      D /0001                 88450100
OCEC 00 4C010CF1  BSC      L J806,D    BR ON OF                88450110
OCEE 00 44000FB3  BSI      L F000      DVD/0001-OVRFLW CFF    88450120
OCF0 0 3134      DC        /3134      ERR ID                   88450130
OCF1 00 44000FDE  J806  BSI      L F005      CK LCKK ON ERROR      88450140
OCF3 0 70F5      MDX      B806      LOOP                     88450150
OCF4 0 7023      MDX      B807      EXIT TO NEXT ROUTINE   88450160
OCF5 0 0000      N800  DC        /0000          88450170
OCF6 0 0000      BSS   E          88450180
OCF6 0 4000      N802  DC        /4000          88450190
OCF7 0 7FFF      DC        /7FFF          88450200
OCF8 0 1C71      N804  DC        /1C71          88450210
OCF9 0 BBE3      DC        /BBE3          88450220
OCFA 0 0000      N806  DC        /0000          88450230
OCFB 0 0001      N807  DC        /0001          88450240
OCFC 0 4000      N808  DC        /4000          88450250
OCFD 0 0000      DC        /0000          88450260
OCFE 0 A000      N80A  DC        /A000          88450270
OCFF 0 0000      DC        /0000          88450280
OD00 0 C000      N80C  DC        /C000          88450290
OD01 0 0000      DC        /0000          88450300
OD02 0 0000      N80E  DC        /0000          88450310
OD03 0 FFFF      N80F  DC        /FFFF          88450320
OD04 0 FFFF      N810  DC        /FFFF          88450330

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PROCESSOR-CONTROLLER FUNCTION TEST

0005 0	7FFF	N811 DC	/7FFF	88450350
0006 0	8000	N812 DC	/8000	88450360
0007 0	5555	N813 DC	/5555	88450370
0008 0	20AA	N816 DC	/20AA	88450380
0009 0	C000	N817 DC	/C000	88450390
000A 0	6100	N818 DC	/6100	88450400
000B 0	0000	DC	/0000	88450410
000C 0	8000	N819 DC	/8000	88450420
000D 0	0000	DC	/0000	88450430
000E 0	0002	N820 DC	/0002	88450440
000F 0	0000	N821 DC	0	88450450
0010 0	2001	DC	/2001	88450460
0011 0	4000	DC	/4000	88450470
0012 0	C000	DC	/C000	88450480
0013 0	8000	N822 DC	/8000	88450490
0014 0	FFFF	N823 DC	/FFFF	88450500
0015 0	FFFF	DC	/FFFF	88450510
0016 0	0000	BSS E	0	88450520
0016 0	0000	N824 DC	0	88450530
0017 0	0000	DC	0	88450540

```

*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
0018 0 2000 B807 LDS 0 SET C AND OF OFF 88450600
0019 0 C8F0 LDD N818 LD A=/6100 Q=/0000 88450610
001A 0 A8EE D N817 D /C000 88450620
001B 00 4C010D20 BSC L J808,C BR ON OF 88450630
001D 00 4400F83 BSI L F000 OVERFLOW OFF 88450640
001F 0 316A DC /316A ERR ID 88450650
0020 00 4400CFDE J808 BSI L F005 CK LOCK ON ERROR 88450660
0022 0 70F5 MDX B807 LOOP 88450670
*****
0023 0 2000 B808 LDS 0 SET C AND OF OFF 88450680
0024 0 C8E7 LDD N819 LD A=/8000 Q=/0000 88450690
0025 0 A80D D N80F D /FFFF 88450710
0026 00 4C010D2B BSC L J809,C BR ON OF 88450720
0028 00 4400F83 BSI L F000 OVERFLOW OFF 88450730
002A 0 316B DC /316B ERR ID 88450740
002B 00 4400CFDE J809 BSI L F005 CK LOCK ON ERROR 88450750
002D 0 70F5 MDX B808 LOOP 88450760
*****
002E 0 2000 B809 LDS 0 SET C AND OF OFF 88450770
002F 0 C8E4 LDD N823 LD A=/FFFF Q=/FFFF 88450780
0030 0 A80D D N820 D /0002 88450790
0031 00 4C010D34 BSC L J815,C BR ON OF 88450800
0033 0 7003 MDX J810 OVERFLOW OFF 88450810
0034 00 4400F83 J815 BSI L F000 88450820
0036 0 316C DC /316C ERR ID 88450830
0037 00 4400CFDE J810 BSI L F005 CK LOCK ON ERROR 88450840
0039 0 70F4 MDX B809 LOOP 88450860
*****

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*****
MULTIPLY-DIV TEST (B810)
*****
THIS TEST TAKES 4 NUMBERS
/8000, /C000, /4000 AND
/2001 AND MULTIPLIES AND
DIVIDES THE RESULT OF THE
MULTIPLICATION BY ALL
VALUES OF NEGATIVE AND
POSITIVE NUMBERS. THIS
PROCEDURE IS REPEATED
UNTIL ALL FOUR NUMBERS

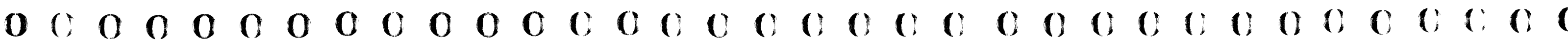
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PROCESSOR-CONTROLLER FUNCTION TEST

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* HAVE BEEN USED. 88451030
* 88451040
* STEP1 SET MULTIPLICAND AND 88451050
* DIVISOR TO LARGEST NEG. 88451060
* NUMBER. 88451070
* STEP2 TAKE ONE OF FOUR NUMBERS 88451080
* AND USE IT AS THE 88451090
* MULTIPLIER 88451100
* STEP3 MULTIPLY 88451110
* STEP4 STORE RESULTS IN SYMBOLIC 88451120
* LOCATION N824 88451130
* STEP5 DIVIDE 88451140
* STEP6 CHECK RESULT 88451150
* STEP7 INCREMENT MULTIPLICAND 88451160
* AND DIVISOR BY 1. 88451170
* STEP8 GO TO STEP 2 IF ALL 88451180
* VALUES HAVE NOT BEEN 88451190
* USED AS MULTIPLICANDS AND 88451200
* DIVISORS. 88451210
* STEP9 SET UP FOR NEXT ONE OF 4 88451220
* MULTIPLIERS. 88451230
* STEP10 GO TO STEP 2 IF ALL 4 88451240
* NUMBERS HAVE NOT BEEN 88451250
* USED. 88451260
* 88451270
* 88451280
* 88451290
* NOTE -- THREE WORD LOCATIONS ARE AVAILABLE FOR 88451300
* MANUAL INSERTION OF ANY VALUE DESIRED. 88451310
* THEY ARE AT LABEL ADDRESS N821+1, N821+2 88451320
* AND N821+3. 88451330
* 88451340
* CAUTION ** DO NOT CHANGE THE WORD AT LABEL 88451350
* LOCATION N822 (/8000). 88451360
* 88451370
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
003A 0 6104 DX 1 4 LD XR 1 WITH /0C04 88451420
003B 00 0C000FEC J814 X10 L F003 CK BYPASS MPY/DIV SW 88451430
003D 00 C4000FF0 LD L Z000 LD SWITCH SETTINGS 88451440
003F 0 1808 SRA 8 SHIFT BIT 7 TO BIT POS 15 88451450
0040 0 4804 BSC E SK IF BIT 15=0 88451460
0041 0 7028 MDX A840 SW BIT 6 ON (BYPASS) 88451470
0042 0 C0D0 LD N822 CONST /8000 88451480
0043 0 D0CB STO N821 STORE /8000 AT N821 88451490
0044 0 C0CA LD N821 LD C(N821) /8000 88451500
0045 00 A5000D0F M LI N821 88451510
0047 0 D8CE STD N824 STORE A AND Q 88451520
0048 0 2000 LDS 0 SET C AND OF OFF 88451530
0049 0 A8C5 D N821 D /8000 88451540
004A 00 F5000D0F EOR LI N821 ZERO WITH /8000 88451550
004C 00 4C180D51 BSC L J812,+ BRANCH ON ZERO 88451560
004E 00 4400F83 BSI L F000 ACC NOT ZERO 88451570
0050 0 316D DC /316D ERR ID 88451580
0051 00 4400FB2 J812 BSI L F00E CK LOCK ON ERROR 88451590
0053 0 70F0 MDX J811 LOOP ON MPL/DIV 88451600
0054 0 18D0 RTE 16 NOW A=/0000 Q=/0000 88451610
0055 00 4C180D5A BSC L J813,+ BRANCH ON ZERO 88451620
0057 00 4400F83 BSI L F000 REMAINDER IN Q REG 88451630
0059 0 316E DC /316E ERR ID 88451640
005A 00 4400FB2 J813 BSI L F00E CK LOCK ON ERROR 88451650
005C 0 70E7 MDX J811 LOOP ON MPL/DIV 88451660
005D 0 C0B1 J816 LD N821 LD /8000 88451670
005E 0 809C A N807 ADD ONE 88451680
005F 0 D0AF STO N821 88451690
0060 00 4C180D5D BSC L J816,+ BRANCH ON ZERO 88451700

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PROCESSOR-CONTROLLER FUNCTION TEST

0062 0 F080 EOR N822 88451710
0063 00 4C200D44 BSC L J811,Z BR IF NOT ZERO 88451720
0065 0 71FF MDX 1 -1 88451730
0066 0 7004 MDX J814 LOOP TO CK SWITCHES 88451740
0067 00 44000FDE BSI L F005 CK LOCK ON ERROR 88451750
0069 0 7000 MDX 8810 LOOP 88451760

TEST OF MDX OPERATION

006A 0 6100 AB40 LDX 1 0 LD XR 1 WITH ZERO 88451770
006B 0 71FF MDX 1 -1 SK IF SIGN CHANGES 88451780
006C 0 3000 WAIT MDX FAILED TO SKIP 88451790
006D 0 696E STX 1 N840 STORE C(XR 1) AT N840 88451800
006E 0 C060 LD N840 LD VALUE OF XR 1 88451810
006F 0 F060 EDR N841 ZERO ACC WITH /FFFF 88451820
0070 00 4C180D75 BSC L G840,+ BRANCH ON ZERO 88451830
0072 00 44000F83 JSI L F000 MDX XR 1 FAILED 88451840
0074 0 3135 DC /3135 ERR ID 88451850
0075 00 44000FDE G840 BSI L F005 CK LOCK ON ERROR 88451860
0077 0 70F2 MDX AB40 LOOP 88451870

0078 0 C069 AB42 LD N845 LD WITH ADDR OF 88451880
* * LABEL N844 88451890
MDX L N842,+ BR TO LABEL ADDR N842 +1 88451900
EOR N845 88451910
BSC L N842,+ BRANCH ON ZERO 88451920
BSI L F000 ACC DESTROYED AFTER MDX 88451930
DC /316F ERR ID 88451940
LD N842 LD A=/3000 88451950
EOR N846 ACC NOW /0001 88451960
BSC L G842,+ BRANCH ON ZERO 88451970
BSI L F000 ADD TO MEM FAILED 88451980
DC /3136 ERR ID 88451990
LD N843 LD /3000 88452000
STC N842 88452010
BSI L F005 CK LOCK ON ERROR 88452020
MDX AB42 LOOP 88452030

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

0080 00 6600FFFE AB44 LDX L2 -2 LD XR 2 WITH -2 88452040
0081 00 76000001 MDX L2 1 ADD ONE TO XR 2 88452050
0091 0 6A4A STX 2 N840 STORE XR 2 88452060
0092 0 C049 LD N840 LD WITH XR 2 VALUE 88452070
0093 0 F049 FOR N841 ZERO ACC WITH /FFFF 88452080
0094 00 4C180D99 BSC L G844,+ BRANCH ON ZERO 88452090
0096 00 44000F83 BSI L F000 MDX LONG XR 2 FAILED 88452100
0098 0 3137 DC /3137 ERR ID 88452110
0099 00 44000FDE G844 BSI L F005 CK LOCK ON ERROR 88452120
0098 0 70F1 MDX AB44 LOOP 88452130

009C 0 63FF AB46 LDX 3 -1 LD XR 3 WITH -1 88452140
009D 0 7301 MDX 3 1 ADD ONE TO XR 3 88452150
009E 0 7001 MDX G846 DID NOT SK ON MDX 88452160
009F 0 7003 MDX H846 88452170
00A0 00 44000F83 G846 BSI L F000 XR 3 NO SKIP AT 0 88452180
00A2 0 3138 DC /3138 ERR ID 88452190
00A3 00 44000FDE G846 BSI L F005 CK LOCK ON ERROR 88452200
00A5 0 70F6 MDX AB46 LOOP 88452210

00A6 0 61FF AB48 LDX 1 -1 LD XR 1 WITH -1 88452220
00A7 0 7104 MDX 1 4 ADD 4 TO XR 1 88452230
00A8 0 7001 MDX G848 DID NOT SK ON MDX 88452240
00A9 0 7003 MDX H848 88452250

PROCESSOR-CONTROLLER FUNCTION TEST

00AA 00 44000F83 G848 BSI L F000 SIGN CHANGE-NO SKIP 88452390
00AC 0 3139 DC /3139 ERR ID 88452400
00AD 00 44000FDE H848 BSI L F005 CK LOCK ON ERROR 88452410
00AF 0 70F6 MDX AB48 LOOP 88452420

00B0 00 6500FFFE AB49 LDX LI -2 LD XR 1 WITH -2 88452430
00B2 0 C0FF H849 LD H849 88452440
00B3 00 75800DE2 MDX LI N845 88452450
00B5 0 6926 STX 1 N840 STORE C(XR 1) AT N840 88452460
00B6 0 F0F9 EOR H849 88452470
00B7 00 4C180D8C BSC L K849,+ BRANCH ON ZERO 88452480
00B9 00 44000F83 BSI L F000 ACC GONE AFTER MDX INDEXED 88452490
00C0 0 3168 DC /3168 ERR ID 88452500
00C0 0 C01F K849 LD N840 LD VALUE OF XR 1 AFTER * MDX OP 88452510
00C0 0 F01F EOR N841 ZERO ACC WITH /FFFF 88452520
00C0 00 4C180D83 BSC L G849,+ BRANCH ON ZERO 88452530
00C0 00 44000F83 BSI L F000 INDIRECT MDX FAILED 88452540
00C2 0 313A DC /313A ERR ID 88452550
00C3 00 44000FDE G849 BSI L F005 CK LOCK ON ERROR 88452560
00C5 0 70EA MDX AB49 LOOP 88452570

00C6 00 7400CDDA AB4A MDX J N84A,C TEST SKIP IF ZERO 88452580
00C8 0 7001 MDX G84A BYPASS IF CORRECT OP 88452590
00C9 0 7003 MDX H84A 88452600
00CA 00 44000F83 G84A BSI L F000 MDX L FAILED TO SKIP 88452610
00CC 0 3171 DC /3171 ERR ID 88452620
00CD 00 44000FDE H84A BSI L F005 CK LOCK ON ERROR 88452630
00CF 0 70F6 MDX AB4A LOOP 88452640

00D0 00 7400CDD8 AB5A MDX L N85A,C TEST NON SKIP 88452650
00D2 0 7003 MDX H85A 88452660
00D3 00 44000F83 BSI L F000 MDX L SKIPPED 88452670
00D5 0 3172 DC /3172 ERR ID 88452680
00D6 00 44000FDE H85A BSI L F005 CK LOCK ON ERROR 88452690
00D8 0 70F7 MDX AB5A LOOP 88452700
00D9 0 700A MDX AB80 EXIT TO NEXT ROUTINE 88452710
00DA 0 0000 N84A DC 0 CONSTANT ZERO 88452720
00DB 0 0001 N85A DC 1 NON ZERO CONSTANT 88452730

CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT

00DC 0 0000 N840 DC /0000 88452740
00DD 0 FFFF N841 DC /FFFF 88452750
00DE 0 3000 N842 WAIT ADD TO MEM FAILED 88452760
00DF 0 3000 WAIT ADD TO MEM FAILED 88452770
00E0 0 3000 N843 WAIT ADD TO MEM FAILED 88452780
00E1 0 0001 N844 DC /0001 88452790
00E2 0 0DE1 N845 DC N844 88452800
00E3 0 3001 N846 DC /3001 88452810

TEST OF SLC OPERATION

00E4 0 610A AB80 LDX 1 10 LD XR 1 WITH +10 88452820
00E5 00 C0000EDE LDD L N882 LD A=/0000 Q=/FFFF 88452830
00E7 0 2002 LDS 2 SET C ON 88452840
00E8 0 1140 SLCA 1 0 NOW A=/0000 Q=/FFFF 88452850
00E9 00 6D000EDC STX LI N880 STORE C(XR 1) 88452860
00EB 0 2812 STS G881 STORE CARRY CONDITION 88452870
00EC 00 4C180DF1 BSC L G880,+ BRANCH ON ZERO 88452880
00EE 00 44000F83 BSI L F000 ACC NOT=ZERO 88452890
00FO 0 3138 DC /3138 ERR ID 88452900
00F1 00 44000F82 G880 BSI L F005 CK LOCK ON ERROR 88452910
00F3 0 70F0 MDX AB80 LOOP 88452920
00F4 00 C4000EDC LD L N880 LD PREVIOUS C(XR 1) 88452930

PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

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UDF6 00 4C18UDFB      BSC L G882,+--  BRANCH ON ZERO      88453070
UDF8 00 4400DFB3      BSI L F000        XR 1 NOT=ZERO      88453080
UDFA 0 313C           DC /313C          ERR ID              88453090
UDFB 00 4400DFB2      G882 BSI L F00E    CK LOCK ON ERROR   88453100
UDFD 0 70E6           MDX A880          LOOP                88453110
UDFE 0 2000           G881 LDS G         SAVED BY STS ABOVE  88453120
UDFF 0 4802           BSC C             SK IF CARRY OFF    88453130
UE00 0 7004           MDX G883         CARRY ON           88453140
UE01 00 4400DFDE      BSI L F005        CK LOCK ON ERROR   88453150
UE03 0 70E0           MDX A880          LOOP                88453160
UE04 0 7006           MDX A884          EXIT TO NEXT ROUTINE 88453170
UE05 00 4400DFB3      G883 BSI L F000    CARRY ON (SHOULD NOT BE) 88453180
UE07 0 3160           DC /3160         ERR ID              88453190
UE08 00 4400DFDE      BSI L F005        CK LOCK ON ERROR   88453200
UE0A 0 70D9           MDX A880          LOOP                88453210
*****
UE0B 00 6580DFE3      A884 LDX I1 N887  LD XR 1 WITH /FFD0  88453220
UE0D 00 CC00DF00      LDD L N886        LD A=/0001 Q=/0010 88453240
UE0F 0 2000           LDS 0             SET C AND OF OFF   88453250
UE10 0 1140           >LCA 1 0         ACC NOW /8000      88453260
UE11 0 2818           STS G885         STORE C AND OF CONDITION 88453270
UE12 00 F400DFE2      EOR L N886        ZERO WITH /8000    88453280
UE14 00 4C18DFE19     BSC L G884,+--   BRANCH ON ZERO     88453290
UE16 00 4400DFB3      BSI L F000        ACC NOT=/8000      88453300
UE18 0 313D           DC /313D         ERR ID              88453310
UE19 00 4400DFB2      G884 BSI L F00E    CHECK LOOP SWITCH  88453320
UE1B 0 70E1           MDX A884          LOOP                88453330
UE1C 00 6D00DFDC      STX L1 N880      STORE C(XR 1) AT N880 88453340
UE1E 00 C400DFDC      LD L N880         LD C(N880)          88453350
UE20 00 F400DFEA      EOR L N88E        ZERO WITH /FF01    88453360
UE22 00 4C18DFE27     BSC L G886,+--   BRANCH ON ZERO     88453370
UE24 00 4400DFB3      BSI L F000        XR-1 NOT FF01      88453380
UE26 0 313E           DC /313E         ERR ID              88453390
UE27 00 4400DFB2      G886 BSI L F00E    CK LOCK ON ERROR   88453400
UE29 0 70E1           MDX A884          LOOP                88453410
UE2A 0 2000           G885 LDS 0         SAVED BY STS ABOVE 88453420
UE2B 0 4802           BSC C             SK IF CARRY OFF    88453430
UE2C 0 7003           MDX G887         CARRY ON           88453440
UE2D 00 4C00DFB3      BSC L F000        CARRY OFF (SHOULD BE ON) 88453450
UE2F 0 3161           DC /3161         ERR ID              88453460
UE30 00 4400DFDE      G887 BSI L F005    CK LOCK ON ERROR   88453470
UE32 0 70D8           MDX A884          LOOP                88453480
*****
CORE DATA UR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
UE33 00 6580DFE1      A884 LDX I1 N885  LD XR 1 WITH /0010  88453500
UE35 00 CC00DFE2      LDD L N886        LD A=/8000 Q=/FFD0 88453510
UE37 0 1140           >LCA 1 0         ACC NOW /8000      88453520
UE38 00 F400DFE2      EOR L N886        ZERO WITH /8000    88453530
UE3A 00 4C18DFE3F     BSC L G888,+--   BRANCH ON ZERO     88453540
UE3C 00 4400DFB3      BSI L F000        ACC NOT=8000      88453550
UE3E 0 313F           DC /313F         ERR ID              88453560
UE3F 00 4400DFB2      G888 BSI L F00E    CK LOCK ON ERROR   88453570
UE41 0 70F1           MDX A888          LOOP                88453580
UE42 00 6D00DFDC      STX L1 N880      STORE C(XR 1) IN N880 88453590
UE44 00 C400DFDC      LD L N880         LD C(N880)          88453600
UE46 00 F400DFE1      EOR L N885        ZERO WITH /0010    88453610
UE48 00 4C18DFE4D     BSC L G88A,+--   BRANCH ON ZERO     88453620
UE4A 00 4400DFB3      BSI L F000        XR 1 NOT=0010      88453630
UE4C 0 3140           DC /3140         ERR ID              88453640
UE4D 00 4400DFDE      G88A BSI L F005    CK LOCK ON ERROR   88453650
UE4F 0 70F3           MDX A888          LOOP                88453660
*****
UE50 0 6110           A889 LDX 1 16     LD XR 1 WITH /0010  88453700
UE51 0 6210           LDX 2 16         LD XR 2 WITH /0010 88453710
UE52 0 6310           LDX 3 16         LD XR 3 WITH /0010 88453720

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OE53 00 C400DFE0      LD L N884         LD A=/0001          88453750
OE55 0 1041           SLCA 1           ACC NOW /0002      88453760
OE56 00 F400DFE7      EOR L N888        ZERO WITH /0002    88453770
OE58 00 4C18DFE5D     BSC L G889,+--   BRANCH ON ZERO     88453780
OE5A 00 4400DFB3      BSI L F000        NON INDEXED SLCA FAILED 88453790
OE5C 0 3162           DC /3162         ERR ID              88453800
OE5D 00 4400DFDE      G889 BSI L F005    CK LOCK ON ERROR   88453810
OE5F 0 70F0           MDX A889          LOOP                88453820
*****
OE60 0 6110           A88A LDX 1 16     LD XR 1 WITH /0010  88453830
OE61 0 6210           LDX 2 16         LD XR 2 WITH /0010 88453840
OE62 0 6310           LDX 3 16         LD XR 3 WITH /0010 88453850
OE63 00 CC00DFDE      LDD L N882        LD A=/0000 Q=/FFFF 88453860
OE65 0 10CF           SLC 15          NOW A-/7FFF Q=/1000 88453870
OE66 00 F400DFEB      EOR L N88F        ZERO WITH /7FFF    88453880
OE68 00 4C18DFE6B     BSC L G885,+--   NON INDEXED SLC FAILED 88453890
OE6A 0 3173           DC /3173         ERR ID              88453900
OE6B 00 4400DFDE      G88B BSI L F005    CK LOCK ON ERROR   88453910
OE6D 0 70F2           MDX A88A          LOOP                88453920
*****
OE6E 00 6580DFE8      A88C LDX I1 N88C  LD XR 1 WITH /0020  88453930
OE70 0 C873           LDD N88E        LD A=/0000 Q=/0000 88453940
OE71 0 11C0           SLC 1 0         ACC NOW A=/0000 Q=/0000 88453950
OE72 00 4C18DFE77     BSC L G88C,+--   BRANCH ON ZERO     88453960
OE74 00 4400DFB3      BSI L F000        ACC NOT=0000      88453970
OE76 0 3141           DC /3141         ERR ID              88453980
OE77 00 4400DFB2      G88C BSI L F00E    CK LOCK ON ERROR   88453990
OE79 0 70F4           MDX A88C          LOOP                88454000
OE7A 0 18D0           RTE 16          ACC NOW A=/0000 Q=/0000 88454010
OE7B 00 4C18DFE80     BSC L G88F,+--   BRANCH ON ZERO     88454020
OE7D 00 4400DFB3      BSI L F000        Q REG NCT=0000    88454030
OE7F 0 3142           DC /3142         ERR ID              88454040
OE80 00 4400DFB2      G88E BSI L F00E    CK LOCK ON ERROR   88454050
OE82 0 70EB           MDX A88C          LOOP                88454060
OE83 0 6958           STX 1 N880      STORE C(XR 1) IN N880 88454070
OE84 0 C057           LD N880         LD C(N880)          88454080
OE85 00 4C18DFE8A     BSC L J880,+--   BRANCH ON ZERO     88454090
OE87 00 4400DFB3      BSI L F000        XR 1 NOT=0000      88454100
OE89 0 3143           DC /3143         ERR ID              88454110
OE8A 00 4400DFDE      J880 BSI L F005    CK LOCK ON ERROR   88454120
OE8C 0 70E1           MDX A88C          LOOP                88454130
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OE8D 00 6580DFE9      B882 LDX I1 N88D  LD XR 1 WITH /FFDF  88454140
OE8F 0 C856           LDD N88A        LD A=/0000 Q=/0002 88454150
OE90 0 11C0           SLC 1 0         NOW A=/0000 Q=/0000 88454160
OE91 0 F050           EOR N886        ZERO WITH /8000    88454170
OE92 00 4C18DFE97     BSC L J882,+--   BRANCH ON ZERO     88454180
OE94 00 4400DFB3      BSI L F000        ACC NOT=/8000      88454190
OE96 0 3144           DC /3144         ERR ID              88454200
OE97 00 4400DFB2      J882 BSI L F00E    CK LOCK ON ERROR   88454210
OE99 0 70F3           MDX B882          LOOP                88454220
OE9A 0 18D0           RTE 16          NOW A=/0000 Q=/8000 88454230
OE9B 00 4C18DFEA0     BSC L J884,+--   BRANCH ON ZERO     88454240
OE9D 00 4400DFB3      BSI L F000        Q REG NOT=0000    88454250
OE9F 0 3145           DC /3145         ERR ID              88454260
OE40 00 4400DFB2      J884 BSI L F00E    CK LOCK ON ERROR   88454270
OE42 0 70EA           MDX B882          LOOP                88454280
OE43 0 6938           STX 1 N880      STORE C(XR 1) AT N880 88454290
OE44 0 C037           LD N880         LD C(N880)          88454300
OE45 00 F400DFEA      EOR L N88E        ZERO WITH /FF01    88454310
OE47 00 4C18DFEAC     BSC L J886,+--   BRANCH ON ZERO     88454320
OE49 00 4400DFB3      BSI L F000        XR-1 NOT FF01      88454330
OE4B 0 3146           DC /3146         ERR ID              88454340

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PROCESSOR-CONTROLLER FUNCTION TEST

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OFAC 0J 44000FDE  J886 BSI L F005      CK LOCK ON ERROR      88454430
OEAE 0 70DE      MDX  B882      LOOP                    88454440
*****
OEAF 0 C836      B8R4 LDD  N88A      LD A=/0000 Q=/0002    88454450
OE80 0 611F      LDX  1 31      LD XR 1 WITH /001F    88454460
OE81 0 1100      SLC  1 0      NOW A=/8000 Q=/0000 88454470
OE82 0 4802      BSC  C        SK IF CARRY OFF     88454480
OE83 0 7003      MDX  J887      CARRY ON              88454490
OE84 0J 44000F83 BSI L F000      CARRY NOT ON         88454500
OF86 0 3147      DC   /3147     ERR ID                88454510
OE87 0J 44000F82 J887 BSI L F00E      CK LOCK ON ERROR     88454520
OE89 0 70F5      MDX  B884      LOOP                    88454530
OE8A 0 F027      EOR  N886      ZERC WITH /8000     88454540
OE8B 0J 4C180ECC BSC L J888,+   BRANCH ON ZERO      88454550
OE8D 0J 44000F83 BSI L F000      ACC NOT EQUAL 8000  88454560
OE8E 0 3148      DC   /3148     ERR ID                88454570
OE8F 0J 44000F82 J888 BSI L F00E      CK LOCK ON ERROR     88454580
OEC0 0 70EC      MDX  B884      LOOP                    88454590
OEC1 0J 6D000EDC STX  LI N880    STORE XR 1 WITH C(N880) 88454600
OEC2 0 C016      LD   N880      LD C(N880)           88454610
OEC3 0 F019      EOR  N884      ZERO WITH /0001     88454620
OEC4 0J 4C180ECC BSC L J889,+   BRANCH ON ZERO      88454630
OEC5 0J 44000F83 BSI L F000      XR 1 NOT EQUAL 0001 88454640
OEC6 0 3149      DC   /3149     ERR ID                88454650
OEC7 0J 44000FDE J889 BSI L F005      CK LOCK ON ERROR     88454660
OEC8 0 70E0      MDX  B884      LOOP                    88454670
*****
OEC9 0 611C      B885 LDX  1 28      LD XR 1 WITH /001C    88454680
OED0 0 C815      LDD  N88A      LD A=/0000 Q=/0002    88454690
OED1 0 1100      SLA  1 0      NOW A=/2000 Q=/0000 88454700
OED2 0 4802      BSC  C        SKIP IF CARRY OFF     88454710
OED3 0 7001      MDX  J88A      CARRY IS ON          88454720
OED4 0 7003      MDX  J88B      ERR ID                88454730
OED5 0J 44000F83 J88A BSI L F000      CARRY IS ON          88454740
OED7 0 314A      DC   /314A     ERR ID                88454750
OED8 0J 44000FDE J88B BSI L F005      CK LOCK ON ERROR     88454760
OEDA 0 70F4      MDX  9885      LOOP                    88454770
OEDB 0 7010      MDX  B8A0      EXIT TO NEXT ROUTINE 88454780
OEDC 0 0000      N880 DC   /0000     LOOP                    88454790
OEDD 0 0000      BSS  E        LOOP                    88454800
OEE0 0 0000      N882 DC   /0000     LOOP                    88454810
OEE1 0 0010      N884 DC   /0001     LOOP                    88454820
OEE2 0 8000      N885 DC   /0010     LOOP                    88454830
OEE3 0 FF00      N886 DC   /8000     LOOP                    88454840
OEE4 0 0000      N887 DC   /FF00     LOOP                    88454850
OEE5 0 0000      N888 DC   /0000     LOOP                    88454860
OEE6 0 3000      N88A DC   /0000     LOOP                    88454870
OEE7 0 0002      N88B DC   /0002     LOOP                    88454880
OEE8 0 0020      N88C DC   /0020     LOOP                    88454890
OEE9 0 FFDF      N88D DC   /FFDF     LOOP                    88454900
OEEA 0 FF01      N88E DC   /FF01     LOOP                    88454910
OEEB 0 7FFF      N88F DC   /7FFF     LOOP                    88454920
*****
TEST COMPARE INSTRUCTION
*****
A = ACCUMULATOR
Q = ACCUMULATOR EXTENTION
M = WORD BEING COMPARED
M+1 = 2ND WORD ON DCM
*****
THE 1800 HAS A COMPARE INSTRUCTION
BUT THE 1130 DOES NOT. THIS ROUTINE
DETERMINES WHICH MACHINE IS BEING
TESTED BEFORE ATTEMPTING A COMPARE
*****

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PROCESSOR-CONTROLLER FUNCTION TEST

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*****
INSTRUCTION.
*****
INDEX REGISTERS ARE HARDWARE IN 1800
AND CORE STORAGE LOCATIONS IN 1130.
*****
CORE DATA OR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ- AT RIGHT
*****
OEEC 0 1810      B8A0 SRA  16      CK FOR 1130 OR 1800  88455110
OEEED 0J D4000001 STO  L /0001     STOPE /0000 AT ACCR /0001 88455120
OEEF 0 61FF      LDX  1 -1      LD XR 1 WITH /FFFF     88455130
OEF0 0J C4000001 LD   L /0001     LD C(/0001)             88455140
OEF2 0J 4C200F76 BSC  L W8C0,Z  BRANCH IF 1130        88455150
OEF4 0 C075      LD   N8A2      LD C(N8A2) /4000      88455160
OEF5 0 6072      CMP  N8A0      A GREATER THAN M     88455170
OEF6 0 7004      MDX  J8A0      A GREATER THAN M     88455180
OEF7 0 1000      SIA  0        A LESS THAN M        88455190
OEF8 0J 44000F83 BSI  L F000      A GREATER THAN M FAILED 88455200
OEF9 0 3148      DC   /3148     ERR ID                88455210
OEFB 0J 44000F82 J8A0 BSI  L F00E      CK LOCK ON ERROR     88455220
OEFD 0 70EE      MDX  B8A0      LOOP                    88455230
OEFE 0 F06B      EOR  N8A2      ZERO WITH /4000     88455240
OEFF 0J 40180F07 BSC  L B8A1,+   BRANCH ON ZERO      88455250
OF01 0J 44000F83 BSI  L F000      ACC CHANGED ERROR    88455260
OF03 0 314C      DC   /314C     EPR ID                88455270
OF04 0J 44000FDE BSI  L F005      CK LOCK ON ERROR     88455280
OF06 0 70E5      MDX  B8A0      LOOP                    88455290
*****
OF07 0 C060      B8A1 LD   N8A0      N8A0 =/0000          88455300
OF08 0 8060      CMP  N8A1      N8A1 =/1000          88455310
OF09 0 7001      MDX  J8A2      A LESS THAN M FAILED 88455320
OF0A 0 7003      MDX  J8A1      A LESS THAN M        88455330
OF0B 0J 44000F83 J8A2 BSI  L F000      A LESS THAN M FAILED 88455340
OF0D 0 314D      DC   /314D     ERR ID                88455350
OF0E 0J 44000FDE J8A1 BSI  L F005      CK LOCK ON ERROR     88455360
OF10 0 70F6      MDX  B8A1      LOOP                    88455370
*****
OF11 0 C056      B8A2 LD   N8A0      N8A0 =/0000          88455380
OF12 0 8058      CMP  N8A3      N8A3 =/2000          88455390
OF13 0 7001      MDX  J8A4      A LESS THAN M FAILED 88455400
OF14 0 7003      MDX  J8A3      A LESS THAN M        88455410
OF15 0J 44000F83 J8A4 BSI  L F000      A LESS THAN M FAILED 88455420
OF17 0 314E      DC   /314E     ERR ID                88455430
OF18 0J 44000FDE J8A3 BSI  L F005      CK LOCK ON ERROR     88455440
OF1A 0 70F6      MDX  B8A2      LOOP                    88455450
*****
OF1B 0 C04C      B8A3 LD   N8A0      N8A0 =/0000          88455460
OF1C 0 804D      CMP  N8A2      N8A2 =/4000          88455470
OF1D 0 7001      MDX  J8A6      A LESS THAN M FAILED 88455480
OF1E 0 7003      MDX  J8A5      A LESS THAN M        88455490
OF1F 0J 44000F83 J8A6 BSI  L F000      A LESS THAN M FAILED 88455500
OF21 0 314F      DC   /314F     ERR ID                88455510
OF22 0J 44000FDE J8A5 BSI  L F005      CK LOCK ON ERROR     88455520
OF24 0 70F6      MDX  B8A3      LOOP                    88455530
*****
OF25 0 C046      B8A4 LD   N8A4      LD /8000              88455540
OF26 0 8041      CMP  N8A0      COMPARE C(N8A0) /0000 88455550
OF27 0 7001      MDX  J8A8      A LESS THAN M FAILED 88455560
OF28 0 7003      MDX  J8A7      A LESS THAN M        88455570
OF29 0J 44000F83 J8A8 BSI  L F000      A LESS THAN M FAILED 88455700
OF2B 0 3150      DC   /3150     ERR ID                88455710
OF2C 0J 44000FDE J8A7 BSI  L F005      CK LOCK ON ERROR     88455720
OF2E 0 70F6      MDX  B8A4      LOOP                    88455730
*****
OF2F 0 C039      B8A5 LD   N8A1      LD /1000              88455740
OF30 0 8038      CMP  N8A1      CMP /1000              88455750
OF31 0 7002      MDX  J8AA      A EQUAL M FAILED     88455760
*****

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PROCESSOR-CONTROLLER FUNCTION TEST

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OF32 0 7001      MDX   J8AA   A EQUAL M FAILED      8B455790
OF33 0 7003      MDX   J8A9   A=M                    8B455800
OF34 0 4400F83  J8AA  BSI  L  F000   A=M FAILED          8B455810
OF36 0 3151      DC     /3151  ERR ID              8B455820
OF37 0 4400FDE  J8A9  BSI  L  F005   CK LOCK ON ERROR   8B455830
OF39 0 70F5      MDX   B8A5   LOOP                8B455840
*****
*
*          TEST DOUBLE COMPARE
*
*****
CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF3A 0 C835      B8C0  LDD   N8C6   LD A=/8000 Q=/0001  8B455930
OF3B 0 B832      DCM   N8C5   AQ GREATER THAN M, M+1 8B455940
OF3C 0 7003      MDX   J8C0   NO-OP              8B455950
OF3D 0 1000      SLA   0       NO-OP              8B455960
OF3E 0 4044      BSI   F000   FAILED A,Q NOT GREATER 8B455970
OF3F 0 3152      DC     /3152  ERR ID              8B455980
OF40 0 4400FH2  J8C0  BSI  L  F00E   CK LOCK ON ERROR   8B455990
OF42 0 70F7      MDX   B8C0   LOOP                8B456000
OF43 0 F02C      EOR   N8C6   ZERO WITH /8000    8B456010
OF44 0 4C18UF4B BSC  L  J8C1,+-- BRANCH ON ZERO     8B456020
OF46 0 403C      BSI   F000   ACC CHANGED        8B456030
OF47 0 3153      DC     /3153  ERR ID              8B456040
OF48 0 4400FB2  J8C1  BSI  L  F00E   CK LOCK ON ERROR   8B456050
OF4A 0 70EF      MDX   B8C0   LOOP                8B456060
OF4B 0 1800      RTE   16     NOW A=/0001 Q=/0000 8B456070
OF4C 0 F024      EOR   N8C6+1 ZERO WITH /0001    8B456080
OF4D 0 4C18OF51 BSC  L  J8C2,+-- BRANCH ON ZERO     8B456090
OF4F 0 4033      BSI   F000   Q REG CHANGED      8B456100
OF50 0 3154      DC     /3154  ERR ID              8B456110
OF51 0 4400FDE  J8C2  BSI  L  F005   CK LOCK ON ERROR   8B456120
OF53 0 70E6      MDX   B8C0   LOOP                8B456130
*****
OF54 0 C81D      B8C1  LDD   N8C7   LD A=/0000 Q=/8000  8B456140
OF55 0 B81E      DCM   N8C8   A,Q LESS THAN M, M+1 8B456160
OF56 0 7001      MDX   J8C3   A,Q GREATER THAN M,M+1 8B456170
OF57 0 7002      MDX   J8C4   A,Q LESS THAN M,M+1 8B456180
OF58 0 402A      J8C3  BSI  F000   FAILED A,Q GREATER 8B456190
OF59 0 3155      DC     /3155  ERR ID              8B456200
OF5A 0 4400FDE  J8C4  BSI  L  F005   CK LOCK ON ERROR   8B456210
OF5C 0 70F7      MDX   B8C1   LOOP                8B456220
*****
OF5D 0 C814      B8C2  LDD   N8C7   LD A=/0000 Q=/8000  8B456230
OF5E 0 B813      DCM   N8C7   A,Q EQUQL M,M+1    8B456240
OF5F 0 7002      MDX   J8C5   A,Q GREATER        8B456250
OF60 0 7001      MDX   J8C5   A,Q LESS           8B456270
OF61 0 7012      MDX   J8C6   A,Q = M,M+1       8B456280
OF62 0 4020      J8C5  BSI  F000   A,Q = M,M+1 FAILED 8B456290
OF63 0 3156      DC     /3156  ERR ID              8B456300
OF64 0 4400FDE  J8C6  BSI  L  F005   CK LOCK ON ERROR   8B456310
OF66 0 70F6      MDX   B8C2   LOOP                8B456320
OF67 0 700E      MDX   W8C0   EXIT TO NEXT ROUTINE 8B456330
OF68 0 0000      BSS   E 0     8B456340
OF68 0 0000      N8A0  DC     /0000  8B456350
OF69 0 1000      N8A1  DC     /1000  8B456360
OF6A 0 4000      N8A2  DC     /4000  8B456370
OF6B 0 2000      N8A3  DC     /2000  8B456380
OF6C 0 8000      N8A4  DC     /8000  8B456390
OF6E 0 0000      BSS   E 0     8B456400
OF6F 0 8000      N8C5  DC     /8000  8B456410
OF6F 0 0000      DC     /0000  8B456420
OF70 0 8000      N8C6  DC     /8000  8B456430
OF71 0 0001      DC     /0001  8B456440
OF72 0 0000      N8C7  DC     /0000  8B456450
OF73 0 8000      DC     /8000  8B456460

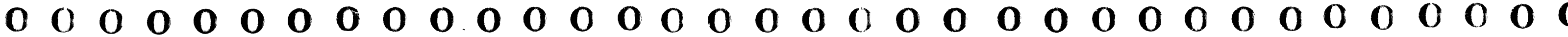
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PROCESSOR-CONTROLLER FUNCTION TEST

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OF74 0 0000      N8C8  DC     /0000  8B456470
OF75 0 8001      DC     /8001  8B456480
*****
*****
CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF76 0 0809      W8C0  XIC   N8C1   READ SWITCHES      8B456550
OF77 0 C00A      LD    N8C3   LD SW BITS          8B456560
OF78 0 1804      SRA   4       PLACE SW 11 AT BIT 15 POS. 8B456570
OF79 0 4804      BSC   E       IS SWITCH 11 ON     8B456580
OF7A 0 7002      MDX   W8C4   SWITCH 11 ON       8B456590
OF7B 0 C003      LD    Z020   SWITCH 11 IS OFF-WAIT 8B456600
OF7C 0 3003      X007  DC     /3003  PROGRAM FINISHED    8B456610
OF7D 0 4C000154 W8C4  BSC  L  A140  8B456620
OF7F 0 0003      Z020  DC     /0003  8B456630
OF80 0 0000      BSS   E       8B456640
OF80 0 0F82      N8C1  DC     N8C3   8B456650
OF81 0 0240      N8C2  DC     /0240  EQUAL /3A00 IN 1130 8B456660
OF82 0 0000      N8C3  DC     /0000  8B456670
*
*          3B-56680
*          8B456690
*****
*
*          ERKOR CONTROL ROUTINE
*
OF83 0 0000      F000  DC     0       REENTER ADDRESS     8B456740
OF84 0 2816      STS   F00X   SAVE STATUS         8B456750
OF85 0 0063      STO   U000   SAVE A REG          8B456760
OF86 0 1800      RTE   16     8B456770
OF87 0 0062      STO   U001   SAVE Q REG          8B456780
OF88 0 0863      XIC   F003   READ SWITCHES      8B456790
OF89 0 C066      LD    Z000   LD SW READINGS    8B456800
OF8A 0 1807      SRA   7       PLACE SW 8 AT BIT POS 15 8B456810
OF8B 0 4804      BSC   E       CK LOOP ON INSTRUCTION 8B456820
OF8C 0 7012      MDX   F00A   * BEING TESTED SW   8B456830
OF8D 0 4C800F83 F000  LD    I  F000   GET WAIT ERROR ID   8B456840
OF8E 0 000C      STO   F002   STORE ERROR ID AT F002 8B456850
OF90 0 C0F2      LD    F000   GET RETURN ADDR    8B456860
OF91 0 001F      STO   U00B   STORE AT U00B       8B456870
OF92 0 805C      A     U006   ADD ONE             8B456880
OF93 0 00EF      STG   F000   STORE NEW RETURN ADDRESS 8B456890
OF94 0 C05B      FOOL  LD    Z000   CK BYPASS EPROR SW  8B456900
OF95 0 1801      SRA   1       PLACE SW 14 AT BIT POS 15 8B456910
OF96 0 4804      BSC   E       SKIP IF SW 14 OFF    8B456920
OF97 0 700D      MDX   F00F   CK FOR 8 OR 12 ON ALSO 8B456930
OF98 0 C051      LD    U001   RESTORE REG AND WAIT  8B456940
OF99 0 1800      RTE   16     PLACE IN Q REG       8B456950
OF9A 0 C04E      LD    U000   RESTORE A REG       8B456960
OF9B 0 2000      F00X  LDS   U       RESTORE C AND OF IND. 8B456970
OF9C 0 3000      F002  WAIT  0     ERKOR WAIT B REG    8B456980
*          * SHOWS ERROR ID 8B456990
*          EXIT FROM ROUTINE 8B457000
*          * C(F000)IS NOW ONE 8B457010
*          * GREATER THAN AT THE 8B457020
*          * BEGINNING OF ROUTINE 8B457030
*
*          8B457040
*          8B457050
*          8B457060
*          8B457070
*          8B457080
*          8B457090
*          LOOP ON INSTRUCTION BEING
*          * TESTED
*****
CORE DATA DR *LA- OPER-
ADDR INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OF9F 0 C0E3      F00A  LD    F000   GET RETURN ADDR AT F000 8B457140

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PROCESSOR-CONTROLLER FUNCTION TEST

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OFA0 0 D010      STO  U00B      STORE RETURN ADDRESS  88457150
OFA1 0 800D      A          U003      ADD 3                88457160
OFA2 0 D0E0      STO  F000      UPDATE RETURN ADDRESS 88457170
OFA3 00 4C800F83 BSC  I  F000      BR TO UPDATAD ADDRESS 88457180
                        *          CK FOR SW 8 OP 12    88457190
OFA5 0 1802      F00F  SRA   2        PLACE SW 12 AT BIT POS 15 88457200
OFA6 0 4804      BSC   F          SKIP IF SW 12 OFF        88457210
OFA7 0 70F5      MDX   F00B      BR TO EXIT IF SW 12 ON   88457220
OFA8 0 1804      SPA   4        PLACE SW 8 AT BIT POS 15 88457230
OFA9 0 4804      BSC   E          SKIP IF SW 8 OFF        88457240
OFAA 0 70F2      MDX   F00B      BR TO EXIT IF SW 8 ON   88457250
OFAB 0 C044      LD    Z000      LD SWITCH READINGS     88457260
OFAC 0 0000      DC          IMPROPER BIT SWS, 14 ON 88457270
OFAD 0 083E      XIO   F003      *WITHOUT 8 OR 12 CN    88457280
OFAE 0 70E5      MDX   F00L      88457290
OFAF 0 0003      U003  DC    3        CONSTANT 3           88457300
OFB0 0 FFFD      U00A  DC   -3       CONSTANT -3          88457310
OFB1 0 0000      U00B  DC    0        ERROR OCCURED CONTROL 88457320
                        *          88457330
                        *          88457340
                        *          88457350
                        *          LOCK ON ERROR RT  88457360
                        *          88457370
*****
CORE  DATA OR *LA- DPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OFB2 0 0000      F00E  DC    0        CONTAINS RETURN ADDRESS 88457420
OFB3 0 281A      STS   F00H      SAVE REGS C AND OF    88457430
OFB4 0 D040      STO  U00X      ACCUMULATOR  88457440
OFB5 0 1800      RTE   16        88457450
OFB6 0 D03F      STO  U00X+1    ACC EXTENTION  88457460
                        *          88457470
*****
*          SET UP FOR RESTART *          88457480
*          *          88457490
*          *          88457500
*          TO RESTART -- PRESS STOP, RESET AND START. *          88457510
*          *          88457520
OFB7 0 C03F      LD    RST1      LD /6004      *          88457530
                        *          88457540
OFB8 00 D4000000 *          STO  L  /0000      STO IN WORD ZERO *          88457550
                        *          88457560
OFBA 0 C03D      LD    RST2      LD /4C00      *          88457570
                        *          88457580
OFBB 00 D4000004 *          STO  L  /0004      STO IN WORD FOUR *          88457590
                        *          88457600
OFBD 0 C03B      LD    RST2+1    LD /012C      *          88457610
                        *          88457620
OFBE 00 D4000005 *          STO  L  /0005      STO IN WORD FIVE *          88457630
                        *          88457640
*****
OFE0 0 082B      XIO   F003      READ SWITCHES        88457660
OFC1 0 C02E      LD    Z000      CK LOOP ON INST BEING 88457670
OFC2 0 1807      SRA   7          * TESTED SW          88457680
OFC3 0 4804      BSC   E          SKIP IF EVEN          88457690
OFC4 0 700A      MDX   F00B      EXIT TO LOOP INST    88457700
OFC5 0 C0EB      LD    U00B      CK IF ERROR HAS      88457710
OFC6 0 4820      BSC   Z          * OCCURRED          88457720
OFC7 0 7009      MDX   F009      88457730
OFC8 0 C0E9      F00K  LD    F00E      GOT RETURN ADDR     88457740
OFC9 0 8025      A          U006      ADD ONE              88457750
OFCA 0 D0E7      STO  F00E      STORE RETURN ADDRESS 88457760
OFCB 0 C02A      LD    U00X+1    RESTORE REGS        88457770
OFCC 0 18D0      RTE   16        88457780
OFCD 0 C027      LD    U00X      88457790
OFCE 0 2000      F00H  LDS    0        SET C AND OF OFF    88457800
OFCF 00 4C800FB2 F00B  BSC  I  F00E      BR TO RETURN ADDRESS 88457810
OFD1 0 C01E      F009  LD    Z000      CHECK LOCK ON ERROR SW 88457820

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PROCESSOR-CONTROLLER FUNCTION TEST

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OFD2 0 1803      SRA   3        SHIFT BIT 12 TO POS 15 88457830
OFD3 0 4804      BSC   E          SKIP IF OFF          88457840
OFD4 0 7003      MDX   F00C      ERRCR SW (B 12) ON   88457850
OFD5 0 1810      SRA   16       RESET ERROR OCCURRED 88457860
OFD6 0 D0DA      STO  U00B      * CONTROL          88457870
OFD7 0 70F0      MDX   F00K      BR TO GET RETURN ADDRESS 88457880
OFD8 0 C0D9      F00C  LD    F00E      GOT ADDR          88457890
OFD9 0 80D6      A          U00A      ADD MINUS THREE     88457900
OFDA 0 F0D6      EOR   U00B      COMPARE TO ERR CNTR 88457910
                        *          * ADDR          88457920
OFDB 0 4820      BSC   Z          SKIP ON ZERO        88457930
OFDC 0 70EB      MDX   F00K      BR TO GET RETURN ADDRESS 88457940
OFDD 0 70F1      MDX   F00B      EXIT                88457950
                        *          88457960
                        *          88457970
                        *          CK LOOP RT SW RT 88457980
                        *          88457990
*****
CORE  DATA OR *LA- DPER-
ADDR  INSTRUCTION *BEL ATION FT OPERANDS + REMARKS ID+SEQ= AT RIGHT
*****
OFDE 0 00C0      F005  DC    0        WILL CONTAIN RETURN ADDR 88458040
OFDF 0 080C      XIO   F003      READ SWS - PLACE IN LABEL 88458050
                        *          * ADDRESS Z000 88458060
OFEE 0 C00F      LD    Z000      CK LCOP ROUTINE SW  88458070
OFEF 0 1805      SRA   5          CHECK FOR BIT 11    88458080
OFE0 0 4804      BSC   E          NO SKIP FOR LOOP    88458090
OFE1 0 7003      MDX   F00G      LOOP ROUTINE SWITCH ON 88458100
OFE2 0 C0F9      LD    F005      LD RETURN ADDRESS  88458110
OFE3 0 D0CC      STO  F00E      SAVE FOR LOCK ON ERROR RTN 88458120
OFE4 0 70CC      MDX   F00E+1    BR TO SAVE REGISTERS 88458130
OFE5 00 4C800FDE F00G  BSC  I  F005      BR TO MAIN PROGRAM  88458140
                        *          * RETURN ADDRESS 88458150
OFE6 0 0000      U000  DC   /0000    A REG SAVED HERE   88458160
OFE7 0 0000      U001  DC   /0000    Q REG SAVED HERE   88458170
OFE8 0 0000      BSS   E          88458180
OFE9 0 00F0      F003  DC    Z000    88458190
OFEA 0 0240      F004  DC   /0240    EQUAL /3A00 IN 1130 88458200
OFEB 0 0002      U004  DC   /0002    88458210
OFEF 0 0001      U006  DC   /0001    88458220
OFF0 0 0000      Z000  DC   /0000    SW READING STORED HERE 88458230
OFF1 0 0001      DC    /0001    88458240
OFF2 0 0000      BSS   E          88458250
OFF3 0 0000      U008  DC   /0000    88458260
OFF4 0 0000      U009  DC   /0000    88458270
OFF5 0 0240      F007  DC   /0240    EQUAL /3A00 IN 1130 88458280
OFF6 0 0002      U00X  BSS    Z          SAVED FOR A+Q STORAGE 88458290
OFF7 0 6004      RST1  LDX   /0004    88458300
OFF8 00 4C00012E RST2  BSC  L  A080    88458310
OFFA 0 012D      END   X000      88458320

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PROCESSOR-CONTROLLER FUNCTION TEST

PROCESSOR-CONTROLLER FUNCTION TEST

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
A0C0	013F	300F, 3010, 3011, 3012, 013A
A0B0	012E	3004, 3005, 3006, 3007, 3008, 3009, 300A, 300B, 300C, 300D, 300E, 0FF8
A1C0	01EB	303D, 303E, 01E9
A1D0	01F5	303F, 3040, 3041, 3042, 3043, 3044, 3045, 01F2
A1E0	0214	3046, 3047, 0210
A1F0	0220	3048, 3049, 021D
A100	014C	3013, 3014, 3015
A140	0154	3016, 3017, 3018, 3019, 301A, 301B, 301C, 301D, 301E, 301F, 3020, 3021, 3022, 3023, 3024, 3025, 3026, 3027, 3028, 3029, 302E, 0F7D
A180	01A0	302A, 3029, 302C, 302D, 302F, 3030, 3031, 3032, 3033, 3034, 3035, 3036, 3037, 3038, 3039, 303A, 303B, 303C, 019E
A2CC	0337	3072, 0341
A2C0	0319	306F, 0312, 0322
A2C4	0323	3070, 032C
A2C8	032D	3071, 0336
A200	022D	304A, 304B, 304C, 304D, 304E, 304F, 3050, 3051, 3052, 3053, 3054, 3055, 3056, 3057, 3058, 3059, 305A, 305B, 305C, 305D, 305E, 305F, 0229
A240	0270	3060, 3061, 3062, 3063, 3064, 026B
A280	02D9	306A, 028B, 02C8, 02E2
A281	02E3	306B, 02ED
A282	02FE	306C, 02F8
A283	02F9	306D, 0303
A284	0304	306E, 0311
A3C0	03DD	3080, 3081, 03D6, 03E9, 03F3
A3C4	03F4	3082, 3083, 0407, 0410
A300	0345	3073, 0342, 034E
A302	034F	3074, 0359
A304	035A	3075, 0364
A340	0368	3076, 3077, 0365, 0373, 037D
A38C	03BD	307E, 307F, 03CB, 03D5
A380	0381	3078, 3079, 037E, 038B, 0395
A384	0396	307A, 307B, 039F, 03A8
A388	03A9	307C, 307D, 0382, 038C
A4CC	05AC	30A8, 30A9, 05B8, 05C1
A4C0	0566	30A1, 0562, 0570
A4C2	0571	30A2, 30A3, 30A4, 30A5, 0571, 0574, 0582, 058B, 0595
A4C8	0596	30A6, 30A7, 05A2, 05AB
A400	0419	3084, 3085, 3086, 0411, 0426, 042E, 0439
A408	043A	3087, 3088, 3089, 0448, 0450, 0459
A44A	0500	3099, 309A, 309B, 050C, 0515, 051F
A440	04C4	3093, 3094, 3095, 04BC, 04CF, 04D7, 04E0
A444	04E1	3096, 3097, 3098, 04ED, 04F6, 04FF
A480	0549	309F, 0542, 0554
A482	0555	30A0, 0561
A5C0	074C	30CE, 30CF, 0747, 0756, 075F
A5C4	0760	30D0, 30D1, 076E, 0778
A5C8	0779	30D2, 30D3, 0788, 0792
A50A	0600	30AF, 30B0, 3170, 060B, 0612
A50C	061D	30B1, 30B2, 061C, 0629, 0630
A50E	0631	30B3, 30B4, 062A, 063D, 0644
A500	05C8	30AA, 05C2, 05D2
A502	05D3	30AB, 05DD
A504	05DE	30AC, 30AD, 05E9, 05F4
A508	05F5	30AE, 05FF
A54A	06C1	30C0, 30C1, 06B9, 06CA, 06D2
A54C	06D3	30C2, 30C3, 06CB, 06DC, 06E4
A54E	06E5	30C4, 30C5, 06DD, 06EE, 06F6
A54F	06F7	30C6, 30C7, 06EF, 0701, 0709
A540	0663	3037, 30B8, 30B9, 0654, 065B, 066F, 0676, 0686
A544	0687	30BA, 30BB, 0670, 0692, 0699
A546	069C	30BC, 30BD, 0693, 069A, 06A7, 06AE
A548	06AF	30BE, 30BF, 06A8, 06B8, 06C0

A580	070F	30C8, 30C9, 0702, 070A, 0717, 0720
A584	0721	30CA, 30CB, 072A, 0734
A588	0735	30CC, 30CD, 073D, 0746
A6C0	0964	30F0, 30F1, 0954, 095B, 0974, 097B
A6C2	097C	30F2, 30F3, 0975, 098C, 0993
A6C4	0994	30F4, 30F5, 098D, 09A3, 09AA
A6C6	09AB	30F6, 30F7, 09A4, 09BB, 09C2
A6C8	09C3	30F8, 30F9, 098C, 09D3, 09DA, 09EB
A6D0	09EC	315D, 09D4, 09DB, 09F7
A6D2	09F8	315E, 0A03
A6D3	0A04	315F, 0A0F
A6D5	0A10	3163, 0A1B
A6D6	0A1C	3164, 0A27
A6F0	0A28	3165, 0A36
A6F1	0A39	3166, 0A37, 0A47
A60A	07D7	30D9, 07E3
A60C	07E4	30DA, 07F0
A60E	07F1	30DB, 07FD
A600	079F	30D4, 0796, 07A7
A602	07A8	30D5, 07B0
A604	07B1	30D6, 07BC
A606	07BD	30D7, 07C9
A608	07CA	30D8, 07D6
A64A	086C	30E3, 0879
A64C	087A	30E4, 0887
A640	0820	30DE, 3167, 081A, 082C, 0836
A642	0837	30DF, 0843
A644	0844	30E0, 0850
A646	0851	30E1, 085D
A648	085E	30E2, 086B
A660	088F	3157, 3158, 088A, 089C, 08A6
A662	08A7	3159, 315A, 08B4, 08BE
A664	08BF	315B, 315C, 08CC, 08D6
A670	08D9	3169, 08D7, 08E2, 08E9
A68C	0928	30EB, 30EC, 0932, 093B
A680	08EC	30E5, 30E6, 08E3, 08F7, 0900
A684	0901	30C7, 30E8, 090B, 0913
A688	0914	30E9, 30EA, 091E, 0927
A7C0	0C3D	3125, 3126, 0C46, 0C4F
A7C0	0C01	311F, 3120, 0BF3, 0C0B, 0C15
A7C4	0C16	3121, 3122, 0C1F, 0C29
A7C8	0C2A	3123, 3124, 0C33, 0C3C
A70C	0A8D	3100, 3101, 3102, 0A99, 0AA7, 0AAE
A700	0A48	30FA, 30FB, 0A54, 0A5E
A704	0A5F	30FC, 30FD, 0A6B, 0A75
A708	0A76	30FE, 30FF, 0A82, 0A8C
A74C	0814	310B, 310C, 310D, 310E, 0B20, 0B2A, 0B36, 0B3D
A740	0A88	3103, 3104, 3105, 3106, 0AAB, 0AAF, 0AC8, 0AD3, 0ADF, 0AE6
A746	0AE7	3107, 3108, 3109, 310A, 0AF5, 0AFE, 0B0C, 0B13
A78A	08CB	311B, 311C, 08D4, 08DE
A78E	08DF	311D, 311E, 08E8, 08F2
A780	088C	3115, 3116, 3117, 3118, 087D, 089B, 08A2, 08AE, 08B5
A786	08B6	3119, 311A, 08C0, 08CA
A80C	0C82	312F, 0CAB, 0CBC
A80E	0CB0	3130, 0CC7
A800	0C58	3127, 3128, 3129, 312A, 0C50, 0C68, 0C73, 0C7F, 0C86
A806	0C87	312B, 312C, 312D, 312E, 0C80, 0C94, 0C9F, 0CAA, 0CB1
A84A	0DC6	3171, 0DCF
A840	0D6A	3135, 0D41, 0D77
A842	0D78	3136, 316F, 0D8C
A844	0D8D	3137, 0D9B
A846	0D9C	3138, 0DA5
A848	0DA6	3139, 0DAF
A849	0DB0	313A, 3168, 0DC5
A85A	0DD0	3172, 0DD8
A88A	0E60	3173, 0E6D
A88C	0E6E	3141, 3142, 3143, 0E79, 0E82, 0E8C
A880	0DE4	313B, 313C, 3160, 0DD9, 0DF3, 0DFD, 0E03, 0E0A



PROCESSOR-CONTROLLER FUNCTION TEST

AH84	UF0B	313D, 313F, 3161, UF04, UF1B, OE29, OE32
AH88	OE 33	313F, 3140, OE41, OE4F
AH89	UF 50	3162, OE 5F
A900	0482	3065, 3066, 3067, 3068, 3069
B40A	0497	3090, 3091, 3092, 04AA, 04B2, 04BB
B400	045A	308A, 308B, 308C, 0465, 046E, 0477
B406	047B	308D, 308E, 308F, 0484, 048D, 0496
F440	0570	309C, 309D, 309E, 0530, 0538, 0541
B500	0645	30B5, 30B6, 063E, 0653, 065A
B600	07FE	30DC, 080B
B602	C80C	30DD, 0819
B680	093C	30FD, 30EE, 30FF, 0947, 0953, 095A
B742	0B3E	310F, 3110, 3111, 3112, 0B4A, 0B54, 0B60, 0B67
B747	0B68	3113, 3114, 0B72, 0B7C
B8A0	UEEC	314B, 314C, UEDB, OEF0, OF06
B8A1	UF07	314D, OEEF, UF10
B8A2	UF11	314E, OF1A
F8A3	OF1B	314F, OF24
B8A4	OF25	3150, OF2E
B8A5	OF2F	3151, OF39
B8C0	OF3A	3152, 3153, 3154, OF42, OF4A, OF53
B8C1	OF54	3155, OF5C
B8C2	OF5D	3156, OF66
B800	UCC8	3131, UC02
B802	OC03	3132, OC0D
B804	OC0E	3133, OC08
B806	OC09	3134, OC03
B807	UD18	316A, OC04, OD22
B808	OD23	316B, OD2D
B809	OD2E	316C, OD39
B810	UD3A	316D, 316F, UD69
B8B7	OE8D	3144, 3145, 3146, OE99, OEA2, OEAE
B8B4	UEAF	3147, 3148, 3149, OEB9, OEC2, OECE
B8B5	OECF	314A, OEDA
F00A	OF9F	OF8C
F00B	OF9D	OFA7, OFAA
F00C	OFD8	OFD4
F00E	OFB2	0371, 0389, 0380, 03C9, 03E7, 0405, 0424, 042C, 0446, 044E, 0463, 046C, 0482, 048B, 04A8, 04B0, 04CD, 04D5, 04EB, 04F4, 050A, 0513, 052E, 0536, 0580, 0589, 05A0, 05B6, 05E7, 0609, 0610, 0660, 0674, 0715, 0728, 0738, 0754, 076C, 0786, 082A, 089A, 08B2, 08CA, 08E7, 08F5, 0909, 091C, 0930, 0945, 0A52, 0A59, 0A80, 0A97, 0AC6, 0AD1, 0AF3, 0AFC, 0B1E, 0B28, 0B48, 0B52, 0B70, 0B96, 0BA0, 0BBE, 0BD2, 0BE6, 0C09, 0C1D, 0C31, 0C44, 0C66, 0C71, 0C92, 0C9D, 0D51, 0D5A, 0DF1, 0DFB, 0E19, 0E27, 0E3F, 0E77, 0E80, 0E97, 0FA0, 0EB7, 0ECO, 0EFB, 0F40, 0F4B, 0F68, 0FCA, 0FCF, 0FD8, 0FE5, 0FE6
F00F	CFA5	OF97
F00G	OFE7	OFE3
F00H	OFCE	OFB3
F00K	OF08	OFD7, OFDC
F00L	OF94	OFAE
F00X	OF9B	OFB4
F000	UF83	3174, 02DD, 02E8, 02F3, 02FE, 030C, 031D, 0327, 0331, 033C, 0349, 0354, 035F, 036E, 0378, 0386, 0390, 039A, 03A3, 03AD, 03B7, 03C6, 03D0, 03E4, 03EE, 0402, 040B, 0421, 0429, 0434, 0443, 044B, 0454, 0460, 0469, 0472, 047F, 0488, 0491, 04A5, 04AD, 04B6, 04CA, 04D2, 04DB, 04EB, 04F1, 04FA, 0507, 0510, 051A, 0526, 0533, 053C, 054F, 055C, 056B, 0577, 057D, 0586, 0590, 0597, 05A6, 05B3, 05BC, 05CD, 05D8, 05E4, 05EF, 05FA, 0606, 060D, 0617, 0624, 062B, 0638, 063F, 064F, 0655, 066A, 0671, 0681, 068D, 0694, 06A2, 06A9, 06B3, 06BB, 06C5, 06CD, 06D7, 06DF, 06E9, 06F1, 06FB, 0704, 0717, 0718, 0725, 072F, 0738, 0741, 0751, 075A, 0769, 0773, 0783, 078D, 07A2, 07AB, 07B7, 07C4, 07D1, 07DE, 07EB, 07F8, 0806, 0814, 0827, 0831, 083E, 084B, 0858, 0866, 0874, 0882, 0897, 08A1, 08AF, 08B9, 08C7, 08D1, 08E4, 08F2, 08FB, 0906, 090E, 0919, 0922, 092D, 0936, 0947, 094E, 0955, 096F, 0976, 0987, 098E, 099E, 09A5,

PROCESSOR-CONTROLLER FUNCTION TEST

F002	OF9C	09B6, 09BD, 09CE, 09D5, 09F2, 09FE, 0A0A, 0A16, 0A22, 0A31, 0A42, 0A4F, 0A59, 0A66, 0A70, 0A7D, 0A87, 0A94, 0AA2, 0AA9, 0AC3, 0ACE, 0ADA, 0AE1, 0AFO, 0AF9, 0B07, 0B0E, 0B18, 0B25, 0B31, 0B38, 0B45, 0B4F, 0B5B, 0B62, 0B6D, 0B77, 0B93, 0B9D, 0BA9, 0BB0, 0BBB, 0BC5, 0BCF, 0BD9, 0BE3, 0BED, 0C06, 0C10, 0C1A, 0C24, 0C2E, 0C37, 0C41, 0C4A, 0C63, 0C6E, 0C7A, 0C81, 0C8F, 0C9A, 0CA5, 0CAC, 0CB7, 0CC2, 0CCD, 0CDB, 0CE3, 0CEE, 0D1D, 0D2B, 0D34, 0D4E, 0D57, 0D72, 0D7E, 0D85, 0D96, 0DA0, 0DAA, 0DB9, 0DC0, 0DCA, 0DD3, 0DEE, 0DF8, 0E05, 0E16, 0E24, 0E2D, 0E3C, 0E4A, 0E5A, 0E74, 0E7D, 0E87, 0E94, 0E9D, 0EA9, 0EB4, 0EBD, 0EC9, 0ED5, 0EF8, 0F01, 0F0B, 0F15, 0F1F, 0F29, 0F34, 0F3E, 0F46, 0F4F, 0F58, 0F62, 0F8D, 0F90, 0F93, 0F9D, 0F9F, 0FA2, 0FA3
F003	OFFC	0F8F
F004	OFED	0D38, 0F88, 0FAD, 0FC0, 0FDF
F005	OFDE	029A, 02AA
		02E0, 02FB, 02F6, 0301, 030F, 0320, 032A, 0334, 033F, 034C, 0357, 0362, 0378, 0393, 039D, 03A5, 03BA, 03D3, 03F1, 040E, 0437, 0457, 0475, 0494, 04B9, 04UE, 04FD, 051D, 053F, 0552, 055F, 056E, 0593, 05A9, 05BF, 05D0, 05D8, 05F2, 05FD, 061A, 0627, 062E, 063B, 0642, 0651, 065B, 0664, 0690, 0697, 06A5, 06AC, 06B6, 06BE, 06C8, 06D0, 06DA, 06E2, 06EC, 06F4, 06FF, 0707, 071E, 0732, 0744, 075D, 0776, 0790, 07A5, 07AE, 07BA, 07C7, 07D4, 07E1, 07FE, 07FB, 0809, 0817, 0834, 0841, 084E, 085B, 0869, 0877, 0885, 08A4, 088C, 08D4, 08E0, 08FE, 0911, 0925, 0939, 0951, 095E, 0972, 0979, 098A, 0991, 09A1, 09A8, 09B9, 09C0, 09D1, 09DB, 09F5, 0A01, 0A0D, 0A19, 0A25, 0A34, 0A45, 0A5C, 0A73, 0A8A, 0AA5, 0AAC, 0ADD, 0AE4, 0B0A, 0B11, 0B34, 0B3B, 0B5E, 0B65, 0B7A, 0BAC, 0BB3, 0BC8, 0BDC, 0BF0, 0C13, 0C27, 0C3A, 0C4D, 0C7D, 0CP4, 0CA8, 0CAF, 0CBA, 0CC5, 0CDD, 0CDB, 0CE6, 0CF1, 0D20, 0C2B, 0D37, 0D67, 0D75, 0E8A, 0D99, 0DA3, 0DAD, 0DC3, 0DCD, 0DD6, 0E01, 0E08, 0E30, 0E4D, 0E5D, 0E6B, 0E8A, 0EAC, 0ECC, 0ED8, 0F04, 0F0E, 0F18, 0F22, 0F2C, 0F37, 0F51, 0F5A, 0F64, 0FE4, 0FE7
F007	OFF4	029C, 02AC
F008	OF0C	0FC4, 0FDD
F009	OFD1	0FC7
F907	02CA	02AE, 02C1
F903	02C8	0297, 02A7, 02B7
F904	02CC	02CC
F911	02CD	0282, 0286
F912	02CE	0283, 0285, 02CD
F913	02CF	0284
F915	02D0	0280, 0283
F916	02D1	028C
F917	02D2	02AF, 02C2, 02CA
F918	02D3	0289, 028D
F919	02D4	0296
F920	02D5	02A6, 02B8
F922	02D6	029E, 02BB
F923	02D8	029F, 02A0, 02A3, 02BC
G0C1	0144	0141
G0C2	0147	0145
G0B0	0130	012E
G0B1	0133	0130
G0B2	0138	0133
G0B3	013A	0138
G0B4	013B	0138
G14A	0181	017F
G14B	0185	0183
G14C	0189	0187
G14D	018D	018B
G14E	0191	018F
G14F	0195	0193
G140	015A	0158
G141	015D	015B
G142	0161	015F
G143	0165	0163

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G144	0169	0167
G145	016D	016B
G146	0171	016F
G147	0175	0173
G148	0179	0177
G149	017D	017B
G150	0199	0197
G18A	01D0	01CE
G18B	01D4	01D2
G18C	01D8	01D6
G18D	01DC	01DA
G18E	01E0	01DE
G18F	01E4	01E2
G181	01AC	01AA
G182	01B0	01AE
G183	01B4	01B2
G184	01B8	01B6
G185	01BC	01BA
G186	01C0	01BE
G187	01C4	01C2
G188	01C8	01C6
G189	01CC	01CA
G2CC	033F	033A
G2CO	0320	031B
G2C4	032A	0325
G2C8	0334	032F
G20A	025D	0259
G20B	0267	0265
G20C	026B	026F
G20D	0262	0260
G200	0231	022D
G201	0236	0232
G202	023A	0236
G203	023E	023A
G204	0242	0240
G205	0247	0243
G206	024F	024D
G207	0254	0252
G208	024B	0247
G209	0258	0256
G280	02E0	02DB
G281	02EB	02E6
G282	02F6	02F1
G283	0301	02FC
G284	030F	030A
G3C0	03E7	03E2
G3C2	03F1	03EC
G3C4	0405	0400
G3C6	040E	0409
G300	034C	0347
G302	0357	0352
G304	0362	035D
G340	0371	036C
G342	0376	0376
G38A	038A	0385
G38C	03C9	03C4
G38E	03D3	03CE
G380	0389	0384
G382	0393	038E
G384	039D	0398
G386	03A6	03A1
G388	03B0	03AB
G4CA	05A9	05A4
G4CC	05B6	05B1
G4CD	05BF	05BA
G4CO	056E	0569
G4C2	0580	057C
G4C4	0589	0585

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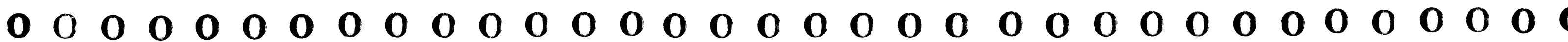
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PROCESSOR-CONTROLLER FUNCTION TEST

G4C6	0593	058E
G4C8	05A0	059B
G40C	0446	0442
G40E	0457	0452
G400	042C	0427
G404	0424	041F
G406	0437	0432
G407	0443	0440
G408	044E	0449
G44A	0513	050E
G44C	0507	0504
G44D	050A	0506
G44E	051D	0518
G440	04D5	04D0
G442	04CD	04C8
G443	04DE	04D9
G444	04F4	04EF
G446	04E8	04E5
G447	04EB	04E7
G448	04FD	04FB
G480	0552	054D
G482	055F	055A
G5CA	0790	078E
G5C0	0754	074F
G5C2	075D	0758
G5C4	076C	0767
G5C6	0776	0771
G5C8	0786	0781
G50A	0613	0602, 060C
G50C	0623	061F
G50E	063E	0634
G500	05D0	05CC
G502	05DB	05D7
G504	05E4	05E2
G505	05E7	05E3
G506	05EF	05ED
G507	05F2	05EE
G508	05FA	05FB
G54A	06CC	06C2
G54C	06DE	06D4
G54E	06F0	06E6
G54F	0703	06FB
G540	0678	0666, 0677
G542	0684	067F
G544	069B	0689
G546	06A1	069D
G548	06BA	0680
G58A	0744	073F
G580	0715	0710
G582	071E	0719
G584	0728	0723
G586	0732	072D
G588	073B	0736
G6C0	0979	096D
G6C2	0991	0985
G6C4	09A8	099C
G6C6	09C0	0984
G6C8	09D8	09CC
G60A	07E1	07DC
G60C	07EE	07E9
G60E	07FB	07F6
G600	07A2	079F
G602	07AB	07A8
G604	07BA	07B5
G606	07C7	07C2
G608	07D4	07CF
G64A	0877	0872
G64C	0885	0880

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PROCESSOR-CONTROLLER FUNCTION TEST

G640	0820	0825
G641	0834	082F
G642	0841	083C
G644	084E	0849
G646	0858	0856
G648	0869	0864
G660	089A	0895
G661	08A4	089F
G662	08B2	08AD
G663	08BC	08B7
G664	08CA	08C5
G665	08D4	08CF
G670	08E4	08DB
G671	08DB	08DF
G672	08DD	08EA
G6AA	0925	0920
G68C	0930	0928
G68E	0939	0934
G68U	08F2	08EF
G68Z	08FE	08F9
G684	0909	0904
G686	0911	090C
G688	091C	0917
G7CA	0C3A	0C35
G7CC	0C44	0C3F
G7CE	0C4D	0C48
G7CG	0C09	0C04
G7C2	0C13	0C0E
G7C4	0C1D	0C18
G7C6	0C27	0C22
G7C8	0C31	0C2C
G7UA	0A8A	0A85
G7UC	0A97	0A92
G7UE	0AAC	0A9C
G7U0	0A52	0A4D
G7O2	0A5C	0A57
G7O4	0A69	0A64
G7U6	0A73	0A6E
G7U8	0A80	0A7B
G74A	0B11	0B03, 0B0D
G74C	0B1E	0B19
G74E	0B28	0B23
G740	0AC6	0AC1
G742	0AD1	0ACC
G744	0AE4	0AD6, 0AE0
G746	0AF3	0AE7
G748	0AFC	0AF7
G74A	0B02	0B0D
G74C	0B0C	0B07
G74E	0B06	0B01
G78U	0B96	0B91
G78Z	0BA0	0B9B
G784	0BB3	0BA5, 0BAF
G786	0BBE	0BB9
G788	0BC4	0BC3
G8UA	0CAF	0CA1
G8UC	0CBA	0CB5
G8UE	0CC5	0CC0
G8U0	0C66	0C61
G8U2	0C71	0C6C
G8U4	0C84	0C76
G8U6	0C92	0C8D
G8U8	0C9D	0C9E
G34A	0DCA	0DC8
G34U	0D75	0D70
G34Z	0D88	0D83
G444	0D99	0D94
G846	0DAU	0D9E

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PROCESSOR-CONTROLLER FUNCTION TEST

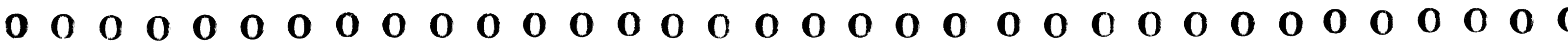
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G849	0DC3	0DBE
G88A	0E4D	0E48
G88B	0E68	0E68
G88C	0E77	0E72
G88E	0E80	0E7B
G880	0DF1	0DEC
G881	0DFE	0DEB
G882	0DFB	0DF6
G883	0E05	0E00
G884	0E19	0E14
G885	0E2A	0E11
G886	0E27	0E22
G887	0E30	0E2C
G888	0E3F	0E3A
G889	0E5D	0E58
G900	02AE	02A1, 02B5
G901	02A6	0295
G902	029E	02A5
G903	028B	02C0
G904	02C1	02B9, 02BD, 02C6
H4C2	057D	057A
H4C3	057A	0575
H4C4	0586	0583
H40A	0480	04AB
H40D	04A8	04A3
H40E	04B9	04B4
H400	046C	0467
H402	0463	045E
H404	0475	0470
H405	0482	047E
H406	048B	0486
H407	047F	047C
H408	0494	048F
H440	0536	0531
H443	052E	0529
H444	053F	053A
H50A	0606	0604
H50B	0617	0615
H50C	0624	0621
H50E	0642	0636
H508	05FD	05F9
H54A	06D0	06C4
H54C	06E2	06D6
H54E	06F4	06E8
H54F	0707	06FA
H540	066A	0668
H544	068D	068B
H546	06A9	06A0
H548	068E	0682
H6C0	0976	0968
H6C2	098E	0980
H6C4	09A5	0998
H6C6	09BD	09AF
H6C8	09D5	09C7
H6D0	09F5	09F0
H6D2	0A01	09FC
H6D3	0A0D	0A08
H6D5	0A19	0A14
H6D6	0A25	0A20
H6F0	0A34	0A2F
H6F1	0A42	0A3C, 0A3D, 0A3F, 0A40
H6F2	0A45	0A41
H600	07A5	07A1
H602	07AE	07AA
H640	082E	0822, 0824
H680	08F5	08F1
H74A	0B0E	0B05

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H744	OAE1	OADB
H780	09F0	0BEB
H784	UBB0	UBA7
H80A	OCAC	OCA3
H804	OC81	OC78
H84A	ODCD	ODC9
H842	OD81	OD7C
H846	ODA3	OD9F
H848	ODAD	ODA9
H849	OD82	OD82,0DB6
H85A	ODD6	ODD2
J50A	060D	0605
J50C	062B	0622
J50E	063F	0637
J540	0671	0669
J544	0694	068C
J546	06AC	069F
J600	0809	0804
J602	0817	0812
J680	0945	0940
J682	0958	094A
J70E	0AA9	0AA0
J74A	0B7A	0B75
J740	0B3B	0B2D,0B37
J742	0B48	0B43
J744	0B52	0B4D
J746	0B65	0B57,0B61
J748	0B70	0B6B
J8AA	0F34	0F31,0F32
J8A0	0EFB	0EF6
J8A1	0F0E	0F0A
J8A2	0F0B	0F09
J8A3	0F18	0F14
J8A4	0F15	0F13
J8A5	0F22	0F1E
J8A6	0F1F	0F1D
J8A7	0F2C	0F28
J8A8	0F29	0F27
J8A9	0F37	0F33
J8C0	0F40	0F3C
J8C1	0F48	0F44
J8C2	0F51	0F4D
J8C3	0F58	0F56
J8C4	0F5A	0F57
J8C5	0F62	0F5F,0F60
J8C6	0F64	0F61
J800	0CDB	0CCB
J802	0CDB	0CD6
J804	0CE6	0CE1
J806	0CF1	0CEC
J808	0D20	0D1B
J809	0D2B	0D26
J810	0D37	0D33
J811	0D44	0D53,0D5C,0D63
J812	0D51	0D4C
J813	0D5A	0D55
J814	0D3B	0D66
J815	0D34	0D31
J816	0D5D	0D60
J86A	0ED5	0ED3
J888	0ED8	0ED4
J880	0E8A	0E85
J882	0E97	0E92
J884	0EA0	0E9B
J886	0EAC	0EA7
J887	0EB7	0EB3
J888	0ECO	0EBB
J869	0ECC	0ECT

PROCESSOR-CONTROLLER FUNCTION TEST

K50B	061A	0616
K50C	062E	0623
K640	0824	088C
K682	0955	094C
K740	0B38	0B2F
K746	0B62	0B59
K849	0DBC	0DB7
N1C0	01F3	01EB
N1C1	01F4	01EE
N1D0	0211	01FB,01FC,01FF,0207
N1D1	0212	01F5,01F8,0208
N1D2	0213	0204,020D
N1E0	021E	0218,021A,021E
N1E1	021F	0214
N1F0	022A	022C
N1F1	022B	0224,0226,022B
N1F2	022C	0220
N100	0143	014C,014F
N140	019F	0154
N180	01EA	01A0,01A4
N2C0	0343	0319,031A,0323,032E
N2C2	0344	0324,032D,0337,0338,0339
N200	026C	0231,025D
N201	026D	0258
N202	026E	0262
N203	026F	0267
N240	0271	0271,0275
N241	0273	0270,0274
N242	027C	027F
N243	027D	0278,027E
N280	0313	02D9
N281	0314	02E3,0304
N282	0315	02E5,0309
N283	0316	02EE
N284	0317	02F0,02F9
N285	0318	02FB
N3C0	0412	03DF
N3C1	0413	03DD
N3C2	0414	03F6
N3C3	0415	03F4
N3C4	0416	03E1
N3C5	0417	03EB
N3C6	0418	03FF
N300	0366	0345,0346,034F
N302	0367	0350,0351,035A,035B,035C
N340	037F	0368
N341	0380	036A,0375
N380	03D7	0381
N381	03D8	0383,038D
N382	03D9	0396
N383	03DA	03A9,03BD
N384	03DB	03B4
N385	03DC	03CD
N4C0	05C3	0567,0568,0573,058C,0597,0599,05AD,05AF
N4C1	05C4	058D
N4C2	05C5	0598,05A3,05AE,05B9
N4C3	05C6	059A
N4C4	05C7	05B0
N400	048D	0419,041C,0430
N401	048E	0499
N402	048F	047A
N403	04C0	045C,0485
N404	04C1	0466
N405	04C2	043A,045A,0478,0497
N406	04C3	043D
N440	0543	04C4,0520
N441	0544	04C6,04E3,0502,0522
N442	0545	04E1,04EE



PROCESSOR-CONTROLLER FUNCTION TEST

N443	U546	0500
N444	U547	0500
N445	U548	0517
N480	U563	0549,0557
N481	U564	054B,0555,0559
N482	U565	054A,054C,0556,0558
N5C1	U798	074C,0760,077D,077F
N5C3	U79A	0763,0766,0770,0779,078A,0793
N5C5	U79C	074D,074E,0761,0764,0765,077A
N5C6	U79D	0757,0762,076F,077B,077E,0780,0794
N5C7	U79E	077C,0789,0795
N500	U65C	05C9,0601,0613,0633
N501	U65D	05D4
N502	U65E	05DF
N503	U65F	05F6,0646
N504	U660	061E
N505	U661	0649,064A
N506	U662	064B
N507	U5EA	05E0
N540	U70B	0664,06AF
N541	U70C	0679,067B
N542	U70D	067D,0687,06C1,06D3
N543	U70E	069C
N581	U748	070F
N582	U749	0735
N583	U74A	0721
N584	U74B	0722,072C
N6CA	U9E6	096C
N6CB	U9E7	0984,0A13,0A1D
N6CD	U9E8	0983,0A11,0A1F
N6CF	U9EA	09C4,09CB
N6CU	U9DC	0967,09DC,09EF
N6C1	U9DU	09DD,09EC,09F8,0A04,0A07
N6C2	U9DE	09DE,09FB
N6C3	U9DF	09DF
N6C4	U9E0	0965,097D,0995,0997,09AC,09C6,09E0,09E9
N6C5	U9E1	09AE,09E1
N6C6	U9E2	09E2
N6C7	U9E3	09E3
N6C8	U9E4	097F,09E4
N6C9	U9E5	096A,096B,0982,0983,099A,099B,09B1,09B2,09C9,09CA
N6F0	0A2D	0A2A
N6F1	0A38	0A29,0A2E,0A38
N6F2	0A3D	0A3A
N6F3	0A41	0A3E
N600	081B	0708,07E8,07F5,0811,081B
N601	081C	07B2,07B4,07B8,07C1,07CC,07CE,07D9,07E6,07F3,0801,080F,081C
N602	081D	0803,081D
N603	081E	07BE,07CB,07D8,07F5,07F2,0800,080C
N604	081F	080E
N640	088B	0821,0823,082D,0838,083A,083B,0845,0847,0848,0852,0854,0855,085F,0861,0862,086D,086F,0870,087B,087D,087E,0889
N642	089C	082E
N643	088D	085E,086C,087A,088B
N644	088E	0820,0837,0844,0851,0863,0871,087F
N660	08D8	0893,0894,089D,089E,08AB,08AC,08B5,08B6,08C3,08C4,08CD,08CE
N670	08EB	08DA
N660	095C	08ED,08F8,0902,0915,0916
N681	095D	08EE
N682	095E	0903
N683	095F	0929,092A
N684	0960	0933,093D,093F
N686	0961	0949
N687	0962	091F
N688	0963	093F,0948

PROCESSOR-CONTROLLER FUNCTION TEST

N7C0	OC51	OC01
N7C1	OC52	OC02
N7C2	OC53	OC03
N7C3	OC54	OC00
N7C4	OC55	OC16,OC17,OC23,OC3D
N7C5	OC56	OC21
N7C6	OC57	OC2A,OC3E
N700	0AB0	0A49,0A60,0A8E
N701	0AB1	0A4A,0A63,0A78,0A84,0A9F
N702	0AB2	0A4B,0A55,0A67,0A6C,0A79,0A83,0A9C,0A9A,0A9E
N703	0AB3	0A4C,0A61
N704	0AB4	0A56,0A6D
N705	0AB5	0A77,0A6F,0A91
N706	0AB6	0A98
N707	0AB7	0A7A
N74A	0B88	0B22
N74B	0B89	0B4C
N74C	0B8A	0B3F
N74D	0B7E	0ABD,0AD4,0AEC,0AFF,0B17,0B2B,0B41,0B55
N742	0B80	0A69,0ABF,0ACA,0AEA,0E15,0B16,0B18,0B40,0B42
N744	0B82	0A6B
N746	0B84	0AE8,0B68
N747	0B85	0B69,0B6A
N748	0B86	0B01,0B2C,0B56,0B74
N78A	0BF6	0B6B
N78D	0C00	0B06
N780	0BF4	0B8F,0BA3
N782	0BF6	0B8D,0BB7,0BDF
N784	0BF8	0BBE
N785	0BF9	0B82,0BEA
N786	0BFA	0B90,0B9A,0BB8,0BCC
N787	0BFB	0BF0
N788	0BFC	0BA4
N8A0	0F68	0EF5,0F07,0F11,0F1B,0F26
N8A1	0F69	0F08,0F2F,0F30
N8A2	0F6A	0EF4,0EFE,0F1C
N8A3	0F6B	0F12
N8A4	0F6C	0F25
N8C1	0F80	0F76
N8C2	0F81	0298,02A8
N8C3	0F82	0F77,0F80
N8C5	0F6E	0F3B
N8C6	0F70	0F3A,0F43,0F4C
N8C7	0F72	0F54,0F5D,0F5E
N8C8	0F74	0F55
N80A	0CFE	0CC9
N80C	0D00	0CD4
N80E	0D02	0CB4,0CDF
N80F	0D03	0D25
N800	0CF5	0C5D,0C74,0C8A,0CA0
N802	0CF6	0C59
N804	0CF8	0C87
N806	0CFA	0CB3
N807	0CFB	0CBF,0CE0,0CEB,0D5E
N808	0CFC	0CBE,0CCA
N810	0D04	0CEA
N811	0D05	0C6A
N812	0D06	0C5B,0C5F,0CD5
N813	0D07	0C88,0C8B
N816	0D08	0C96
N817	0D09	0D1A
N818	0D0A	0D19
N819	0D0C	0D24
N820	0D0E	0D30
N821	0D0F	0D43,0D44,0D45,0D49,0D4A,0D5D,0D5F
N822	0D13	0D42,0D62
N823	0D14	0D2F
N824	0D16	0D47

PROCESSOR-CONTROLLER FUNCTION TEST

NR4A	ODDA	ODC6
NR40	ODDC	OD6D, OD6E, OD91, OD92, OD85, OD8C
NR41	ODDD	OD6F, OD93, OD8D
NR42	ODDE	OD79, OD81, OD89
NR43	ODE0	OD88
NR44	ODE1	ODE2
NR45	ODE2	OD78, OD7B, OD83
NR46	ODE3	OD82
NR5A	ODDB	ODD0
NR6A	OEE6	OE8F, OEAF, OED0
NR8B	UEE7	OE56
NR8C	UEE8	OE6E
NR8D	UEE9	OE8D
NR8E	UEEA	OE20, OE45
NR8F	UEEB	OE66
NR80	UEEC	ODE0, ODF4, OE1C, OE1E, OE42, OF44, OE83, OE84, OFA3, OFA4, OEC3, OEC5
NR62	UEDE	ODE5, OE63
NR84	UEEO	OE0D, OE53, OCC6
NR85	UEE1	OE33, OE46
NR86	UEE2	OE12, OE35, OE38, OE91, OE8A
NR87	UEE3	OE0B
NR88	UEE4	OE70
RST1	OFF7	OFB7
RST2	OFF8	OFBA, OFBD
S501	O655	O648
S503	U658	O64C
U00A	OFB0	OFD9
U00B	OFB1	OF91, OFA0, OFC5, OFD6, OFDA
U00X	OFF5	OFB4, OFB6, OFCB, OFCD
U000	OFE9	OF85, OF9A
U001	OFEA	OF87, OF98
U003	OFAF	OFA1
U004	OFEE	
U006	OFFF	OF92, OFC9
U008	OFF2	
U009	OFF3	
V1AC	O27A	O27C
V154	O241	O23E
V168	O24E	O24B
V170	O253	O250
V174	O257	O254
V180	O261	O25E
V184	O266	O263
W8C0	OF76	OF72, OF67
W8C4	OF7D	OF7A
X000	O12D	3000, OFFA
X001	O286	3001, O281
X003	O2C7	3002, O2C3
X007	OF7C	3003
Z000	OFF0	OD3D, OF89, OF94, OFAB, OFC1, OFD1, OFE0, OFEC
Z020	OF7F	OF7B

DATE 28FEB66 01MAY66 04NOV66
FC NO. 415120 415120A 415233

PRG ID 0884-1
PAGE 50



```

3001      ABS      8B500020
          ORG      /3001 8B500030
          *          8B500040
          *          ** PROGRAM WAITS **
          *          8B500050
          *          8B500060
          *          8B500070
          *          8B500080
          *          8B500090
          *          8B500100
          *          8B500110
          *          8B500120
          *          8B500130
          *          8B500140
          *          8B500150
          *          8B500160
          *          8B500170
          *          8B500180
          *          8B500190
          *          8B500200
          *          8B500210
          *          8B500220
          *          8B500230
          *          8B500240
          *          8B500250
          *          8B500260
          *          8B500270
          *          8B500280
          *          8B500290
          *          8B500300
          *          8B500310
          *          8B500320
          *          8B500330
          *          8B500340
          *          8B500350
          *          8B500360
          *          8B500370
          *          8B500380
          *          8B500390
          *          8B500400
          *          8B500410
          *          8B500420
          *          8B500430
          *          8B500440
          *          8B500450
          *          8B500460
          *          8B500470
          *          8B500480
          *          8B500490
          *          8B500500
          *          8B500510
          *          8B500520
          *          8B500530
          *          8B500540
          *          8B500550
          *          8B500560
          *          8B500570
          *          8B500580
          *          8B500590
          *          8B500600
          *          8B500610
          *          8B500620
          *          8B500630
          *          8B500640
          *          8B500650
          *          8B500660
          *          8B500670
          *          8B500680
          *          8B500690
          *          8B500690
  
```

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3008 0 01B4      *          DC      WT8&1      WAIT 8
          *          *
          *          SCOPE ROUTINE WAIT. ENTER
          *          *          DESIRED NUMBER OF CAR
          *          *          STEPS IN DATA ENTRY SWITS.
          *          *          PUSH START TO CONTINUE.
          *          *
          *          8B500700
          *          8B500710
          *          8B500720
          *          8B500730
          *          8B500740
          *          8B500750
          *          8B500760
          *          8B500770
          *          8B500780
          *          8B500790
          *          8B500800
          *          8B500810
          *          8B500820
          *          8B500830
          *          8B500840
          *          8B500850
          *          8B500860
          *          8B500870
          *          8B500880
          *          8B500890
          *          8B500900
          *          8B500910
          *          8B500920
          *          8B500930
          *          8B500940
          *          8B500950
          *          8B500960
          *          8B500970
          *          8B500980
          *          8B500990
          *          8B501000
          *          8B501010
          *          8B501020
          *          8B501030
          *          8B501040
          *          8B501050
          *          8B501060
          *          8B501070
          *          8B501080
          *          8B501090
          *          8B501100
          *          8B501110
          *          8B501120
          *          8B501130
          *          8B501140
          *          8B501150
          *          8B501160
          *          8B501170
          *          8B501180
          *          8B501190
          *          8B501200
          *          8B501210
          *          8B501220
          *          8B501230
          *          8B501240
          *          8B501250
          *          8B501260
          *          8B501270
          *          8B501280
          *          8B501290
          *          8B501300
          *          8B501310
          *          8B501320
          *          8B501330
          *          8B501340
          *          8B501350
          *          8B501360
          *          8B501370
  
```

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*
013C 0 3001    * WT1  WAIT  1      SET BIT SW 8 TO
*              *              *SELECT MANUAL MODE
*
013D 0 086C    XIO  BSW  GO TO MANUAL ROUTINE
013E 0 003E    LD   BSW1 *IF SELECTED
013F 0 1008    SLA  8
0140 0 4C28 01AE BSC  L  CARMN,&Z
*
0142 0 0048    LD   ADDR&16  LOAD CAR
0143 0 0062    STO  LOAD
0144 0 0861    XIO  LOAD
*
0145 0 3002    * WT2  WAIT  2      CAR SHOULD BE 0000
*
0146 0 0038    LD   ADDR&1  LOAD CAR
0147 0 005E    STO  LOAD
0148 0 085D    XIO  LOAD
*
0149 0 3003    * WT3  WAIT  3      CAR SHOULD BE 7FFF
*
*              **RIPPLE ONE BIT THROUGH**
*              **ALL CAR BIT POSITIONS **
*
014A 0 6301    LDX  3 1      SET UP CHECK STORAGE
014B 0 6B30    STX  3 RIPL
014C 0 6310    LDX  3 16
*
014D 0 0700 018E CAR1  LD   L3 RIPPL-1  GFT BIT LOAD ADDRESS
014F 0 0056    STO  LOAD          SET ADDRESS IN IOCC
0150 0 0855    XIO  LOAD          LOAD CAR
0151 0 002A    LD   RIPL         LOAD A WITH EXP ADRS
*
0152 0 3004    * WT4  WAIT  4      CAR SHOULD BE THE
*              *SAME AS A REG
*
0153 0 1001    SLA  1      SFT CHECK STORAGE FOR
0154 0 0027    STO  RIPL         *NEXT BIT POSITION
0155 0 73FF    MDX  3 -1      SKIP WHEN ALL BIT
0156 0 70F6    MDX  CAR1         *POSITIONS CHECKED
*
*              ** CHECK CAR INCREMENT **
*
0157 0 6110    LOX  1 16      ADDRESS INDEX
0158 0 0500 017D CAR2  LD   L1 ADDR&-1  SET STARTING ADDRESS
015A 0 004B    STO  LOAD          *IN IOCC AND IN
015B 0 801E    A    ONE          *COUNTER
015C 0 001E    STO  COUNT
015D 0 6232    LDX  2 50      STEP INDEX
015E 0 0847    XIO  LOAD          LOAD CAR
*
015F 0 0848    CAR3  XIO  STEP  STEP CAR
0160 0 7401 017B MDX  L  COUNT,1  STEP COUNTER
0162 0 1000    NOP
0163 0 72FF    MDX  2 -1      SKIP IF 50 STEPS
0164 0 70FA    MDX  CAR3
0165 0 0015    LD   COUNT
0166 0 1000    NOP  0          ELIMINATE BIT POS.
0167 0 1000    NOP  0          *0 FROM CK WORD
*
0168 0 3005    * WT5  WAIT  5      CAR SHOULD BE THE
*              *SAME AS A REG
*
0169 0 71FF    MDX  1 -1      SKIP IF ALL ADDRESS
016A 0 70ED    MDX  CAR2         *USED
*
*              **CHECK INCREMENT FROM**
*              **ZERO TO 7FFF **

```

8B501380
8B501390
8B501400
8B501410
8B501420
8B501430
8B501440
8B501450
8B501460
8B501470
8B501480
8B501490
8B501500
8B501510
8B501520
8B501530
8B501540
8B501550
8B501560
8B501570
8B501580
8B501590
8B501600
8B501610
8B501620
8B501630
8B501640
8B501650
8B501660
8B501670
8B501680
8B501690
8B501700
8B501710
8B501720
8B501730
8B501740
8B501750
8B501760
8B501770
8B501780
8B501790
8B501800
8B501810
8B501820
8B501830
8B501840
8B501850
8B501860
8B501870
8B501880
8B501890
8B501900
8B501910
8B501920
8B501930
8B501940
8B501950
8B501960
8B501970
8B501980
8B501990
8B502000
8B502010
8B502020
8B502030
8B502040
8B502050

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*
016B 0 1010    * SLA  16
016C 0 0039    STC  LOAD
016D 0 0011    LD   ADDR&1  SET UP STEP COUNTER
016E 0 000C    STO  COUNT
016F 0 0836    XIO  LOAD          LOAD CAR
*
0170 0 0837    * CAR4 XIO  STEP  STEP CAR
0171 0 74FF 017B MDX  L  COUNT,-1  STEP COUNTER -1
0173 0 1000    NOP
0174 0 0006    LD   COUNT
0175 0 4820    BSC  7          SKIP IF ALL STEPS
0176 0 70F9    MDX  CAR4
*
0177 0 3006    * WT6  WAIT  6      CAR SHOULD BE 7FFF
*
0178 0 70B4    * MDX  CARCK     REPEAT CHECK
*
*              ** PROGRAM CONSTANTS **
*
0179 0 01F6    CONST DC  SVINT  INTERRUPT ADDRESS
017A 0 0001    ONE  DC  1      CONSTANT ONE
017B 0 0000    COUNT DC  0      STEP COUNTER
017C 0 0000    RIPL DC  0      RIPPLE CHECK WORD
017D 0 0000    BSW1 DC  0      BIT SWITCH READ IN
017E 0 FFFF    ADDRS DC /FFFF   TABLE OF STARTING
017F 0 7FFE    DC /7FFE   *ADDRESSES
0180 0 7FEF    DC /7FEF
0181 0 7F0E    DC /7F0E
0182 0 7EFF    DC /7EFF
0183 0 70FE    DC /70FE
0184 0 70EF    DC /70EF
0185 0 700E    DC /700E
0186 0 6FFF    DC /6FFF
0187 0 0FFF    DC /0FFF
0188 0 0FEF    DC /0FEF
0189 0 0F0E    DC /0F0E
018A 0 0EFF    DC /0EFF
018B 0 00FF    DC /00FF
018C 0 00EF    DC /00EF
018D 0 000E    DC /000E
018E 0 FFFF    DC /FFFF
018F 0 7FFF    RIPPL DC /7FFF   RIPPLE TEST LOAD
0190 0 3FFF    DC /3FFF   *ADDRESSES
0191 0 1FFF    DC /1FFF
0192 0 0FFF    DC /0FFF
0193 0 07FF    DC /07FF
0194 0 03FF    DC /03FF
0195 0 01FF    DC /01FF
0196 0 00FF    DC /00FF
0197 0 007F    DC /007F
0198 0 003F    DC /003F
0199 0 001F    DC /001F
019A 0 000F    DC /000F
019B 0 0007    DC /0007
019C 0 0003    DC /0003
019D 0 0001    DC /0001
019E 0 0000    DC /0000
01A0 0000    BSS  E  0
01A0 0 4C00    RESRT DC /4C00  RESTART INSTRUCTION
01A1 0 012D    DC  CARCK
01A2 0 0000    UMSKO DC /0000  UNMASK INTERRUPTS
01A3 0 0480    DC /0480  *IOCC
01A4 0 0000    UMSK1 DC /0000
01A5 0 0481    DC /0481
01A6 0 0000    LOAD  DC  0      LOAD CAR IOCC
01A7 0 05A0    DC /05A0
01A8 0 0000    STEP  DC  0      INCREMENT CAR IOCC

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8B502060
8B502070
8B502080
8B502090
8B502100
8B502110
8B502120
8B502130
8B502140
8B502150
8B502160
8B502170
8B502180
8B502190
8B502200
8B502210
8B502220
8B502230
8B502240
8B502250
8B502260
8B502270
8B502280
8B502290
8B502300
8B502310
8B502320
8B502330
8B502340
8B502350
8B502360
8B502370
8B502380
8B502390
8B502400
8B502410
8B502420
8B502430
8B502440
8B502450
8B502455
8B502460
8B502470
8B502480
8B502490
8B502500
8B502510
8B502520
8B502530
8B502540
8B502550
8B502560
8B502570
8B502580
8B502590
8B502600
8B502610
8B502620
8B502630
8B502640
8B502650
8B502660
8B502670
8B502680
8B502690
8B502700
8B502710

01A9 0 01A0 DC /01A0 8B502720
01AA 0 017D BSW DC BSW1 READ BIT SWITCH IOCC 8B502730
01AB 0 0240 DC /0240 8B502740
01AC 0 0000 SNSW DC /0000 READ SNS SWITCH IOCC 8B502750
01AD 0 0760 DC /0760 8B502760
* 8B502770
* *****
* MANUAL AND SCOPE ROUTINE
* *****
01AE 0 1000 CARMN NOP
01AF 0 3007 WT7 WAIT 7 ENTER STARTING ADDR 8B502780
* 8B502790
01B0 0 08F9 XIO BSW READ IN ADDRESS AND 8B502800
01B1 0 C0CB LD BSW1 *SAVE 8B502810
01B2 0 D040 STO MNAD 8B502820
* 8B502830
01B3 0 3008 WT8 WAIT 8 ENTER NUMBER OF CNTS 8B502840
01B4 0 08F5 XIO BSW 8B502850
01B5 0 C0C7 LD BSW1 READ IN NUMBER OF 8B502860
01B6 0 D03D STO MNCT *CNTS AND SAVE 8B502870
* 8B502880
01B7 0 300C WTC WAIT 12 ENTER CNTRL OPTIONS 8B502890
* 8B502900
01B8 0 C03A CARM2 LD MNAD SET STARTING ADDRESS 8B502910
01B9 0 D0EC STO LOAD *INTO IOCC AND INTO 8B502920
01BA 0 D0C0 STO COUNT *COUNTER 8B502930
01BB 0 C038 LD MNCT SET NUMBER OF STEPS 8B502940
01BC 0 D038 STO MNCTR *IN STEP COUNTER 8B502950
01BD 0 08E8 XIO LOAD LOAD CAR 8B502960
* 8B502970
01BE 0 08EB CARM3 XIO BSW READ BIT SWITCHES 8B502980
01BF 0 C0BD LD BSW1 8B502990
01C0 0 1004 SLA 4 SKIP IF BIT SW 4 ON 8B503000
01C1 0 4810 BSC - SKIP IF BIT SW 4 ON 8B503010
01C2 0 700F MDX CARM4 8B503020
* 8B503030
01C3 0 3009 WT9 WAIT 9 SINGLE STEP CAR WITH 8B503040
* *START BUTTON 8B503050
* 8B503060
01C4 0 C02F LD MNCT IF NUMBER OF STEPS 8B503070
01C5 0 4818 BSC &- *ENTERED IS ZERO 8B503080
01C6 0 70F1 MDX CARM2 *SS WILL LOAD CAR 8B503090
01C7 0 08E0 XIO STEP STEP CAR 8B503100
01C8 0 7401 017B MDX L COUNT,1 STEP COUNTER 8B503110
01CA 0 1000 NOP 8B503120
01CB 0 74FF 01F5 MDX L MNCTR,-1 DECREMENT STEP CNTR 8B503130
01CD 0 1000 NOP 8B503140
01CE 0 C026 LD MNCTR 8B503150
01CF 0 4820 BSC 7 SKIP IF COUNTER ZERO 8B503160
01D0 0 70ED MDX CARM1 CONTINUE STEP 8B503170
01D1 0 70E6 MDX CARM2 RELOAD CAR 8B503180
* 8B503190
* ** NOT BIT SWITCH 4 ** 8B503200
* 8B503210
01D2 0 C021 CARM4 LD MNCT CHECK NUMBER OF STEP 8B503220
01D3 0 4818 BSC &- *SKIP IF STEPS NOT 0 8B503230
01D4 0 700A MDX CARM6 8B503240
01D5 0 08D2 CARM5 XIO STEP STEP CAR 8B503250
01D6 0 7401 017B MDX L COUNT,1 STEP COUNTER 8B503260
01D8 0 1000 NOP 8B503270
01D9 0 74FF 01F5 MDX L MNCTR,-1 DECREMENT STEP CNTR 8B503280
01DB 0 1000 NOP 8B503290
01DC 0 C018 LD MNCTR 8B503300
01DD 0 4820 BSC Z SKIP IF COUNTER ZERO 8B503310
01DE 0 70F6 MDX CARM5 8B503320
* 8B503330
* **COUNT CMPLT CK BIT SW2** 8B503340
* 8B503350
* 8B503360
* 8B503370
* 8B503380
* 8B503390

01DF 0 08CA * CARM6 XIO BSW READ BIT SWITCHFS 8B503400
01E0 0 C09C LD BSW1 8B503410
01E1 0 1002 SLA 2 8B503420
01E2 0 4810 BSC - SKIP IF BIT SW 2 ON 8B503430
01E3 0 7001 MDX *&1 8B503440
01E4 0 70C9 MDX CARMN CHANGE PARAMETERS 8B503450
01E5 0 1001 SLA 1 8B503460
01E6 0 4810 BSC - SKIP IF BIT SW 3 ON 8B503470
01E7 0 70D0 MDX CARM2 LOOP ROUTINE 8B503480
01E8 0 C092 LD COUNT 8B503490
01E9 0 8090 A ONE 8B503500
01EA 0 1001 SLA 1 ELIMINATE BIT POS. 8B503510
01EB 0 1801 SRA 1 *0 FROM CK WORD 8B503520
* 8B503530
01EC 0 300A * WTA WAIT 10 CAR SHOULD BE SAME 8B503540
* *AS A REG 8B503550
* 8B503560
01ED 0 08BC XIO BSW READ BIT SWITCHES 8B503570
01EE 0 C08E LD RSW1 8B503580
01EF 0 1002 SLA 2 8B503590
01F0 0 4810 BSC - SKIP IF BIT SW 2 ON 8B503600
01F1 0 70C6 MDX CARM2 RERUN PRESENT SETUP 8B503610
01F2 0 70BB MDX CARMN CHANGE PARAMETERS 8B503620
* 8B503630
01F3 0 0000 MNAD DC 0 ADDRESS ENTRY 8B503640
01F4 0 0000 MNCT DC 0 NUMBER STEPS ENTRY 8B503650
01F5 0 0000 MNCTR DC 0 STEP COUNTFR 8B503660
* 8B503670
* *****
* INTERRUPT TRAP ROUTINE
* *****
01F6 0 0000 SVINT DC 0 IF 8B503720
01F7 0 D02C STO SVIO SAVE ACCUMULATOR 8B503730
01F8 0 082D XIO ILSW RESET ILSW 8B503740
01F9 0 7402 0223 MDX L SV7,2 SET PASS SWITCH 8B503750
01FB 0 1010 SLA 16 8B503760
01FC 0 D023 STO SV4 CLEAR AREA CODE CNTR 8B503770
01FD 0 C020 LD SV2 8B503780
01FE 0 D023 STO SV6 SET IOCC IN USE SW 8B503790
01FF 0 C01D SVINO LD SV1 8B503800
0200 0 D020 STO SV5 SET MODIFIER COUNTER 8B503810
0201 0 C01F SVINI LD SV4 * 8B503820
0202 0 1008 SLA 11 * 8B503830
0203 0 F81D OR SV5 *BUILD IOCC 8B503840
0204 0 F81D OR SV6 * 8B503850
0205 0 D01F STO SVIO&1 * 8B503860
0206 0 081D XIO SVIO SENSE/RESET ILSW 8B503870
0207 0 74FF 0221 MDX L SV5,-1 8B503880
0209 0 70F7 MDX SVINI BRNCH IF NOT ALL MOD 8B503890
020A 0 7401 0220 MDX L SV4,1 INCREMENT AREA CODE 8B503900
020C 0 C013 LD SV4 8B503910
020D 0 900E S SV0 8B503920
020E 0 4808 BSC & CHECK IF ALL AC USED 8B503930
020F 0 70EF MDX SVINO SKIP IF ALL AC USED 8B503940
0210 0 74FF 0223 MDX L SV7,-1 GO SENSE WITH NXT AC 8B503950
0212 0 7001 MDX *&1 SKIP IF SECOND PASS 8B503960
0213 0 7005 MDX SVEXT-1 8B503970
0214 0 C00A LD SV3 SET IOCC FOR PI 8B503980
0215 0 D00C STO SV6 8B503990
0216 0 1010 SLA 16 8B504000
0217 0 D008 STO SV4 SET AC FOR NEXT PASS 8B504010
0218 0 70E6 MDX SVINO 8B504020
0219 0 C00A LD SVIO RESTORE ACCUMULATOR 8B504030
021A 0 4CC0 01F6 SVEXT BOSC I SVINT EXIT 8B504040
* IX
* 8B504050
* 8B504060
* ** CONSTANTS **
* 8B504070

CAR EXERCISER

CAR EXERCISER

```

*
021C 0 001F SV0 DC /001F NUMBER OF AREA CODES 88504080
021D 0 00FF SV1 DC /00FF NUMBER OF MODIFIERS 88504090
021E 0 0701 SV2 DC /0701 SENSE/RESET DSW 88504100
021F 0 0700 SV3 DC /0700 SENSE/RESET PISW 88504120
0220 0 0000 SV4 DC 0 AREA CODE INDICATOR 88504130
0221 0 0000 SV5 DC 0 MODIFIER INDICATOR 88504140
0222 0 0000 SV6 DC 0 IOCC IN USE 88504150
0223 0 0000 SV7 DC 0 PASS SWITCH 88504160
0224 0000 BSS F 0 88504170
0224 0 0000 SVIO DC 0 SENSE DSW/PISW IOCC 88504180
0225 0 0000 DC 0 88504190
0226 0 0000 ILSW DC 0 SENSE ILSW IOCC 88504200
0227 0 0300 DC /0300 88504210
* 88504220
* ***** 88504230
* ERROR TRAP ROUTINE 88504240
* ***** 88504250
* 88504260
0228 0 0000 ERROR DC 0 ENTRY POINT IE 88504270
0229 0 08FC XIO ILSW SENSE ILSW 88504280
022A 0 300B WTB WAIT 11 ILSW IN A REG 88504290
022B 0 4CC0 0228 BOSC I ERROR EXIT IX 88504300
* 88504310
022E 012D END CARCK 88504320
NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY

```

```

ADDRS 017E 0142 0146 0158 016D
BSW 01AA 013D 0180 0184 018E 01DF 01ED
BSW1 017D 013E 01AA 0181 0185 01BF 01E0 01EE
CARCK 012D 0178 01A1 022E
CARMN 01AE 0140 01E4 01F2
CARM2 0188 01C6 01D1 01E7 01F1
CARM3 018E 01D0
CARM4 01D2 01C2
CARM5 01D5 01DE
CARM6 01DF 01D4
CAR1 014D 0156
CAR2 0158 016A
CAR3 015F 0164
CAR4 0170 0176
CONST 0179 012E
COUNT 017B 015C 0160 0165 016E 0171 0174 01BA 01C8 01D6 01E8
ERROR 0228 0133 022B
ILSW 0226 01F8 0229
LOAD 01A6 0143 0144 0147 0148 014F 0150 015A 015E 016C 016F 01B9 01BD
MNAD 01F3 0182 0188
MNCT 01F4 0186 018B 01C4 01D2
MNCTR 01F5 018C 01CB 01CE 01D9 01DC
ONE 017A 0158 01E9
RESRT 01A0 0137
RIPL 017C 0148 0151 0154
RIPPL 018F 014D
SNSW 01AC
STEP 01A8 015F 0170 01C7 01D5
SVEXT 021A 0213
SVINT 01F6 0179 021A
SVINO 01FF 020F 0218
SVINI 0201 0209
SVIO 0224 01F7 0205 0206 0219
SV0 021C 020D
SV1 021D 01FF
SV2 021E 01FD
SV3 021F 0214
SV4 0220 01FC 0201 020A 020C 0217
SV5 0221 0200 0203 0207
SV6 0222 01FE 0204 0215
SV7 0223 01F9 0210
UMSK0 01A2 013A
UMSK1 01A4 013B
WTA 01EC 300A
WTB 022A 300B
WTC 01B7 300C
WT1 013C 3001
WT2 0145 3002
WT3 0149 3003
WT4 0152 3004
WT5 0168 3005
WT6 0177 3006
WT7 01AF 3007
WT8 01B3 3008
WT9 01C3 3009
END OF ASSEMBLY

```

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1. PURPOSE

THE CAR EXERCISER PROGRAM IS TO BE USED IN CONJUNCTION WITH THE CYCLE STEAL REQUEST TEST AND THE CYCLE STEAL ACKNOWLEDGE TEST FEATURES OF THE DATA CHANNEL. THE PROGRAM IS USED TO LOAD AND STEP THE CAR SELECTED FOR TEST. ALL BIT POSITIONS IN THE C.A.R. ARE TESTED.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

THE BASIC DIAGNOSTIC LOADER IS REQUIRED TO LOAD THIS PROGRAM.

2.2 EQUIPMENT PREREQUISITES

THE FOLLOWING EQUIPMENT IS REQUIRED.

- A. 1800 PROCESSOR/CONTROLLER
- B. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.

3. USE PROCEDURE

3.1 PROGRAM LOADING

REFER TO BASIC DIAGNOSTIC LOADER DOCUMENTATION FOR PROGRAM LOADING PROCEDURES.

3.2 PROGRAM OPERATION

WITH PROGRAM STOPPED AT WAIT 1, B REG = 3001,

- A. INSERT 3 JUMPERS AS FOLLOWS, TO ACTIVATE THE CYCLE STEAL REQUEST TEST AND CYCLE STEAL ACKNOWLEDGE TEST LEVELS.

JUMPER 1 - B-B1G2D02 (CR221) TO B-B1G5D09 (CQ111)

THIS JUMPER ACTIVATES THE SET CAR FUNCTION DURING AN INITIALIZE READ OR WRITE.

JUMPERS 2 AND 3 - REFER TO LOGIC PAGE CT971. INSTALL JUMPER 2 FROM CS REQUEST TEST SIGNAL TO CS REQUEST LEVEL TO BE TESTED. INSTALL JUMPER 3 FROM CS ACKNOWLEDGE TEST TO CS ACKNOWLEDGE LEVEL TO BE TESTED. INSTALL JUMPER 3 FROM CS ACKNOWLEDGE TEST TO CS ACKNOWLEDGE LEVEL TO BE TESTED.

NOTE - POINTS FOR CHANNEL 0 - 8 ARE ON THE 60 B - B1 BOARD AND POINTS FOR CHANNEL 9 - 14 ARE ON THE 60 D - A1 BOARD.

- B. SET 'DISPLAY ADDRESS REGISTER' SWITCH TO DISPLAY C.A.R. BEING TESTED.
- C. TO RUN PROGRAM CONTROLLED MODE, SET DATA ENTRY SWITCHES TO 0000. DEPRESS START. PROCEED WITH PROGRAM ACCORDING TO WAIT INSTRUCTIONS 2 THROUGH 6.
- D. TO RUN MANUAL CONTROLLED (SCOPING) MODE, SET DATA ENTRY SWITCH 8 ON AND DEPRESS START. PROGRAM WILL COME TO WAIT 7. PROCEED WITH PROGRAM ACCORDING TO WAIT INSTRUCTIONS 7 THROUGH A.

TABLE 1
 PROGRAM OPTIONS - DATA ENTRY SWITCHES

NOTE -- FUNCTIONS OF SWITCHES 2,3,4 ARE FOR SCOPING ROUTINES ONLY.

```

*****
*
* NOTE. TABLE 1 PROGRAM OPTIONS MAY BE ENTERED ONLY WHEN PROGRAM IS STOPPED AT
* WAIT 1.
*
*****
*
* DATA ENTRY SWITCHES * OPTION DESCRIPTION
*****
* 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 *
*****
*
* . . . 0.....RUN PROGRAM CONTROLLED MODE
* . . . 1.....RUN MANUAL CONTROLLED (SCOPING) MODE
* . . 1.....SINGLE STEP C.A.R. WITH START
* . . . . .PUSHBUTTON
* . 1.....STOP AFTER EACH PASS THROUGH SCOPE
* . . . . .ROUTINE
* 1.....RETURN TO WAIT 7 TO CHANGE ADDRESS
* . . . . .AND NUMBER OF STEPS
*****
    
```

3.3 PROGRAM TERMINATION

IF RUNNING PROGRAM CONTROLLED MODE, PROGRAM WILL EXECUTE ONCE AND STOP AT WAIT 6. DEPRESSING THE START PUSHBUTTON WILL RETURN THE PROGRAM TO WAIT 1, WHICH IS THE START OF THE PROGRAM.

IF RUNNING MANUAL (SCOPE) CONTROLLED MODE, PROGRAM MAY BE TERMINATED BY DEPRESSING THE STOP PUSHBUTTON. DEPRESSING RESET AND START WILL RETURN THE PROGRAM TO WAIT 1.

IMPORTANT NOTE

BEFORE RETURNING SYSTEM TO THE CUSTOMER, INSURE THAT THE 3 JUMPERS INSERTED AT WAIT 1, ARE REMOVED FROM THE CHANNEL.

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND 1 REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```

3001 0 01ED          DC      WAIT1+1
                   *          WAIT 1
                   *
                   *          ONE OF THE METERED I/O UNITS
                   *          FAILED TO SEND A RESPONSE
                   *          INTERRUPT TO THE PROGRAM. INDEX
                   *          REGISTER 1 WILL HAVE THE ADDRESS
                   *          OF THE IOCC. THE AREA CODE WILL
                   *          INDICATE THE I/O UNIT NOT READY.
                   *          IF A 2401/02 DRIVE IS NOT READY,
                   *          PROGRAM WILL NOT STOP AT WAIT 1.
                   *
*****

```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

1 REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO 1 REG READING.

4. PRINTOUTS

THERE ARE NO PRINTOUTS ASSOCIATED WITH THE CAR EXERCISER PROGRAM.

5. COMMENTS

(THE CAR EXERCISER CONSISTS OF A PROGRAM CONTROL ROUTINE, AND A MANUAL (OPERATOR CONTROL) ROUTINE.

THE PROGRAM CONTROL ROUTINE CONTAINS THE STARTING ADDRESS AND PREDEFINED NUMBER OF STEPS USED TO LOAD AND INCREMENT THE C.A.R. BEING TESTED.

THE C.A.R. IS LOADED USING AN XIO INSTRUCTION WHOSE IOCC IS 0000 05A0. THE CAR WILL AUTOMATICALLY BE INCREMENTED BY 1 EACH TIME IT IS LOADED. THE INCREMENT BY 1 IS A HARDWARE FUNCTION. THE CAR IS STEPPED USING AN XIO INSTRUCTION WHOSE IOCC IS 0000 01A0. EACH XIO CAUSES THE CAR TO BE INCREMENTED BY 1.

WAITS 2 AND 3 ARE USED TO DISPLAY THE RESULTS OF LOADING TESTS. THE C.A.R. IS FIRST LOADED TO 7FFF. THE AUTO INCREMENT SHOULD STEP IT TO 0000. THIS RESULT IS DISPLAYED AT WAIT 2. THE C.A.R. IS THEN LOADED TO 7FFE. THE AUTO INCREMENT SHOULD STEP IT TO 7FFF. THIS IS DISPLAYED AT WAIT 3.

WAIT 4 IS USED TO DISPLAY THE RESULTS OF RIPPLING A 1 BIT THROUGH ALL CAR BIT POSITIONS. EXCEPT BIT 0. EACH TIME THE WAIT OCCURS, THE A REG WILL CONTAIN THE VALUE THAT SHOULD APPEAR IN CAR.

THE LOAD ADDRESSES ARE AS FOLLOWS,

- 1. 0000 9. 00FF
- 2. 0001 10. 01FF
- 3. 0003 11. 03FF

- 4. 0007 12. 07FF
- 5. 000F 13. 0FFF
- 6. 001F 14. 1FFF
- 7. 003F 15. 3FFF
- 8. 007F

WAIT 5 IS USED TO DISPLAY THE RESULTS OF THE C.A.R. INCREMENT TEST. THE C.A.R. IS LOADED 16 TIMES WITH 16 DIFFERENT STARTING ADDRESSES, AND AFTER EACH LOAD THE C.A.R. IS INCREMENTED 50 TIMES. EACH TIME THE WAIT OCCURS, THE A REG. WILL CONTAIN THE VALUE WHICH SHOULD APPEAR IN THE C.A.R.

THE STARTING ADDRESSES USED ARE AS FOLLOWS,

- 1. 7FFF 9. 6FFF
- 2. 000E 10. 700E
- 3. 00EF 11. 70FE
- 4. 00FE 12. 70FE
- 5. 0EFF 13. 7EFF
- 6. 0FOE 14. 7FOE
- 7. 0FEF 15. 7FEF
- 8. 0FFE 16. 7FFE

FOLLOWING THE INCREMENT TEST, THE CAR. IS LOADED TO 0000 AND STEPPED 7FFE TIMES. AT WAIT 6, THE CAR SHOULD CONTAIN 7FFF.

ANY ERRORS OBSERVED BY THE OPERATOR CAN BE LOOPED BY SETTING THE STARTING ADDRESS AND NUMBER OF STEPS USED BY THE AUTO ROUTINE INTO THE MANUAL ROUTINE, AND RUNNING THE MANUAL ROUTINE WITH SENSE SWITCH 0 ON.

THE MANUAL CONTROLLED ROUTINE WILL LOAD THE C.A.R. WITH THE ADDRESS ENTERED BY THE OPERATOR AT WAIT 7. IT WILL THEN STEP THE C.A.R. THE NUMBER OF TIMES SPECIFIED BY THE OPERATOR AT WAIT 8. CONTROL OF THE ROUTINE IS TRANSMITTED VIA THE DATA ENTRY SWITCHES. (SEE TABLE 1). IF THE NUMBER OF STEPS ENTERED BY THE OPERATOR AT WAIT 8 IS ZERO, THEN THE PROGRAM WILL ISSUE CONTINUOUS LOAD C.A.R. INSTRUCTION ACCORDING TO THE DATA ENTRY SWITCH SETTING. IF NO CONTROL OPTIONS ARE ENTERED, THE SCOPING ROUTINE WILL LOOP CONTINUOUSLY USING THE DATA ENTERED AT WAITS 7 AND 8 AS INPUT PARAMETERS.

6. APPENDIX (NONE)

----- LAST PAGE -----



```
3001          ABS
              ORG   /3001
              -----
*              PROGRAM WAIT SECTION
*
3001 0 01F6   DC    WAIT1&1   WAIT 1
*
*              ONE OF THE METERED I/O UNITS
*              FAILED TO SEND A RESPONSE
*              INTERRUPT TO THE PROGRAM. INDEX
*              REGISTER 1 WILL HAVE THE ADDRESS
*              OF THE IOCC. THE AREA CODE WILL
*              INDICATE THE I/O UNIT NOT READY.
*              IF A 2401/02 DRIVE IS NOT READY,
*              PROGRAM WILL NOT STOP AT WAIT 1.
*
3002 0 0210   DC    WAIT2&5   WAIT 2
*
*              SET THE PC DATA ENTRY SWITCHES TO
*              INDICATE THE NUMBER OF 72 SECOND
*              DELAY LOOPS DESIRED. PRESS THE
*              PC START BUTTON TO START THE
*              EXERCISE.
*
3003 0 0230   DC    WAIT3&1   WAIT 3
*
*              END OF DELAY. METERS SHOULD BE
*              READ AND THE ELAPSED TIME COMPUTED
*              %BY HAND<. TO RUN THE TEST AGAIN,
*              SET THE PC DATA ENTRY SWITCHES
*              TO INDICATE THE NUMBER OF 72
*              SECOND DELAY LOOPS DESIRED AND
*              PRESS THE PC START BUTTON.
*
3004 0 0200   DC    WAIT4&1   WAIT 4
*
*              DID NOT RECEIVE A PRINTER COMPLETE
*              INTERRUPT FROM THE 1443.
*              MAKE THE 1443 READY AND THEN PRESS
*              THE PC START BUTTON.
*
*-----
0123          ORG   /0123
0123 0 B600   DC    /B600
*****
*              TO RESTART THE PROGRAM,
*              PRESS THE PC RESET BUTTON AND
*              START BUTTON. THE PROGRAM
*              WILL START ALL METERS AGAIN
*              AND STOP AT @WAIT 2@.
*****
0124 0 012C   DC    LOAD
012C          ORG   /012C
012C 0 C400 02D0  LOAD  LD  L  RSTR
012E 0 D400 0000          STO  L  0
0130 0 C400 02D1          LD  L  RSTR&1
0132 0 D400 0001          STO  L  1
0134 0 610D          LDX  1 13          XR1# NO OF EDITS
0135 0 6203          LDX  2 /0003
0136 0 6700 02A9          LDX  L3 CNFIG
*
*****
*              TRANSFER THE EDIT CARD INFO
*              TO I/O CONFIGURATION
```

```
0138 0 C200
0139 0 D300
013A 0 7201
013B 0 7301
013C 0 71FF
013D 0 70FA
*
013E 0 C400 02B8
0140 0 D400 02B7
0142 0 C400 02C8
0144 0 D400 02B6
0146 0 6500 0280
0148 0 6600 02A9
014A 0 C200
014B 0 4828
014C 0 7028
014D 0 1808
014E 0 4818
014F 0 7022
0150 0 D400 02BA
0152 0 6780 02BA
0154 0 C400 02CB
0156 0 B400 02BA
0158 0 7010
0159 0 1000
015A 0 73F2
015B 0 7005
015C 0 C400 02B8
015E 0 EC00 02C9
0160 0 700D
0161 0 C400 02B8
0163 0 1801
0164 0 73FF
0165 0 70FD
0166 0 EC00 02C9
0168 0 7005
0169 0 C400 02B8
016B 0 1801
016C 0 73FF
016D 0 70FD
016E 0 D480 02B7
0170 0 4C00 017F
0172 0 C400 02B8
0174 0 70F9
0175 0 D500 0000
0177 0 7102
0178 0 7402 02B7
017A 0 7202
017B 0 74FE 02B6
017D 0 70CC
017E 0 7023
*
017F 0 7401 02B7
0181 0 C200
0182 0 1008
0183 0 180C
0184 0 4818
0185 0 700F
```

```
*              TABLE. %CNFIG<
*****
*              BUILD INTR LVL WORD AND STORE
*              IN INTERRUPT TABLE. %ITBLE<
*****
RERUN LD  L  ITBLE      ADDR OF ITBLE
      STO  L  ITBL1
      LD  L  EDCT1      SET EDIT COUNT
      STO  L  EDCT1      *TO 12.
      LDX  L1 IOCC
      LDX  L2 CNFIG
BUILD LD  2 0
      BSC  &Z          IS THIS DEV ON SYS
      MDX  NODEV      DEV NOT ON SYSTEM
      SRA  8
      BSC  &-
      MDX  BITO
      STC  L  WORD
      LDX  I3 WORD
      LD  L  EASY      A REG # 000E
      CMP  L  WORD      IS INTR LVL LESS
      MDX  LES14      *THAN FOURTEEN
      NOP  0
      MDX  3 -14
      MDX  ILWD1
      LD  L  EIGHT      INTR LVL # 14
      OR  L  ONE
      MDX  ST01
ILWD1 LD  L  EIGHT
SHRT2 SRA  1
      MDX  3 -1
      MDX  SHRT2
      OR  L  ONE      BIT 15# 1 MEANS IL
      MDX  ST01      *IS GREATER THAN 13.
LES14 LD  L  EIGHT
SHRT1 SRA  1
      MDX  3 -1
      MDX  SHRT1
ST01  STO  I  ITBL1      STO IL WD IN TABLE
      BSC  L  ILSWD      GO TO BUILD ILSW WD
      BITO  LD  L  EIGHT      A0# 1
      MDX  ST01
NODEV STO  L1 0
      MDX  1 2
      MDX  L  ITBL1,2
      MDX  2 2
      MDX  L  EDCT1,-2
      MDX  BUILD
      MDX  ADDR
*
*****
*              BUILD ILSW WORD AND STORE IN
*              INTERRUPT TABLE. %ITBLE<
*****
ILSWD MDX  L  ITBL1,1
      LD  2 0
      SLA  8
      SRA  12
      BSC  &-
      MDX  BZERO
```

METER EXERCISER

METER EXERCISER

```

0186 0 D400 02BA      STC  L  WORD
0188 0 6780 02BA      LDX  I3 WORD
018A 0 C400 02B8      LD   L  EIGHT
018C 0 1801          SHRT3 SRA  1
018D 0 73FF          MDX  3 -1
018E 0 70FD          MDX          SHRT3
018F 0 D480 02B7      STO2  STO  I  ITBL1
0191 0 7401 02B7      MDX  L  ITBL1,1  INCR ITBLE
0193 0 4C00 0198      BSC  L  CTLWD
0195 0 C400 02B8      BZERO LD  L  EIGHT
0197 0 70F7          MDX          STO2
*
*****
* BUILD IOCC CONTROL WORD
*****
0198 0 7201          CTLWD MDX  2 1
0199 0 7101          MDX  1 1
019A 0 C200          LD   2 0
019B 0 E900          OR   1 0
019C 0 D100          STO  1 0          PUT CTRL WD IN IOCC
*
* DECR REGISTERS
*
019D 0 7201          MDX  2 1
019E 0 7101          MDX  1 1
019F 0 74FE 02B6      MDX  L  EDCT1,-2
01A1 0 70A8          MDX          BUILD
*
*****
* LOAD THE ADDR OF %SVINT< INTO
* ALL INTR ADDR LOCATIONS.
*****
01A2 0 6218          ADDR  LDX  2 24
01A3 0 6108          LDX  1 8
01A4 0 C400 02CC      LD   L  INTRN
01A6 0 D100          ADDR1 STO  1 0
01A7 0 7101          MDX  1 1
01A8 0 72FF          MDX  2 -1
01A9 0 70FC          MDX          ADDR1
*
*****
* DETERMINE IF THIS DEVICE IS
* ON THE SYSTEM AND IF DESIRED
* INTR IS GREATER THAN
* LEVEL 13
*****
01AA 0 C400 02C8      LD   L  EDCT
01AC 0 D400 02B6      STO  L  EDCT1
01AE 0 6500 0280      LDX  L1 IOCC          XR1# IOCC
01B0 0 6600 02BC      LDX  L2 ITBLE&1      LOC OF INTR TABLE
01B2 0 C100          LDRT2 LD  1 0          PUT IOCC IN ACCUM
01B3 0 F400 02B9      EOR  L  FFFF
01B5 0 4818          BSC  &-              IS THIS DEV ON SYS
01B6 0 700C          MDX  INCR           NO
01B7 0 C200          LDRT1 LD  2 0          PUT IL WD IN ACCUM
01B8 0 4804          BSC  E              GREATER THAN 13
01B9 0 7013          MDX  GREAT
01BA 0 C400 028D      LD   L  MASK&1
01BC 0 E400 02CA      AND  L  FFFE          SET BIT 15# ZERO
01BE 0 D400 028D      STO  L  MASK&1
01C0 0 C200          LD   2 0          PUT IL WD IN ACCUM
01C1 0 630B          LDX  3 /000B        XR3# 11
01C2 0 7012          MDX  VECT
01C3 0 C400 02B9      INCR  LD  L  FFFF
01C5 0 D400 02D3      STO  L  PASS1
01C7 0 7102          MDX  1 2
01C8 0 7202          MDX  2 2
01C9 0 74FE 02B6      MDX  L  EDCT1,-2
01CB 0 70E6          MDX  LDRT2          GO TO NEXT IOCC

```

```

01CC 0 703E          MDX  WAIT2          RDY TO START DELAY
*
*****
* PLACE THE XFER VECTOR IN THE
* CORRECT ADDRESS
*****
01CD 0 C400 028D      GRFAT LD  L  MASK&1
01CF 0 EC00 02C9      OR   L  ONE
01D1 0 D400 028D      STO  L  MASK&1          SET BIT 15# ONE
01D3 0 6319          LDX  3 /0019
01D4 0 C200          LD   2 0          PUT IL WD IN ACCUM
*
01D5 0 4828          VECT  BSC  &Z          IS BIT 0 ON
01D6 0 7003          MDX  VECT1          * YES
01D7 0 1001          SLA  1              * NO
01D8 0 7301          MDX  3 1
01D9 0 70FB          MDX  VECT
01DA 0 C400 02CD      VECT1 LD  L  XFER1          ADDR OF DESIRED LVL
01DC 0 D300          STC  3 0
*
* COMPLEMENT THE IL WD AND STORE
*
01DD 0 C200          LD   2 0          PUT IL WD IN ACCUM
01DE 0 F400 02B9      EOR  L  FFFF        COMPLEMENT IL WORD
01E0 0 D400 028C      STO  L  MASK
01E2 0 0C00 028C      XIO  L  MASK          UNMASK THE DESIRED
*INTR LEVEL
*
*****
* PUT CORRECT AREA CODE IN SENSE
* DEVICE IOCC WORD AND START
* THE I/O METER%<.
*****
01E4 0 C101          LD   1 1          PUT CTRL WD IN ACCUM
01E5 0 EC00 02D2      OR   L  SENSE
01E7 0 D400 0293      STO  L  SNSD&1
01E9 0 7201          MDX  2 1
01EA 0 C400 02D3      LD   L  PASS1          XR2# ILSW WORD
01EC 0 4810          BSC  -              BYPASS THE FIRST 1442
01ED 0 7009          MDX  BYPAS          *IF THIS IS THE FIRST
01EE 0 0C00 0292      XIO  L  SNSD          *PASS THRU THE PROGRAM.
01F0 0 7401 02BA      MDX  L  WORD,1        THESE 3 OPS NEEDED
01F2 0 0C00 0292      XIO  L  SNSD          *IF THE DEVICE BEING
01F4 0 0900          XIO  1 0            *STARTED IS A TAPE DR
01F5 0 3001          WAIT1 WAIT 1          START A METER
01F6 0 1000          NOP  0
01F7 0 C400 02B9      BYPAS LD  L  FFFF
01F9 0 D400 02D3      STO  L  PASS1
01FB 0 C100          LD   1 0          ADDR WORD OF IOCC
01FC 0 F400 0284      EOR  L  IOCC&4        1443 IOCC ADDR WORD
01FE 0 4818          BSC  &-              IS THIS DEVICE 1443
01FF 0 3004          WAIT4 WAIT 4          YES, WAIT FOR
*PRINTER COMPL INTR
*
0200 0 1000          NOP  0
0201 0 C400 02CC      LD   L  INTRN          RESTORE ALL XFER
0203 0 D300          STO  3 0            *VECTORS TO SVINT
0204 0 74FE 02B6      MDX  L  EDCT1,-2
0206 0 7001          MDX  INCR1
0207 0 7003          MDX  WAIT2
0208 0 7102          INCR1 MDX  1 2          RDY TO START DELAY
0209 0 7201          MDX  2 1            XR1# NEXT IOCC
020A 0 70A7          MDX  LDRT2          XR2# I/O DEV INT TBLE
020B 0 0C00 028E      WAIT2 XIO  L  MASK1          MASK INTRs 0-13
020D 0 0C00 0290      XIO  L  MASK2          MASK INTRs 14-23
020F 0 3002          WAIT  2
0210 0 1000          NOP  0
*
*****

```



```
*
* DETERMINE THE NO OF 72 SEC
* LOOPS TO BE TAKEN FROM DATA
* ENTRY SWS< AND THEN START DLY.
*****
0211 0 0C00 027E DLY0 XIO L DESWS RD DATA ENTRY SWS
0213 0 6580 02CE LDX I1 COUNT XR1# NO OF 72 SEC LP
0215 0 C400 02B5 LD L CNFIG&12 CORE STOR SPEED
0217 0 18C1 SRA 1
0218 0 4804 BSC E 2 OR 4 USEC STORAGE
0219 0 7019 MDX FAST 2 US STORAGE
021A 0 C400 02A6 LD L FORUS 4 US STORAGE
021C 0 D400 02A8 DLY1 STO L CONST
021E 0 6365 DLY2 LDX 3 I01 XR3# 101
021F 0 C400 02A8 DLY3 LD L CONST
0221 0 8400 02C9 DLY4 A L ONE ADD 1 TO ACCUM
0223 0 4820 BSC Z
0224 0 70FC MDX DLY4
0225 0 73FF MDX 3 -1
0226 0 70F8 MDX DLY3
0227 0 71FF MDX 1 -1 MODIFY LOOP COUNT
0228 0 70F5 MDX DLY2
0229 0 C400 028B LD L MASK-1 CHANGE IOCC FOR
022B 0 F400 02D4 EOR L TWTY *THE 2402
022D 0 D400 028B STD L MASK-1
022F 0 30C3 WAIT3 WAIT 3 END OF TEST
*
*****
* TO RUN TEST AGAIN, SET DATA
* SWITCHES TO THE NUMBER OF
* 72 SECOND LOOPS DESIRED, AND
* PRESS THE START BUTTON.
*****
0230 0 1000 NOP 0
0231 0 4C00 0211 BSC L DLY0
0233 0 C400 02A7 FAST LD L TWOUS
0235 0 70E6 MDX DLY1
*
*****
* ROUTINE TO SERVICE PROGRAM
* GENERATED INTERRUPTS.
*****
0236 0 0000 IRTN DC 0
0237 0 0CC0 027A XIO L SV8 SENSE ILSW
0239 0 E200 AND 2 0
023A 0 4C20 023F BSC L SENS1,Z DOES ILSW MATCH
023C 0 C0F9 LD IRTN NO
023D 0 D005 STO SVINT
023E 0 7005 MDX SVINT&1 GO TO COMM INTR RTN
023F 0 0CC0 0292 SENS1 XIO L SNSD SENSE AND RESET DEV
0241 0 4CC0 0236 BOSC I IRTN EXIT
*
*****
* ROUTINE TO SERVICE NON
* PROGRAM GENERATED INTERPT
* WILL HANDLE ONLY ONE
* INTERRUPT AT A TIME<
*****
0243 0 0000 SVINT DC 0
0244 0 D037 STO SVIO SAVE ACCUMULATOR
0245 0 0C00 027A XIO L SV8 RESET ILSW
0247 0 7402 0279 MDX L SV7,2 SET PASS SWITCH
0249 0 1010 SLA 16
024A 0 D02B STO SV4 CLEAR AREA CODE CNTR
024B 0 C028 LD SV2
024C 0 D02B STO SV6 SET IOCC IN USE SW
024D 0 C025 SVINO LD SV1
024E 0 D028 STO SV5 SET MODIFIER COUNTER
024F 0 C026 SVINI LD SV4 *
```

```
0250 0 100B SLA 11 *
0251 0 E825 OR SV5 *BUILD IOCC
0252 0 E825 OR SV6 *
0253 0 D029 STO SVIO&1 *
0254 0 0827 XIO SVIO SENSE DSW AND RESET
0255 0 74FF 0277 MDX L SV5,-1
0257 0 70F7 MDX SVIN1 BRANCH IF NOT ALL MD
0258 0 7401 0276 MDX L SV4,1 INCREMENT AREA CODE
025A 0 C01B LD SV4
025B 0 9016 S SV0 CHECK IF ALL AC USED
025C 0 4808 BSC & SKIP IF ALL AC USED
025D 0 70EF MDX SVINO GO SENSE WITH NXT AC
025E 0 74FF 0279 MDX L SV7,-1 SKIP IF SECOND PASS
0260 0 7001 MDX *&1
0261 0 7005 MDX SVEXT
0262 0 C012 LD SV3
0263 0 D014 STO SV6 SET IOCC FOR PI
0264 0 1010 SLA 16
0265 0 D010 STO SV4 SET AC FOR NEXT
0266 0 70E6 MDX SVINO *PASS
0267 0 C400 02CF SVEXT LD L NOOP IF INTERRUPTED OUT
0269 0 F480 0243 EOR I SVINT *OF ANY WAIT, RETURN
026B 0 4C20 026F BSC L SV11,Z *TO THAT WAIT.
026D 0 74FF 0243 MDX L SVINT,-1
026F 0 C00C SV11 LD SVIC RESTORE ACCUMULATOR
0270 0 4CC0 0243 BOSC I SVINT EXIT
*
* ** CONSTANTS **
*
0272 0 001F SV0 DC /001F NUMBER OF AREA CODES
0273 0 00FF SV1 DC /00FF NUMBER OF MODIFIERS
0274 0 0701 SV2 DC /0701 SENSE/RESET DSW
0275 0 0700 SV3 DC /0700 SENSE/RESET PISW
0276 0 0000 SV4 DC 0 AREA CODE INDICATOR
0277 0 0000 SV5 DC 0 MODIFIER INDICATOR
0278 0 0000 SV6 DC 0 IOCC IN USE
0279 0 0000 SV7 DC 0 PASS SWITCH
*
027A 0000 BSS E I/O CONTROL COMMANDS
027A 0 0000 SV8 DC /0000 IOCC TO SENSE
027B 0 0300 SV9 DC /0300 THE ILSW
027C 0 0000 SVIO DC 0 SENSE DSW IOCC
027D 0 0000 DC 0
027E 0 02CE DESWS DC COUNT IOCC TO READ THE
027F 0 0240 DC /0240 DATA ENTRY SWITCHES
0280 0 0000 IOCC DC /0000 IOCC TO START
0281 0 0402 DC /0402 1442 NO1 METER
0282 0 0000 DC 0 IOCC TO START
0283 0 0402 DC /0402 1442 NO2 METER
0284 0 0294 DC PRINT IOCC TO START
0285 0 0500 DC /0500 1443 NO1 METER
0286 0 029D DC READ IOCC TO START
0287 0 0600 DC /0600 2401 NO1 METER
0288 0 029D DC READ IOCC TO START
0289 0 0620 DC /0620 2401 NO2 METER
028A 0 029D DC READ IOCC TO START
028B 0 0600 DC /0600 2402 METER
028C 0 0000 MASK DC /0000 IOCC TO SET THE
028D 0 0480 DC /0480 MASK REGISTER
028E 0 FFFF MASK1 DC /FFFF IOCC TO MASK
028F 0 0480 DC /0480 INTERRUPTS 0-13
0290 0 FFFF MASK2 DC /FFFF IOCC TO MASK
0291 0 0481 DC /0481 INTERRUPTS 14-23
0292 0 0000 SNSD DC /0000 IOCC TO SENSE
0293 0 0701 DC /0701 A DEVICE
0294 0 0008 PRINT DC 8 1443 PRINT TABLE
0295 0 2435 DC /2435 ME
0296 0 1335 DC /1335 TE
```

METER EXERCISER

METER EXERCISER

```

0297 0 29C0          DC      /2900          R
0298 0 3517          DC      /3517          EX
0299 0 3529          DC      /3529          ER
029A 0 3339          DC      /3339          CI
029B 0 1235          DC      /1235          SE
029C 0 2900          DC      /2900          R
029D 0 40C8          READ DC      /4008
029E 0 00C8          BSS      8
02A6 0 84AA          FORUS DC      /84AA      CONST FOR 4US SYS
02A7 0 0900          TWOUS DC     /0900      CONST FOR 2US SYS
02A8 0 0000          CONST DC     /0000      NO OF 72 SEC LOOPS
02A9 0 000D          CNFIG BSS    13          EDIT CD INFORMATION
02B6 0 000C          EDCT1 DC     /000C      KEEP TRACK OF ED CTS
02B7 0 02BC          ITBL1 DC     ITBLE&1    LOC OF INTR TABLE
02B8 0 8000          EIGHT DC     /8000      CONSTANT# /8000
02B9 0 FFFF          FFFF DC     /FFFF      CONSTANT# MINUS ONE
02BA 0 0000          WORD DC     /0000      A UTILITY LOCATION
02BB 0 02BC          ITBLE DC     ITBLE&1    ITBLE ADDRESS
02BC 0 000C          BSS      12
02C8 0 000C          EDCT DC     /000C      NO OF EDIT FIELDS
02C9 0 0001          ONE DC      /0001      CONSTANT# /0001
02CA 0 FFFE          FFFE DC     /FFFF      CONSTANT# /FFFF
02CB 0 000E          EASY DC     /000E      CONSTANT# /000E
02CC 0 0243          INTRN DC    SVINT      SPURIOUS INTR RTN
02CD 0 0236          XFER1 DC    IRTN      PROG GEN INTR RTN
02CE 0 0000          COUNT DC    /0000      DATA ENTRY SW SETNG
02CF 0 1000          NOOP DC     /1000
02D0 0 4C00          RSTR DC     /4C00
02D1 0 013E          DC          RERUN
02D2 0 0701          SENSE DC    /0701
0050 0              BEGIN EQU    /50
02D3 0 00C0          PASS1 DC    0
02D4 0 0020          TWTY DC     /0020      CONSTANT FOR 2402
02D6 0 0050          END      BEGIN

```

NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY

```

ADDR 01A2 017E
ADDR1 01A6 01A9
BEGIN 0050 02D6
BIT0 0172 014F
BUILD 014A 017D 01A1
BYPAS 01F7 01ED
BZERO 0195 0185
CNFIG 02A9 0136 0148 0215
CONST 02A8 021C 021F
COUNT 02CE 0213 027E
CTLWD 0198 0193
DESWS 027E 0211
DLY0 0211 0231
DLY1 021C 0235
DLY2 021E 0228
DLY3 021F 0226
DLY4 0221 0224
EASY 02CB 0154
EDCT 02C8 0142 01AA
EDCT1 02B6 0144 017B 019F 01AC 01C9 0204
EIGHT 02B8 015C 0161 0169 0172 018A 0195
FAST 0233 0219
FFFF 02CA 01BC
FFFF 02B9 01B3 01C3 01DE 01F7
FORUS 02A6 021A
GREAT 01CD 01B9
ILSWD 017F 0170
ILWD1 0161 015B
INCR 01C3 0186
INCR1 0208 0206
INTRN 02CC 01A4 0201
IOCC 0280 0146 01AE 01FC
IRTN 0236 023C 0241 02CD
ITBLE 02BB 013E 0180 0287 02BB
ITBL1 02B7 0140 016E 0178 017F 018F 0191
LDRT1 01B7
LDRT2 01B2 01CB 020A
LES14 0169 0158
LOAD 012C 0124
MASK 028C 01BA 01BE 01CD 01D1 01E0 01E2 0229 022D
MASK1 028E 020B
MASK2 0290 020D
NODEV 0175 014C
NOOP 02CF 0267
ONE 02C9 015E 0166 01CF 0221
PASS1 02D3 01C5 01EA 01F9
PRINT 0294 0284
READ 029D 0286 0288 028A
RERUN 013E 02D1
RSTR 02D0 012C 0130
SENSE 02D2 01E5
SENS1 023F 023A
SHRT1 016B 016D
SHRT2 0163 0165
SHRT3 018C 018E
SNSD 0292 01E7 01EE 01F2 023F
STO1 016E 0160 0168 0174
STO2 018F 0197
SVEXT 0267 0261
SVINT 0243 023D 023E 0269 026D 0270 02CC
SVINO 024D 025D 0266
SVIN1 024F 0257
SVIO 027C 0244 0253 0254 026F
SVO 0272 025B
SV1 0273 024D
SV11 026F 026B
SV2 0274 024B
SV3 0275 0262

```



IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
METER EXERCISER

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SV4 0276 024A 024F 0258 025A 0265
SV5 0277 024E 0251 0255
SV6 0278 024C 0252 0263
SV7 0279 0247 025E
SV8 027A 0237 0245
SV9 027B
TWOUS 02A7 0233
TWTY 02D4 022B
VECT 01D5 01C2 01D9
VECT1 01DA 01D6
WAIT1 01F5 3001
WAIT2 07CB 01CC 0207 3002
WAIT3 022F 3003
WAIT4 01FF 3004
WORD 02BA 0150 0152 0156 0186 0188 01F0
XFER 0138 013D
XFER1 02CD 01DA
END OF ASSEMBLY

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1. PURPOSE

- A. CHECK THE ACCURACY OF ALL OF THE USE METERS.
- B. INSURE THAT NO METERS ADVANCE WHEN THE METER KEY IS SWITCHED TO CE MODE.
- C. CHECK THE METER CONTROL CIRCUITS.

2. PREREQUISITES

2.1 PROGRAM

THIS PROGRAM IS LOADED BY THE RELOCATABLE DIAGNOSTIC LOADER. AN EDIT CARD, CONTAINING THE NECESSARY IOCC INFORMATION FOR THIS 1800 SYSTEM, MUST FOLLOW THE LAST CARD OF THE PROGRAM. (SEE SEC 6.1)

2.2 EQUIPMENT

- A. CUSTOMER ENGINEER USE METER KEY.
- B. CARD OR PAPER TAPE READER.

3. USE PROCEDURE

THERE ARE 7 STEPS TO THE USE PROCEDURE. A DETAILED DESCRIPTION STARTS IN SECTION 3.1.

1. RECORD ALL METER READINGS.
2. MAKE ALL METERED I/O UNITS READY.
3. LOAD THE PROGRAM.
4. SET THE DESIRED NUMBER OF 72 SECOND LOOPS IN THE PC DATA ENTRY SWITCHES.
5. CHECK CUSTOMER METERS
6. COMPUTE THE ELAPSED TIME. (BY HAND)
7. CHECK THE METER CONTROL CIRCUITS.

3.1 LOADING

1. RECORD THE READINGS ON ALL OF THE CUSTOMER METERS. THE CUSTOMER

2. MAKE ALL METERED I/O UNITS READY.

1442 (FIRST) IF THE 1800 SYSTEM IS EQUIPPED WITH A 1442, IT MUST BE USED TO LOAD THE PROGRAM. AFTER THE PROGRAM HAS LOADED, DO NOT PRESS THE 1442 NPRO BUTTON, AS THIS WILL PREVENT THE METER FROM RUNNING

1442 (SECOND ONLY) PLACE A FEW CARDS IN THE FEED HOPPER AND PRESS THE 1442 START KEY. THE 1442 READY LAMP SHOULD GLOW.

1443 TURN ON THE POWER SWITCH AND PRESS THE 1443 START KEY. THE 1443 READY LAMP SHOULD GLOW.

2310 DISK STORAGE-TURN THE SWITCH ON THE FRONT OF THE UNIT TO 'ENABLE'.

2401 PLACE A REEL OF TAPE IN EACH TAPE DRIVE. PRESS THE 2401 LOAD REWIND BUTTON(S). PRESS THE 2401 START BUTTON(S).

2402 PLACE A REEL OF TAPE IN EACH TAPE DRIVE. PRESS BOTH LOAD REWIND BUTTONS AND BOTH START BUTTONS. IT IS NECESSARY TO RUN THE DELAY LOOP TWICE TO ENSURE THAT EACH DRIVE CAN CONTROL THE METER. DURING THE FIRST TIMING LOOP THE METER WILL BE UNDER CONTROL OF DRIVE ZERO. WHEN THE TIMING LOOP TERMINATES THE TAPE IN DRIVE ZERO SHOULD BE RETURNED TO LOAD POINT SO THAT ONLY DRIVE ONE CAN CONTROL THE METER DURING THE SECOND TIMING LOOP. DRIVE ZERO WILL BE STARTED BY THE PROGRAM ON ALL ODD NUMBERED RUNS AND DRIVE ONE ON ALL EVEN NUMBERED RUNS.

4. REFER TO THE RELOCATABLE DIAGNOSTIC LOADER DOCUMENTATION FOR THE LOADING PROCEDURE.

NOTE

IF THE PROGRAM LOADED CORRECTLY, AND ALL METERED I/O UNITS ARE IN A READY STATUS, THE PROGRAM WILL STOP AT WAIT 2. (B REG 3002)

3.2 OPERATION.

3.2.1 TO CHECK CUSTOMER METERS.

1. SET THE DATA ENTRY SWITCHES TO INDICATE THE NUMBER OF 72 SECOND LOOPS THAT YOU WISH TO MAKE.
2. IF THE 1442 WAS USED TO LOAD THE PROGRAM, RECORD THE METER READING. (AGAIN).
3. PRESS PC START BUTTON.
4. THE PROGRAM WILL STOP AT WAIT 3 (B REG 3003) WHEN THE DESIRED DELAY IS COMPLETED. ALL CUSTOMER METERS SHOULD HAVE ADVANCED .02 HOURS FOR EACH 72 SECOND LOOP RUN.
5. IF THE 2402 METER IS BEING CHECKED, IT IS NECESSARY TO RUN THE DELAY LOOP TWICE. DURING THE FIRST RUN (AND ALL SUBSEQUENT ODD NUMBERED RUNS), THE METER WILL BE UNDER CONTROL OF DRIVE ZERO. WHEN THE RUN IS COMPLETED (PROGRAM STOPS AT WAIT 3), PRESS THE 'RESET' AND THE 'LOAD REWIND' BUTTONS ON DRIVE ZERO, PLACE A FEW CARDS IN THE 1442 AND PRESS THE 1802 'RESET' AND 'START' BUTTONS. PROGRAM WILL STOP AT WAIT 2. PRESS THE 1802 'START' BUTTON TO START THE DELAY LOOP. DURING THE SECOND RUN (AND ALL SUBSEQUENT EVEN NUMBERED RUNS), THE METER WILL BE UNDER CONTROL OF DRIVE NUMBER 1. BEFORE EACH RUN, CHECK THAT BOTH DRIVES ARE AT LOAD POINT AND 'READY'.
6. TO REPEAT TEST, SLT LOOP COUNT IN THE DATA ENTRY SWITCHES AND PRESS THE PC START BUTTON.

3.2.2 CONTROL CIRCUITRY CHECK.

EACH METER WILL NOW BE CHECKED TO INSURE THAT IT RUNS AND STOPS

RUNNING AT THE CORRECT TIMES.

1. CHECK THAT NO METERS ARE RUNNING WHILE THE PROGRAM IS AT WAIT 3.
2. TURN THE METER KEY TO C.E. MODE. WHILE THE PROGRAM IS RUNNING CHECK THAT NO METERS ARE RUNNING. RETURN THE METER KEY TO THE NORMAL POSITION.
3. WHILE THE PROGRAM IS RUNNING IN A 72 SECOND DELAY LOOP,
1442-THE METER SHOULD STOP WHEN THE NPRO BUTTON IS PRESSED.
(HOPPER MUST BE EMPTY)
1443-THE METER SHOULD STOP IF THE 1443 STOP BUTTON IS PRESSED.
2310-THE METER IS CONTROLLED BY THE 'ENABLE/DISABLE' SWITCH ON THE FRONT COVER. IF THE POSITION OF THE SWITCH IS CHANGED, THIS CHANGE SHOULD NOT AFFECT THE METER UNTIL AFTER THE PC HAS COME TO A WAIT. (AND THEN HAS STARTED AGAIN).
2401/02 THE METER SHOULD RUN WHENEVER THE TAPE IS LOADED AND NOT AT LOAD POINT. IF A TAPE DRIVE IS LOADED AND NOT AT LOAD POINT, PRESSING THE 'LOAD REWIND' BUTTON SHOULD STOP THE METER WHEN THE TAPE REACHES LOAD POINT.

3.3 TERMINATION

THE PROGRAM WILL STOP AT WAIT 3 (B REG 3003) WHEN THE EXERCISE IS COMPLETED. TO REPEAT EXERCISE, REFER TO WAIT 3 DESCRIPTION.

3.4 RESTART

PLACE A FEW BLANK CARDS IN THE FEED HOPPER OF THE FIRST 1442 AND PRESS THE START KEY. THE 1442 READY LAMP SHOULD GLOW. PRESS THE PC RESET BUTTON THEN THE START BUTTON. THIS WILL REINITIALIZE THE PROGRAM, CONDITION ALL THE METERS TO RUN, THEN STOP AT WAIT 2 (B REG 3002).

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```
3001 0 01ED      DC      WAIT1+1
                   *      WAIT 1
                   *
                   *      ONE OF THE METERED I/O UNITS
                   *      FAILED TO SEND A RESPONSE
                   *      INTERRUPT TO THE PROGRAM. INDEX
                   *      REGISTER 1 WILL HAVE THE ADDRESS
                   *      OF THE IOCC. THE AREA CODE WILL
                   *      INDICATE THE I/O UNIT NOT READY.
                   *      IF A 2401/02 DRIVE IS NOT READY,
                   *      PROGRAM WILL NOT STOP AT WAIT 1.
                   *
```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

- 4 PRINTOUTS
THERE ARE NO PRINTOUTS.
5. COMMENTS (NONE)

----- LAST PAGE -----

6.1 EDIT PROCEDURE

THE FOLLOWING EDIT PROCEDURE IS FOR CARD INPUT. THE EDIT PROCEDURE FOR PAPER TAPE INPUT IS LOCATED IN THE PAPER TAPE EDIT UTILITY PROGRAM DOCUMENTATION. THE PROPER EDIT CARDS MUST BE THE LAST CARDS IN THIS PROGRAM DECK. THE FOLLOWING FORMS ARE PROVIDED TO AID IN MANUALLY PREPARING THESE EDIT CARDS OR UPDATING EXISTING EDIT CARDS. IF IT IS NECESSARY TO PREPARE OR MODIFY EDIT CARDS, FILL IN THE NECESSARY DATA IN THE FORMS PRIOR TO PUNCHING THE CARDS. CARD COLUMNS THAT ARE SHADED SHOULD BE LEFT BLANK.

- DDEF STANDS FOR DEVICE DEFINITION EDIT FIELD. IT INCLUDES:
1. THE INTERRUPT LEVEL ASSOCIATED WITH THIS DEVICE (USE HEX NOTATION, 00-17).
 2. THE ILSW BIT POSITION ASSOCIATED WITH THIS DEVICE (USE HEX NOTATION, 0-F).
 3. THE CHANNEL ASSIGNED TO THIS DEVICE (0-8). IF THIS IS A DPC DEVICE, PUNCH AN "F" IN THE CARD COLUMN.

COLUMN	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	26	31	36	41	46	51	56	61	66	71							
PROGRAM I.D.																																						
CARD SEQUENCE NUMBER																																						
NUMBER OF EDIT ENTRIES																																						
1442 (FIRST) DDEF																																						
1442 (FIRST) AREA CODE																																						
1442 (SECOND) DDEF																																						
1442 (SECOND) AREA CODE																																						
1443 (FIRST) DDEF																																						
1443 (FIRST) AREA CODE																																						
2401 (FIRST) DDEF																																						
2401 (FIRST) AREA CODE																																						
2401 (SECOND) DDEF																																						
2401 (SECOND) AREA CODE																																						
2402 DDEF																																						
2402 AREA CODE																																						
CARD 0	E	B	6	0	0		E	D	0	0		0	0	0	D																							
END	E	B	6	0	0		F	F	F	F																												

CARD COLUMNS 77-80 WILL CONTAIN THE SPEED OF THE CORE STORAGE. FOR 2 MICRO SEC. STORAGE PUNCH 0002, AND FOR 4 MICRO SEC. STORAGE, PUNCH 0004.

NOTE: IF A DEVICE IS NOT ON THIS SYSTEM, PUNCH THE CORRESPONDING ENTRY FFFF FFFF.

NO "END EDIT" CARD IS REQUIRED FOR THIS PROGRAM.

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

PART NO. 2242251
PAGE 1

DIMAL INITIAL LOADER (CARD)

```

* SEE PID 0802 FOR DESCRIPTION
028C ABS ORG /3500 88700010
* DIMAL INITIAL LOADER PROGRAM WAIT 88700020
* DESCRIPTION. 88700030
* 3500 0 0010 DC W3500+1 WAIT 500 88700040
* A DSW ERROR WAS 88700050
* DETECTED DURING LOAD 88700060
* OPERATIONS.RELOAD THE 88700070
* INITIAL LOADER. 88700080
* 3501 0 0037 DC W3501+1 WAIT 501 88700090
* AN INITIAL LADER EDIT 88700100
* CARD ERROR HAS BEEN 88700110
* DETECTED.CHECK THE EDIT 88700120
* CARDS.INSURE THAT COLUMN 88700130
* 1 OF BOTH CARDS CONTAINS 88700140
* AN 'E',THAT THE PID ON 88700150
* BOTH CARDS IS 0200,THAT 88700160
* CARD 1 SEQUENCE NUMBER IS 88700170
* E000 AND CARD 2 IS FFFF. 88700180
* CORRECT ANY ERRORS,PLACE 88700190
* BOTH CARDS IN THE 1442. 88700200
* MAKE IT READY AND CONTINUE 88700210
* 3502 0 0056 DC W3502+1 WAIT 502 88700220
* 1442 IS NOT READY.READY 88700230
* THE 1442 WITH INITIAL 88700240
* LOADER EDIT CARDS AND 88700250
* CONTINUE. 88700260
* 3503 0 0058 DC W3503+1 WAIT 503 88700270
* A 1442 DSW ERROR WAS 88700280
* DETECTED DURING EDIT CARD 88700290
* INPUT. REENTER BOTH EDIT 88700300
* CARDS IN THE 1442 HOPPER, 88700310
* MAKE IT READY AND CCNTINUE 88700320
* 3504 0 00A7 DC W3504+1 WAIT 504 88700330
* THE DISK PACK CE WORD 88700340
* WAS NOT FOUND ON THE 88700350
* HISTORY TRACK. INSURE THE 88700360
* CE DISK PACK HAS BEEN 88700370
* LOADED.DEPRESS START TO 88700380
* TRY AGAIN.IF ERROR PER- 88700390
* SISTS,REINITIALIZE THE 88700400
* CE DISK PACK. 88700410
* 3505 0 00AE DC W3505+1 WAIT 505 88700420
* HISTORY DATA FOUND ON THE 88700430
* HISTORY TRACK INDICATES 88700440
* THAT 4 OR MORE BAD CYLIN- 88700450
* DERS EXIST ON THE CE DISK 88700460
* PACK.THIS IS CONSIDERED A 88700470
* BAD PACK.A NEW CE PACK 88700480
* SHOULD BE USED.IF IT IS 88700490
* DESIRED TO USE THE BAD 88700500
* PACK,SET SNS/PGM SWITCH 0 88700510
* AND CONTINUE. 88700520
* 3506 0 0147 DC W3506+1 WAIT 506 88700530
* 88700540
* 88700550
* 88700560
* 88700570
* 88700580
* 88700590
* 88700600
* 88700610
* 88700620
* 88700630
* 88700640
* 88700650
* 88700660
* 88700670
* 88700680

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DIMAL INITIAL LOADER (CARD)

```

* THE 1442 IS NOT READY 88700690
* READY THE 1442 WITH DIMAL 88700700
* OBJECT DECK,CR PRESS 1442 88700710
* START FOR LAST CARD AND 88700720
* CONTINUE. 88700730
* 3507 0 0149 DC W3507+1 WAIT 507 88700740
* A 1442 DSW ERROR WAS 88700750
* DETECTED WHILE LOADING 88700760
* DIMAL. RELOAD THE DIMAL 88700770
* OBJECT DECK IN THE 1442 88700780
* HOPPER AND MAKE IT READY. 88700790
* DEPRESS THE 1800 RESET 88700800
* AND START BUTTONS.PROGRAM 88700810
* LOADING SHOULD OCCUR. 88700820
* 3508 0 016E DC W3508+1 WAIT 508 88700830
* A CHECKSUM ERROR WAS 88700840
* DETECTED DURING DIMAL 88700850
* LOADING.NPRO THE 1442.THE 88700860
* 1ST CARD EJECTED IS THE 88700870
* CARD IN ERROR.INSURE CARD 88700880
* IS IN CORRECT SEQUENCE.IF 88700890
* NO PROBLEM IS APPARENT, 88700900
* REENTER BOTH EJECTED CARDS 88700910
* AND CONTINUE.RELCADING 88700920
* MAY ALSO BE DONE BY PLAC- 88700930
* ING THE ENTIRE DIMAL DECK 88700940
* IN THE 1442 HOPPER AND 88700950
* MAKE IT READY.DEPRESS 1800 88700960
* RESET AND START BUTTONS. 88700970
* 3509 0 01C8 DC W3509+1 WAIT 509 88700980
* A LAST CARD SEQUENCE WAS 88700990
* INITIATED BEFORE THE 88701000
* COMPLETE DIMAL DECK WAS 88701010
* READ IN.INSURE THAT THE 88701020
* COMPLETE DIMAL DECK HAS 88701030
* BEEN LOADED. 88701040
* 350A 0 020E DC W350A+1 WAIT 50A 88701050
* DISK HOME BIT DID NOT 88701060
* COME ON IN THE DSW AFTER 88701070
* THE 3RD ATTEMPT TO SEEK 88701080
* HOME.CORRECT FAILURE AND 88701090
* CONTINUE.IF CORE IS DES- 88701100
* TROYED,RELOADING MUST BE 88701110
* ACCOMPLISHED. 88701120
* 350B 0 021C DC W350B+1 WAIT 50B 88701130
* 2310 DISK DRIVE NOT READY. 88701140
* READY THE 2310 AND 88701150
* CONTINUE. 88701160
* 350C 0 022D DC W350C+1 WAIT 50C 88701170
* ATTEMPTED DISK READ.DRIVE 88701180
* WENT NOT READY.MAKE DRIVE 88701190
* READY AND CONTINUE. IF 88701200
* DISK ARM POSITICN IS 88701210
* CHANGED,RELOADING DIMAL 88701220
* DECK IS REQUIRED. 88701230
* 88701240
* 88701250
* 88701260
* 88701270
* 88701280
* 88701290
* 88701300
* 88701310
* 88701320
* 88701330
* 88701340
* 88701350
* 88701360

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DIMAL INITIAL LOADER (CARD)

```

350D 0 023A      *      DC      W350D+1  WAIT 50D      88701370
*                *                *                88701380
*                *                *                88701390
*                *                *                A DSW ERROR OCCURED ON A 88701400
*                *                *                DISK READ OP ON EACH OF 88701410
*                *                *                3 ATTEMPTS.THE A PEG. 88701420
*                *                *                CONTAINS THE ERROR BITS. 88701430
*                *                *                PRESSING START AFTER THE 88701440
*                *                *                ERROR WILL CAUSE PROGRAH 88701450
*                *                *                TO MAKE 3 MORE TRIES TO 88701460
*                *                *                READ. IF ERROR PERSISTS, 88701470
*                *                *                CORRECT AND RELOAD DIMAL. 88701480
*                *                *                88701490
350E 0 0245      *      DC      W350E+1  WAIT 50E      88701500
*                *                *                88701510
*                *                *                ATTEMPTED DISK WRITE.DRIVE 88701520
*                *                *                NOT READY.MAKE DRIVE READY 88701530
*                *                *                AND CONTINUE.IF DISK ARM 88701540
*                *                *                POSITION IS CHANGED, 88701550
*                *                *                RELOADING DIMAL DECK IS 88701560
*                *                *                REQUIRED. 88701570
*                *                *                88701580
350F 0 0253      *      DC      W350F+1  WAIT 50F      88701590
*                *                *                88701600
*                *                *                A DISK WRITE OR MODULO 4 88701610
*                *                *                CHECK ERROR EXISTED ON 88701620
*                *                *                EACH OF 3 ATTEMTS TO WRITE 88701630
*                *                *                DEPRESS START BUTTON WILL 88701640
*                *                *                CAUSE PROGRAM TO MAKE 3 88701650
*                *                *                ADDITIONAL ATTEMPTS TO 88701660
*                *                *                WRITE. IF ERROR PERSISTS, 88701670
*                *                *                CORRECT AND RELOAD DIMAL. 88701680
*                *                *                88701690
3510 0 0299      *      DC      W3510+1  WAIT 510      88701700
*                *                *                88701710
*                *                *                THIS WAIT INDICATES THAT 88701720
*                *                *                THE LOADING OF THE CARD 88701730
*                *                *                DECK REPRESENTING THE 88701740
*                *                *                PAPER TAPE VERSION OF 88701750
*                *                *                DIMAL HAS BEEN CCMPLETED 88701760
*                *                *                88701770
3511              *      ORG      0          CARD 1      88701780
*                *                *                88701790
03E0             IN      EQU      1000      88701800
044C             IOA     EQU      1100      88701810
044D             SID     EQU      IOA+1     88701820
044E             OUT     EQU      SID+1     88701830
*                *                *                88701840
*                *                *                DIMAL SYSTEM INITIAL LOADER 88701850
*                *                *                88701860
*                *                *                THE INITIAL LOADER PERFORMS THE 88701870
*                *                *                FOLLOWING FUNCTIONS. 88701880
*                *                *                1. INPUT AND CONVERT THE LOADER EDIT 88701890
*                *                *                CARDS. 88701900
*                *                *                2. INSURES THAT THE CE DISK PACK IS 88701910
*                *                *                LOADED,AND USABLE. 88701920
*                *                *                3. ASSIGNS THE CYLINDERS TO BE USE BY 88701930
*                *                *                THE DISK DIAGNOSTIC MONITOR. 88701940
*                *                *                4. INPUTS THE DDM HEADER,COLD START 88701950
*                *                *                LOADER,DDM LOADER/ORGANIZER AND THE 88701960
*                *                *                DDM SELECT/EXECUTE PROGRAMS AND 88701970
*                *                *                WRITES THEM ON THE DISK. 88701980
*                *                *                5. INPUTS THE DDM LOADER/ORGANIZER FROM 88701990
*                *                *                THE DISK UPON COMPLETION OF INITIAL- 88702000
*                *                *                IZING THE DISK WITH THE DIMAL SYSTEM 88702010
*                *                *                88702020
*                *                *                88702030
*                *                *                THIS IS THE 1ST CARD OF THE LOADER.IT 88702040

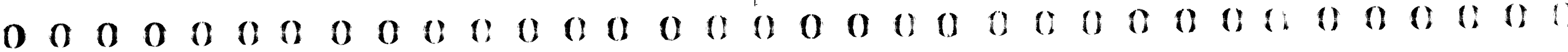
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DIMAL INITIAL LOADER (CARD)

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*                *                *                IS READ IN BY THE IPI OPERATION AND IS 88702050
*                *                *                USED TO LOAD THE REST OF THE LOADER. 88702060
*                *                *                88702070
0000 0 0819      *                *                *                IPI1 XIO      RDPAC      READ PACKED MODE 88702080
0001 0 081A      *                *                *                IPI2 XIO      DSW        SENSE 1442 STATUS 88702090
0002 00 4C040001 *                *                *                BSC L IPI2,F  BRANCH IF NOT READY 88702100
0004 0 8017      *                *                *                CMP      DSW        LOOK FOR OP COMPLETE 88702110
0005 0 7009      *                *                *                MDX      W3500     BRANCH - BIT 2 ON 88702120
0006 0 7006      *                *                *                MDX      IPI3      BRANCH - BITS 5 OR 6 88702130
0007 00 7424001A *                *                *                MDX L RDPAC,36  SET IOCC FOR NXT CD 88702140
0009 00 74FF001E *                *                *                MDX L CDCT,-1  SKIP WHEN LOADER IN 88702150
000B 0 70F4      *                *                *                MDX      IPI1      GO READ NEXT CARD 88702160
000C 0 7004      *                *                *                MDX      PREP      PREPARE INPUT AREA 88702170
000D 0 8011      *                *                *                IPI3 CMP      KO100  CHECK IF BITS 5 OR 6 88702180
000E 0 1000      *                *                *                NOP                                     88702190
000F 0 35G0      *                *                *                W3500 DC      /3500  DSW INDICATES ERROR 88702200
0010 0 70F0      *                *                *                MDX      IPI2      88702210
0011 00 67000141 *                *                *                PREP LDX L3 321  SET CONSTANT 321 IN 88702220
0013 00 6F00044C *                *                *                STX L3 IOA  *INPUT AREA 88702230
0015 0 C802      *                *                *                LDD      BRN      PICKUP RESTART INSTR 88702240
0016 0 08E9      *                *                *                STD      0        SET IN LOCS 0 AND 1 88702250
0017 0 700C      *                *                *                MDX      EDIT     BRANCH TO INPUT EDIT 88702260
0018 0 0000      *                *                *                BSS      E 0      88702270
0018 00 4C00027E *                *                *                BRN BSC L RSTRT  RESTART INSTRUCTION 88702280
001A C 0024      *                *                *                RDPAC DC      36  READ 1442 PACKED 88702290
001B 0 16G1      *                *                *                DC      /1601  *IOCC WORD 88702300
001C 0 0800      *                *                *                DSW      DC      /0800  SENSE 1442 DSW IOCC 88702310
001D 0 1700      *                *                *                DC      /1700  88702320
001E 0 0013      *                *                *                CDCT DC      19  LOADER CARD COUNT 88702330
001F 0 0100      *                *                *                KO100 DC     /0100  ERR CK USW CONSTANT 88702340
0020 0 FFFF      *                *                *                KFFFF DC     /FFFF  CONSTANT 88702350
*                *                *                ***** 88702360
*                *                *                ORG      36  CARD 2 88702370
*                *                *                ***** 88702380
*                *                *                *                88702390
*                *                *                *                THIS SECTION READS,CONVERTS AND CHECKS 88702400
*                *                *                *                THE INITIAL LCADER EDIT CARDS. 88702410
*                *                *                *                88702420
0024 0 08F7      *                *                *                EDIT XIO      DSW        SENSE FOR STATUS 88702430
0025 00 4C040055 *                *                *                BSC L W3502,E  BRANCH ON NOT READY 88702440
0027 0 6301      *                *                *                LDX      3 1      SET EDIT CARD IND. 88702450
0028 0 6B35      *                *                *                STX      3 ED5W  88702460
0029 0 0830      *                *                *                XIO      RD0D      READ A CARD 88702470
002A 0 08F1      *                *                *                EDIT1 XIO      DSW        SENSE STATUS 88702480
002B 0 1801      *                *                *                SRA      1        POSITION DSW 88702490
002C 00 4C04002A *                *                *                BSC L EDIT1,E  BRANCH IF BUSY 88702500
002E 0 180C      *                *                *                SRA      12       POSITION DSW 88702510
002F 00 4C040057 *                *                *                BSC L W3503,E  BRANCH CN ERROR DSW 88702520
0031 0 082A      *                *                *                XIO      DSW1     RESET DSW 88702530
0032 00 C40003E8 *                *                *                LD L IP.        PICKUP 1ST WORD 88702540
0034 00 4C280038 *                *                *                BSC L EDIT2,+Z BRANCH IF EDIT CARD 88702550
0036 0 3501      *                *                *                W3501 DC      /3501  EDIT CARD ERROR 88702560
0037 0 70EC      *                *                *                MDX      EDIT     RESTART 88702570
0038 0 7026      *                *                *                EDIT2 MDX      HBCV  GO CONVERT HEX TO BI 88702580
0039 00 C400044E *                *                *                LD L OUT        GET 1ST CONVERTED WD 88702590
003B 0 F020      *                *                *                EOR      DSW1     CHECK FOR PID 802 88702600
003C 0 4820      *                *                *                BSC      Z        SKIP IF PROPER PID 88702610
003D 0 70F8      *                *                *                MDX      W3501  BRANCH ON WRONG PID 88702620
003E 0 C01F      *                *                *                LD      ED5W     PICKUP EDIT SWITCH 88702630
003F 0 4808      *                *                *                BSC      +        SKIP IF EDIT SW = 1 88702640
0040 0 700E      *                *                *                MDX      EDIT3  88702650
0041 00 C400044F *                *                *                LD L OUT+1     PICKUP SEQUENCE MBR 88702660
0043 0 F015      *                *                *                EOR      KED00  CHECK FOR CARD EDOO 88702670
0044 0 4820      *                *                *                BSC      Z        SKIP IF PROPER CARD 88702680
0045 0 70F0      *                *                *                MDX      W3501  BRANCH ON WRONG SEQUENCE 88702690
0046 0 D017      *                *                *                STD      ED5W  CLEAR EDIT SWITCH 88702700
0047 0 6303      *                *                *                LDX      3 3     SET WORD INDEX 88702710
*                *                *                ***** 88702720

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DIMAL INITIAL LOADER (CARD)

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0048 00 C7000450
004A 00 D70001FF
004C 0 73FF
004D 0 70FA
004E 0 70DA
004F 00 C400044F
0051 0 FOCE
0052 0 4820
0053 0 70E2
0054 0 7034
0055 0 3502
0056 0 70CD
0057 0 3503
0058 0 70CB
0059 0 ED00

005A 0000
005A 0 03E8
005B 0 1600
005C 0 0200
005D 0 1703
005E 0 0000

005F 0 61E1
0060 0 1010
0061 0 D024
0062 0 6204
0063 0 7101
0064 0 7001
0065 0 70D3
0066 0 1010
0067 0 D01F
0068 0 D01F
0069 0 1004
006A 0 D01D
006E 0 6300

006C 00 C5000407
006E 0 4828
006F 0 7309
0070 0 1003
0071 00 4C180078
0073 0 7301
0074 00 4C280078
0076 0 1001
0077 0 70F8
0078 0 680E
0079 0 C0CD
007A 0 E80D
007B 0 7101
007C 0 72FF
007D 0 70E8
007E 00 67800086
0080 0 1000
0081 00 D700044E
0083 00 74010086
0085 0 70CD
0086 0 0000
0087 0 0000
0088 0 0000

ORG 72 CARD 3
*****
LD L3 OUT+2 PLACE CONVERTED EDIT
STO L3 EDWD-1 *WDS IN SAVE LOCATNS
MDX 3 -1 SKIP WHEN DONE
MDX *-6 CONTINUE SAVE OP
LD EDIT1-1 GO READ NEXT CARD
LD L OUT+1 PICK UP 2ND EDIT ETY
EOR KFFFF CHECK FOR TERMINATOR
BSC 2 SKIP IF TERM CARD
MDX W3501 BRANCH NOT TERM CARD
INT INT TRY AGAIN
DC /3502 1442 NOT READY
MDX EDIT TRY AGAIN
DC /3503 DSW INDICATES ERROR
MDX EDIT TRY FOR REREAD
DC /ED00 CONSTANT

BSS E 0
RDED DC IN 1442 READ IOCC
DC /1600
DSW1 DC /0200 1442 SENSE/RESET
DC /1703 *DSW IOCC
EDSW DC 0 EDIT SWITCH

*****
THIS ROUTINE CONVERTS 1 HEXIDECIMAL
CARD TO BINARY.
HBCV LDX 1 -31 SET XR TO CONV.30 WD
SLA 16 CLEAR CONVERTED WORD
STO LOC *STORE POINTER
HBCV1 LDX 2 4 SET COLUMN XR = 4
MDX 1 1 SKIP WHEN DONE
MDX *-1 CONVERT A WORD
SLA EDIT2+1 CONTINUE MAINLINE
SLA 16 CLEAR CONVERSION
STO SAVE *WORK LOCATIONS.
STO SAVE1
HBCV2 SLA 4 POSITION FOR NXT CHR
STO SAVE1 SAVE CONVERTED CHARS
LCLX 3 0 SET CHARAC XR = 0

ORG 108 CARD 4
*****
LD L1 IN+31 PICK UP HEX COLUMN
BSC +2 SKIP IF NOT ALPHA
MDX 3 9 ADD 9 FOR ALPHA CHAR
SLA 3 REMOVE ZONE BITS
BSC L HBCV4,+ XFER IF CHAR = 0
MDX 3 1 ADD 1 TO CHARACT XR
BSC L HBCV4,+2 XFER IF DIGIT FOUND
SLA 1 POSITION NEXT BIT
MDX HBCV3 CHECK NEXT BIT
HBCV4 STX 3 SAVE STORE BIN CHARACTER
LD SAVE FETCH BIN CHARACTER
HBCV5 OR SAVE1 ADD TO PREVIOUS CHAR
MDX 1 1 ADD 1 TO HEX WORD XR
MDX 2 -1 SUB 1 FROM COLUMN XR
MDX HBCV2 GO FOR NEXT COLUMN
LDX 13 LOC PICK UP STORE POINTR
STO L3 OUT SET IN OUTPUT AREA
MDX L LOC,1 ADD 1 TO POINTER
MDX HBCV1 GO FOR NEXT WORD
LOC DC 0 STORAGE POINTER
SAVE DC 0 *CONVERSION WORK
SAVE1 DC 0 *LOCATIONS

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```

0089 00 C4000202
008A 0 D031
008C 00 C4000200
008E 0 D02D
008F 0 1803

0090 00 D400026A
0092 0 6308
0093 00 C4000201
0095 00 EF000264
0097 00 D7000264
0099 0 73FE
009A 0 70F8
009B 00 44000203
009C 00 44000216
009F 00 44000225
COA1 00 C400044E
00A3 0 F00E
00A4 00 4C1800A8
00A6 0 3504
00A7 0 70F3
00A8 00 C400044F
00AA 0 F008
00AB 00 4C1000BE
00AD 0 3505
00AE 0 0805
00AF 00 4C2800BE
00B1 0 70F6
00B2 0 CEDC
00B3 0 0018

00B4 0000
00B4 0 0030
00B5 0 0760

00B6 0 0000
00B7 0 0000
00B8 0 0000
00B9 0 0000
00BA 0 0000
00BB 0 0000
00BC 0 0000
00BD 0 0000

00BE 0 62FA
00BF 0 C0F4
00C0 00 6780044F
00C2 0 7300
00C3 0 7007
00C4 00 D60000BC
00C6 0 7201
00C7 0 7001
00C8 0 7008
00C9 0 8009
00CA 0 70F5
00CB 00 B700044F
00CD 0 7002
00CE 0 7001

INT LD L EDWD+2 GET OUTPUT DEVICE IN D
STO USTB+7 SET IN USE TABLE
LD L EDWD GET HISTORY CYLINDER
STO USTB+6 SAVE IN USE TABLE
SRA 3 REMOVE SECTOR BITS

*****
ORG 144 CARD 5
*****
STO L DSK SET IN SEEK COMMAND
LDX 3 11 SET BUILD INDEX
INT1 LD L EDWD+1 PICKUP DISK AREA CD
OR L3 HOME ADD AREA CCDE TO
STO L3 HOME *DISK IOCC
MDX 3 -2 SKIP WHEN DONE
MDX INT1 CONTINUE
INT2 BSI L HM GO SEEK HM
INT3 BSI L SEEK SEEK TO HISTORY TRK
BSI L READ READ HISTORY TRACK
LD L IOA+2 PICK UP CE WORD LOC
EOR CK CHECK FOR CE PACK
BSC L INT4,+ BRANCH IF CE WORD
DC /3504 CE WORD NOT READ
MDX INT2 TRY AGAIN
INT4 LD L IOA+3 GET BAD CYL COUNT
EOR CK1 CHECK FOR MORE THAN
BSC L INT5,- *3 BAD CYLINDERS
DC /3505 4 OR MORE BAD CYLS
XIO SNSW SENSE SWITCH INPUT
BSC L INT5,+Z IF SSO USE BAD PACK
MDX INT4
CK DC /CEDC
CK1 DC 24

*****
ORG 180 CARD 6
*****
BSS E 0
SNSW DC /0030 SENSE S/P SWITCH
DC /0760 *IOCC

*
* THIS SECTION ASSIGNS THE DDM CYLINDERS
*
USTB DC 0 HEADER/LDR CYLINDER
DC 0 DDM LDR/ORG CYLINDER
DC 0 DDM SEL/EXC CYLINDER
DC 0 WORK CYLINDER
DC 0 WORK CYLINDER
DC 0 LOC.DIR - EDIT TABLE
DC 0 HISTORY TRACK
DC 0 OUTPUT DEVICE

*
INT5 LDX 2 -6 SET TABLE XF
LD SNSW PICKUP CYL 6 ADDRESS
INT6 LDX 13 SID+2 SET ERROR TABLE XR
MDX 3 0 SKIP IF ERROR XR = 0
MDX INT9 GO CHECK FOR GOOD CY
STO L2 USTB+6 SET CYL NMBR IN TBL
MDX 2 1 ADD 1 TO TABLE XR
MDX *-1 CONTINUE
MDX GEN GO TO NEXT SECTION
INT8 A K8 ADD 8 TO CYLINDER XR
MDX INT6 CHECK NEXT CYLINDER
INT9 CMP L3 SID+2 COMPARE WITH ERR CYL
MDX INT10 OK
MDX INT10 OK

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00CF 0 70F9 MDX INT8 CONTINUE CHECK 88704090
00D0 0 73FF INT10 MDX 3 -1 DECREMENT ERR XR 88704100
00D1 0 70F9 MDX INT9 CHECK NEXT ERR ENTRY 88704110
00D2 0 70F1 MDX INT7 CHECK NEXT CYLINDER 88704120
00D3 0 0008 DC 8 CONSTANT 8 88704130
*
* THE FOLLOWING SECTIONS WILL INPUT THE 88704140
* DDM AND WRITE IT ON THE DISK. 88704150
* 88704160
00D4 00 44000203 GEN BSI L HM GO SEEK HOME 88704170
00D6 0 00DF LD USTB PICKUP 1ST CYLINDER 88704180
00D7 0 1803 SRA 3 REMOVE SECTOR BITS 88704190
***** 88704200
00D8 ORG 216 CARD 7 88704210
***** 88704220
00D8 00 D400026A STO L DSK SET IN SEEK COMMAND 88704230
00DA 00 44000216 BSI L SEEK GO SEEK TO DES. CYL. 88704240
00DC 0 6300 GEN1 LDX 3 0 INITIALIZE CARD 88704250
00DC 0 6B4A STX 3 CDC *COUNT INDICATOR 88704260
00DE 0 6100 LDX 1 0 SET OUTPUT AREA XK 88704270
00DF 0 7056 MDX RDCD GO READ A CARD 88704280
00E0 0 0C49 LD LCD PICK UP LAST CARD SW 88704290
00E1 00 4C2001BF BSC L LAST,Z BRANCH IF CN 88704300
00E3 0 7068 MDX PACK GO PACK BINARY DATA 88704310
00E4 0 0C43 LD CDC PICK UP CARD COUNT 88704320
00E5 00 4C2000EA BSC L GEN4,Z BRANCH IF NOT 1ST CD 88704330
00E7 00 74010128 MDX L CDC,1 ADD 1 TO CD COUNT 88704340
00E9 0 70F5 MDX GEN3 IGNORE HEADER CD 88704350
00EA 00 7400C129 GEN4 MDX L ADRS,0 SKIP IF NO DATA IN 88704360
00EC 0 7035 MDX ENDCK BRANCH TO END CD CK 88704370
00EC 00 4C000270 BSC L CKAD BRANCH TO CHECK ADRS 88704380
00EF 0 D039 GEN9 STO ADRS SAVE CARD ADRS 88704390
00F0 0 0C3A LD SCID PICK UP SECTION ID 88704400
00F1 00 4C1800FC BSC L GEN6,+ BRANCH IF SECTION 1 88704410
00F3 0 6308 LDX 3 11 TRANSFER USE TABLE 88704420
00F4 0 62F8 LDX 2 -8 *TO PROGRAM SECTION 88704430
00F5 00 C600008E GEN5 LD L2 USTB+8 * 88704440
00F7 00 D70003E8 STO L3 IN * 88704450
00F9 0 7301 MDX 3 1 * 88704460
00FA 0 7201 MDX 2 1 *SKIP WHEN 8 WDS XF 88704470
00FB 0 70F9 MDX GEN5 * 88704480
***** 88704490
00FC ORG 252 CARD 8 88704500
***** 88704510
00FC 0 1010 GEN6 SLA 16 CLEAR ZEROS SWITCH 88704520
00FD 0 D02E STO ZERO * 88704530
00FE 00 4C0033E8 LD L IN PICK UP CARD ADDRESS 88704540
0100 0 F028 EOR ADRS CHECK IF EXPECTED 88704550
0101 00 4C20011F BSC L FILL,Z BRANCH IF NOT EXPECT 88704560
0103 00 4C0003EA LD L IN+2 PICK UP CARD WD CNT 88704570
0105 0 E02A AND RSN SAVE WORD COUNT BITS 88704580
0106 00 D40003EA STO L IN+2 RESTORE TO DPG LCC 88704590
0108 00 678003EA LDX 13 IN+2 CARD WORD COUNT XR 88704600
010A 0 6209 LDX 2 9 SET INPUT DATA XR 88704610
010B 00 C60003E8 LD L2 IN MOVE DATA FROM INPUT 88704620
010D 00 D500044E STO L1 OUT TO OUTPUT AREA 88704630
010F 00 74010129 GEN8 MDX L ADRS,1 ADD 1 TO ADRS INDC 88704640
0111 0 7101 MDX 1 1 ADD 1 TO OUTPUT XR 88704650
0112 0 7201 MDX 2 1 ADD 1 TO INPUT DT XR 88704660
0113 00 7401012F MDX L WDCT,1 ADD 1 TO WORD CCUNTR 88704670
0115 0 C019 LD WDCT PICK UP WORD COUNT 88704680
0116 0 F016 EOR K320 CHECK FOR 320 WORDS 88704690
0117 00 441801A5 BSI L WRITE,+ WRITE DISK IF WC 320 88704700
0119 00 7400012C MDX L ZERO,0 SKIP IF ZERO SW OFF 88704710
011B 0 70E0 MDX GEN6 GO CHECK NEXT ADRS 88704720
011C 0 73FF MDX 3 -1 SKIP IF ALL WORDS 88704730
011D 0 70ED MDX GEN7 GO MOVE NEXT WORD 88704740
011E 0 70C8 MDX GEN4-3 GO READ NEXT CARD 88704750
88704760

DIMAL INITIAL LOADER (CARD)

011F 0 680C FILL STX ZERO SET ZEROS SWITCH 88704770
***** 88704780
0120 ORG 288 CARD 9 88704790
***** 88704800
0120 0 1010 SLA 16 CLEAR A REG 88704810
0121 0 70E8 MDX GEN7+2 FILL OA WITH ZERO 88704820
0122 00 C40003EA ENDCK LD L IN+2 PICK UP WORD CNT LOC 88704830
0124 0 F009 EOR KOF00 CHECK FOR END CARD 88704840
0125 00 4C180179 BSC L ENG,+ BRANCH IF END CARD 88704850
0127 0 70D4 MDX GEN6 CONTINUE 88704860
0128 0 0000 CDC DC 0 CARD COUNTER 88704870
0129 0 0000 ADRS DC 0 ADDRESS INDICATOR 88704880
012A 0 0000 ICD DC 0 LAST CARD INDICATOR 88704890
012B 0 0000 SCID DC 0 SECTION ID 88704900
012C 0 0000 ZERO DC 0 ZERO FILL INDICATOR 88704910
012D 0 0140 K320 DC 320 CONSTANT 88704920
012E 0 0F00 KOF00 DC /OF00 CONSTANT HEX OF00 88704930
012F 0 0000 WDCT DC 0 OUTPUT AREA WD CNTR 88704940
0130 0030 BSS E 0 88704950
0130 0 003F RSN DC /003F SENSE 1442 IOCC 88704960
0131 0 1700 DC /1700 88704970
0132 0 03E8 RD DC IN READ 1442 IOCC 88704980
0133 0 1600 DC /1600 88704990
0134 0 0001 RESN DC 1 RESET/SENSE IOCC 88705000
0135 0 1703 DC /1703 88705010
* 88705020
* THIS ROUTINE READS THE DDM OBJECT CARDS 88705030
* 88705040
0136 0 08F9 RDCD XIO RSN SENSE STATUS 88705050
0137 00 4C040146 BSC L W3506,E BRANCH IF NOT READY 88705060
0139 0 08F8 XIO RD READ A CARD 88705070
013A 0 08F5 RDCD1 XIO RSN SENSE STATUS 88705080
013B 0 1801 SRA 1 POSITION 88705090
013C 00 4C04013A BSC L RDCD1,E SPIN WHILE BUSY 88705100
013E 0 1808 SRA 11 POSITION 88705110
013F 00 4C04014A BSC L LST,E BRANCH IF LAST CARD 88705120
0141 0 1801 RDCD2 SRA 1 POSITION 88705130
0142 00 4C040148 BSC L W3507,E BRANCH IF ERROR 88705140
***** 88705150
0144 ORG 324 CARD 10 88705160
***** 88705170
0144 0 08EF XIO RESN RESET DSW 88705180
0145 0 709A MDX GEN3+1 EXIT 88705190
0146 0 3506 W3506 DC /3506 1442 NOT READY 88705200
0147 0 70EE MDX RDCD TRY AGAIN 88705210
0148 0 3507 W3507 DC /3507 DSW INDICATES ERROR 88705220
0149 0 70EC MDX RDCD TRY AGAIN 88705230
014A 0 68DF LST STX LCD SET LAST CARD SWITCH 88705240
014B 0 70F5 MDX RDCD2 CONTINUE 88705250
* 88705260
* THIS ROUTINE PACKS BINARY 12-4 DATA 88705270
* 88705280
014C 0 6924 PACK STX 1 PACK4+1 SAVE INDEX 1 88705290
014D 0 6188 LDX 1 -72 SET UP WORD INDEX 88705300
014E 0 6300 LDX 3 0 SET UP STORE INDEX 88705310
014F 0 62FD PACK1 LDX 2 -3 SET UP SHIFT INDEX 88705320
0150 00 C6000177 PACK2 LD L2 SHIFT+3 PICK UP SHIFT INSTRN 88705330
0152 0 D006 STO PACK3 SET IN ROUTINE 88705340
0153 00 C5000431 LD L1 IN+73 PICK UP 2ND HALF WD 88705350
0155 0 18D0 RTE 16 SET IN G REG. 88705360
0156 00 C5000430 LD L1 IN+72 PICK UP 1ST HALF WD 88705370
0158 0 1804 SRA 4 POSITION 88705380
0159 0 1000 PACK3 SLA 0 PACK A AND Q 88705390
015A 00 D70003E8 STO L3 IN STORE CONVERTED WD 88705400
015C 0 7301 MDX 3 1 MODIFY STORE INDEX 88705410
015D 0 7101 MDX 1 1 MODIFY WORD INDEX 88705420
015E 0 7201 MDX 2 1 MODIFY SHIFT INDEX 88705430
015F 0 70F0 MDX PACK2 GO CONVERT NXT WCRD 88705440



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DIMAL INITIAL LOADER (CARD)

0160 0 7101 MDX 1 1 MODIFY FOR NXT GROUP
0161 0 70ED MDX PACK1 GO CONVERT NXT GROUP
* THIS ROUTINE PERFORMS THE CHECKSUM
LDX 2 -54 SET DATA INDEX
LD CDC GET CARD COUNT
SUM A L2 IN+54 SUM DATA WORD
BSC C SKIP ON CARRY
A RESN ADD 1

ORG 360 CARD 11

MDX 2 1 SKIP WHEN DONE
MDX SUM CONTINUE
A RESN ADD 1
BSC L PACK4,+ BRANCH IF CHECKSUM OK
W3508 BSC /3508 CHECKSUM ERROR
BSC L GEN3 GO REREAD CARD
PACK4 LDX L1 0 RESTORE INDEX 1
BSC L GEN3+5 RETURN TO MAIN LINE
SHIFT SLT 4 SHIFT 4
SLT 8 SHIFT 8
SLT 12 SHIFT 12
* THIS SECTION SERVICES THE END CARD
KFFFO DC /FFFO CONSTANT
K3 DC 3 CONSTANT 3
END MDX 1 0 SKIP IF NO DATA TO WRITE
BSI WRITE GO WRITE DISK
SLA 16 CLEAR ACC
STO ADRS CLEAR LOC ADRS
MDX L SCID,0 SKIP IF SECTION 1
MDY END1
MDX L TEST,-1 SKIP IF ALL H-TESTS
MDX END2
LD SCID PICKUP SECTION ID
EOR K3 CHECK FOR SECT 3
BSC L LAST,+ BRANCH IF 4TH SECT
MDX L SCID,1 ADD 1 TO SECT IND
LD SCID PICKUP SECT IND
SRA 1 CHECK FOR SECTION 1
BSC 2 SKIP IF SECTION 1

ORG 396 CARD 12

MDX END3 BRANCH IF NOT SEC 1
END2 BSC L GEN1 GO INPUT NXT SECTION
END3 MDX L REF,1 INCR TABLE REF
LDX I3 REF SET XR = REF.
LD L3 USTB PICK UP NEXT CYL
S I3 USTB-1 SUB PREVIOUS CYL
SRA 3 REMCVE SECTOR BITS
STO L DSK SET SEEK COUNT
BSI SEEK SEEK TO NEXT CYLINDR
LD L DWRT+1 ZERO WRITE IOCC
AND KFFFO *SECTOR COUNT
STO L DWRT+1
MDX END2 EXIT
* REF DC 0 USE TABLE REFERENCE
K7 DC 7 CONSTANT
K321 DC 321 WRT/RD CONSTANT
TEST DC 7 NUMBER OF HEADER TESTS
* THIS ROUTINE SETUPS TO WRITE A DISK RECORD.

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DIMAL INITIAL LOADER (CARD)

01A5 0 0000 WRITE DC 0 ENTRY POINT
01A6 0 6101 LDX 1 1 SET UP FOR 1 WORD
01A7 00 6D00044C STX L1 IOA * DISK READ
01A9 00 C4000269 LD L DWRT+1 SETUP THE DISK READ
01AB 0 1883 SRT 3 *IOCC TO PEAD THE
01AC 00 C4000267 LD L DRD+1 *SECTOR ID OF THE
01AE 0 1803 SRA 3 *SECTOR TO BE WRITN
01AF 0 1083 SLT 3

0180 ORG 432 CARD 13

0180 00 D4000267 STO L CRD+1 *
0182 0 4072 BSI READ GO READ DISK SID
0183 0 COEF LD K321 SET UP WORD COUNT
G184 00 D400044C STO L IOA *FOR WRITE TABLE
0186 00 4400023F BSI L WRIT GO WRITE THE DISK
0188 00 74010269 MDX L DWRT+1,1 ADD 1 TO SECTOR CNT
018A 0 6100 LDX 1 0 SET OUTPUT AREA XR
018B 0C 6D00012F STX L1 WDCT CLEAR OUTPUT WC
018D 0J 4C8001A5 BSC I WRITE EXIT SUBROUTINE
* THE FOLLOWING ROUTINES ARE PERFORMED ON COMPLETION OF WRITING DIMAL ON DISK.
LAST SLA 16 CLEAR ACC
STO L LCD CLEAR LAST CARD SW
LD L SCID PICKUP SECT IND
EOR K3 CHECK FOR 4TH SECT
W3509 DC /3509 BSC L CKPT,+ BRANCH IF 4TH SECT
BSC L GEN1 DIMAL NCT ALL LOADED
LAST1 BSI HM SEEK TO HOME
* THIS ROUTINE WRITES THE MAINTNANCE PACK ID (ABCD) ON THE HISTORY TRACK,
LD L USTB+6 PICKUP SECTOR ADRS
SRA 3 REMOVE SECTOR BITS
STO L DSK SET IN SEEK IOCC
BSI SEEK GO SEEK TO HIST TRAK
LD L DRD+1 SET READ IOCC FOR
AND KFFFO *SECTOR 0

01D4 ORG 468 CARD 14

01D4 00 D4000267 STO L DRD+1 *
01D6 0 6303 LDX 3 3 SET WORD COUNT TO 3
01D7 00 6F00044C STX L3 IOA
01D9 00 C4000269 LD L DWRT+1 SET DISK WRITE IOCC
01DB 0 E09B AND KFFFO *FOR SECTOR 0
01DC 00 D4000269 STO L DWRT+1 *
01DE 0 4046 BSI READ READ HIST TRK SEC 0
01DF 00 6700ABCD LDX L3 /ABCD SET MAINT. PACK ID
01E1 00 6F00044E STX L3 OUT *IN OUTPUT AREA
01E3 00 C4000086 LD L USTB+5 GET LAST USED CYL
01E5 00 D400044F STO L OUT+1 SET IN OUTPUT AREA
01E7 00 4400023F BSI L WRIT WRITE ID ON HIST TRK
* THIS ROUTINE WILL INPUT THE LOADER WHICH IN TURN INPUTS THE DDM LOADER ORGANIZER PROGRAM
LD BSI HM SEEK TO HOME
LD L USTB PICKUP LOADER CYL AD
SRA 3 REMCVE SECTOR BITS
STO DSK SET IN SEEK COMMAND
BSI SEEK SEEK TO LOADER CYL

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DIMAL INITIAL LOADER (CARD)

01EF 00 67000DAA LDX L3 /ODAA SET XR = INPUT ADDR
01F1 0 6874 STX 3 DRD SET ADRS IN READ CMD
01F2 0 C080 LD K321 PICKUP RD WORD COUNT
01F3 0 D300 STJ 3 0 SET IN LOC HEX ODA
01F4 0 C072 LD DRD+1 PICKUP READ COMMAND
01F5 0 E8AC OR K7 SET SECTOR BITS = 7
01F6 0 D070 STJ DRD+1 RESTORE READ COMMAND
01F7 0 4020 BSI READ GO INPUT LCADER

01F8 ORG 504 CARD 15

01F8 0 1010 SLA 16 SETUP INIT LDR CALL
01F9 00 D400000C STO L /C SET IN LOC HEX C
01FB 0 C005 LD EDWD+1 PICKUP DRIVE A.C.
01FC 00 D400000D STO L /D SET IN LOC HEX D
01FE 00 4C000DAD BSC L /ODAD BRANCH TO LOADER
0200 0 0000 EDWD DC 0 CE HISTORY TRACK
0201 0 0000 DC 0 DRIVE AREA CODE
0202 0 0000 DC 0 OUTPUT DEVICE
*
* THIS ROUTINE SEEKS THE 2310 TO ITS
* HOME POSITION
*
0203 0 0000 HM DC 0 ENTRY POINT
0204 0 6304 LDX 3 4 SET TRY INDEX
0205 0 0868 HM1 XIO DSNSR SENSE/RESET STATUS
0206 0 D00E STO SKST SAVE STATUS
0207 0 1004 SLA 4 POSITION HOME BIT
0208 00 4CA80203 BSC I HM,+Z EXIT IF DISK HOME
020A 0 73FF MDX 3 -1 SKIP IF 3RD TRY
020B 0 7003 MDX HM2 GO ISSUE SEEK CMND
020C 0 C008 LD SKST RETRIEVE LAST DSW
020D 0 350A W350A DC /350A SEEK HOME ERROR
020E 0 70F5 MDX HM+1 TRY AGAIN
020F 0 0854 HM2 XIO HOME SEEK TO HOME
0210 0 0858 XIO DSNS SENSE DISK STATUS
0211 0 1001 SLA 1 POSITION OP CP BIT
0212 00 4C100210 BSC L HM2+1,- BRANCH IF NOT DONE
0214 0 70F0 MDX HM1 GO CHECK HOME BIT
*
0215 0 0000 SKST DC 0 SEEK DSW SAVE LOC
*
* THIS ROUTINE SEEKS 2310 TO DESIRED CYL
*
0216 0 0000 SEEK DC 0 ENTRY PCINT
0217 0 0854 XIO DSNS SENSE DISK STATUS
0218 0 1002 SLA 2 POSITION DSW
0219 00 4C10021D BSC L SEEK1,- BRANCH ON READY
021B 0 350B W350B DC /350B DISK NOT READY

021C ORG 540 CARD 16

021C 0 70FA MDX SEEK+1 TRY AGAIN
021D 0 084C SEEK1 XIO DSK SEEK DISK
021E 0 084D XIO DSNS SENSE STATUS
021F 0 1001 SLA 1 POSITION DSW
0220 00 4C10021E BSC L SEEK1+1,- BRANCH TILL DONE
0222 0 084B XIO DSNSR RESET DSW
0223 00 4C800216 BSC I SEEK EXIT
*
* THIS ROUTINE READS THE DISK
*
0225 0 0000 READ DC 0 ENTRY POINT
0226 0 6815 STX 3 READ2+1 SAVE INDEX REG 3
0227 0 6303 LDX 3 3 SET UP TRY COUNTER
0228 0 0843 XIO DSNS SENSE DISK STATUS
0229 0 1002 SLA 2 POSITION DSW
022A 00 4C10022E BSC L READ1,- BRANCH ON READY

DIMAL INITIAL LOADER (CARD)

022C 0 350C W350C DC /350C DISK NOT READY-READ
022D 0 70F9 MDX READ+2 TRY AGAIN
022E 0 0837 READ1 XIO DRD READ DISK
022F 0 083C XIO DSNS SENSE STATUS
0230 0 1001 SLA 1 POSITION DSW
0231 00 4C10022F BSC L READ1+1,- BRANCH TILL NOT BUSY
0233 0 083A XIO DSNSR RESET DSW
0234 0 E039 AND DSNSR CHECK FOR ERROR
0235 00 4C18023B BSC L READ2,+ BRANCH IF NO ERROR
0237 0 73FF MDX 3 -1 SKIP IF 3 TRIES
0238 0 70EF MDX READ+3 TRY AGAIN
0239 0 350D W350D DC /350D DISK READ ERROR
023A 0 70EC MDX READ+2 REPEAT
023B 00 67000000 READ2 LDX L3 0 RESTORE XR3
023D 00 4C800225 BSC I READ RETURN TO USER
*
* THIS ROUTINE WRITES THE DISK AND PER-
* FORMS A MODULO 4 CHECK
*
023F 0 0000 WRIT DC 0 ENTRY POINT

0240 ORG 576 CARD 17

0240 0 0828 XIO DSNS SENSE DISK STATUS
0241 0 1002 SLA 2 POSITION DSW
0242 0 481C BSC - SKIP IF NOT READY
0243 0 7002 MDX WRIT1 OK CONTINUE
0244 0 350E W350E DC /350E DISK NOT READY-WRITE
0245 0 70FA MDX WRIT+1 TRY AGAIN
0246 0 6103 WRIT1 LDX 1 3 SET TRY INDEX
0247 0 0820 XIO DWRT WRITE DISK
0248 0 0823 XIO DSNS SENSE STATUS
0249 0 1001 SLA 1 POSITION DSW
024A 00 4C100248 BSC L WRIT1+2,- BRANCH TILL DONE
024C 0 0821 XIO DSNSR RESET DSW
024D 0 E020 AND DSNSR CHECK FOR ERROR
024E 00 4C180254 BSC L WRIT2,+ BRANCH IF NO ERROR
0250 0 71FF MDX 1 -1 SKIP IF 3 TRIES
0251 0 70F5 MDX WRIT1+1 TRY AGAIN
0252 0 350F W350F DC /350F DISK WRT/MOD 4 ERROR
0253 0 70ED MDX WRIT+2 REPEAT
0254 0 C014 WRIT2 LD DWRT+1 SETUP MODULO 4 ICC
0255 0 F016 EOR DSNS
0256 0 D012 STO DWRT+1 SET IN WRITE ICC
0257 0 0810 XIO DWRT DO MODULO 4 CHECK
0258 0 0813 XIO DSNS SENSE STATUS
0259 0 1001 SLA 1 POSITION DSW
025A 00 4C100258 BSC L WRIT2+4,- SPIN TILL NOT BUSY
025C 0 C00C LD DWRT+1 RESTORE WRITE IOCC
025D 0 F00E EOR DSNS
025E 0 D00A STO DWRT+1 SET IN IOCC
025F 0 080E XIO DSNSR RESET DSW
0260 0 E00D AND DSNSR CHECK FOR ERROR
0261 00 4C98023F BSC I WRIT,+ RETURN TO USER IF OK
0263 0 70EC MDX W350F-2 ERROR - BRANCH
*

0264 ORG 612 CARD 18

0264 0000 BSS E 0
*
0264 0 00CA HOME DC 202 SEEK HOME IOCC
0265 0 0404 DC /0404
0266 0 044C DRD DC IOA READ DISK IOCC
0267 0 0603 DC /0603
0268 0 044C DWRT DC IOA WRITE DISK IOCC
0269 0 0500 DC /0500
026A 0 0000 DSK DC 0 SEEK DISK IOCC

DIMAL INITIAL LOADER (CARD)

026B 0 0400					
026C 0 0380	DSNS	DC	/0380	SENSE DISK ICC	88708170
026D 0 0700		DC	/0700		88708180
026E 0 8740	DSNSR	DC	/8740	SENSE/RESET DSK IOCC	88708190
026F 0 0701		DC	/0701		88708200
*					
0270 00 C40003E8	CKAD	LD	I IN	PICK UP STARTING ADR	88708210
0272 0 8009		CHP	K3000	CK ADRS FOR LEGAL	88708220
0273 0 7001		MDX	CKAD1	GREATER	88708230
0274 0 7005		MDX	CKAD2	ADRS OK-LESS	88708240
0275 0 8007	CKAD1	CHP	K70FF	CK ADRS FOR LEGAL	88708250
0276 0 7003		MDX	CKAD2	ADRS OK	88708260
0277 0 1000		NOP	0		88708270
0278 00 4C0000E7		BSC	L GEN4-3	IGNORE CARD	88708280
027A 00 4C0000EF	CKAD2	BSC	L GEN9	ADRS IS OK-MOVE DATA	88708290
027C 0 3000	K3000	DC	/3000	ADRS CK CONSTANTS	88708300
027D 0 70FF	K70FF	DC	/70FF		88708310
*					
PROGRAM RESTART OPERATION.					
*					
027E 0 1010	RSTRT	SLA	16	CLEAR PROGRAM CONTRCL	88708320
027F 00 D40001A1		STO	L REF	*SWITCHES	88708330
0281 00 D400012B		STO	L SCID	*	88708340
0283 00 D4000129		STO	L ADRS	*	88708350
0285 00 D400012F		STO	L WDCT	*	88708360
0287 0 C0E1		LD	DIRT+1	PICKUP WRITE COMMAND	88708370

0288	ORG		648	CARD 19	88708380

0288 00 E4000177	AND	L	KFFF0	SET SECTOR BITS TO 0	88708390
028A 0 B9DE		STO	DIRT+1	REPLACE COMMAND	88708400
028B 00 C40001A2		LD	L K7	GET CONSTANT 7	88708410
028D 00 D40001A4		STO	L TEST	SET IN HEADER TEST SW	88708420
028F 00 4C0000D4	BSC	L	GEN	GO INPUT DIMAL	88708430
0291 0 0808	CKPT	~IO	DESW	CK FOR PT LOAD FROM CARD	88708440
0292 00 F400002C		EOR	L KFFFF	PT IF SWS = FFFF	88708450
0294 00 4C2001CA		BSC	L LAST1,2	BRANCH IF NORM CARD LOAD	88708500
0296 00 44000203		BSI	L HM	RETRN ARM TO HOME	88708510
0298 0 3510	W3510	DC	/3510	PT DIMAL LOADED	88708520
0299 0 70FE		MDX	W3510	END OF PT LOAD TRAP	88708530
029A 0 0000		BSS	E 0		88708540
029A 0 0000	DESW	DC	0	SENSE DE SWITCHES IOCC	88708550
029B 0 0740		DC	/0740		88708560
029C 0000		END	0		88708570
					88708580
					88708590
					88708600

DIMAL INITIAL LOADER (CARD)

CROSS REFERENCE LISTING

SYMBOL	VALUE	REFERENCES
ADRS	0129	00EA, 00EF, 0100, 010F, 017C, 0283
BRN	0018	0015
CDC	0128	00DD, 00E4, 00F7, 0163
CDCT	001E	0009
CK	00B2	00A3
CKAD	0270	00ED
CKAD1	0275	0273
CKAD2	027A	0274, 0276
CKPT	0297	01C5
CK1	00B3	00AA
DESW	029A	0291
DRD	0266	01AC, 0180, 01D1, 01D4, 01F1, 01F4, 01F6, 022E
DSK	026A	0090, 00D8, 0198, 01CE, 01ED, 021D
DSNS	026C	0210, 0217, 021E, 0228, 022F, 0240, 0248, 0255, 0258, 025D
DSNSR	026E	0205, 0222, 0233, 0234, 024C, 024D, 025F, 0260
DSW	001C	0001, 0004, 0024, 002A
DSW1	005C	0031, 003B
DWRT	0268	019B, 019E, 01A9, 01B8, 01D9, 01DC, 0247, 0254, 0256, 0257, 025C, 025E, 0287, 028A
EDIT	0024	0017, 0037, 0056, 0058
EDIT1	002A	002C, 004E
EDIT2	0038	0034, 0065
EDIT3	004F	0040
EDSW	005E	0028, 003E, 0046
EDWD	0200	004A, 0089, 008C, 0093, 01FB
END	0179	0125
ENDCK	0122	00EC
END1	0183	017F
END2	018D	0182, 01A0
END3	018F	018C
FILL	011F	0101
GEN	00D4	00C8, 028F
GEN1	00DC	018D, 01C8
GEN3	00DF	00E9, 0145, 016E, 0172
GEN4	00EA	00E5, 011E, 0278
GEN5	00F5	00FB
GEN6	00FC	00F1, 0118, 0127
GEN7	010B	011D, 0121
GEN8	010F	
GEN9	00EF	027A
HBCV	005F	0038
HBCV1	0062	0085
HBCV2	0069	007D
HBCV3	0073	0077
HBCV4	0078	0071, 0074
HBCV5	007A	
HM	0203	009B, 00D4, 01CA, 01E9, 0208, 020E, 0296
HM1	0205	0214
HM2	020F	020B, 0212
HOME	0264	0095, 0097, 020F
IN	03E8	0032, 005A, 006C, 00F7, 00FE, 0103, 0106, 0108, 010B, 0122, 0132, 0153, 0156, 015A, 0164, 0270
INT	0089	0054
INT1	0093	009A
INT10	00D0	00CD, 00CE
INT2	009B	00A7
INT3	009D	
INT4	00A8	00A4, 00B1
INT5	00BE	00AB, 00AF
INT6	00C0	00CA
INT7	00C4	00D2
INT8	00C9	00CF
INT9	00CB	00C3, 00D1
IOA	044C	0000, 0013, 00A1, 00A8, 01A7, 01B4, 01D7, 0266, 0268
IPL1	0000	0008

IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

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DIMAL INITIAL LOADER (CARD)

IPL2	0001	0002,0010
IPL3	0000	0006
KED00	0059	0043
KFFFF	0020	0051,0292
KFFF0	0177	0190,0103,0108,0288
KOF00	012E	0124
K0100	001F	0000
K3	0178	0184,01C4
K3000	027C	0272
K320	0120	0116
K321	01A3	0183,01F2
K7	01A2	01F5,0288
K70FF	027D	0275
K8	0003	30C9
LAST	018F	00E1,0185
LAST1	01CA	0294
LCD	012A	00E0,014A,01C0
LD	01E9	
LOC	0086	0061,007E,0083
LST	014A	013F
OUT	044E	0039,0041,0048,004F,0081,010D,01E1,01E5
PACK	014C	00E3
PACK1	014F	0161
PACK2	0150	015F
PACK3	0159	0152
PACK4	0170	014C,0168
PREP	0011	000C
RD	0132	0139
RDCD	0136	00DF,0147,0149
RDCD1	013A	013C
RDCD2	0141	0148
RDED	0C5A	0C29
RUPAC	001A	00C0,0007
READ	0225	009F,0182,01DE,01F7,022D,0238,023A,023D
READ1	022E	022A,0231
READ2	0238	0226,0235
REF	01A1	018F,0191,027F
RESN	0134	0144,0167,016A
RSN	0130	0105,0136,013A
RSTRT	027E	0018
SAVE	0087	0067,0078,0079
SAVE1	0088	0068,006A,007A
SCID	0128	00F0,017D,0183,01E7,0189,01C2,0281
SEEK	0216	009D,00DA,019A,01D0,01EE,021C,0223
SEEK1	021D	0219,0220
SHIFT	0174	0150
SID	044D	0000,00C0,00C8
SKST	0215	0206,020C
SNSW	0084	00AE,00BF
SUM	0164	0169
TEST	01A4	0180,028D
USTB	0086	008B,008E,00C4,00D6,00F5,0193,0195,01C8,01E3,01EA
WDCY	012F	0113,0115,0188,0285
WRIT	022F	0186,01E7,0245,0253,0261
WRITE	01A5	0117,017A,018D
WRIT1	0246	0243,024A,0251
WRIT2	0254	024E,025A
W350A	020D	350A
W350B	0218	350B
W350C	022C	350C
W350D	0239	350D
W350E	0244	350E
W350F	0252	350F,0263
W3500	000F	3500,0005
W3501	0036	3501,003D,0045,0053
W3502	0055	3502,0025
W3503	0057	3503,002F
W3504	00A6	3504

DATE 04NOV66
FC NO. 415233

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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

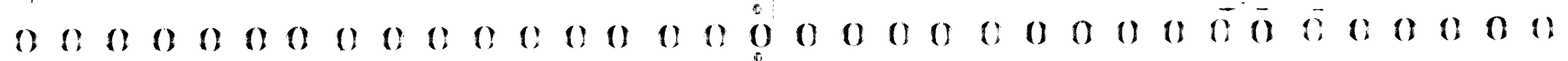
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DIMAL INITIAL LOADER (CARD)

W3505	00AD	3505
W3506	0146	3506,0137
W3507	0148	3507,0142
W3508	0160	3508
W3509	01C7	3509
W3510	0298	3510,0299
ZERG	012C	00F0,0119,011F

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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
BASIC DIAGNOSTIC LOADER

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PAGE 1

```
      ABS
0000 * CARD 01 -----
      ORG      0
      * ----- LOADER SHOULD BE IN LOCS. 00 - 28 -----
      *-----SEE THAT B IS 3OFF. PRESS START -----
0000 0 30FF      WAIT      -1      --BEGINNING OF LOADER.
0001 0 0C00 000C XIO L B1      READ ONE CARD INTO LOC. 28
0003 0 0C00 0010 XIO L K0800  RESET DSW
0005 0 0C00 000E A1 XIO L K0003  SENSE DSW FOR 1442
0007 0 F400 000E EOR L K0003  CHECK BITS 14&15 ONLY
0009 0 4820      BSC      Z      SKIP BITS 14&15 ONLY
000A 0 6016      LDX      E1      CONTINUE DSW ANALYSIS
000B 0 6005      LDX      A1      CARD IS BEING READ
000C 0 0028      B1 DC /0028  READ CONTROL
000D 0 1601      DC /1601  1442, 8/8 FORMAT
000E 0 0003      K0003 DC /0003  SENSE DSW CONTROL
000F 0 1700      DC /1700  WITHOUT TURN OFF
0010 0 0800      K0800 DC /0800  SENSE DSW CONTROL
0011 0 1703      DC /1703  TURN OFF REQUEST
0012 0 F400 0010 D1 EOR L K0800  RETURN DSW WORD TO ACC.
0014 0 3001      WAIT      /1      **ERR. SEE ACC. DSW NOT RIGHT
0015 0 6001      LDX      /1      TRY AGAIN
0016 0 0C00 0010 E1 XIO L K0800  SENSE AND TURN OFF DSW
0018 0 F400 0010 EOR L K0800  CHECK FOR BIT 4 ONLY
001A 0 4820      BSC      Z      SKIP OPERATION COMPLETE
001B 0 6012      LDX      D1      DSW ERROR CONDITION
001C 0 6023      LDX      /23
001D 0 0000      DC      0      SPACE FILLER
001E 0 0000      DC /0000  SPACE FILLER
001F 0 0000      DC /0000  SPACE FILLER
0020 0 0000      DC /0000  SPACE FILLER
0021 0 0000      DC /0000  SPACE FILLER
0022 0 0000      DC /0000  SPACE FILLER
0023 0 6028      LDX      /28  GO TO PROG. LOADED

* CARD 02 -----
0024 ORG /0028
0028 0 0C00 0038 XIO L K0800  RESET DSW
002A 0 0C00 0034 XIO L B2      READ A CARD INTO /0000
002C 0 0C00 0036 XIO L K0003  SENSE DSW FOR 1442
002E 0 F400 0036 EOR L K0003  CHECK BITS 14&15 ONLY
0030 0 4820      BSC      Z      SKIP BITS 14 & 15 ONLY
0031 0 603E      LDX      E2      CONTINUE DSW ANALYSIS
0032 0 602C      LDX      A2      CARD IS BEING READ
0033 0 0000      DC /0000  SPACE FILLER
0034 0 0000      B2 DC /0000  READ CONTROL & CONSTANT
0035 0 1601      DC /1601  1442, 818 FORMAT
0036 0 0003      K0003 DC /0003  SENSE DSW CONTROL
0037 0 1700      DC /1700  WITHOUT TURN OFF
0038 0 0800      K0800 DC /0800  SENSE DSW CONTROL
0039 0 1703      DC /1703  TURN OFF REQUEST
003A 0 0C00 0038 D2 XIO L K0800  RESET DSW TO ACC
003C 0 3002      WAIT      /2      **ERR. SEE ACC. DSW NOT RIGHT
003D 0 6028      LDX      /28  TRY AGAIN
003E 0 0C00 0036 E2 XIO L K0003  SENSE DSW
0040 0 F400 0038 EOR L K0800  CHECK FOR BIT 4
0042 0 1801      SRA      1      REMOVE NOT READY BIT
0043 0 4820      BSC      Z      SKIP OPERATION COMPLETE
0044 0 603A      LDX      D2      DSW ERROR CONDITION
0045 0 0C00 0038 XIO L K0800  SENSE - RESET DSW
0047 0 C400 003D * BLD LD L R2      CARD 11 WILL START HERE
0049 0 D400 0023 STD L /23  SET PROG. LOADED TO RETURN
004B 0 6000      LDX      /0000  TO THIS PROG.
                                GO TO PROG. LOADED

* CARD 03 -----
* TEST FOR XIO OF READ AND SENSE DSW
*-----*** RUN ONLY ON IPL ***-----
88800020
88800030
88800040
88800050
88800060
88800070
88800080
88800090
88800100
88800110
88800120
88800130
88800140
88800150
88800160
88800170
88800180
88800190
88800200
88800210
88800220
88800230
88800240
88800250
88800260
88800270
88800280
88800290
88800300
88800310
88800320
88800330
88800340
88800350
88800360
88800370
88800380
88800390
88800400
88800410
88800420
88800430
88800440
88800450
88800460
88800470
88800480
88800490
88800500
88800510
88800520
88800530
88800540
88800550
88800560
88800570
88800580
88800590
88800600
88800610
88800620
88800630
88800640
88800650
88800660
88800670
88800680
88800690
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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM
BASIC DIAGNOSTIC LOADER

PART NO. 2196487
PAGE 1A

```
004C ORG 0
0000 0 6023 LDX /23
0001 0 0C00 0016 XIO L B3      READ A CARD
0003 0 0C00 001A XIO L K0800  RESET DSW
0005 0 0C00 0018 A3 XIO L K0003  SENSE DSW FOR 1442
0007 0 3003      WAIT /0003  --ACC. HAS DSW. SHOULD BE0003
                                IF OK PRESS START
0008 0 0C00 001A * XIO L K0800  SENSE AND RESET DSW
000A 0 3003      WAIT /0003  --ACC. HAS DSW SHOULD BE0800

*-----
000B 0 0C00 0016 LP XIO L B3      LOOP FOR TESTING READ CARD
000D 0 0C00 001A XIO L K0800  RESET DSW
000F 0 0C00 0018 A3A XIO L K0003  SHOULD READ AS LONG AS
0011 0 F400 0018 EOR L K0003  CARDS IN READER
0013 0 4820      BSC      Z      SKIP BITS 14&15 #BUSY,READY
0014 0 601C      LDX      E3
0015 0 600F      LDX      A3A  BUSY AND NOT READY GET DSW
0016 0 0028      B3 DC /0028  RD CARD CONTROL WORDS
0017 0 1601      DC /1601
0018 0 0003      K0003 DC /0003  SENSE DSW CONTROL
0019 0 1700      DC /1700  WITHOUT TURN OFF
001A 0 0800      K0800 DC /0800  SENSE DSW CONTROL
001B 0 1703      DC /1703  TURN OFF REQUEST
001C 0 0C00 001A E3 XIO L K0800  SENSE AND TURN OFF DSW
001E 0 F400 001A EOR L K0800  CHECK BIT 4 ONLY
0020 0 4820      BSC      Z      SKIP OPERATION COMPLETE
0021 0 3003      WAIT /3      **ERR. DSW WRONG. ACC. HAS
                                DSW AFTER AN EOR WITH BIT 4
0022 0 600B      LDX      LP      GO READ NEXT CARD
0023 0 6001      LDX      /1      CHANGED TO LDX 28 BY LOADER

* CARD 04 -----
* TEST OF LDX FOR LOCATIONS 0 THRU 3F
0024 ORG /0000
0000 0 6002 LDX /0002
0001 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0002 0 601D LDX /001D
0003 0 3004 WAIT /4
0004 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0005 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0006 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0007 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0008 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0009 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000A 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000B 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000C 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000D 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000E 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
000F 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0010 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0011 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0012 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0013 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0014 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0015 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0016 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0017 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0018 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0019 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
001A 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
001B 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
001C 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
001D 0 6023 LDX /0023
001E 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
001F 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0020 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
0021 0 3004 WAIT /4      **ERR. LDX, IPL CARD-4
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BASIC DIAGNOSTIC LOADER

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0022 0 3004      WAIT  /4      **ERR. LDX, IPL CARD-4      8B801380
0023 0 6000      LDX   /0000  --CHANGED TO LDX 28 BY LOADER 8B801390
* CARDS 5 6 7 & 8 TEST THE FOLLOWING-----
*1.CRP READS IN 0 & 1 EACH BIT POSITION      8B801400
*2.LD 1 PUTS 0 & 1 IN EACH BIT OF ACC.      8B801410
*3.BSC Z SKIPS ON ACC#0000 & NOT SKIP ANY 1 BIT. 8B801420
* 4 TEST EOR FOR 1,1 & 0,0                  8B801430
*                                             8B801440
*                                             8B801450
* CARD 05 -----
0024      ORG   /0000      8B801460
0000 0 C400 0009  LD   L K0000      SET ACC. TO 0000      8B801470
0002 0 4820      BSC   Z                  TEST SKIP ON ZERO    8B801480
0003 0 3005      WAIT  /5      **ERR. BSC Z FAILED OR ACC. 8B801490
                                NOT QUAL 0000.      8B801500
0004 0 C400 000A BIT00 LD   L K8000      SET BIT 0 TO 1, OTHERS 0. 8B801510
0006 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801520
0007 0 6008      LDX   BIT01      8B801530
0008 0 3005      WAIT  /5      **ERR. BSC Z FAIL OR ACC#0000 8B801540
0009 0 0000      K0000 DC  /0000      8B801550
000A 0 8000      K8000 DC  /8000      8B801560
000B 0 C400 0010 BIT01 LD   L K4000      SET BIT 1 TO 1, OTHERS 0. 8B801570
000D 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801580
000E 0 6011      LDX   BIT02      8B801590
000F 0 3005      WAIT  /5      **ERR. BSC Z FAIL OR ACC#0000 8B801600
0010 0 4000      K4000 DC  /4000      8B801610
0011 0 C400 0016 BIT02 LD   L K2000      SET BIT 2 TO 2, OTHERS 0. 8B801620
0013 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801630
0014 0 6017      LDX   BIT03      8B801640
0015 0 3005      WAIT  /5      **ERR. BSC Z FAIL OR ACC#0000 8B801650
0016 0 2000      K2000 DC  /2000      8B801660
0017 0 C400 001C BIT03 LD   L K1000      SET BIT 3 TO 1, OTHERS 0. 8B801670
0019 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801680
001A 0 601D      LDX   BIT04      8B801690
001B 0 3005      WAIT  /5      **ERR. BSC Z FAIL OR ACC#0000 8B801700
001C 0 1000      K1000 DC  /1000      8B801710
001D 0 C400 0022 BIT04 LD   L K0800      SET BIT 4 TO 1, OTHERS 0. 8B801720
001F 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801730
0020 0 6023      LDX   /0023      8B801740
0021 0 3005      WAIT  /5      **ERR. BSC Z FAIL OR ACC#0000 8B801750
0022 0 0800      K0800 DC  /0800      8B801760
0023 0 6000      LDX   /0000      CHANGED TO LDX 28 BY LOADER 8B801770
                                8B801780
                                8B801790
* CARD 06 -----
0024      ORG   /0000      CARD 6--BSC Z & LD L TEST-- 8B801800
0000 0 C400 0005 BIT05 LD   L K0400      SET BIT 5 TO 1, OTHERS 0. 8B801810
0002 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801820
0003 0 6006      LDX   BIT06      8B801830
0004 0 3006      WAIT  /6      **ERR. BSC Z FAIL OR ACC#0000 8B801840
0005 0 0400      K0400 DC  /0400      8B801850
0006 0 C400 0008 BIT06 LD   L K0200      SET BIT 6 TO 2, OTHERS 0. 8B801860
0008 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801870
0009 0 600C      LDX   BIT07      8B801880
000A 0 3006      WAIT  /6      **ERR. BSC Z FAIL OR ACC#0000 8B801890
000B 0 0200      K0200 DC  /0200      8B801900
000C 0 C400 0011 BIT07 LD   L K0100      SET BIT 7 TO 1, OTHERS 0. 8B801910
000E 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801920
000F 0 6012      LDX   BIT08      8B801930
0010 0 3006      WAIT  /6      **ERR. BSC Z FAIL OR ACC#0000 8B801940
0011 0 0100      K0100 DC  /0100      8B801950
0012 0 C400 0017 BIT08 LD   L K0080      SET BIT 8 TO 1, OTHERS 0. 8B801960
0014 0 4820      BSC   Z                  SHOULD NOT SKIP      8B801970
0015 0 6018      LDX   BIT09      8B801980
0016 0 3006      WAIT  /6      **ERR. BSC Z FAIL OR ACC#0000 8B801990
0017 0 0080      K0080 DC  /0080      8B802000
0018 0 C400 001D BIT09 LD   L K0040      SET BIT 9 TO 1, OTHERS 0. 8B802010
001A 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802020
001B 0 6023      LDX   /0023      8B802030
001C 0 3006      WAIT  /6      **ERR. BSC Z FAIL OR ACC#0000 8B802040
                                8B802050

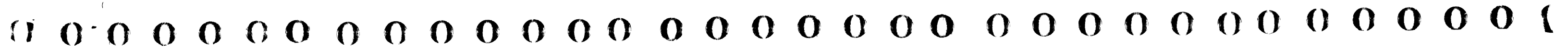
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BASIC DIAGNOSTIC LOADER

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001D 0 0040      K0040 DC  /0040      8B802060
001E 0 0000      DC   /0000      SPACE FILLER      8B802070
001F 0 0000      DC   /0000      SPACE FILLER      8B802080
0020 0 0000      DC   /0000      SPACE FILLER      8B802090
0021 0 0000      DC   /0000      SPACE FILLER      8B802100
0022 0 0000      DC   /0000      SPACE FILLER      8B802110
0023 0 6000      LDX   /0000      CHANGED TO LDX 28 BY LOADER 8B802120
                                8B802130
* CARD 07 -----
0024      ORG   /0000      CARD 7--BSC Z & LD 1 TEST-- 8B802140
0000 0 C400 0005 BIT10 LD   L K0020      SET BIT 10 TO 1, OTHERS 0. 8B802150
0002 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802160
0003 0 6006      LDX   BIT11      8B802170
0004 0 3007      WAIT  /7      **ERR. BSC Z FAIL OR ACC#0000 8B802180
0005 0 0020      K0020 DC  /0020      8B802190
0006 0 C400 0008 BIT11 LD   L K0010      SET BIT 11 TO 1, OTHERS 0. 8B802200
0008 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802210
0009 0 600C      LDX   BIT12      8B802220
000A 0 3007      WAIT  /7      **ERR. BSC Z FAIL OR ACC#0000 8B802230
000B 0 0010      K0010 DC  /0010      8B802240
000C 0 C400 0011 BIT12 LD   L K0008      SET BIT 12 TO 1, OTHERS 0. 8B802250
000E 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802260
000F 0 6012      LDX   BIT13      8B802270
0010 0 3007      WAIT  /7      **ERR. BSC Z FAIL OR ACC#0000 8B802280
0011 0 0008      K0008 DC  /0008      8B802290
0012 0 C400 0017 BIT13 LD   L K0004      SET BIT 13 TO 1, OTHERS 0. 8B802300
0014 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802310
0015 0 6018      LDX   BIT14      8B802320
0016 0 3007      WAIT  /7      **ERR. BSC Z FAIL OR ACC#0000 8B802330
0017 0 0004      K0004 DC  /0004      8B802340
0018 0 C400 001D BIT14 LD   L K0002      SET BIT 14 TO 1, OTHERS 0. 8B802350
001A 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802360
001B 0 6023      LDX   /0023      8B802370
001C 0 3007      WAIT  /7      **ERR. BSC Z FAIL OR ACC#0000 8B802380
001D 0 0002      K0002 DC  /0002      8B802390
001E 0 0000      DC   /0000      SPACE FILLER      8B802400
001F 0 0000      DC   /0000      SPACE FILLER      8B802410
0020 0 0000      DC   /0000      SPACE FILLER      8B802420
0021 0 0000      DC   /0000      SPACE FILLER      8B802430
0022 0 0000      DC   /0000      SPACE FILLER      8B802440
0023 0 6000      LDX   /0000      CHANGED TO LDX 28 BY LOADER 8B802450
                                8B802460
* CARD 08 -----
0024      ORG   /0000      CARD 8 BSC 7 & LD BIT 15 8B802470
                                AND EOR 1,1 & 0,0      8B802480
0000 0 C400 0005 BIT15 LD   L K0001      SET BIT 15 TO 1, OTHERS 0. 8B802490
0002 0 4820      BSC   Z                  SHOULD NOT SKIP      8B802500
0003 0 6006      LDX   TEOR      8B802510
0004 0 3008      WAIT  /8      **ERR. BSC Z FAIL OR ACC#0000 8B802520
0005 0 0001      K0001 DC  /0001      8B802530
0006 0 C400 0012 TEOR LD   L KFFFF      TEST D#FFFF & A#FFFF 8B802540
0008 0 F400 0012 EOR   L KFFFF      SHOULD SKIP      8B802550
000A 0 4820      BSC   Z                  **ERR. SEE ACC. SHOULD # 0000 8B802560
000B 0 3008      WAIT  /8      TEST 0#0000 & A#0000 8B802570
000C 0 F400 0011 EOR   L K0000      SHOULD SKIP      8B802580
000E 0 4820      BSC   Z                  **ERR. SEE ACC. SHOULD # 0000 8B802590
000F 0 3008      WAIT  /8      8B802600
0010 0 6023      LDX   /0023      8B802610
0011 0 0000      K0000 DC  /0000      8B802620
0012 0 FFFF      KFFFF DC  /FFFF      8B802630
0013 0 0000      DC   /0000      SPACE FILLER      8B802640
0014 0 0000      DC   /0000      SPACE FILLER      8B802650
0015 0 0000      DC   /0000      SPACE FILLER      8B802660
0016 0 0000      DC   /0000      SPACE FILLER      8B802670
0017 0 0000      DC   /0000      SPACE FILLER      8B802680
0018 0 0000      DC   /0000      SPACE FILLER      8B802690
0019 0 0000      DC   /0000      SPACE FILLER      8B802700
001A 0 0000      DC   /0000      SPACE FILLER      8B802710
                                SPACE FILLER      8B802720
                                SPACE FILLER      8B802730

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001B 0 0000 DC /0000 SPACE FILLER 8B802740
001C 0 0000 DC /0000 SPACE FILLER 8B802750
001D 0 0000 DC /0000 SPACE FILLER 8B802760
001E 0 0000 DC /0000 SPACE FILLER 8B802770
001F 0 0000 DC /0000 SPACE FILLER 8B802780
0020 0 0000 DC /0000 SPACE FILLER 8B802790
0021 0 0000 DC /0000 SPACE FILLER 8B802800
0022 0 0000 DC /0000 SPACE FILLER 8B802810
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B802820

* CARD 09
* TEST LOAD AND STORE LONG FORM
* TEST ALL BITS TRANSFER B-D-A-U-A, A-M, A-B
* EOR L & BSC Z COULD CAUSE FAILURE 8B802830
8B802840
8B802850
8B802860
8B802870

0024 ORG /0000
0000 0 C400 0000 TST1 LD L KON1&1 LOAD A TO 3333 8B802880
0002 0 D400 CCCC KON2 STO L /CCCC STORE 3333 IN CCCC 8B802890
0004 0 C400 CCCC LD L /CCCC GET 3333 FROM CCCC 8B802900
0006 0 F400 0000 EOR L KON1&1 EOR A#3333 TO D#3333 8B802910
0008 0 4820 BSC Z SHOULD SKIP 8B802920
0009 0 3009 WAIT /9 **ERR. A NOT 0000. GO TO TST1 8B802930
000A 0 C400 0003 TST2 LD L KON2&1 LD A TO CCCC 8B802940
000C 0 D400 3333 KON1 STO L /3333 STORE CCCC AT 3333 8B802950
000E 0 C400 3333 LD L /3333 GET CCCC FROM 3333 8B802960
0010 0 F400 0003 EOR L KON2&1 EOR A#CCCC TO D#CCCC 8B802970
0012 0 4820 BSC Z SHOULD SKIP 8B802980
0013 0 3009 WAIT /9 **ERR. A NOT 0000. GO TO TST2 8B802990
0014 0 6023 LDX /0023 8B803000
0015 0 0000 DC /0000 SPACE FILLER 8B803010
0016 0 0000 DC /0000 SPACE FILLER 8B803020
0017 0 0000 DC /0000 SPACE FILLER 8B803030
0018 0 0000 DC /0000 SPACE FILLER 8B803040
0019 0 0000 DC /0000 SPACE FILLER 8B803050
001A 0 0000 DC /0000 SPACE FILLER 8B803060
001B 0 0000 DC /0000 SPACE FILLER 8B803070
001C 0 0000 DC /0000 SPACE FILLER 8B803080
001D 0 0000 DC /0000 SPACE FILLER 8B803090
001E 0 0000 DC /0000 SPACE FILLER 8B803100
001F 0 0000 DC /0000 SPACE FILLER 8B803110
0020 0 0000 DC /0000 SPACE FILLER 8B803120
0021 0 0000 DC /0000 SPACE FILLER 8B803130
0022 0 0000 DC /0000 SPACE FILLER 8B803140
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B803150
8B803160
8B803170
8B803180
8B803190
8B803200

* CARD A TEST EOR FOR 1,0 0,1
* SRA 1 PARTLY TESTED 8B803170
8B803180
8B803190
8B803200

* CARD 0A
0024 ORG /0000
0000 0 C400 001B LD L K8000 PUT BIT 0#1 IN ACC. 8B803210
0002 0 6008 LDX RETRY-2 GO STORE IT IN TEST 8B803220
0003 0 1801 SHIFT SRA 1 SHIFT TO TEST NEXT BIT 8B803230
0004 0 4820 BSC Z SHOULD NOT SKIP 8B803240
0005 0 6008 LDX RETRY-2 8B803250
0006 0 300A WAIT /A **ERR. SRA 1 DROPPED BIT 8B803260
8B803270
8B803280
8B803290
8B803300

0007 0 600A LDX RETRY 8B803310
0008 0 D400 0018 STO L TEST STORE BIT FOR EOR TEST 0&1 8B803320
000A 0 C400 0019 LD L K0000 CLEAR ACC. 8B803330
000C 0 F400 0018 EOR L TEST EOR ACC#0 D HAS A 1. 8B803340
000E 0 4820 BSC Z SHOULD NOT SKIP 8B803350
000F 0 6012 LDX CONTA 8B803360
0010 0 300A WAIT /A **ERR. A IS 0000 GO TO RETRY 8B803370
0011 0 600A LDX RETRY 8B803380
0012 0 F400 0019 CONTA EOR L K0000 EOR ACC. HAS A 1 & D#0 8B803390
0014 0 4820 BSC Z SHOULD NOT SKIP 8B803400
0015 0 601C LDX CONTB 8B803410
0016 0 300A WAIT /A **ERR. A IS 0000 GO TO RETRY 8B803420

0017 0 600A LDX RETRY 8B803420
0018 0 0000 TEST DC /0000 BIT WITH 1 IS BEING TESTED 8B803430
0019 0 0000 K0000 DC /0000 8B803440
001A 0 0001 K0001 DC /0001 8B803450
001B 0 8000 K8000 DC /8000 8B803460
001C 0 F400 001A CONTB EOR L K0001 TEST FOR BIT 15#1 8B803470
001E 0 4820 BSC Z SKIP BIT 15#1, ALL POS. DONE 8B803480
001F 0 6003 LDX SHIFT GO TO DO NEXT BIT POSITION. 8B803490
0020 0 6023 LDX /0023 8B803500
0021 0 0000 DC /0000 SPACE FILLER 8B803510
0022 0 0000 DC /0000 SPACE FILLER 8B803520
0023 0 6000 LDX /0000 CHANGED TO LDX 28 BY LOADER 8B803530
8B803540

* CARD 0B
0024 ORG 0 8B803550

0000 0 C400 001C LD L H0000 TEST ADD BY POSITIVE AND NEGATIVE ONES 8B803560
0002 0 D400 001B STO L SUMMI CLEAR TO ZERO 8B803570
0004 0 D400 001A STO L SUMPL SUM OF MINUS ONES 8B803580
0006 0 C400 001B ADD LD L SUMMI SUM OF PLUS ONES 8B803590
0008 0 8400 001E A L HFFFF GET SUM OF MINUS ONES 8B803600
000A 0 D400 001B STO L SUMMI ADD MINUS ONE 8B803610
000C 0 C400 001A LD L SUMPL STORE SUM OF MINUS ONES 8B803620
000E 0 8400 001D A L H0001 GET SUM OF PLUS ONES 8B803630
0010 0 D400 001A STO L SUMPL ADD PLUS ONE 8B803640
0012 0 4820 BSC Z STORE SUM OF PLUS ONES 8B803650
0013 0 6015 LDX TOTAL SKIP WHEN SUM IS 0000 8B803660
0014 0 6023 LDX /0023 8B803670
0015 0 8400 001B TOTAL A L SUMMI BRANCH WHEN ONE PASS DONE. 8B803680
0017 0 4820 BSC Z ADD SUMMI TO SUMPL 8B803690
0018 0 300B WAIT /B SHOULD SKIP 8B803700
0019 0 6006 LDX ADD **ERR. TOTAL SHOULD BE ZERO 8B803710
001A 0 0000 SUMPL DC /0000 LOC. FOR SUM OF PLUS ONES 8B803720
001B 0 0000 SUMMI DC /0000 LOC. FOR SUM OF MINUS ONES 8B803730
001C 0 0000 H0000 DC 0 8B803740
001D 0 0001 H0001 DC 1 8B803750
001E 0 FFFF HFFFF DC -1 8B803760
001F 0 0000 DC 0 8B803770
0020 0 0000 DC 0 SPACE FILLER 8B803780
0021 0 0000 DC 0 SPACE FILLER 8B803790
0022 0 0000 DC 0 SPACE FILLER 8B803800
0023 0 6000 LDX /0000 SPACE FILLER 8B803810
8B803820
8B803830
8B803840
8B803850

* CARD 10
0024 ORG 0 8B803860

0000 0 0C00 000C XIO L B10 READ ONE CARD INTO BLD 8B803870
0002 0 0C00 0038 XIO L K0800 RESET DSW 8B803880
0004 0 0C00 0036 A10 XIO L K0003 SENSE DSW FOR 1442 8B803890
0006 0 F400 0036 EOR L K0003 CHECK BITS 14&15 ONLY 8B803900
0008 0 4820 BSC Z SKIP BITS 14 & 15 ONLY 8B803910
0009 0 6012 LDX E10 CONTINUE DSW ANALYSIS 8B803920
000A 0 6004 LDX A10 CARD IS BEING READ 8B803930
000B 0 6028 C10 LDX /0028 8B803940
000C 0 0047 B10 DC BLD 8B803950
000D 0 1601 DC /1601 READ CONTROL 8B803960
000E 0 F400 0038 D10 EOR L K0800 1442 8/8 FORMAT 8B803970
0010 0 3010 WAIT /10 RETURN DSW WORD TO ACC. 8B803980
0011 0 6000 LDX /0 **ERR. SEE ACC. DSW NOT RIGHT 8B803990
0012 0 0C00 0038 E10 XIO L K0800 TRY AGAIN 8B804000
0014 0 F400 0038 EOR L K0800 SENSE AND TURN OFF DSW 8B804010
0016 0 4820 BSC Z CHECK FOR BIT 4 ONLY 8B804020
0017 0 600E LDX D10 SKIP OPERATION COMPLETE 8B804030
0018 0 6028 LDX /0028 DSW ERROR CONDITION 8B804040
8B804040
8B804050
8B804060

* CARD 11
0019 ORG BLD 8B804070

* THIS CARD IS READ OVER THE LOADER IN 46 THRU 68 8B804080
* USES ITS INSTRUCTIONS FROM 0028 THRU 0045 8B804090

BASIC DIAGNOSTIC LOADER

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0047 0 C400 0024 LD L /0024 GET WORD COUNT 8B804100
0049 0 4820 BSC Z SKIP IF WORD COUNT ZERO 8B804110
004A 0 604F LDX SUM1 8B804120
004B 0 3011 WAIT /11 **ERR. WORD COUNT IS ZERO 8B804130
004C 0 6028 LDX /0028 START LOADS NEXT CARD 8B804140
004D 0 0000 K0000 DC /0000 8B804150
004E 0 0001 K0001 DC /0001 8B804160
004F 0 C400 004D SUM1 LD L K0000 RESTORE MODIFIED ADDRESS 8B804170
0051 0 D400 0058 STO L CKL0D&1 8B804180
0053 0 D400 0027 STO L /0027 CLEAR SUM LOC. 8B804190
0055 0 C400 0027 LD L /0027 8B804200
0057 0 8400 FFFF CKL0D A L /FFFF FORM SUM OF LOCS. 0 THRU 26 8B804210
0059 0 D400 0027 STO L /0027 8B804220
005B 0 C400 0058 LD L CKL0D&1 MODIFY ADDRESS 8B804230
005D 0 8400 004E A L K0001 8B804240
005F 0 D400 0058 STO L CKL0D&1 8B804250
0061 0 F400 0056 EOR L CKL0D-1 CHECK THAT ALL WORDS DONE 8B804260
0063 0 4820 BSC Z SKIP ALL LOCS. ADDED 8B804270
0064 0 6055 LDX CKL0D-2 8B804280
0065 0 C400 0027 LD L /0027 LOAD SUM 0 THRU 26 8B804290
0067 0 4820 BSC Z SKIP READ IN OK. 8B804300
0068 0 3011 WAIT /11 **ERR. IN CHECK SU. START 8B804310
LOADS NEXT CARD. 8B804320
* MOVE LDX /0000 RUN CARD LOADED. CARD 13 8B804330
* BEGINS LOADING HERE. 8B804340
* 8B804350
* CARD 12 -----
006A ORG 0 8B804360
* CHECK SUM CHECK CARD. THIS IS USED TO DETECT 8B804370
* ERRORS THAT OCCUR AS THE RESULT OF WRONG CHECK 8B804380
* SUM FOR CARD IMAGE IN LOCS. 0000 THRU 0026 8B804390
* CHECK SUM ROUTINES ADD LOC. 0 THRU 26 IN SEQUENCE 8B804400
* . CORRECT ACC. AFTER ADD IS SHOWN BELOW IN 8B804410
* SEQUENCE THE PROG. FOLLOWS. 8B804420
* CORE CONTENTS. CORRECT SUM. CORE LOC. 8B804430
0000 0 6028 DC /6028 6028 0000 8B804450
0001 0 9FD7 DC /9FD7 FFFF 0001 8B804460
0002 0 FFFF DC /FFFF FFFF 0002 8B804470
0003 0 0001 DC /0001 FFFF 0003 8B804480
0004 0 0001 DC /0001 0000 0004 8B804490
0005 0 0001 DC /0001 0001 0005 8B804500
0006 0 0001 DC /0001 0002 0006 8B804510
0007 0 0002 DC /0002 0004 0007 8B804520
0008 0 0004 DC /0004 0008 0008 8B804530
0009 0 0008 DC /0008 0010 0009 8B804540
000A 0 0010 DC /0010 0020 000A 8B804550
000B 0 0020 DC /0020 0040 000B 8B804560
000C 0 0040 DC /0040 0080 000C 8B804570
000D 0 0080 DC /0080 0100 000D 8B804580
000E 0 0100 DC /0100 0200 000E 8B804590
000F 0 0200 DC /0200 0400 000F 8B804600
0010 0 0400 DC /0400 0800 0010 8B804610
0011 0 0800 DC /0800 1000 0011 8B804620
0012 0 1000 DC /1000 2000 0012 8B804630
0013 0 2000 DC /2000 4000 0013 8B804640
0014 0 4000 DC /4000 8000 0014 8B804650
0015 0 8000 DC /8000 0000 0015 8B804660
0016 0 5555 DC /5555 5555 0016 8B804670
0017 0 5555 DC /5555 AAAA 0017 8B804680
0018 0 AAAA DC /AAAA 5554 0018 8B804690
0019 0 0001 DC /0001 5555 0019 8B804700
001A 0 AAAA DC /AAAA FFFF 001A 8B804710
001B 0 AAAA DC /AAAA AAA9 001B 8B804720
001C 0 5557 DC /5557 FFFF 001C 8B804730
001D 0 5555 DC /5555 5554 001D 8B804740
001E 0 AAAB DC /AAAB FFFF 001E 8B804750
001F 0 1000 DC /1000 OFFF 001F 8B804760
0020 0 F100 DC /F100 O0FF 0020 8B804770

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BASIC DIAGNOSTIC LOADER

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0021 0 FF10 DC /FF10 000F 0021 8B804780
0022 0 FFF1 DC /FFF1 0000 0022 8B804790
0023 0 3210 DC /3210 3210 0023 8B804800
* /0024 3234 0024 8B804810
* /0100 3334 0025 8B804820
* /CCCC 0000 0026 8B804830
* 8B804840
* 8B804850
* CARD 13 -----
0024 ORG 0 8B804860
0000 0 0C00 000C X10 L B13 READ TWO CARDS 8B804870
0002 0 0C00 0038 X10 L K0800 RESET DSW 8B804880
0004 0 0C00 0036 A13 X10 L K0003 SENSE DSW FOR 1442 8B804890
0006 0 F400 0036 EOR L K0003 8B804900
0008 0 4820 BSC Z SKIP BITS 14 & 15 ONLY 8B804910
0009 0 6013 LDX E13 CONTINUE DSW ANALYSIS 8B804920
000A 0 6004 LDX A13 CARD IS BEING READ 8B804930
000B 0 008A CON2 DC CD15 8B804940
000C 0 0069 B13 DC MOVE READ CONTROL PROG. MODIFYS. 8B804950
000D 0 1601 DC /1601 1442 8/8 FORMAT 8B804960
000E 0 6028 C13 LDX /0028 8B804970
000F 0 F400 0038 D13 EOR L K0800 RETURN DSW WORD TO ACC. 8B804980
0011 0 3013 WAIT /13 **ERR. SEE ACC. DSW NOT RIGHT 8B804990
0012 0 6000 LDX /0 TRY AGAIN 8B805000
0013 0 0C00 0038 E13 X10 L K0800 SENSE AND RESET DSW 8B805010
0015 0 F400 0038 EOR L K0800 CHECK FOR BIT 4 ONLY 8B805020
0017 0 4820 BSC Z SKIP OPERATION COMPLETE 8B805030
0018 0 600F LDX D13 DSW ERROR CONDITION 8B805040
0019 0 C400 000E MOD1 LD L C13 SET THIS PROG. TO READ 2ND 8B805050
001B 0 D400 0019 STO L MOD1 CARD & BR. TO READ IN 8B805060
001D 0 C400 000B LD L CON2 FIRST PROG. CARD 8B805070
001F 0 D400 000C STO L B13 8B805080
0021 0 6000 LDX /0 8B805090
* 8B805100
* CARD 14 -----
0022 ORG MOVE 8B805110
0069 0 C400 0025 MOVE LD L /0025 GET ADDRESS FOR FIRST WORD 8B805130
006B 0 4820 BSC Z SKIP ADDRESS EQU. 0000 8B805140
006C 0 6070 LDX STORE 8B805150
006D 0 6000 LDX /0000 8B805160
006E 0 6400 0092 HOP LDX L CKMOV 8B805170
0070 0 D400 0079 STORE STO L PUT&1 SET FIRST WORD ADDRESS 8B805180
0072 0 C400 004D LD L K0000 8B805190
0074 0 D400 0077 STO L GET&1 SET TO GET FIRST WORD AT 0 8B805200
0076 0 C400 FFFF GET LD L /FFFF GET PROG. WORD 8B805210
0078 0 D400 FFFF PUT STO L /FFFF PUT PROG. WORD 8B805220
007A 0 C400 0079 LD L PUT&1 MODIFY PUT 8B805230
007C 0 8400 004E A L K0001 8B805240
007E 0 D400 0079 STO L PUT&1 8B805250
0080 0 C400 0077 LD L GET&1 MODIFY GET 8B805260
0082 0 8400 004E A L K0001 8B805270
0084 0 D400 0077 STO L GET&1 8B805280
0086 0 F400 0024 EOR L /0024 CHECK FOR ALL WORDS MOVED 8B805290
0088 0 4820 BSC Z SKIP ALL WORDS MOVED 8B805300
0089 0 6076 LDX GET 8B805310
008A 0 3014 CD15 WAIT /14 **ERR. CARD 15 SHOULD READ 8B805320
OVER THIS WAIT. 8B805330
* 8B805340
* 8B805350
* CARD 15 -----
008B ORG CD15 8B805360
008A 0 C400 0025 SUM2 LD L /0025 GET ADDRESS OF FIRST WORD 8B805370
008C 0 D400 0093 STO L CKMOV&1 PUT IT INTO ROUTINE 8B805380
008E 0 C400 004D LD L K0000 GET ADDRESS OF FIRST WORD 8B805390
0090 0 D400 0095 STO L COMP&1 IN IMAGE & STORE IT. 8B805400
0092 0 C400 FFFF CKMOV LD L /FFFF GET WORD MOVED 8B805410
0094 0 F400 FFFF COMP EOR L /FFFF COMPARE WITH CARD IMAGE 8B805420
0096 0 4820 BSC Z SKIP WORD STORED OK 8B805430
0097 0 3015 WAIT /15 **ERR. WORD NOT STORED OK. 8B805440
0098 0 C400 0093 LD L CKMOV&1 MODIFY FOR NEXT WORD 8B805450

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BASIC DIAGNOSTIC LOADER

009A 0 8400 004E	A	L	K0001	88805460
009C 0 D400 0093	STD	L	CKMOV&1	88805470
009E 0 C400 0095	LD	L	COMP&1	88805480
00A0 0 8400 004E	A	L	K0001	88805490
00A2 0 D400 0095	STD	L	COMP&1	88805500
00A4 0 F400 0024	EDR	L	/0024	88805510
00A6 0 4820	BSC	Z	CHECK IF ALL DONE	88805520
00A7 0 606E	LDX	HOP	SKIP ALL WORDS CHECKED	88805530
00A8 0 6028	LDX	/0028	GO TO CKMOV	88805540
			GET NEXT CARD	88805550

*
* CARD 16 -----
* THIS CARD IS USED TO CHECK THAT THE MOVE
* PORTION OF THE LOADER WORKS. THE CARD SHOULD
* BE PLACED IN LOCS. 0100 THRU 0123 .
* EACH LOCATION SHOULD CONTAIN ITS OWN ADDRESS.
* THE LISTING SHOWS THE SUM DURING CHECK SUM ADD.
*
* -SUM OF LOCS.-

00A9	ORG	/0100		88805560
0100 0 0100	DC	/0100	0100	88805570
0101 0 0101	DC	/0101	0201	88805580
0102 0 0102	DC	/0102	0303	88805590
0103 0 0103	DC	/0103	0406	88805600
0104 0 0104	DC	/0104	050A	88805610
0105 0 0105	DC	/0105	060F	88805620
0106 0 0106	DC	/0106	0715	88805630
0107 0 0107	DC	/0107	081C	88805640
0108 0 0108	DC	/0108	0824	88805650
0109 0 0109	DC	/0109	092D	88805660
010A 0 010A	DC	/010A	0937	88805670
010B 0 010B	DC	/010B	0942	88805680
010C 0 010C	DC	/010C	0A4E	88805690
010D 0 010D	DC	/010D	0B5B	88805700
010E 0 010E	DC	/010E	0C69	88805710
010F 0 010F	DC	/010F	0D78	88805720
0110 0 0110	DC	/0110	0E88	88805730
0111 0 0111	DC	/0111	0F99	88805740
0112 0 0112	DC	/0112	10AB	88805750
0113 0 0113	DC	/0113	11BE	88805760
0114 0 0114	DC	/0114	12D2	88805770
0115 0 0115	DC	/0115	13E7	88805780
0116 0 0116	DC	/0116	14FD	88805790
0117 0 0117	DC	/0117	1614	88805800
0118 0 0118	DC	/0118	1726	88805810
0119 0 0119	DC	/0119	183F	88805820
011A 0 011A	DC	/011A	1959	88805830
011B 0 011B	DC	/011B	1A76	88805840
011C 0 011C	DC	/011C	1B92	88805850
011D 0 011D	DC	/011D	1CAF	88805860
011E 0 011E	DC	/011E	1DCD	88805870
011F 0 011F	DC	/011F	1EEC	88805880
0120 0 0120	DC	/0120	200C	88805890
0121 0 0121	DC	/0121	212D	88805900
0122 0 0122	DC	/0122	224F	88805910
0123 0 0123	DC	/0123	2472	88805920
0124 0000	END	0		88805930
				88805940
				88805950
				88805960
				88805970
				88805980
				88805990
				88806000

NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY

BASIC DIAGNOSTIC LOADER

CROSS REFERENCE

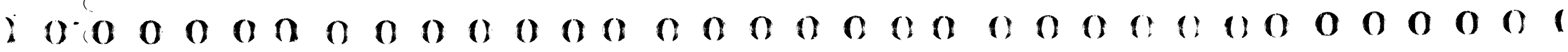
NAME	VALUE	REFERENCES
ADD	0006	0019
A1	0005	000B
A10	0004	000A
A13	0004	000A
A2	002C	0032
A3	0005	
A3A	000F	0015
BIT00	0004	
BIT01	000B	0007
BIT02	0011	000E
BIT03	0017	0014
BIT04	001D	001A
BIT05	0000	
BIT06	0006	0003
BIT07	000C	0009
BIT08	0012	000F
BIT09	0018	0015
BIT10	0000	
BIT11	0006	0003
BIT12	000C	0009
BIT13	0012	000F
BIT14	0018	0015
BIT15	0000	
BLD	0047	000C
B1	000C	0001
B10	000C	0000
B13	000C	0000,001F
B2	0034	002A
B3	0016	0001,000B
CD15	008A	000B
CKLDD	0057	0051,005B,005F,0061,0064
CKMOV	0092	006E,008C,0098,009C
COMP	0094	0090,009E,00A2
CONTA	0012	000F
CONTB	001C	0015
CON2	000B	001D
C10	000B	
C13	000E	0019
D1	0012	001B
D10	000E	0017
D13	000F	0018
D2	003A	0044
E1	0016	000A
E10	0012	0009
E13	0013	0009
E2	003E	0031
E3	001C	0014
GET	0076	0074,0080,0084,0089
HFFFF	001E	000B
HOP	006E	00A7
H0000	001C	0000
H0001	001D	000E
KFFFF	0012	0006,0008
K0N1	000C	0000,0006
K0N2	0002	000A,0010
K0000	0009	0000
K0001	0005	0000
K0003	000E	0005,0007
K0800	0010	0003,0012,0016,0018
K0000	0011	000C
K0000	0019	000A,0012
K0001	001A	001C
K0003	0018	0005,000F,0011
K0000	004D	004F,0072,008E
K0001	004E	005D,007C,0082,009A,00A0
K0002	001D	0018

F
L

K0003 0036 0004,0004,0006,0006,002C,002E,003E
K0004 0017 0012
K0008 0011 000C
K0010 000B 0006
K0020 0005 0000
K0040 001D 0018
K0080 0017 0012
K0100 0011 000C
K0200 000B 0006
K0400 0005 0000
K0800 001A 0003,0008,000D,001C,001E
K0800 0022 001D
K0800 0038 0002,0002,000E,000F,0012,0013,0014,0015,0028,003A,0040,0045
K1000 001C 0017
K2000 0016 0011
K4000 0010 000B
K8000 001B 0000
K8000 000A 0004
LP 000B 0022
MOD1 0019 001B
MOVE 0069 000C
MOVE 0069
PUT 0078 0070,007A,007E
RETRY 000A 0002,0005,0007,0011,0017
R2 003D 0047
SHIFT 0003 001F
STORE 0070 006C
SUMMI 001B 0002,0006,000A,0015
SUMPL 001A 0004,000C,0010
SUM1 004F 004A
SUM2 008A
TEOR 0006 0003
TEST 0018 0008,000C
TOTAL 0015 0013
TST1 0000
TST2 000A

END OF ASSEMBLY

----- LAST PAGE -----



BASIC DIAGNOSTIC LOADER (CARD)

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1. PURPOSE

THE 1800 BASIC DIAGNOSTIC LOADER IS A SELF-CHECKING PROGRAM USED TO LOAD THE PROCESSOR DIAGNOSTIC PROGRAMS AND TO VERIFY THEIR CORRECT LOADING. THE LOADER CONTAINS ONE-CARD PROGRAMS USED AS AIDS IN DIAGNOSIS OF BASIC FAILURES IN THE PROCESSOR.

2. PREREQUISITES

2.1 PROGRAM PREREQUISITES

AN 1800 PROCESSOR DIAGNOSTIC PROGRAM PUNCHED IN 8-8 FORMAT IS REQUIRED.

2.2 EQUIPMENT PREREQUISITES

- A. 1800 DATA ACQUISITION AND CONTROL SYSTEM PROCESSOR.
- B. 1442 SERIAL CARD READ/PUNCH.

3. OPERATING PROCEDURE

3.1 PROGRAM LOADING

A. AT 1442 SERIAL CARD READ/PUNCH,

- 1. DEPRESS NPRO PUSHBUTTON TO RUN OUT ANY CARDS REMAINING IN FEED.
- 2. PLACE BASIC LOADER DECK FOLLOWED BY MAIN PROGRAM AND ONE BLANK IN READER HOPPER.
- 3. DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.

BASIC DIAGNOSTIC LOADER (CARD)

B. USING CONTROLS OF 1800 PROCESSOR CLEAR STORAGE TO 7OFF AS FOLLOWS,

- 1. SET MODE SWITCH TO RUN.
- 2. SET CHECK STOP SWITCH TO OFF.
- 3. SET WRITE STOR PROT BITS SWITCH TO YES.
- 4. SET DATA ENTRY SWITCHES TO 7OFF.
- 5. HOLD DOWN THE CLEAR STOR PUSHBUTTON AND DEPRESS START PUSHBUTTON TO CLEAR STORAGE.
- 6. DEPRESS STOP BUTTON TO TERMINATE CLEAR OPERATION.

C. AT 1800 PROCESSOR SET SWITCHES AS FOLLOWS,

- 1. SET CHECK STOP SWITCH TO ON.
- 2. SET WRITE STOR PROT BITS TO NO.

D. DEPRESS RESET PUSHBUTTON.

E. DEPRESS PROG LOAD PUSHBUTTON. CHECK THAT ONLY ONE CARD FEEDS. POSSIBLE FAILURES FOLLOW.

```

*****
*                                     *
*          FAILURE                     *          FAILURE / ACTION          *
*                                     *                                     *
*****
* AFTER DEPRESSION OF                 * REPEAT LOAD PROCEDURE WITH MODE SWITCH IN SI *
* PROG LOAD BUTTON NO                 * POSITION. IF FAILURE REOCCURS A PROG LOAD FAILURE *
* CARD FEEDS, OR MORE                 * IS POSSIBLE. FEEDING MORE THAN ONE CARD COULD ALSO *
* THAN ONE CARD FEEDS.                * BE CAUSED BY FAILURE TO PERFORM WAIT INSTRUCTION *
*                                     * (B REG=30FF) THAT SHOULD HAVE READ INTO LOCATION *
*                                     * 0000. *
*                                     *
*****

```

F. CHECK THAT PROGRAM HAS STOPPED WITH I REG=0001, AND B REG=30FF. POSSIBLE FAILURES FOLLOW.

```

*****
*                                     *
*          FAILURE                     *          FAILURE / ACTION          *
*                                     *                                     *
*****
* PROGRAM DOES NOT STOP                * IF LOCATION 0000 CONTAINS A WAIT INSTRUCTION (30FF), *
* WITH I REG=0001 AND                 * EITHER THE WAIT OPERATION IS FAILING, OR PROG LOAD *
* B REG=30FF.                         * IS NOT SETTING THE I REGISTER TO 0000. *
*                                     *
*****
* PROGRAM STOPS WITH                   * THE FIRST WORD OF CARD 01 OF LOADER IS 30FF. FIND *
* I REG=0001, BUT B REG               * OUT WHY THE WORD WAS NOT READ INTO LOCATION 0000. *
* READING IS NOT 30FF.                *
*                                     *
*****

```


BASIC DIAGNOSTIC LOADER (CARD)

G. DEPRESS START PUSHBUTTON. CHECK THAT ALL LOADER AND PROGRAM CARDS FEED, AND THAT PROGRAM BEGINS EXECUTION. POSSIBLE FAILURES FOLLOW

FAILURE	FAILURE / ACTION
PROGRAM STOPS AT WAIT WITH I REG=0001, AND B REG=30FF.	CARD 02 PROGRAM READS A CARD WHICH REMOVES WAIT FROM LOCATION 0000. AN XIO FAILURE IS INDICATED. RUN CARD 03 TO HELP ISOLATE FAILURE. (PAR. 3.2.1).
PROGRAM STOPS AT ERROR WAIT. (B REG READING BETWEEN 3001 AND 3015).	REFER TO TABLE 1- ERROR WAIT DIAGNOSTIC GUIDE.
PROGRAM STOPS AT OTHER THAN A WAIT INSTRUCTION	DETERMINE WHICH PROGRAM CAUSED FAILURE. IF MORE THAN TWO CARDS HAVE FED, FAILURE IS MOST LIKELY DUE TO LAST CARD READ.
PROGRAM RUNNING BUT CARDS DO NOT FEED.	STOP PROGRAM. DETERMINE WHICH PROGRAM IS ACTIVE. THE ACTIVE PROGRAM SHOULD BE STORED BETWEEN LOCATIONS 0000 AND 0025, OR BETWEEN LOCATIONS 0028 AND 004F. DISPLAY ACTIVE PROGRAM UNTIL A WAIT IS FOUND. DETERMINE THE PROGRAM NUMBER BY REFERENCING THE LAST 10 BITS OF THE WAIT INSTRUCTION. REFER TO LISTING FOR THE PROGRAM AND RUN IN SI MODE TO DETERMINE FAILURE. EXECUTION OF ONE-CARD PROGRAMS MAY ALSO BE HELPFUL. (PAR. 3.2.1 AND 3.2.2).

TABLE 1
ERROR WAIT DIAGNOSTIC GUIDE

WAIT B REGISTER I REGISTER	FAILURE / RECOMMENDED ACTION
3001	CHECK A REGISTER. IT CONTAINS THE 1442 DSW. IF THE DSW IS OTHER THAN 0003, OR 0000, THE DSW IS IN ERROR. DEPRESS PROG LOAD BUTTON TO LOAD CARD 03 ONE-CARD PROGRAM. (CARD 03 IS AN XIO TEST PROGRAM.) SEE CARD 03 TEST PROCEDURE (PARAGRAPH 3.2.1).
3002	SAME FAILURE AS 3001. PRESS PROG LOAD BUTTON TO LOAD CARD 03 TO HELP ISOLATE FAILURE. (REFER PARAGRAPH 3.2.1).

BASIC DIAGNOSTIC LOADER (CARD)

WAIT B REGISTER I REGISTER	FAILURE / RECOMMENDED ACTION
3003	THIS WAIT WILL NOT NORMALLY OCCUR WHILE LOADING A MAIN PROGRAM AS CARD 03 IS BYPASSED. REFER TO CARD 03 PROGRAM TEST PROCEDURE (PARAGRAPH 3.2.1).
3004	FAILURE OF LDX INSTRUCTION. REFER TO PROGRAM LISTING. RUN CARD 04 SEPARATELY. IF WAIT REOCCURS A SCOPING LOOP MAY BE SET UP BY REPLACING ERROR WAIT BY AN LDX /0000 INSTRUCTION (6000).
3005 3006 3007 3008	0005 READ IN FAILURE FROM CARD READER, OR BIT TRANSFER INTO A REG FAILURE, OR BSC Z INSTRUCTION FAILURE. REFER TO LISTING. SET I REG TO ADDRESS OF LD INSTRUCTION JUST BEFORE WAIT INSTRUCTION AND STEP THROUGH PROGRAM IN SI MODE TO LOCATE FAILING INSTRUCTION.
3008	000C EOR OF ALL ONES AGAINST ALL ONES DID NOT RESULT IN A REG EQUAL 0000. REDEVELOP ERROR BY STARTING PROGRAM AT LOCATION 0000. STEP THROUGH IN SI MODE.
3008	0010 EOR OF ALL ZEROES AGAINST ALL ZEROES DID NOT RESULT IN A REG EQUAL 0000. REDEVELOP ERROR BY STARTING AT LOCATION 0000 IN SI MODE.
3009	LOAD LONG FAILURE, STORE LONG FAILURE, OR POSSIBLE EOR FAILURE. REFER TO LISTING. RUN IN SI MODE CHECKING THAT AFTER A LOAD INSTRUCTION A REG IS CORRECT, AND THAT AFTER A STORE INSTRUCTION THE A, B, AND M REGISTERS ARE CORRECT. DATA PATH TEST MAY ALSO HELP (SEE PARAGRAPH 3.5).
300A	0007 SRA 1 DROPPED THE 1 DURING THE SHIFT. FOLLOWING THIS WAIT, PROGRAM RUNS AGAIN THROUGH SAME CONDITIONS THAT CAUSED THE ERROR. STEP THROUGH IN SI MODE TO LOCATE FAILURE.
300A	0011 EOR OF A 1 IN STORAGE AGAINST A 0 IN A REG RESULTED IN A 0 IN A REG. RUN IN SI MODE TO LOCATE ERROR.



BASIC DIAGNOSTIC LOADER (CARD)

WAIT	B REGISTER	I REGISTER	FAILURE / RECOMMENDED ACTION
	300A	0017	* EOR OF A 0 IN STORAGE AGAINST A 1 IN A REG RESULTED IN A 0 IN A REG. RUN IN SI MODE TO LOCATE ERROR.
	300B		* ADD FAILURE. SUM OF SUMPL AND SUMMI NOT EQUAL 0000. ERROR SUM IS IN A REG. DISPLAY SUMPL AND SUMMI, AND DETERMINE IF THEIR SUM SHOULD BE 0000. IF THEIR SUM SHOULD BE 0000, DIAGNOSE THE PROBLEM. IF THEIR SUM SHOULD NOT BE 0000, EITHER SUMPL OR SUMMI IS IN ERROR. RUN MANUAL ENTRY ADD TEST (PARAGRAPH 3.4).
	3010		* ERROR DSW DETECTED. CHECK A REG. FAILURE COULD BE IN THE 1442 READER, OR IN ITS ATTACHMENT CIRCUITRY, OR COULD BE CAUSED BY INTERMITTENT PROCESSOR FAILURES. RUN CARD 03 ONE-CARD PROGRAM.
	3011	004B	* WORD COUNT OF CARD JUST READ IN WAS FOUND TO BE 0000. WORD COUNT IS READ INTO LOCATION 0024. CHECK CARD JUST READ. IT SHOULD HAVE INFORMATION PUNCHED IN COLUMN 73. IF CORRECTLY PUNCHED, THE CARD CAN BE RE-LOADED BY DEPRESSING THE START BUTTON ON THE PROCESSOR CONSOLE. A READ-IN FAILURE IS POSSIBLE.
	3011	006B	* SUM OF LOCATION 0000 THROUGH 0026 IS NOT 0000. A REG CONTAINS THE DEVELOPED SUM. COMPARE THE CARD READ WITH ITS IMAGE IN LOCATION 0000 THROUGH 0027. IT MAY HAVE READ IN INCORRECTLY. THE SUM ROUTINE MAY BE RUN BY STARTING AT LOCATION SUM1 OF CARD 11. REFER TO LISTING. IF ERROR OCCURED ON CARD 12, REFER TO ITS LISTING. CARD 12 IS USED TO CHECK THE SUM ROUTINE.
	3013		* CARD 13 PROGRAM HAS DETECTED AN ERROR DSW. THE ERROR DSW IS IN THE A REG. ERROR COULD BE IN THE 1442 READER, OR IN THE DSW CIRCUITRY, OR COULD BE CAUSED BY AN INTERMITTENT FAILURE IN THE PROCESSOR. IF THE SOURCE OF ERROR IS NOT EVIDENT, RUN ONE-CARD PROGRAMS (CARDS 04 THROUGH 08).
	3014		* THIS WAIT IS IN A LOCATION THAT SHOULD NOT BE EXECUTED UNTIL CARD 15 IS READ. CARD 15 HAS ITS FIRST WORD STORED IN THAT LOCATION BY CARD 13. THE MOST LIKELY CAUSE OF THIS ERROR IS CARDS OUT OF SEQUENCE. THE SEQUENCE NUMBER IS PUNCHED IN COLUMNS 79 AND 80 IN HOLLERITH CODED HEXADECIMAL.

BASIC DIAGNOSTIC LOADER (CARD)

WAIT	B REGISTER	I REGISTER	FAILURE / RECOMMENDED ACTION
	3015		* ERROR IN STORING THE PROGRAM FROM THE CARD IMAGE AREA INTO ITS PROPER PLACE IN STORAGE. AFTER THE NUMBER OF WORDS SPECIFIED BY THE WORD COUNT HAS BEEN STORED, EACH STORED WORD IS COMPARED WITH ITS CORRESPONDING IMAGE WORD TO CHECK FOR CORRECT TRANSFER. THE FAILING ADDRESS CAN BE FOUND BY REFERRING TO LISTING FOR CARD 15. (SWITCH TO DISPLAY MODE AND LOOK AT THE ADDRESS POSITION OF THE NEXT INSTRUCTION). * IF THE ERROR OCCURRED WHILE LOADING CARD 16, REFER TO ITS LISTING. CARD 16 IS LOADED IN LOCATION 0100 AND ABOVE, AND IS DESIGNED TO AID IN DIAGNOSING FAILURES IN THE ' MOVE ' SECTION OF THE LOADER.

3.2 ONE-CARD PROGRAMS TEST PROCEDURE

1800 BASIC DIAGNOSTIC LOADER CARDS 03 THROUGH 08 ARE THE ONE-CARD PROGRAMS. EXCEPT FOR CARD 03 WHICH IS BYPASSED, ONE-CARD PROGRAMS ARE EXECUTED BY THE LOADER IN THE NORMAL PROCESS OF BUILDING UP THE LOADER. EACH ONE-CARD PROGRAM CAN BE RUN INDIVIDUALLY BY LOADING INTO CORE STORAGE UNDER PROGRAM LOAD MODE.

PROVIDED NO ERRORS OCCUR, EACH ONE-CARD PROGRAM RUNS CONTINUOUSLY UNTIL STOPPED BY DEPRESSION OF STOP PUSH-BUTTON ON OPERATORS CONSOLE.

ERRORS ENCOUNTERED DURING EXECUTION ARE SIGNALLED BY PROGRAM STOPPING AT A UNIQUE ERROR WAIT WITH THE LAST 10 BITS OF B REGISTER CONTAINING THE PROGRAM NUMBER. FOR EXAMPLE, THE B REGISTER WILL HAVE A READING OF 3008 IF CARD 08 PROGRAM STOPS AT AN ERROR WAIT. THE I REGISTER READING IS USED TO REFERENCE AN ERROR WAIT WHEN THERE IS MORE THAN ONE ERROR WAIT IN A PROGRAM.

3.2.1 CARD 03 PROGRAM TEST PROCEDURE

- A. EXECUTE ONE-CARD PROGRAMS 04 THROUGH 08 TO BECOME REASONABLY CERTAIN PROCESSOR IS OPERATING CORRECTLY. (PAR. 3.2.2).
- B. ON 1442 SERIAL CARD READ/PUNCH,
 1. DEPRESS NPRO PUSHBUTTON TO CLEAR FEED.
 2. PLACE CARD 03 FOLLOWED BY SUBSTANTIAL DECK OF BLANK CARDS IN HOPPER.
 3. DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.

- C. ON 1800 PROCESSOR OPERATOR'S CONSOLE.
 - 1. SET CHECK STOP SWITCH TO ON.
 - 2. DEPRESS RESET PUSHBUTTON.
 - 3. DEPRESS PROG LOAD PUSHBUTTON. CARD 04 SHOULD FEED, LOAD, AND BEGIN EXECUTION.
- D. PROGRAM SHOULD CAUSE ONE CARD TO FEED. CHECK THAT CARD HAS FED.
- E. CHECK THAT PROGRAM HAS STOPPED AT WAIT WITH I REGISTER READING OF 0008, B REGISTER READING OF 3003, AND A REGISTER READING OF 0003.

IF PROGRAM DOES NOT STOP AT ABOVE MENTIONED WAIT, CARD 03 MAY NOT HAVE LOADED CORRECTLY. REFER TO PROGRAM LISTING AND DISPLAY EACH LOCATION.

IF A REGISTER IS NOT 0003 (CARD READER BUSY AND NOT READY) A DSW FAILURE IS INDICATED. REFER TO LISTING. SET UP APPLICABLE SCOPE LOOP TO AID IN DIAGNOSIS.

IF ALL REGISTERS MENTIONED ARE CORRECT DEPRESS START PUSHBUTTON.
- F. CHECK THAT PROGRAM HAS STOPPED AT WAIT WITH I REGISTER READING OF 0008, B REGISTER READING OF 3003, AND A REGISTER READING OF 0800.

IF A REGISTER READING IS NOT 0800 (OP COMPLETE) AN ERROR DSW IS INDICATED. SET UP APPLICABLE SCOPE LOOP. UTILIZE AVAILABLE DIAGNOSTIC AIDS TO LOCATE THE PROBLEM. IF REGISTERS READ CORRECTLY AND IT IS DESIRED TO REPEAT STEPS D THROUGH F, DEPRESS RESET AND START PUSHBUTTONS IN SUCCESSION. IF NOT, PROCEED TO STEP G.
- G. DEPRESS START PUSHBUTTON.
- H. PROGRAM SHOULD FEED CARDS CONTINUOUSLY AND SHOULD NOT STOP UNLESS AN ERROR WAIT OCCURS WITH I REGISTER READING OF 0022, B REGISTER READING OF 3003. THIS WAIT STOP INDICATES THAT AN ERROR DSW HAS BEEN DETECTED. THE DSW IS DISPLAYED BY THE A REGISTER. REFER TO LISTING. DETERMINE THE DSW BITS THAT ARE IN ERROR. THERE ARE ONLY TWO LEGAL DSW READINGS, 0003 AND 0800. ANY OTHER DSW READINGS ARE CONSIDERED TO BE IN ERROR BY THE PROGRAM. SET UP APPLICABLE SCOPE LOOP.

SCOPING LOOPS MAY BE SET UP IN CARD 03 PROGRAM TO FACILITATE SCOPING OF XIO FUNCTIONS.

SCOPE LOOP SETUP

```
*****  
* TO READ CARDS CONTINUOUSLY, INSERT LDX /0001 6001 , *  
* AT LOCATION 0003. *  
* *  
* TO READ CARD, SENSE AND RESET DSW, INSERT LDX /0001 *  
* 6001 , AT LOCATION 0005. *  
* *  
* TO SENSE DSW CONTINUOUSLY WITHOUT CARD READING, *  
* INSERT LDX /0003 6003 , AT LOCATION 0005. *  
*****
```

3.2.2 CARD 04 - 08 PROGRAMS TEST PROCEDURE

THE FOLLOWING TEST PROCEDURE DESCRIPTION APPLIES TO ANY ONE-CARD PROGRAM FROM 04 TO 08.

- A. ON 1442 SERIAL CARD READ/PUNCH.
 - 1. DEPRESS NPRO PUSHBUTTON TO CLEAR FEED.
 - 2. PLACE ONE-CARD PROGRAM FOLLOWED BY TWO BLANK CARDS IN HOPPER.
 - 3. DEPRESS START PUSHBUTTON. READY INDICATOR SHOULD LIGHT.
- B. ON 1800 PROCESSOR OPERATOR'S CONSOLE.
 - 1. DEPRESS RESET PUSHBUTTON.
 - 2. DEPRESS PROG LOAD PUSHBUTTON. CARD SHOULD FEED, LOAD, AND BEGIN EXECUTION.

THE PROGRAM WILL RUN CONTINUOUSLY UNLESS AN ERROR OCCURS, IN WHICH CASE PROGRAM STOPS AT ERROR WAIT. REFER TO PROGRAM LISTING AND TO TABLE 1 - ERROR WAIT DIAGNOSTIC GUIDE.

3.3 MANUAL ENTRY ADD TEST

THIS TEST HELPS LOCATE AN ADD FAILURE THAT CANNOT BE LOCATED WHEN RUNNING CARD 08 OF ONE-CARD PROGRAMS IN SI MODE BECAUSE OF THE DYNAMIC NATURE OF THE PROBLEM. IF THE CONTENTS OF SUMPL AND SUMMI DO NOT ADD TO 0000, THERE HAS BEEN A FAILURE IN ADDING 0001 TO SUMPL OR A FAILURE IN ADDING FFFF TO SUMMI. TO DETERMINE WHICH OF THE TWO SUMS IS IN ERROR, IT MUST BE ASSUMED THAT ONE OF THEM IS CORRECT IN ORDER TO ARRIVE AT THE VALUE OF THE OTHER PRIOR TO THE FAILURE. IN OTHER WORDS, TO DETERMINE VALUE OF SUMPL PRIOR TO FAILURE, IT MUST BE ASSUMED THAT PRESENT VALUE OF SUMMI IS CORRECT AND VICE VERSA.

EXECUTE ADD TEST PROGRAM AS FOLLOWS.

- A. OBTAIN VALUE OF SUMPL PRIOR TO FAILURE BY DETERMINING TWO'S COMPLEMENT OF (SUMMI - FFFF).
- B. OBTAIN VALUE OF SUMMI PRIOR TO FAILURE BY DETERMINING TWO'S COMPLEMENT OF (SUMPL - 0001).
- C. LOAD FOLLOWING PROGRAM BY MEANS OF CONSOLE ENTRY SWITCHES.



BASIC DIAGNOSTIC LOADER (CARD)

NOTE

ALL NUMBERS SHOWN BELOW ARE IN HEXADECIMAL NOTATION.

LOCATION	CONTENTS	MNEMONIC	COMMENTS
0000	VALUE OF SUMPL PRIOR TO ERROR		WILL BE IN ACCUMULATOR WHEN ADD OCCURS.
0001	0001		WILL BE ADDED TO ACCUMULATOR DURING ADD.
0002	CORRECT SUM OF ADDITION		USED TO CHECK ADD OPERATION.
0003	COFC	LD	LOAD ACCUMULATOR FROM LOCATION 0000.
0004	80FC	A	ADD CONTENTS OF LOCATION 0001 TO ACCUMULATOR.
0005	F0FC	EOR	EOR ACCUMULATOR WITH CORRECT ANSWER.
0006	4820	BSC Z	SKIP ON ZERO TO LOCATION 0008.
0007	3000	WAIT	WAIT. AN ERROR HAS OCCURED.
0008	6003	LDX	BRANCH TO LOCATION 0003.

D. LOAD I REGISTER WITH 0003.

E. RUN PROGRAM IN RUN MODE. AN ADD FAILURE WILL CAUSE PROGRAM TO STOP AT WAIT INSTRUCTION WITH I REGISTER INDICATOR INDICATING 0008.

F. IF PROGRAM RUNS CONTINUOUSLY WITHOUT ERRORS,

1. DEPRESS STOP PUSHBUTTON.
2. LOAD LOCATION 0000 WITH VALUE OF SUMMI PRIOR TO ERROR.
3. LOAD LOCATION 0001 WITH FFFF.
4. LOAD LOCATION 0002 WITH CORRECT SUM OF SUMMI PLUS FFFF.
5. RUN AGAIN IN RUN MODE.

3.4 MANUAL ENTRY DATA-PATH TEST

THIS TEST IS LOADED USING THE DATA ENTRY SWITCHES AND TESTS THE ABILITY OF THE 1800 PROCESSOR TO TRANSFER ONES AND ZEROS BETWEEN THE FOLLOWING REGISTERS.

- A. FROM B TO D TO A TO M TO I REGISTER.
- B. FROM A TO U TO A REGISTER.
- C. FROM A REGISTER TO B REGISTER.
- D. FROM I REGISTER TO B REGISTER.
- E. FROM I REGISTER TO A REGISTER.

BASIC DIAGNOSTIC LOADER (CARD)

TEST PROCEDURE

- A. USING CONTROLS OF 1800 PROCESSOR, CLEAR STORAGE TO WAIT INSTRUCTION (33FF). SEE PARAGRAPH 3.1.
- B. ENTER THE FOLLOWING PROGRAM USING DATA ENTRY SWITCHES.

LOCATION	CONTENTS	MNEMONIC	COMMENTS
FFFA	C006	LD	LOAD ACCUMULATOR WITH CONTENTS OF LOCATION 0001.
FFFB	4480	BSI I	STORE CONTENTS OF I COUNTER (FFFF) AT ADDRESS STORED IN LOCATION FFFD. SET I COUNTER TO THAT ADDRESS AND ADD ONE TO I COUNTER.
FFFC	FFFF		ADDRESS POSITION OF BSI I INSTRUCTION
FFFD	FFFF		THIS IS THE ACTUAL BRANCH ADDRESS FOR THE BSI I INSTRUCTION AND IS REPLACED BY THE BSI I.
FFFE	D002	STO	STORE CONTENTS OF ACCUMULATOR AT LOCATION 0001 (SHOULD NOT CHANGE).
FFFF	COFC	LD	LOAD ACCUMULATOR WITH CONTENTS OF LOCATION FFFC.
0000	4480	BSI I	STORE CONTENTS OF I COUNTER (0002) AT ADDRESS STORED IN LOCATION 0002. SET I COUNTER TO THAT ADDRESS AND ADD ONE TO I COUNTER.
0001	0002		THIS IS ADDRESS POSITION OF BSI I INSTRUCTION.
0002	0002		THIS IS THE ACTUAL BRANCH ADDRESS FOR THE BSI I INSTRUCTION AND IS REPLACED BY THE BSI I INSTRUCTION.
0003	D0F8	STO	STORE CONTENTS OF ACCUMULATOR AT LOCATION FFFC (SHOULD NOT CHANGE).
0004	70F5	MDX	BRANCH TO LOCATION FFFA.

C. LOAD I REGISTER WITH FFFA.

D. STEP THROUGH PROGRAM IN SI MODE, CHECKING THAT PROGRAM LOOPS PROPEPLY. ANY DATA-PATH ERROR SHOULD RESULT IN THE IMPROPER BRANCHING OF A BSI I INSTRUCTION AND STOPPING AT A WAIT INSTRUCTION. THE LOCATION BEFORE THE WAIT INSTRUCTION SHOULD CONTAIN THE CONTENTS OF I REGISTER WHEN THE BRANCH OCCURRED. LOGICAL RECONSTRUCTION OF THE ERROR SHOULD ISOLATE A DATA-TRANSFER ERROR AND SUGGEST THE CIRCUIT CARD CAUSING THE ERROR.

NOTE

A BRANCH OUTSIDE OF THE PROGRAM INTO A CORE LOCATION LOADED WITH 33FF INDICATES AN ERROR HAS OCCURRED. SUBTRACT TWO FROM I REGISTER INDICATOR READING AND DISPLAY THAT LOCATION. THE CONTENT OF LOCATION DISPLAYED IS THE I REGISTER SETTING WHEN THE ERRONEOUS BRANCH OCCURED. IF THE BRANCH WAS CAUSED BY A BSI I INSTRUCTION FAILURE, THE LOCATION JUST CHECKED WILL HAVE A VALUE, BY ONE, THAN THE ADDRESS OF THE SECOND WORD OF THE BSI I INSTRUCTION. IF THIS IS THE CASE, DISPLAY LOCATIONS WHERE PROGRAM IS STORED TO DETERMINE IF THE LOCATIONS HAVE CHANGED. THE ADDRESSES OF BSI I INSTRUCTION ARE STORED BY THE STO INSTRUCTIONS, AND THE LOCATIONS FFFD AND 0002 ARE STORED BY THE BSI I INSTRUCTIONS. STATIC OR INTERMITTENT DATA-TRANSFER ERRORS SHOULD BE READILY DETECTED BY THIS MEANS, AND BE EASY TO ISOLATE BECAUSE OF THE UNIQUE FAILURE INDICATIONS.

ERRORS IN THE DATA PATH PROGRAM SHOULD BE CAUSED BY SINGLE BIT FAILURES, OR BY HALF-WORD FAILURES. THUS, DROPPED OR ADDED BITS CAN BE REFERENCED DIRECTLY TO A CIRCUIT CARD. SWAP INDICATED CIRCUIT CARD TO SEE IF FAILURE CHANGES.

THE Q, U, A, AND D REGISTERS CIRCUIT CARDS ARE LOCATED IN ROW 4 OF THE CARD GATE, AND ARE INTERCHANGEABLE.

THE I, B, AND M REGISTERS CIRCUIT CARDS ARE LOCATED IN ROW 6 OF THE CARD GATE, AND ARE INTERCHANGEABLE.

FAILING BIT- 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
COLUMN----- C D E F G H J K

THE FOLLOWING CARDS CONTROL HALF-WORD TRANSFERS AND ARE INTERCHANGEABLE.

M4, M5, M7, L5, AND L6.

PROGRAM DESCRIPTION

THE LD INSTRUCTION AT LOCATION FFFA PERFORMS THE FUNCTION OF SETTING THE ACCUMULATOR TO 0002 SO THAT WHEN THE FOLLOWING BSI I INSTRUCTION IS PERFORMED, A COMPLEMENT BIT PATTERN (FFFD) WILL BE SENT THROUGH THE A REGISTER, THUS TESTING THAT THE A REGISTER IS RETURNED TO 0002 AT THE END OF THE BSI I INSTRUCTION. THIS TEST IS ACCOMPLISHED BY STORING THE CONTENTS OF THE A REGISTER BACK INTO LOCATION 0001 AFTER THE BSI I INSTRUCTION. THE SAME PHILOSOPHY IS USED DURING BSI I INSTRUCTION AT LOCATION 0000 BY SETTING THE A REGISTER TO FFFD WHILE 0002 IS SENT THROUGH IT DURING THE BSI I INSTRUCTION. A FAILURE OF EITHER BSI I INSTRUCTION THAT AFFECTS THE A REGISTER WILL CAUSE THE FOLLOWING BSI I INSTRUCTION TO TAKE ITS ADDRESS FROM THE WRONG LOCATION. THIS LOCATION WILL PROBABLY BE ONE OF THE CORE LOCATIONS LOADED WITH 33FF, THUS CAUSING THE PROGRAM TO STOP.

4. PRINTOUTS (NONE)

5. COMMENTS

5.1 BASIC DIAGNOSTIC LOADER PHILOSOPHY

THE 1800 BASIC DIAGNOSTIC LOADER IS A SELF-CHECKING PROGRAM USED TO LOAD THE PROCESSOR DIAGNOSTIC PROGRAMS, AND TO VERIFY THEIR CORRECT LOADING. THE LOADER CONTAINS ONE-CARD PROGRAMS USED AS AIDS IN DIAGNOSIS OF BASIC FAILURES IN THE PROCESSOR. THESE ONE-CARD PROGRAMS NUMBERED 03 THROUGH 0B IN COLUMNS 79 AND 80 ARE NORMALLY EXECUTED WHILE IN THE PROCESS OF LOADING AND BUILDING OF THE LOADER. CARD 03 IS BYPASSED.

TABLE 3 CONTAINS A BREAKDOWN OF THE 1800 BASIC DIAGNOSTIC LOADER. CARD NUMBER, LOCATION IN STORAGE AND FUNCTION OF EACH CARD ARE SHOWN.

THE FINAL LOADER USED TO LOAD THE MAIN PROGRAM CONSISTS OF THE COMBINED PROGRAMS OF CARDS 02, 11, 14 AND 15. CARDS 01, 10, 12, 13, AND 16 ARE USED IN THE PROCESS OF BUILDING AND CHECKING THE LOADER. CARDS 03 THROUGH 0B ARE ONE-CARD PROGRAMS USED TO CHECK SPECIFIC PROCESSOR FUNCTIONS.

THE FINAL LOADER PERFORMS THE FOLLOWING FUNCTIONS.

- A. READS A CARD INTO LOCATION 0000 TO 0027.
- B. CONTINUOUSLY CHECKS DSW WAITING FOR AN OP COMPLETE. ANY ERROR DSW IS SIGNALLED BY PROGRAM STOPPING AT A SPECIFIED ERROR WAIT INSTRUCTION.
- C. CHECKS WORD COUNT AFTER OP COMPLETE IS RECEIVED. WORD COUNT MUST NOT BE ZERO. IF A WORD COUNT OF ZERO IS DETECTED PROGRAM STOPS AT ERROR WAIT.
- D. FORMS CHECK SUM OF LOCATIONS 0000 THROUGH 0026. THE DEVELOPED CHECK SUM MUST BE 0000 OR PROGRAM STOPS AT ERROR WAIT.
- E. CHECKS LOCATION 0025 FOR STARTING ADDRESS WHERE WORDS MUST BE STORED INTO. IF THE ADDRESS IS 0000 PROGRAM ASSUMES LAST CARD OF PROGRAM HAS BEEN READ AND CONSEQUENTLY BRANCHES TO LOCATION 0000 TO BEGIN EXECUTION OF MAIN PROGRAM. IF THE ADDRESS IS NOT 0000 PROGRAM MOVES THE NUMBER OF WORDS SPECIFIED BY THE WORD COUNT FROM IMAGE AREA (0000 - 0027) TO ADDRESS SPECIFIED IN LOCATION 0025, AND ABOVE.
- F. COMPARES (EOR) EACH WORD MOVED FROM IMAGE AREA WITH THE CORRESPONDING WORD AT THE NEW LOCATION. FAILURE OF ANY ONE WORD TO COMPARE RESULTS IN ERROR WAIT INDICATING A TRANSFER ERROR.
- G. REPEATS ENTIRE PROCEDURE FOR EVERY CARD.

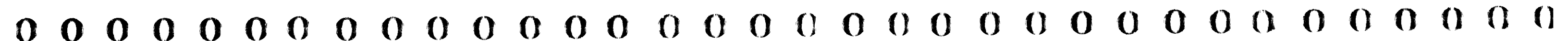


TABLE 2
 REFERENCE DATA

CARD NO.	STORAGE LOCATION	FUNCTION / COMMENTS	LOADED BY
01	0000-0027	LOADS CARD 02	PROGRAM LOAD
02	0028-004F	LOADS CARDS 03 - 10	CARD 01
03	0000-0027	XIO TEST. NORMAL LOAD BYPASSES THIS CARD. TO EXECUTE, LOAD CARD 03 UNDER PROGRAM LOAD.	CARD 02
04	0000-0027	LIX TEST. MAY BE LOADED UNDER PROGRAM LOAD.	CARD 02
05	0000-0027	TESTS READ IN FROM CARD READER, LOAD LONG, BSC Z AND EOR. EACH CARD MAY BE LOADED SEPARATELY UNDER PROGRAM LOAD.	CARD 02
06	0000-0027		
07	0000-0027		
08	0000-0027		
09	0000-0027	DATA PATH TEST. MAY BE LOADED UNDER PROGRAM LOAD.	CARD 02
0A	0000-0027	EOR TEST. MAY BE LOADED UNDER PROGRAM LOAD.	CARD 02
0B	0000-0027	ADD TEST. MAY BE LOADED UNDER PROGRAM LOAD.	CARD 02
10	0000-0027	READS CARD 11 OVER PART OF CARD 02 PROGRAM.	CARD 02
11	0046-0068	WITH LOCATIONS 0028-0045 OF CARD 02, BECOMES LOADER FOR CARDS 12 AND 13.	CARD 10
12	0000-0027	DATA TO CHECK THAT CARD 02 AND 11 COMBINED PROGRAM IS CORRECT.	CARD 02 AND 11
13	0000-0027	READS CARDS 14 AND 15.	CARD 02 AND 11
14	0068-0089	MOVES CARD IMAGE TO SPECIFIED LOCATION.	CARD 13
15	0089-00A7	VERIFIES TRANSFER OF MOVED WORDS.	CARD 13
16	0100-0123	DATA CARD TO CHECK THAT FINAL LOADER IS WORKING PROPERLY.	CARDS 02, 11, 14 AND 15. (FINAL LOADER)

5.2 DESCRIPTION OF ONE-CARD PROGRAMS

ONE-CARD PROGRAMS ARE NUMBERED 03 THROUGH 0B, AND MAY BE RUN INDIVIDUALLY BY LOADING UNDER PROGRAM LOAD MODE. PROGRAMS 04 THROUGH 0B ARE EXECUTED ONCE WHILE LOADING THE BASIC LOADER TO INSURE THAT THE FUNCTIONS TESTED BY THESE ONE-CARD PROGRAMS ARE FUNCTIONING PROPERLY. PROGRAM 03 IS BYPASSED BY THE BASIC LOADER.

- 5.2.1 THE CARD 03 PROGRAM IS A TWO PART PROGRAM. PART 1 READS A CARD, RESETS DSW, SENSES DSW AND STOPS ON WAIT TO PERMIT OPERATOR TO VERIFY THAT THE DSW IS CORRECT. FOLLOWING DEPRESSION OF START PUSHBUTTON, THE PROGRAM SENSES AND RESETS THE DSW AND STOPS ON WAIT TO ALLOW VERIFICATION OF DSW. IF NO ERRORS ARE ENCOUNTERED, PART 2 MAY BE EXECUTED. PART 2 CAUSES CARDS TO FEED CONTINUOUSLY AND CHECKS THE DSW. IF ANY DSW ERRORS ARE ENCOUNTERED, PROGRAM STOPS ON WAIT WITH A REGISTER DISPLAYING THE RESULT OF AN EOR OF THE ERROR DSW WITH 0800.
- 5.2.2 THE CARD 04 PROGRAM PERFORMS A TEST OF THE LIX INSTRUCTION. INCORRECT OPERATION OF THE LIX INSTRUCTION CAUSES PROGRAM TO BRANCH TO AN ERROR WAIT. THE B REGISTER READING FOR THE ERROR WAIT MAY BE 3004 IF THE BRANCH OCCURS WITHIN THE PROGRAM, OR 33FF IF PROGRAM IS MADE TO BRANCH OUTSIDE OF THE PROGRAM.
- 5.2.3 CARD 05, 06, 07 AND 08 TEST THAT EACH BIT POSITION OF THE A REGISTER CAN BE LOADED WITH A 1 AND A 0. AN ERROR IN LOADING THE A REGISTER CAUSES AN ERROR WAIT. THE BSC Z INSTRUCTION IS ALSO TESTED. FAILURE OF THE BSC Z INSTRUCTION WOULD CAUSE AN ERROR WAIT STOP. CARD 05 TESTS BIT POSITIONS 0 THROUGH 4. CARD 06 TESTS BIT POSITIONS 5 THROUGH 9. CARD 07 TESTS BIT POSITIONS 10 THROUGH 14. CARD 08 TESTS BIT POSITION 15 AND IN ADDITION CHECKS THAT EOR OF FFFF AND FFFF RESULTS IN A REGISTER EQUAL 0000, AND THAT EOR OF 0000 AND 0000 ALSO RESULTS IN A REGISTER EQUAL 0000. FAILURE OF EOR CAUSES PROGRAM TO STOP ON ERROR WAIT.
- 5.2.4 CARD 09 PROGRAM IS A TEST OF DATA TRANSFER BETWEEN REGISTERS. THE TEST TRANSFERS CONSTANTS 3333 AND CCCC ALTERNATELY BETWEEN B-D-A-V-A-B AND B-D-A-M REGISTERS BY PERFORMING LOAD, AND STORE INSTRUCTIONS. FAILURE TO OBTAIN EXPECTED RESULTS CAUSES PROGRAM TO STOP ON ERROR WAIT.
- 5.2.5 THE CARD 0A PROGRAM IS A TEST OF EOR ACCOMPLISHED BY TESTING EACH BIT POSITION FOR THE FOLLOWING CONDITIONS.

A = 0	D = 1	A AND D CORRESPOND TO
A = 1	D = 0	BIT POSITIONS IN THE A AND B REGISTERS.

 FAILURE OF EOR RESULTS IN PROGRAM STOPPING ON ERROR WAIT.
- 5.2.6 CARD 0B PROGRAM IS A TEST OF THE ADD OPERATION. THE TEST ADDS FFFF TO CONTENTS OF SYMBOLIC LOCATION SUMMI, ADDS 0001 TO CONTENTS OF SYMBOLIC LOCATION SUMPL, AND ADDS CONTENTS OF SUMMI AND SUMPL. IF THE RESULTANT SUM IS 0000 THE ENTIRE PROCEDURE IS REPEATED. IF THE SUM IS NOT 0000 THE PROGRAM STOPS ON ERROR WAIT.

6. APPENDIX (NONE)

2400 TIMING TEST

2400 TIMING TEST

```

3001          ABS          8B900020
          ORG      /3001    8B900030
          *          8B900040
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3001 0 014C    DC      WAIT1&1  WAIT FOR DATA ENTRY
          *                   SWITCHES TO BE SET.
          *                   PUSH START TO
          *                   CONTINUE THE PROGRAM.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3002 0 03B8    DC      WAIT2&1  WAIT BEFORE ROUTINE,
          *                   TERMINATE PROGRAM OR
          *                   HALT ON ERROR. IF
          *                   HALT ON ERROR OR
          *                   TERMINATE PROGRAM,
          *                   A PRINTOUT WILL OCCUR
          *                   BEFORE THE WAIT.
          *                   PUSH START TO CONTINUE
          *                   OR RESTART.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3003 0 047B    DC      WAIT3&1  1443 PRINTER IS NOT
          *                   READY. MAKE PRINTER
          *                   READY AND PUSH START.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3004 0 0627    DC      WAIT4&1  WAIT BECAUSE TYPE-
          *                   WRITER IS NOT READY.
          *                   MAKE TYPEWRITER READY
          *                   AND PUSH START TO
          *                   CONTINUE PROGRAM.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3005 0 07A8    DC      WAIT5&1  LOST INTRPT. AFTER
          *                   TIMED WRT OR RD.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3006 0 07DF    DC      WAIT6&1  LOST INTRPT. AFTER BSP
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3007 0 3007    DC      /3007    NOT USED
3008 0 3008    DC      /3008    NOT USED
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
          *                   ON ALL LOST INTERRUPT
          *                   WAITS, PUSH RESET
          *                   AND START TO RESTART.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
3009 0 0371    DC      WAIT9&1  NO LEGAL DSW BIT ON
          *                   AT INTERRUPT. PUSH
          *                   RESET AND START TO
          *                   RESTART PROGRAM.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
300A 0 0373    DC      WAITA&1  BLANK ILSW AT
          *                   INTERRUPT. PUSH
          *                   RESET AND START
          *                   TO RESTART THE PROGRAM.
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *

```

```

          012C          *
          012C 0 B900  *
          *
          *                   ORG      300
          *                   DC      /B900  PID
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
          *
          *                   BEGN  LDX  L3 BEGN1  IX 3 # LDR RETURN
          *                   STX  L3 /0124  STORE IN LDR
          *                   LDX  X /0050  GO TO LOADER
          *                   BEGN1 LDX  3 -7  IX # NO ENTRIES
          *                   BEGN2 LD  3 10  GET AN ENTRY
          *                   STO  L3 EDIT&9  SET IN EDIT FIELD
          *                   MDX  3 1  DECR IX
          *                   MDX  BEGN2  LOOP
          *                   LDX  L3 BEGAP  IX 3 # LDR RETURN
          *                   STX  L3 /0124  STORE IN LOADER
          *                   LDX  X /0050  GO TO LOADER
          *                   BEGAP LDX  3 26  IX # NO LVLS
          *                   LD  BEGX3  GET COMMON INTR TRAP
          *                   BEGBA STO  L3 8  SET
          *                   MDX  3 -1  DECR IX REG
          *                   MDX  BEGBA  LOOP
          *                   LDD  L BEGX4  SET RESTART
          *                   STD  L 0  *
          *                   LD  L ACTI&1  GET INTRPT RTN ADRS
          *                   STO  I EDIT&3  SET FOR TAPES
          *                   WAIT1 WAIT 1  WAIT FOR SWS
          *                   BSC  L MQNT  *
          *                   BEGX3 DC  SVINT  COMMON INTR TRAP
          *
          *                   RESTORE PROGRAM TO 4
          *                   MICROSEC MEM AND MOD 3 DRS
          *
          *
          *                   BEGIN DC 0
          *                   LDX  3 115  SE
          *                   BEGAN LD  L3 BEGX8-1  GET BASE CONSTANT
          *                   STO  L3 CON-1  SET
          *                   MDX  3 -1  DECR IX
          *                   MDX  BEGAN  LOOP
          *                   MDX  BEGAC  *
          *                   BEGX8 DC 776  RESTORE CONSTANTS
          *                   DC 1810
          *                   DC 776
          *                   DC 1163
          *                   DC 128
          *                   DC 303
          *                   DC 128
          *                   DC 195
          *                   DC 248
          *                   DC 305
          *                   DC 245
          *                   DC 302
          *                   DC 480
          *                   DC 3
          *                   DC 3392
          *                   DC 2
          *                   DC 28928
          *                   DC 1
          *                   DC 54464
          *                   DC 1
          *                   DC 14464
          *                   DC 0
          *                   DC 40000
          *                   DC 0
          *                   DC 20000
          *                   DC 0
          *                   DC 16000
          *
          *                   014F 0 0000
          *                   0150 0 6373
          *                   0151 0 C700 0157
          *                   0153 0 D700 066C
          *                   0155 0 73FF
          *                   0156 0 70FA
          *                   0157 0 7073
          *                   0158 0 0308
          *                   0159 0 0712
          *                   015A 0 0308
          *                   015B 0 048B
          *                   015C 0 0080
          *                   015D 0 012F
          *                   015E 0 0080
          *                   015F 0 00C3
          *                   0160 0 00F8
          *                   0161 0 0131
          *                   0162 0 00F5
          *                   0163 0 012E
          *                   0164 0 01E0
          *                   0165 0 0003
          *                   0166 0 0D40
          *                   0167 0 0002
          *                   0168 0 7100
          *                   0169 0 0001
          *                   016A 0 D4C0
          *                   016B 0 0001
          *                   016C 0 3880
          *                   016D 0 0000
          *                   016E 0 9C40
          *                   016F 0 0000
          *                   0170 0 4E20
          *                   0171 0 0000
          *                   0172 0 3E80

```

```

8B900700
8B900710
8B900720
8B900730
8B900740
8B900750
8B900760
8B900770
8B900780
8B900790
8B900800
8B900810
8B900820
8B900830
8B900840
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8B900890
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8B900920
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8B900960
8B900970
8B900980
8B900990
8B901000
8B901010
8B901020
8B901030
8B901040
8B901050
8B901060
8B901070
8B901080
8B901090
8B901100
8B901110
8B901120
8B901130
8B901140
8B901150
8B901160
8B901170
8B901180
8B901190
8B901200
8B901210
8B901220
8B901230
8B901240
8B901250
8B901260
8B901270
8B901280
8B901290
8B901300
8B901310
8B901320
8B901330
8B901340
8B901350
8B901360
8B901370

```

FL

2400 TIMING TEST

0173 0 0000	DC	0
0174 0 2EE0	DC	12000
0175 0 0000	DC	0
0176 0 1F40	DC	8000
0177 0 0000	DC	0
0178 0 0FA0	DC	4000
0179 0 0000	DC	0
017A 0 0C80	DC	3200
017B 0 0000	DC	0
017C 0 0960	DC	2400
017D 0 0000	DC	0
017E 0 0640	DC	1600
017F 0 0000	DC	0
0180 0 0578	DC	1400
0181 0 0000	DC	0
0182 0 0480	DC	1200
0183 0 0000	DC	0
0184 0 03E8	DC	1000
0185 0 0000	DC	0
0186 0 0384	DC	900
0187 0 0000	DC	0
0188 0 0320	DC	800
0189 0 0000	DC	0
018A 0 02BC	DC	700
018B 0 0000	DC	0
018C 0 0258	DC	600
018D 0 0000	DC	0
018E 0 01F4	DC	500
018F 0 0000	DC	0
0190 0 0190	DC	400
0191 0 0000	DC	0
0192 0 0168	DC	360
0193 0 0000	DC	0
0194 0 0140	DC	320
0195 0 0000	DC	0
0196 0 0118	DC	280
0197 0 0000	DC	0
0198 0 00F0	DC	240
0199 0 0000	DC	0
019A 0 00DC	DC	220
019B 0 0000	DC	0
019C 0 00C8	DC	200
019D 0 0000	DC	0
019E 0 0084	DC	180
019F 0 0000	DC	0
01A0 0 00A0	DC	160
01A1 0 0000	DC	0
01A2 0 008C	DC	140
01A3 0 0000	DC	0
01A4 0 0078	DC	120
01A5 0 0000	DC	0
01A6 0 006E	DC	110
01A7 0 0000	DC	0
01A8 0 0064	DC	100
01A9 0 0000	DC	0
01AA 0 005A	DC	90
01AB 0 0000	DC	0
01AC 0 0050	DC	80
01AD 0 0000	DC	0
01AE 0 0046	DC	70
01AF 0 0000	DC	0
01B0 0 003C	DC	60
01B1 0 0000	DC	0
01B2 0 0032	DC	50
01B3 0 0000	DC	0
01B4 0 002C	DC	44
01B5 0 0000	DC	0
01B6 0 0028	DC	40

8B901380
8B901390
8B901400
8B901410
8B901420
8B901430
8B901440
8B901450
8B901460
8B901470
8B901480
8B901490
8B901500
8B901510
8B901520
8B901530
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8B901580
8B901590
8B901600
8B901610
8B901620
8B901630
8B901640
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8B901660
8B901670
8B901680
8B901690
8B901700
8B901710
8B901720
8B901730
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8B901770
8B901780
8B901790
8B901800
8B901810
8B901820
8B901830
8B901840
8B901850
8B901860
8B901870
8B901880
8B901890
8B901900
8B901910
8B901920
8B901930
8B901940
8B901950
8B901960
8B901970
8B901980
8B901990
8B902000
8B902010
8B902020
8B902030
8B902040
8B902050

2400 TIMING TEST

01B7 0 0000	DC	0
01B8 0 0024	DC	36
01B9 0 0000	DC	0
01BA 0 0020	DC	32
01BB 0 0000	DC	0
01BC 0 001C	DC	28
01BD 0 0000	DC	0
01BE 0 0018	DC	24
01BF 0 0000	DC	0
01C0 0 0016	DC	22
01C1 0 0000	DC	0
01C2 0 0014	DC	20
01C3 0 03C0	DC	960
01C4 0 0780	DC	1920
01C5 0 4D92	DC	19858
01C6 0 0003	DC	3
01C7 0 0000	DC	0
01C8 0 0025	DC	37
01C9 0 0048	DC	75
01CA 0 0070	DC	112

```

*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXX EDIT ROUTINE XXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
*
*           SET PROGRAM TO MEM SPEED
*
*
BEGAC LD L EDIT&8      GET MEM SPEED
      BSC L BEGAD-1,Z-  BR IF 4.00 US
      BSC L BEGAE,-    BR IF 2.00 US
      LDX 3 111
BAK   SLT 32          SETUP
      LD L3 SPEC      CONSTANTS
      SRT 2           FOR
      M ARIA1+1       2.25 US MEMORY
      D ARIA1+2
      SLA 2
      STO L3 SPEC
      MDX 3 -1
      MDX BAK
      LD ARIA1+3
      STO L MLGX7
      MDX PART2      BR TO CONT. SETUP
ARIA1 DC /FFFF      X
      DC /000F      T
      DC /0011      N
      DC /0025      T
      DC /FFFF      S
*
PART2 LDD ARIA5      SETUP
      STD L MT5X0    SPECIAL
      LDD ARIA5+2    DOUBLE
      STD L MT5X0+2  WORD
      LDD ARIA5+4    CONSTANTS
      STD L MT5X0+4
      LDD ARIA5+6
      STD L MT5X0+6
      LD ARIA5+8
      STO L GPHLM
      LDX L BEGAF
      BSS E 0
ARIA5 DC /0002      D C
      DC /B150      O W O
      DC /0002      U O N
      DC /2778      B R S
      DC /0001      L D T

```

8B902060
8B902070
8B902080
8B902090
8B902100
8B902110
8B902120
8B902130
8B902140
8B902150
8B902160
8B902170
8B902180
8B902190
8B902200
8B902210
8B902220
8B902230
8B902240
8B902250
8B902260
8B902270
8B902280
8B902290
8B902300
8B902310
8B902320
8B902330
8B902340
8B902350
\$ 8B902360
\$ 8B902370
\$ 8B902380
\$ 8B902390
\$ 8B902400
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\$ 8B902490
\$ 8B902500
\$ 8B902510
\$ 8B902520
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\$ 8B902540
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\$ 8B902560
\$ 8B902570
\$ 8B902580
\$ 8B902590
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\$ 8B902660
\$ 8B902670
\$ 8B902680
\$ 8B902690
\$ 8B902700
\$ 8B902710
\$ 8B902720

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2400 TIMING TEST

01FD 0 9D98	DC	/9D98	E	A	\$ 88902730
01FE 0 0001	DC	/0001		N	\$ 88902740
01FF 0 13B8	DC	/13B8		T	\$ 88902750
0200 0 0002	DC	/0002		S	\$ 88902760
0201 0 FFFF	DC	/FFFF			\$ 88902770
					\$ 88902775
					\$ 88902780
0202 0 636F	* LDX	3 111			88902790
0203 0 C700 066C	BEGAD LD	L3 CON-1	GET A CONSTANT		88902800
0205 0 1801	SRA	1	DVD BY 2		88902810
0206 0 D700 066C	STO	L3 CON-1	SET		88902820
0208 0 73FF	MDX	3 -1	DECR IX		88902830
0209 0 70F9	MDX	BEGAD	LOOP		88902840
020A 0 C400 066B	LD	L CONV1	GET CONV CONSTANT		88902850
020C 0 D400 06DC	STO	L MLGX7	SET		88902860
020E 0 7004	MDX	BEGAF			88902870
					88902880
					88902890
					88902900
					88902910
					88902920
					88902930
					88902940
					88902950
					88902960
					88902970
					88902980
					88902990
					88903000
					88903010
					88903020
					88903030
					88903040
					88903050
					88903060
					88903070
					88903080
					88903090
					88903100
					88903110
					88903120
					88903130
					88903140
					88903150
					88903160
					88903170
					88903180
					88903190
					88903200
					88903210
					88903220
					88903230
					88903240
					88903250
					88903260
					88903270
					88903280
					88903290
					88903300
					\$ 88903310
					\$ 88903320
					\$ 88903330
					\$ 88903340
					\$ 88903350
					\$ 88903360
					\$ 88903370
					\$ 88903380
					\$ 88903390

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2400 TIMING TEST

0255 0 D400 0678	STO	L	MT5XA+3	MOD 1	\$ 88903400
0257 0 C008	LD		ARIA2+4	TAPE	\$ 88903410
0258 0 D400 06DB	STO	L	GPHLM	CONSTANTS	\$ 88903420
025A 0 4C00 02D5	BSC	L	BEGAK	BR TO EXIT SETUP	\$ 88903430
					\$ 88903440
					\$ 88903450
					\$ 88903460
					\$ 88903470
					\$ 88903480
					\$ 88903490
					\$ 88903500
					\$ 88903510
					\$ 88903520
					\$ 88903530
					\$ 88903535
					\$ 88903540
					\$ 88903550
					\$ 88903560
					\$ 88903570
					\$ 88903580
					\$ 88903590
					\$ 88903600
					\$ 88903610
					\$ 88903620
					\$ 88903630
					\$ 88903640
					\$ 88903650
					\$ 88903660
					\$ 88903670
					\$ 88903680
					\$ 88903690
					\$ 88903700
					\$ 88903710
					\$ 88903720
					\$ 88903730
					\$ 88903740
					\$ 88903750
					\$ 88903755
					\$ 88903760
					\$ 88903770
					\$ 88903780
					\$ 88903790
					\$ 88903800
					\$ 88903810
					\$ 88903820
					\$ 88903830
					\$ 88903840
					\$ 88903850
					\$ 88903860
					\$ 88903870
					\$ 88903880
					\$ 88903890
					\$ 88903900
					\$ 88903910
					\$ 88903920
					\$ 88903930
					\$ 88903940
					\$ 88903950
					\$ 88903960
					\$ 88903970
					\$ 88903980
					\$ 88903990
					\$ 88904000
					\$ 88904010
					\$ 88904020
					\$ 88904030
					\$ 88904040
					\$ 88904050

2400 TIMING TEST

```

02AD 0 1801          SRA      1          MUL BY .5          88904060
02AE 0 8700 0670    A        L3 CON1-1    MUL BY 1          88904070
02B0 0 D700 0670    STO      L3 CON1-1    SET              88904080
02B2 0 73FF          MDX      3 -1          DECR IX          88904090
02B3 0 70F7          MDX      BEGAS        LOOP            88904100
02B4 0 7401 0677    MDX      L MT5XA&2,1  ADJ GRPH LMT    88904110
02B6 0 C035          LD        EDIT&8      GET MEM SPEED    88904120
02B7 0 4C18 02D1    BSC      L FWD2,+--   BR IF 2.00 US P/C $ 88904130
02B9 0 4C10 02D5    BSC      L BEGAK,-    BR IF 4.00 US P/C $ 88904140
02BB 0 C00F          LD        ARIA4        $ 88904150
02BC 0 D400 0675    STO      L MT5XA      SETUP            $ 88904160
02BE 0 C00D          LD        ARIA4+1     FOR              $ 88904170
02BF 0 D400 0676    STO      L MT5XA+1    2.25 US P/C    $ 88904180
02C1 0 C00B          LD        ARIA4+2     AND              $ 88904190
02C2 0 D400 0677    STO      L MT5XA+2    MOD 2           $ 88904200
02C4 0 C009          LD        ARIA4+3     TAPE            $ 88904210
02C5 0 D400 0678    STO      L MT5XA+3    CONSTANTS       $ 88904220
02C7 0 C007          LD        ARIA4+4     $ 88904230
02C8 0 D400 0679    STO      L MT5XA+4    $ 88904240
02CA 0 700A          MDX      BEGAK        BR TO EXIT      $ 88904250
02CB 0 0148          ARIA4    DC           /0148          $ 88904260
02CC 0 0194          DC       /0194        MOD 2           $ 88904270
02CD 0 0144          DC       /0144        TAPE            $ 88904280
02CE 0 0190          DC       /0190        CONSTANTS       $ 88904290
02CF 0 0004          DC       /0004        $ 88904300
02D0 0 FFFF          DC       /FFFF        $ 88904310
02D1 0 74FF 0678    FWD2    MDX      L MT5XA+3,-1  ADJ GPH CNST    $ 88904320
02D3 0 7401 06DB    MDX      L GPHLM,1      *              88904330
02D5 0 4C80 014F    BEGAK   BSC      I BEGIN      EXIT            SX 88904340
*
*          GO TO PROG INITIALIZATION
*
*          CONSTANTS
*
02D8      0000          BSS      E 0          88904420
02D8 0 4C00 013D    BEGX4   BSC      L BEGAP      RESTART CONSTANT 88904430
02DA 0 02E1          BEGX5   DC        SWO        IOCC-READ BIT SWS 88904440
02DB 0 0240          DC       /0240          88904450
02DC 0 02E2          BEGX7   DC        SW1        READ PROG SWS IOCC 88904460
02DD 0 0260          DC       /0260          88904470
*
*          PROGRAM STATUS TABLE
*
02DE 0 8900          PID      DC         /B900          88904530
02DF 0 0000          RID      DC         0          ROUTINE NUMBER    88904540
02E0 0 0000          RAD      DC         0          ROUTINE ADDRESS   88904550
02E1 0 0000          SWO      DC         0          SWITCH ENTRY 1    88904560
02E2 0 0000          SW1      DC         0          SWITCH ENTRY 2    88904570
02E3 0 FFFF          TERM     DC         /FFFF          TERMINATOR        88904580
02E4 0 7000          EDIT     DC         /7000          TAPE AREA CODE    88904590
02E5 0 3000          DC       /3000          1443 AREA CODE    88904600
02E6 0 0000          DC       0          ILSW BIT -TAPES   88904610
02E7 0 0000          DC       0          INTR ADRS-TAPES   88904620
02E8 0 0000          DC       0          NUMBER TRACKS DR 0 88904630
02E9 0 0000          DC       0          NUMBER TRACKS DR 1 88904640
02EA 0 0000          DC       0          DRIVE 0 MODEL     88904650
02EB 0 0000          DC       0          DRIVE 1 MODEL     88904660
02EC 0 0000          DC       0          MEMORY SPEED      88904670
*
*          DSW TABLE
*
02ED 0 0000          ACTI     DC         0          AREA CODE TAPES   88904710
02EE 0 0313          DC       INTR3&1      INTR RTN TAPES    88904720

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2400 TIMING TEST

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02EF 0 630B          MONT     LDX      3 11          CLEAR DST TABLE 88904730
02F0 0 1010          SLA      16          88904740
02F1 0 D700 06DF    MONT1    STO      L3 DST-1          88904750
02F3 0 73FF          MDX      3 -1          88904760
02F4 0 70FC          MDX      MONT1        88904770
*
*          SET NECESSARY VALUES
*
02F5 0 1010          SLA      16          88904860
02F6 0 D0E8          STO      RID          CLEAR RTN NO      88904870
02F7 0 D400 093B    STO      L PGWS       CLEAR PROG SW     88904880
02F9 0 C0EA          LD        EDIT         GET TAPE AREA CODE 88904890
02FA 0 D0F2          STO      ACTI         SET                88904900
02FB 0 D400 06E8    STO      L DST&8      88904910
02FD 0 0804          XIO      UNMK3        UNMASK ALL LEVELS 88904920
02FE 0 0805          XIO      UNMK4        88904930
02FF 0 4006          BSI      RDSWS        READ SWS          88904940
0300 0 4C00 06F0    BSC      L MONT4      BRANCH            88904950
0302 0 0000          BSS      E 0          88904960
0303 0 0000          UNMK3    DC           0          IOCC-UNMASK LOWER 88904970
0304 0 0480          DC       /0480        88904980
0305 0 0000          UNMK4    DC           0          IOCC-UNMASK UPPER 88904990
0306 0 0481          DC       /0481        88905000
*
RDSWS   DC         0          88905010
XIO     BEGX5      0          READ DATA SWS    88905020
XIO     BEGX7      0          READ PROG SWS     88905030
LD      SW1        0          ADJ PROG SWS      88905040
SLA     5          0          88905050
SRA     13         0          88905060
STO     SW1        *          88905070
BSC     I RDSWS    EXIT          88905080
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88905090
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88905100
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88905110
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88905120
*
TAX1    LDX      L2 0          RESTORE IX        88905130
LDD     TAAQ       RESTORE A AND Q    88905140
INTR3   BOSC     L 0          ENTRY AND EXIT   88905150
STX     2 TAX1&1   SAVE IX 2         88905160
STD     TAAQ       SAVE A AND Q         88905170
XIO     ILSW      SENSE ILSW BIT        88905180
BSI     L TERR,&-  BRANCH ON BLANK ILSW 88905190
AND     EDIT&2    CK IF TAPE         88905200
BSC     L INTRR,2 BRANCH # TAPE         88905210
BSI     SVINT     BRANCH-NOT TAPE       88905220
MDX     TAX1      GO EXIT              88905230
INTRR   LD        DSW         BUILD SENSE DSW   88905240
EOR     ACTI      *                88905250
STO     DSW&1    *                88905260
XIO     DSW       SENSE-NO RESET        88905270
LD      DSW&1    BUILD RESET SENSE     88905280
EOR     ONE      *                88905290
STO     DSW&1    *                88905300
XIO     DSW       SENSE-RESET          88905310
STO     DSW&1    *                88905320
XIO     DSW       SAVE SENSE WD        88905330
STO     TADSW
0327 0 6600 06E0    LDX      L2 DST        SET IX            88905340
LD      TADSW     GET SENSE WD         88905350
AND     TADWC     CK FOR LEGAL         88905360
BSC     &         SKIP # LEGAL         88905370
BSI     ERRI      ILLEGAL DSW         88905380
LD      TADSW     GET SENSE WD        88905390
STO     2 7       SET IN DST          88905400

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2400 TIMING TEST

```

032F 0 70DF          MDX   TAX1   EXIT          88905410
0330 0000          BSS   E    0          88905420
*
DSWSP DC /0701     SENSE=RESET IOCC 88905430
DC 0          88905440
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
SVINT DC 0          IE          88905480
STO SV10          SAVE ACCUMULATOR 88905490
LD TERM          SET ILLEGAL INTR 88905500
STO L DST&7     *          88905510
XIO ILSW        RESET ILSW      88905520
MDX L SV7,2     SET PASS SWITCH 88905530
SLA 16          88905540
STO SV4          CLEAR AREA CODE CNTR 88905550
LD SV2          88905560
STO SV6          SET IOCC IN USE SW 88905570
SVINO LD SV1     *          88905580
STO SV5          SET MODIFIER COUNTER 88905590
SVINI LD SV4     *          88905600
SLA 11          *          88905610
OR SV5          *BUILD IOCC      88905620
OR SV6          *          88905630
STO SV10&1     *          88905640
XIO SVIO        SENSE DSW AND RESET 88905650
MDX L SV5,-1   *          88905660
MDX SVINI       BRANCH IF NOT ALL MD 88905670
MDX L SV4,1    INCREMENT AREA CODE 88905680
LD SV4          *          88905690
S SV0           CHECK IF ALL AC USED 88905700
BSC &           SKIP IF ALL AC USED 88905710
MDX SVINO      GO SENSE WITH NXT AC 88905720
MDX L SV7,-1   SKIP IF SECOND PASS 88905730
MDX *&1        *          88905740
MDX SVEXT-1    *          88905750
LD SV3          *          88905760
STO SV6          SET IOCC FOR PI    88905770
SLA 16          *          88905780
STO SV4          SET AC FOR NEXT    88905790
MDX SVINO      *PASS            88905800
LD SVIO        RESTORE ACCUMULATOR 88905810
SVEXT BOSC I SVINT EXIT          88905820
*
** CONSTANTS **
SV0 DC /001F     NUMBER OF AREA CODES 88905830
SV1 DC /00FF    NUMBER OF MODIFIERS 88905840
SV2 DC /0701    SENSE/RESET DSW     88905850
SV3 DC /0700    SENSE/RESET PISW    88905860
SV4 DC 0        AREA CODE INDICATOR 88905870
SV5 DC 0        MODIFIER INDICATOR  88905880
SV6 DC 0        IOCC IN USE         88905890
SV7 DC 0        PASS SWITCH         88905900
BSS E 0         88905910
SVIO DC 0       SENSE DSW IOCC      88905920
DC 0           88905930
*
CONSTANTS
ILSW BSS E 0    88905940
DC 0           SENSE ILSW IOCC      88905950
DC /0300       88905960

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2400 TIMING TEST

```

0368 0 0000        TAAQ DC 0          A AND Q TEM STOR 88906090
0369 0 0000        DC 0          88906100
036A 0 0700        DSW DC /0700     USW IOCC      88906110
036B 0 0000        DC 0          88906120
036C 0 0000        TADSW DC 0        TAPE DSW STORAGE 88906130
036D 0 3040        TADWC DC /3040    CK TAPE DSW FOR OK 88906140
036E 0 0001        ONE DC 1         CONSTANT     88906150
*
* ERROR HANGS
*
ERRI DC 0          88906160
WAIT9 WAIT 9      ILLEGAL INTRPT 88906170
*
TERR DC 0          88906180
WAITA DC /300A    BLANK ILSW   88906190
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
DCC DC 0          *          88906200
STX 2 DCC2&1     SAVE IX 2      88906210
LDX 13 DCC       IX3 # RETURN 88906220
*
LD 3 0           GET ADRS OF STRING 88906230
STO DCC          88906240
*
LDX 12 DCC       IX2 # ADRS STRING 88906250
*
LD 12 0          SET AREA CODE   88906260
OR 12 1          SET FUNC        88906270
OR 12 2          SET MODIFIER    88906280
*
SRT 16           PUT IN Q        88906290
LD 2 3          GET I/O ADRS     88906300
*
STO DCC3        SET IOCC WD      88906310
DCC2 LDX L2 0   RESTORE IX 2     88906320
*
XIO DCC3        DO COMMAND       88906330
*
BSC L3 1        RETURN          SX 88906340
DCC3 BSS E 2    88906350
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
DELAY DC 0       SE          88906360
LDX 13 DELAY    IX3 # RETURN 88906370
LDD 13 0        GET COUNT      88906380
JDLY2 SD MONE   SUB 1         88906390
STD MST        SAVE          88906400
BSC L JDLY3,Z  CK FOR ZERO    88906410
RTE 16         88906420
BSC L JDLY4,&-  CK FOR ZERO    88906430
JDLY3 LDD MST   GET COUNT      88906440
MDX JDLY2       88906450
JDLY4 MDX L DELAY,1 &1 TO RETURN 88906460
BSC I DELAY     RETURN        SX 88906470
BSS E 0         88906480
MONE DC 0       88906490
DC 1           88906500
MST DC 0       88906510
DC 0           88906520
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0373 0 0000        88906530
0374 0 6A10        88906540
0375 0 6780 0373  88906550
*
0377 0 C300        88906560
0378 0 D0FA        88906570
*
0379 0 6680 0373  88906580
*
037B 0 C680 0000  88906590
037D 0 EE80 0001  88906600
037F 0 EE80 0002  88906610
*
0381 0 1890        88906620
0382 0 C203        88906630
*
0383 0 D806        88906640
0384 0 6600 0000  88906650
*
0386 0 0803        88906660
*
0387 0 4F00 0001  88906670
038A 0002         88906680
*
038C 0 0000        88906690
038D 0 6780 038C  88906700
038F 0 CF80 0000  88906710
0391 0 980C        88906720
0392 0 D80D        88906730
0393 0 4C20 0398  88906740
0395 0 18D0        88906750
0396 0 4C18 039A  88906760
0398 0 C807        88906770
0399 0 70F7        88906780
039A 0 7401 038C  88906790
039C 0 4C80 038C  88906800
039E 0000         88906810
039F 0 0000        88906820
03A0 0 0000        88906830
03A1 0 0000        88906840

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2400 TIMING TEST

2400 TIMING TEST

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XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
03A2 0 0000      DIND DC      0      SE
03A3 0 6780 03A2  LD  I3 DIND      IX 3 # RETURN
03A5 0 C780 0000  LD  I3 0      LOAD AREA CODE
03A7 0 F006      EOR  FNC      SET FUNCT
03A8 0 F780 0001  EOR  I3 1      SET MOD
03AA 0 D006      STO  IOCC1&1  SAVE
03AB 0 0804      XIO  IOCC1     SENSE
03AC 0 4F00 0003  BSC  L3 3      RETURN
03AE 0 0700      FNC  DC      /0700
03B0 0 0000      BSS  E 0
03B0 0 0000      IOCC1 DC 0
03B1 0 0000      DC 0
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
03B2 0 0000      HALT DC 0      SE
03B3 0 0C00 043E  XIO  L MK15     MASK ALL LEVELS
03B5 0 0C00 0440  XIO  L MK27     *
03B7 0 3002      WAIT2 WAIT 2     COMMON WAIT
03B8 0 0C00 0302  XIO  L UNMK3    UNMASK
03BA 0 0C00 0304  XIO  L UNMK4    UNMASK
03BC 0 4C80 03B2  BSC  I HALT     RETURN
03BE 0 0000      WC  DC 0      WD CT STORAGE
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
03BF 0 0000      LOGC DC 0      SE
03C0 0 6D00 0564  STX  L1 LOGC7&1  SAVE IXING
03C2 0 6E00 0566  STX  L2 LOGC8&1  *
03C4 0 6F00 0568  STX  L3 LOGC9&1  *
03C6 0 6500 08F7  LDX  L1 MODO     IX 1 # ADRS OF MSG
03C8 0 6600 0D96  LDX  L2 PRA      IX 2 # ADR OF MSG
03CA 0 631F      LDX  3 31        IX 3 # LNGTH/OUTPUT
03CB 0 1010      SLA  16         SET ACCUM # BLANK
03CC 0 D700 0D91  LOGIC STO L3 PRA4-1 SET I/O AREA # BLANK
03CE 0 73FF      MDX  3 -1
03CF 0 70FC      MDX  LOGIC
03D0 0 6700 08F7  LDX  L3 MODO     IX 3 # ADRS OF MSG
03D2 0 C100      LD  1 0         GET LINE NO/WD CT
03D3 0 1808      SRA  8          SAVE LINE NUMBER
03D4 0 4C18 0499  BSC  L LOGD0,&-  BRANCH # LINE ZERO
03D6 0 7306      LOG2C MDX 3 6    SET IX3 # 2ND MOD
03D7 0 7210      MDX  2 16       SET IX2 # 2ND MOD
03D8 0 C100      LD  1 0         GET WD CT/LINE NO
03D9 0 1008      SLA  8
03DA 0 1808      SRA  8          SAVE WD CT
03DB 0 D0E2      STO  WC
03DC 0 74FC 03BE  MDX  L WC,-4    DECR WD CT
03DE 0 7001      MDX  LOGV1     NOT DONE
03DF 0 7039      MDX  LOG6C     CONV COMPLETE
03E0 0 1010      LOGV1 SLA 16    CLEAR ODD-EVEN SW
03E1 0 D400 0462  STO  L SW      *
03E3 0 C400 02DF  LD  L RID      GET RTN ID
03E5 0 F07D      EOR  K007     CK FOR RTN 7
03E6 0 4820      BSC  Z        SKIP = RTN 7
03E7 0 7016      MDX  LOG3C     BRANCH
03E8 0 6700 0930  LDX  L3 MTTYD  SET IX 3
03EA 0 C300      LOGV2 LD 3 0    GET WD
03EB 0 D400 05E5  STO  L WDCON   SET
03ED 0 4400 05A6  BSI  L HEDEC   CONVERT TO DEC
03EF 0 C400 05DB  LD  L CODE&1  GET CONVERTED WD

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03F1 0 D200      STO  2 0      SET IN MSG
03F2 0 7201      MDX  2 1      INCR IX
03F3 0 7301      MDX  3 1      *
03F4 0 7401 0462  MDX  L SW,1    INCR SWITCH
03F6 0 C068      LD  SW        GET SWITCH
03F7 0 4804      BSC  E        SKIP IF EVEN
03F8 0 70F1      MDX  LOGV2     LOOP
03F9 0 7201      MDX  2 1      INCR IX
03FA 0 74FF 03BE  MDX  L WC,-1   CK FOR DONE
03FC 0 70ED      MDX  LOGV2     LOOP
03FD 0 701B      MDX  LOG6C     PRINT
03FE 0 C101      LOG3C LD 1 1    GET HEX/DEC SW
03FF 0 4C18 0411  BSC  L LOG5C,&- BRANCH # HEX
0401 0 C300      LD  3 0      GET WD TO CONV
0402 0 D400 05E5  STO  L WDCON   SET IN CONV RTN
0404 0 4400 05A6  BSI  L HEDEC   GO CONV TO DEC
0406 0 CC00 05DA  LDD  L CODE     GET PACKED WD
0408 0 D200      LOG4C STO 2 0    SET IN MSG
0409 0 18D0      RTE  16
040A 0 D201      STO  2 1
040B 0 7203      MDX  2 3
040C 0 7301      MDX  3 1      INCR IX 3
040D 0 74FF 03BE  MDX  L WC,-1   CHECK FOR DONE
040F 0 70EE      MDX  LOG3C
0410 0 7008      MDX  LOG6C
0411 0 C300      LOG5C LD 3 0    GET WD TO CONVERT
0412 0 D400 0606  STO  L HEXWD   STO IN HEX CONV RTN
0414 0 4400 05E6  BSI  L HEXCV   GO CONVERT TO HEX
0416 0 CC00 060C  LDD  L HEXCD   GET CONVERTED WD
0418 0 70EF      MDX  LOG4C     GO CK FOR DONE
0419 0 C400 02E3  LOG6C LD L TERM  GET A TERM
041B 0 D200      STO  2 0      SET IN I/O AREA
041C 0 C400 02DF  LD  L RID      GET RTN NO
041E 0 9045      S  K006        SUB 6
041F 0 4820      BSC  Z        SKIP IF RTN 6
0420 0 7015      MDX  HERE
0421 0 C400 0D2E  LD  L LOW      GET LOW CREEP SW
0423 0 4810      BSC  -        SKIP IF NEG
0424 0 7003      MDX  HERE1
0425 0 C03F      LD  K0020     GET NEG SIGN
0426 0 D400 0DA5  STO  L PRA4&19 SET IN MSG
0428 0 C400 0D30  HERE1 LD L AVG  GET AVG SW
042A 0 4810      BSC  -        SKIP IF NEG
042B 0 7003      MDX  HERE2
042C 0 C038      LD  K0020     GET NEG SIGN
042D 0 D400 0DA8  STO  L PRA4&22 SET IN MSG
042F 0 C400 0D2F  HERE2 LD L HI   GET HI SW
0431 0 4810      BSC  -        SKIP IF NEG
0432 0 7003      MDX  HERE
0433 0 C031      LD  K0020     GET NEG SIGN
0434 0 D400 0DAB  STO  L PRA4&25 SET IN MSG
*
0436 0 C400 02E1  HERE  LD L SW0  GET SWS
0438 0 1806      SRA  6
0439 0 4C04 0466  BSC  L LOGAC,E BRANCH # USE PRINTER
043B 0 4C00 052A  BSC  L LOGBC   USE TYPEWRITER
*
043E 0 0000      BSS  E 0
043E 0 FFFF      MK15 DC /FFFF   MASK ALL LVLS IOCCS
043F 0 0480      DC  /0480
0440 0 FFFF      MK27 DC /FFFF
0441 0 0481      DC  /0481
0442 0 0700      SNSPR DC /0700
0443 0 0700      DC  /0700
0444 0 6500 0000  LOG7C LDX L1 0  RESTORE IX 1
0446 0 6600 0000  LOG8C LDX L2 0  RESTORE IX 2
0448 0 6700 0000  LOG9C LDX L3 0  RESTORE IX 3
044A 0 4C80 03BF  BSC  I LOGC    EXIT

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IBM MAINTENANCE DIAGNOSTIC PROGRAM FOR THE 1800 SYSTEM

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2400 TIMING TEST

044C 0 0000 PCCO DC 0 SE 8B908130
044D 0 69F7 STX 1 LOG7C&1 SAVE IX 1 8B908140
044E 0 6AF8 STX 2 LOG8C&1 SAVE IX 2 8B908150
044F 0 6BF9 STX 3 LOG9C&1 SAVE IX 3 8B908160
0450 0 C010 LD PCCX1 GET WD CT 8B908170
0451 0 D400 08F7 STO L MOD0 SAVE 8B908180
0453 0 C0F8 LD PCCO GET RETURN 8B908190
0454 0 D400 03BF STO L LOGC SAVE 8B908200
0456 0 C400 02E3 LD L TERM GET TERMINATOR 8B908210
0458 0 D400 0DAE STO L PRA4&28 SET IN MSG 8B908220
045A 0 C400 02E1 LD L SWO GET SW FNC 0 8B908230
045C 0 1806 SRA 6 8B908240
045D 0 4C04 0466 BSC L LOGAC,E BRANCH # USE PRINTER 8B908250
045F 0 4C00 052A BSC L LOGBC USE TYPEWRITER 8B908260
0461 0 0009 PCCX1 DC 9 PCCO WD CT 8B908270
0462 0 0000 SW DC 0 ODD-EVEN SW 8B908280
0463 0 0007 K007 DC /0007 CONSTANT 7 8B908290
0464 0 0006 K006 DC 6 CONSTANT 8B908300
0465 0 0020 K0020 DC /0020 * 8B908310
*XX
*XXXXXXXXXXXXXXXXXXXX PRINTER OUTPUT ROUTINE XXXX
*XX
*
0466 0 6B18 LOGAC STX 3 LOGAB&1 SAVE IX 3 8B908320
0467 0 C400 08F7 LD L MOD0 GET WD CT/ LINE NO 8B908330
0469 0 1008 SLA 8 SAVE WD CT 8B908340
046A 0 1808 SRA 8 8B908350
046B 0 D001 STO LOGAD&1 8B908360
046C 0 6700 0000 LOGAD LDX L3 0 IX 3 # WD CT 8B908370
046E 0 C700 048F LD L3 PRWC-2 GET FINAL WD CT 8B908380
0470 0 D400 0D92 STO L PRA4 SAVE 8B908390
0472 0 C0CF LD SNSPR GET SENSE IOCC 8B908400
0473 0 F400 02E5 EOR L EDIT&1 SET AREA CODE 8B908410
0475 0 D0CD STO SNSPR&1 SAVE 8B908420
0476 0 08C7 XIO MK15 MASK ALL LVLS 8B908430
0477 0 08C8 XIO MK27 * 8B908440
0478 0 08C9 XIO SNSPR SENSE PRINTER 8B908450
0479 0 4804 BSC E IS PRINTER READY 8B908460
047A 0 3003 WAIT3 WAIT 3 PRINTER NOT READY 8B908470
*
*

047B 0 4400 0373 BSI L DCC GO LOG * 8B908480
047D 0 05A0 DC LOGX3 ADRS OF STRING * 8B908490

047E 0 6700 0000 LOGAB LDX L3 0 RESTORE IX 3 8B908500
0480 0 08C1 LOGAE XIO SNSPR SENSE PRINTER 8B908510
0481 0 1002 SLA 2 8B908520
0482 0 4810 BSC - IS PRTR Cmpl ON 8B908530
0483 0 70FC MDX LOGAE NO 8B908540
0484 0 C0BE LD SNSPR&1 GET IOCC 8B908550
0485 0 F400 036E EOR L ONE SET BIT 15 8B908560
0487 0 D0BB STO SNSPR&1 SAVE 8B908570
0488 0 08B9 LOGAF XIO SNSPR SENSE PRINTER 8B908580
0489 0 1801 SRA 1 8B908590
048A 0 4804 BSC E IS PRTR BUSY 8B908600
048B 0 70FC MDX LOGAF YES 8B908610
048C 0 0C00 0302 XIO L UNMK3 UNMASK ALL LEVELS 8B908620
048E 0 0C00 0304 XIO L UNMK4 8B908630
0490 0 70B3 MDX LOG7C GO EXIT 8B908640
0491 0 000E PRWC DC 14 WORD CTS FOR PRINTER 8B908650
0492 0 0013 DC 19 8B908660
0493 0 0016 DC 22 8B908670
0494 0 0019 DC 25 8B908680
0495 0 001C DC 28 8B908690
0496 0 001F DC 31 8B908700
0497 0 0022 DC 34 8B908710

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0498 0 001C DC 28 8B908810
0499 0 C102 LOGDO LD 1 2 8B908820
049A 0 D400 0606 STO L HEXWD GET WD TO CONV 8B908830
049C 0 4400 05E6 BSI L HEXCV SET IN RTN 8B908840
049E 0 CC00 060C LDD L HEXCD GO CONVERT 8B908850
04A0 0 DC00 0D96 LD L PRA SET IN MSG 8B908860
04A2 0 C104 LD 1 4 GET MSG ID 8B908870
04A3 0 D400 0606 STO L HEXWD SET IN RTN 8B908880
04A5 0 4400 05E6 BSI L HEXCV GO CONVERT TO HEX 8B908890
04A7 0 CC00 060C LDD L HEXCD GET CONVERTED WD 8B908900
04A9 0 D400 0D99 STO L PRA&3 SET IN MSG 8B908910
04AB 0 18D0 RTE 16 MOVE Q TO A 8B908920
04AC 0 D400 0D9A STO L PRA&4 SET IN MSG 8B908930
04AE 0 C400 02DF LD L RID GET RTN NUMBER 8B908940
04B0 0 D400 0606 STO L HEXWD SET IN RTN 8B908950
04B2 0 4400 05E6 BSI L HEXCV GO CONVERT TO HEX 8B908960
04B4 0 CC00 060C LDD L HEXCD GET CONVERTED WD 8B908970
04B6 0 DC00 0D9C STD L PRA&6 SET IN MSG 8B908980
04B8 0 C400 02E0 LD L RAD GET RTN ADRS 8B908990
04BA 0 D400 0606 STO L HEXWD SET IN RTN 8B909000
04BC 0 4400 05E6 BSI L HEXCV GO CONVERT TO HEX 8B909010
04BE 0 CC00 060C LDD L HEXCD GET CONVERTED WD 8B909020
04C0 0 D400 0D9F STO L PRA&9 SET IN MSG 8B909030
04C2 0 18D0 RTE 16 MOVE Q TO A 8B909040
04C3 0 D400 0DA0 STO L PRA&10 SET IN MSG 8B909050
04C5 0 C105 LD 1 5 GET DRIVE NO 8B909060
04C6 0 D400 0606 STO L HEXWD SET IN RTN 8B909070
04C8 0 4400 05E6 BSI L HEXCV GO CONVERT 8B909080
04CA 0 CC00 060C LDD L HEXCD GET CONVERTED WD 8B909090
04CC 0 D400 0DA2 STO L PRA&12 SET IN MSG 8B909100
04CE 0 18D0 RTE 16 8B909110
04CF 0 D400 0DA3 STO L PRA&13 SET IN MSG 8B909120
04D1 0 4C00 03D6 BSC L LOG2C 8B909130
* 8B909140
*XX 8B909150
*XXXXXXXXXXXXXXXXXXXX 1443 PAGE CONTROL ROUTINE X 8B909160
*XX 8B909170
* 8B909180
RST DC 0 8B909190
LD L SWO GET SWITCHES 8B909200
SLA 9 CK FOR 1443 8B909210
BSC L RST2,- BRANCH # NOT 1443 8B909220
XIO RSTX2 SKIP TO CHN 1 8B909230
RST1 XIO RSTX3 SENSE 1443 8B909240
SRA 2 CK FOR BUSY 8B909250
BSC E SKIP # NOT BUSY 8B909260
MDX RST1 LOOP 8B909270
RST2 LD L1 EDIT&4 GET NUMBER OF TRACKS 8B909280
BSC L DR9,&- BRANCH # 9 TRACK 8B909290
LD K0700 SET 7 TRACK 8B909300
MDX DR9&1 BRANCH 8B909310
DR9 LD K0900 GET 9 TRACK 8B909320
STO L LN3B SET IN MSG 8B909330
LD L1 EDIT&6 GET DRIVE MODEL 8B909340
BSC L MD3,&- BRANCH # MODEL 3 8B909350
SLA 8 MOVE TO LHW 8B909360
MDX MD3&1 BRANCH 8B909370
LD K0300 GET MODEL 3 8B909380
STO L LN3A SET IN MSG 8B909390
LD L EDIT&8 GET MEM SPEED 8B909400
BSC L TMIC,+ BRANCH # 2 MIC 8B909410
BSC L TMICA,- BRANCH IF 4 MIC 8B909411
LD L H023B SET MSG TO 2.25 8B909412
STO L LN3C * 8B909413
LD L H0205 * 8B909414
MDX TMICB CONTINUE 8B909415
TMICA LD L H0400 GET 4 MIC 8B909420
MDX TMIC&1 BRANCH 8B909430

FL

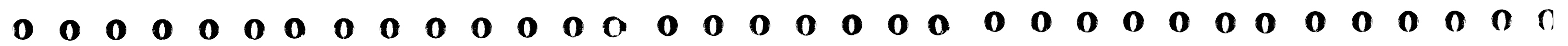
2400 TIMING TEST

2400 TIMING TEST

0500 0 C024	TMIC LD	K0200	GET 2 MIC	8B909440
0501 0 D400 OE5A	STO L	LN3C	SET IN MSG	8B909450
0503 0 C400 0529	LD L	H2020	SET DASHES	\$ 8B909451
0505 0 D400 OE5B	TMICB STO	L LN3C+1	*	\$ 8B909452
0507 0 6913	STX 1	SVE&1	SAVE IX 1	8B909460
0508 0 7100	MDX 1	0	CHECK DR NUMBER	8B909470
0509 0 7003	MDX	SYDR	DRIVE 1	8B909480
050A 0 6500 200A	LDX L1	/200A	IX # -0	8B909490
050C 0 7002	MDX	SYDR1	BRANCH	8B909500
050D 0 6500 2001	SYDR LDX	L1 /2001	IX # -1	8B909510
050F 0 6D00 OE58	SYDR1 STX	L1 LN3D	SET IN MSG	8B909520
0511 0 6700 OE4C	LDX	L3 LN3-1	IX # MSG ADRS	8B909530
0513 0 4400 OC21	BSI L	LOADV	SET MSG - PRINT	8B909540
0515 0 1010	SLA	16	CLEAR ACC	8B909550
0516 0 4400 OC19	BSI L	LOADK	SET BLANK LINE	8B909560
0518 0 4400 044C	BSI L	PCCO	PRINT BLANK	8B909570
051A 0 6500 0000	SVE LDX	L1 *-*	RESTORE IX 1	8B909580
051C 0 4C80 04D3	BSC I	RST	EXIT	8B909590
051E 0000	BSS E	0		8B909600
051E 0 0100	RSTX2 DC	/0100	IOCC - CARRIAGE SKIP	8B909610
051F 0 3400	DC	/3400	*	8B909620
0520 0 0000	RSTX3 DC	/0000	IOCC - SENSE 1443	8B909630
0521 0 3701	DC	/3701	*	8B909640
0522 0 0007	K0700 DC	/0007	CONSTANTS	8B909650
0523 0 0009	K0900 DC	/0009	*	8B909660
0524 0 0300	K0300 DC	/0300	*	8B909670
0525 0 0220	K0200 DC	/0220	*	8B909680
0526 0 0420	H0400 DC	/0420	*	8B909690
0527 0 023B	H023B DC	/023B	*	\$ 8B909691
0528 0 0205	H0205 DC	/0205	*	\$ 8B909692
0529 0 2020	H2020 DC	/2020	*	\$ 8B909693
	*			8B909700
				8B909710
				8B909720
				8B909730
				8B909740
				8B909750
				8B909760
				8B909770
052A 0 1010	LOGBC SLA	16		8B909780
052B 0 D03F	STO	LOX00	CLEAR HALF WD SW	8B909790
052C 0 6937	STX 1	LOGC7&1	SAVE IX 1	8B909800
052D 0 6A38	STX 2	LOGC8&1	SAVE IX 2	8B909810
052E 0 6B39	STX 3	LOGC9&1	SAVE IX 3	8B909820
052F 0 C06F	LD	PRSP	GET CARRIAGE RETURN	8B909830
0530 0 D400 OD92	STO L	PRA4	SET IN MSG	8B909840
0532 0 6700 OD93	LDX L3	PRA4&1	IX 3 # ADRS MSG	8B909850
0534 0 C300	LOGC1 LD	3 0	GET WD TO CONVERT	8B909860
0535 0 D036	STO	LOX02	SAVE	8B909870
0536 0 F400 02E3	EOR L	TERM		8B909880
0538 0 4818	BSC	&-	IS IT A TERM	8B909890
0539 0 7027	MDX	LOGCC		8B909900
053A 0 C031	LOGC2 LD	LOX02	GET WD TO CONVERT	8B909910
053B 0 4C18 0556	BSC L	LOGCA,&-	BRANCH IF ZERO	8B909920
053D 0 180C	SRA	12	SAVE ZONE	8B909930
053E 0 D001	STO	LOGC3&1		8B909940
053F 0 6500 0000	LOGC3 LDX	L1 0	IX 1 # ZONE	8B909950
0541 0 C500 056E	LD L1	LOX04	GET ADRS OF ZONE	8B909960
0543 0 D007	STO	LOGC5&1	SAVE	8B909970
0544 0 C027	LD	LOX02	GET WD TO CONVERT	8B909980
0545 0 1004	SLA	4	SAVE POSITION	8B909990
0546 0 180C	SRA	12		8B910000
0547 0 D001	STO	LOGC4&1		8B910010
0548 0 6600 0000	LOGC4 LDX	L2 0	IX 2 # POSITION	8B910020
054A 0 C600 0000	LOGC5 LD	L2 0	GET TYPEWRITER CODE	8B910030
054C 0 7400 056B	MDX L	LOX00,0	IS THIS FIRST HALF	8B910040
054E 0 700B	MDX	LOGC6	NO	8B910050
054F 0 D01D	STO	LOX03	YES	8B910060
0550 0 7401 056B	MDX L	LOX00,1	SET TO SECOND HALF	8B910070

*XXX
 *XXXXXXXXXXXXXXXXXXXX ROUTINE TO CONVERT XXXXXXXX
 *XXXXXXXXXXXXXXXXXXXX PRINTER CODE XXXXXXXX
 *XXXXXXXXXXXXXXXXXXXX TO TYPEWRITER CODE XXXXXXXX
 *XX

0552 0 C019	LD	LOX02	GET WD TO CONVERT	8B910070
0553 0 1008	SLA	8	SET TO SECOND HALF	8B910080
0554 0 D017	STO	LOX02	SAVE	8B910090
0555 0 70E4	MDX	LOGC2	GO CONVERT	8B910100
0556 0 D0F2	LOGCA STO	LOGC4&1		8B910110
0557 0 C01A	LD	LOX04&4	GET ADRS	8B910120
0558 0 D0F2	STO	LOGC5&1	SET	8B910130
0559 0 70EE	MDX	LOGC4	GO SET BLANK	8B910140
	*			8B910150
	*		SECOND HALF WORD	8B910160
	*			8B910170
055A 0 1808	LOGC6 SRA	8	MOVE TO SECOND HALF	8B910180
055B 0 F011	EOR	LOX03	COMBINE WITH FIRST	8B910190
055C 0 D300	LOGCB STO	3 0	SET IN MSG	8B910200
055D 0 1010	SLA	16		8B910210
055E 0 D00C	STO	LOX00	SET TO FIRST HALF	8B910220
055F 0 7301	MDX	3 1	IX 3 # NEXT WD	8B910230
0560 0 70D3	MDX	LOGC1	CONVERT NEXT WD	8B910240
	*			8B910250
	*		FOUND A TERMINATOR	8B910260
	*			8B910270
0561 0 4400 061E	LOGCC BSI	L LOG	GO PRINT	8B910280
0563 0 6500 0000	LOGC7 LDX	L1 0	RESTORE IX 1	8B910290
0565 0 6600 0000	LOGC8 LDX	L2 0	RESTORE IX 2	8B910300
0567 0 6700 0000	LOGC9 LDX	L3 0	RESTORE IX 3	8B910310
0569 0 4C00 0444	BSC L	LOG7C	GO EXIT	8B910320
	*			8B910330
	*		CONSTANTS	8B910340
	*			8B910350
056B 0 0000	LOX00 DC	0	HALF WORD SWITCH	8B910360
056C 0 0000	LOX02 DC	0	TEMP STORAGE FOR	8B910370
	*		WORD TO CONVERT	8B910380
056D 0 0000	LOX03 DC	0	TEMP STORAGE FOR	8B910390
	*		TYPEWRITER CODE	8B910400
056E 0 0573	LOGC4 DC	PRO0	ADRS OF ZONE 0	8B910410
056F 0 057C	DC	PRO1-2	ADRS OF ZONE 1	8B910420
0570 0 0587	DC	PRO2	ADRS OF ZONE 2	8B910430
0571 0 0593	DC	PRO3-1	ADRS OF ZONE 3	8B910440
0572 0 0586	DC	PRO2-1	ADRS OF BLANK	8B910450
	*			8B910460
	*		PRINTER CODE TO TYPEWRITER	8B910470
	*		CODE CONVERSION TABLE	8B910480
	*			8B910490
0573 0 2100	PRO0 DC	/2100	0	8B910500
0574 0 FC00	DC	/FC00	1	8B910510
0575 0 D800	DC	/D800	2	8B910520
0576 0 DC00	DC	/DC00	3	8B910530
0577 0 F000	DC	/F000	4	8B910540
0578 0 F400	DC	/F400	5	8B910550
0579 0 D000	DC	/D000	6	8B910560
057A 0 D400	DC	/D400	7	8B910570
057B 0 E400	DC	/E400	8	8B910580
057C 0 E000	DC	/E000	9	8B910590
057D 0 C400	DC	/C400	0	8B910600
057E 0 9A00	PRO1 DC	/9A00	S	8B910610
057F 0 9E00	DC	/9E00	T	8B910620
0580 0 B200	DC	/B200	U	8B910630
0581 0 B600	DC	/B600	V	8B910640
0582 0 9200	DC	/9200	W	8B910650
0583 0 9600	DC	/9600	X	8B910660
0584 0 A600	DC	/A600	Y	8B910670
0585 0 A200	DC	/A200	Z	8B910680
0586 0 2100	DC	/2100	BLANK	8B910690
0587 0 8400	PRO2 DC	/8400	-	8B910700
0588 0 7E00	DC	/7E00	J	8B910710
0589 0 5A00	DC	/5A00	K	8B910720
058A 0 5E00	DC	/5E00	L	8B910730
058B 0 7200	DC	/7200	M	8B910740



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058C 0 7600 DC /7600 N 88910750
058D 0 5200 DC /5200 O 88910760
058E 0 5600 DC /5600 P 88910770
058F 0 6600 DC /6600 Q 88910780
0590 0 6200 DC /6200 R 88910790
0591 0 4200 DC /4200 88910800
0592 0 4000 DC /4000 \$ 88910810
0593 0 D600 DC /D600 * 88910820
0594 0 3E00 PR03 DC /3E00 A 88910830
0595 0 1A00 DC /1A00 B 88910840
0596 0 1E00 DC /1E00 C 88910850
0597 0 3200 DC /3200 D 88910860
0598 0 3600 DC /3600 E 88910870
0599 0 1200 DC /1200 F 88910880
059A 0 1600 DC /1600 G 88910890
059B 0 2600 DC /2600 H 88910900
059C 0 2200 DC /2200 I 88910910
059D 0 Q200 DC /Q200 88910920
059E 0 0000 DC /0000 * 88910930
059F 0 8121 PRSP DC /8121 CARRIAGE RETURN 88910940
* 88910950
* CONSTANTS 88910960
* 88910970
* ADRS STRING FOR DCC CALL 88910980
* 88910990
* LOGX3 DC EDIT&1 ADRS OF AREA CODE 88911000
05A0 0 Q2E5 LOGX3 DC EDIT&1 ADRS OF AREA CODE 88911010
05A1 0 05A4 DC LOGX8 ADRS OF FUNCTION 88911020
05A2 0 05A5 DC LOGX9 ADRS OF MODIFIER 88911030
05A3 0 0D92 DC PRA4 ADRS OF MSG 88911040
* 88911050
* FUNCTION AND MODIFIER 88911060
* 88911070
05A4 0000 BSS E 0 88911080
05A4 0 0500 LOGX8 DC /0500 FUNCTION 88911090
05A5 0 0000 LOGX9 DC /0000 MODIFIER 88911100
* 88911110
* MASK CONSTANTS 88911120
* 88911130
* 88911140
*XXX 88911150
*XXXXXXXXXXXXXXXXXXXXX HEX TO DECIMAL CONVERSION X 88911160
*XXXXXXXXXXXXXXXXXXXXX ROUTINE X 88911170
*XXX 88911180
* 88911190
05A6 0 0000 HEDEC DC 0 SE 88911200
05A7 0 6B21 STX 3 HEDE4&1 SAVE IX 3 88911210
05A8 0 6A22 STX 2 HEDE5&1 SAVE IX 2 88911220
05A9 0 6923 STX 1 HEDE6&1 SAVE IX 1 88911230
05AA 0 6500 05E1 LDX L1 OPARA OUTPUT AREA INDEX 88911240
05AC 0 6600 05DC LDX L2 CVTBL CONVERSION TABLE IX 88911250
* 88911260
05AE 0 C036 LD WDCON SET WORD TO CONVERT 88911270
05AF 0 D028 STO WORD IN WORK AREA 88911280
* 88911290
05B0 0 6700 060E HEDE1 LDX L3 CODEH CODE TABLE INDEX 88911300
* 88911310
05B2 0 C200 LD 2 0 SET CONVERSION 88911320
05B3 0 D025 STO CONVO CONSTANT IN SW AREA 88911330
* 88911340
05B4 0 C023 HEDE2 LD WORD CHECK WORD AGAINST 88911350
05B5 0 9023 S CONVO CONVERSION CONSTANT 88911360
05B6 0 4C28 05BF BSC L HEDE3,&Z BRANCH IF MINUS 88911370
* 88911380
05B8 0 8020 A CONVO RESTORE NUMBER 88911390
05B9 0 D01E STO WORD 88911400
* 88911410
05BA 0 C01E LD CONVO SET UP FOR NEXT 88911420

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05BB 0 8200 A 2 0 CHECK 88911430
05BC 0 D01C STO CONVO 88911440
* 88911450
05BD 0 7301 MDX 3 1 CODE TABLE INDEX & 1 88911460
05BE 0 70F5 MDX HEDE2 88911470
* 88911480
* NEGATIVE RESULT 88911490
* 88911500
05BF 0 8200 HEDE3 A 2 0 RESTORE LAST NUMBER 88911510
05C0 0 D017 STO WORD 88911520
* 88911530
05C1 0 C300 LD 3 0 SET 1443 CODE IN 88911540
05C2 0 D100 STO 1 0 OUTPUT AREA 88911550
* 88911560
05C3 0 7101 MDX 1 1 OUTPUT AREA INDEX &1 88911570
05C4 0 7201 MDX 2 1 CONVERSION TBL IX &1 88911580
* 88911590
05C5 0 C200 LD 2 0 88911600
05C6 0 4C20 05B0 BSC L HEDE1,Z 88911610
* 88911620
05C8 0 6700 0000 HEDE4 LDX L3 0 RESTORE INDEX REG 3 88911630
05CA 0 6600 0000 HEDE5 LDX L2 0 RESTORE INDEX REG 2 88911640
05CC 0 6500 0000 HEDE6 LDX L1 0 RESTORE INDEX REG 1 88911650
* 88911660
05CE 0 C012 LD OPARA GET 1ST CODE AND 88911670
05CF 0 1008 SLA 8 PACK WITH 2ND 88911680
05D0 0 E811 OR OPARA&1 88911690
05D1 0 D008 STO CODE 88911700
05D2 0 C010 LD OPARA&2 GET 3RD CODE AND 88911710
05D3 0 1008 SLA 8 PACK WITH 4TH 88911720
05D4 0 E80F OR OPARA&3 88911730
05D5 0 D005 STO CODE&1 88911740
* 88911750
05D6 0 4C80 05A6 BSC I HEDEC RETURN TO USER SX 88911760
* 88911770
* CONVERSION CONSTANTS 88911780
* 88911790
WORD DC 0 WORK AREA 88911800
CONVO DC 0 WORK AREA 88911810
* 88911820
BSS E 0 88911830
* 88911840
05DA 0 0000 CODE DC 0 PACKED WORDS 1 AND 2 88911850
05DB 0 0000 DC 0 PACKED WORDS 3 AND 4 88911860
* 88911870
05DC 0 03E8 CVTBL DC /03E8 1000 88911880
05DD 0 0064 DC /0064 100 88911890
05DE 0 000A DC /000A 10 88911900
05DF 0 0001 DC /0001 1 88911910
05E0 0 0000 DC /0000 0 88911920
* 88911930
05E1 0 0000 OPARA DC 0 OUTPUT WORK AREA 88911940
05E2 0 0000 DC 0 88911950
05E3 0 0000 DC 0 88911960
05E4 0 0000 DC 0 88911970
* 88911980
05E5 0 0000 WDCON DC 0 STORAGE/WD TO CONVRT 88911990
* 88912000
*XXX 88912010
*XXXXXXXXXXXXXXXXXXXXX HEX TO 1443 HEX XXXXXXXX 88912020
*XXXXXXXXXXXXXXXXXXXXX CONVERSION ROUTINE XXXXXXXX 88912030
*XXX 88912040
* 88912050
05E6 0 0000 HEXCV DC 0 SE 88912060
05E7 0 6B1B STX 3 HEXC3&1 SAVE IX 3 88912070
05E8 0 6A18 STX 2 HEXC2&1 SAVE IX 2 88912080
05E9 0 6204 LDX 2 4 CONVERSION INDEX 88912090
* 88912100
05EA 0 C01B LD HEXWD GET WORD TO CONVERT

2400 TIMING TEST

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05EB 0 1890          SRT      16      SET A IN 0          8B912110
05EC 0 1010          SLA      16                      8B912120
05ED 0 1084          HEXC1  SLT      4      GET CHARACTER      8B912130
05EE 0 D001          STO      HEXC1&3  8B912140
05EF 0 6700 0000    LDX      L3 0      SET CODE TABLE INDEX 8B912150
*                                     8B912160
05F1 0 C700 060E    LD       L3 CODEH  GET CODED CHARACTER  8B912170
05F3 0 D600 0606    STO      L2 HEX00-1 AND SAVE          8B912180
05F5 0 1010          SLA      16                      8B912190
*                                     8B912200
05F6 0 72FF          MDX      2 -1     CHECK IF DONE      8B912210
05F7 0 70F5          MDX                      8B912220
*                                     8B912230
05F8 0 C011          LD       HEX00&3  PACK CODED WORDS  8B912240
05F9 0 1008          SLA      8                      8B912250
05FA 0 E80E          OR       HEX00&2  8B912260
05FB 0 D010          STO      HEXCD      8B912270
05FC 0 C00B          LD       HEX00&1  8B912280
05FD 0 1008          SLA      8                      8B912290
05FE 0 E808          OR       HEX00      8B912300
05FF 0 D00D          STO      HEXCD&1  8B912310
0600 0 6600 0000    HEXC2  LDX      L2 0  RESTORE IX 2      8B912320
0602 0 6700 0000    HEXC3  LDX      L3 0  RESTORE IX 3      8B912330
0604 0 4C80 05E6    BSC      I  HEXCV  RETURN TO USER      SX  8B912340
*                                     8B912350
*                                     8B912360
*                                     8B912370
*                                     8B912380
0606 0 0000          HEXWD  DC        0      WORD TO CONVERT    8B912390
0607 0 0000          HEX00  DC        0      *                  8B912390
0608 0 0000          DC        0      * UNPACKED CODED  8B912400
0609 0 0000          DC        0      * WORD            8B912410
060A 0 0000          DC        0      *                  8B912420
*                                     8B912430
*                                     8B912440
060C 0 0000          BSS     E 0          8B912450
*                                     8B912460
060C 0 0000          HEXCD  DC        0      * PACKED CODED WORD 8B912470
060D 0 0000          DC        0      *                  8B912480
*                                     8B912490
*                                     8B912500
*                                     8B912510
*                                     8B912520
*                                     8B912530
060E 0 000A          CODEH  DC        /000A 0  8B912540
060F 0 0001          DC        /0001 1  8B912550
0610 0 0002          DC        /0002 2  8B912560
0611 0 0003          DC        /0003 3  8B912570
0612 0 0004          DC        /0004 4  8B912580
0613 0 0005          DC        /0005 5  8B912590
0614 0 0006          DC        /0006 6  8B912600
0615 0 0007          DC        /0007 7  8B912610
0616 0 0008          DC        /0008 8  8B912620
0617 0 0009          DC        /0009 9  8B912630
0618 0 0031          DC        /0031 A  8B912640
0619 0 0032          DC        /0032 B  8B912650
061A 0 0033          DC        /0033 C  8B912660
061B 0 0034          DC        /0034 D  8B912670
061C 0 0035          DC        /0035 E  8B912680
061D 0 0036          DC        /0036 F  8B912690
*                                     8B912700
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX*
*
061E 0 0000          LOG     DC        0          SE 8B912710
*                                     8B912720
061F 0 0C00 043E    XIO     L  MK15    MASK ALL LVLS    8B912730
0621 0 0C00 0440    XIO     L  MK27    8B912740
*                                     8B912750
0623 0 0838          XIO     SENSE     SENSE FOR READY  8B912760
0624 0 180A          SRA      10          8B912770
*                                     8B912780

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0625 0 4804          BSC      E          8B912790
*                                     8B912800
0626 0 3004          WAIT4  WAIT      4      TYPEWRTR NOT RDY  8B912810
*                                     8B912820
0627 0 1010          SLA      16          8B912830
0628 0 D036          STO      WRDSW     CLEAR 1/2 WD SWITCH 8B912840
*                                     8B912850
0629 0 C036          LD       ADRS      GET MESSAGE ADDRESS 8B912860
062A 0 D010          STO      LOG01&1  8B912870
062B 0 D001          STO      LOG04&1  8B912880
062C 0 C400 0D92    LOG04  LD       L  PRA4      GET MSG WORD      8B912890
062E 0 F400 02E3    EOR     L  TERM     CHECK FOR TERM    8B912900
0630 0 4C18 0654    BSC     L  LOG02,&-  BRANCH IF TERM    8B912910
0632 0 C480 062D    LD       I  LOG04&1  GET MSG WORD      8B912920
0634 0 F02C          EOR     K2121      CK FOR BLANK      8B912930
0635 0 4C20 063A    BSC     L  LOG01,2  BRANCH IF NOT BLANK 8B912940
0637 0 7401 062D    LOG05  MDX     L  LOG04&1,1 INCR SCAN ADRS    8B912950
0639 0 70F2          MDX     LOG04      LOOP              8B912960
063A 0 C400 0D92    LOG01  LD       L  PRA4      GET WORD TO PRINT 8B912970
063C 0 D021          STO     IOARA      SET IN OUTPUT AREA 8B912980
*                                     8B912990
*                                     8B913000
*                                     8B913010
*                                     8B913020
*                                     8B913030
063D 0 081C          XIOWR  XIO      WRITE      WRITE CHARACTER  8B913040
*                                     8B913050
063E 0 081D          XIOSN  XIO      SENSE      CHECK BUSY      8B913060
*                                     8B913070
063F 0 180B          SRA      11          8B913070
0640 0 4804          BSC      E          8B913080
0641 0 70FC          MDX     XIOSN     BUSY      8B913090
*                                     8B913100
*                                     8B913110
*                                     8B913120
*                                     8B913130
*                                     8B913140
*                                     8B913150
0642 0 C01C          LD       WRDSW     GET 1/2 WORD SWITCH 8B913160
0643 0 4804          BSC      E          8B913170
0644 0 7006          MDX     LOG03     GO SETUP FOR NEXT WD 8B913180
*                                     8B913190
*                                     8B913200
*                                     8B913210
*                                     8B913220
0645 0 C018          LD       IOARA     GET WORD IN IO AREA  8B913230
0646 0 1008          SLA      8          POSITION 2ND 1/2 WD  8B913240
0647 0 D016          STO      IOARA     8B913250
0648 0 7401 065F    MDX     L  WRDSW,1  BUMP WORD SWITCH  8B913260
064A 0 70F2          MDX     XIOWR     GO WRITE 2ND 1/2 WD 8B913270
*                                     8B913280
*                                     8B913290
*                                     8B913300
*                                     8B913310
*                                     8B913320
*                                     8B913330
064B 0 7401 063B    LOG03  MDX     L  LOG01&1,1 INCR ADRS TO PRINT 8B913340
064D 0 7401 065F    MDX     L  WRDSW,1  INCR WORD SW      8B913350
064F 0 C0DD          LD       LOG04&1  GET SCAN ADRS     8B913360
0650 0 90EA          S        LOG01&1  SUB PRINT ADRS     8B913370
0651 0 4C10 063A    BSC     L  LOG01,-  BRANCH # MORE TO PRT 8B913380
0653 0 70E3          MDX     LOG05     BRANCH TO SCAN     8B913390
*                                     8B913400
*                                     8B913410
*                                     8B913420
0654 0 0C00 0302    LOG02  XIO     L  UNMK3    UNMASK ALL INTERRUPT 8B913430
0656 0 0C00 0304    XIO     L  UNMK4    LEVELS           8B913440
*                                     8B913450
0658 0 4C80 061E    BSC     I  LOG      EXIT              SX 8B913460
*                                     8B913470
*                                     8B913480
*                                     8B913490
*                                     8B913500

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065A 0000      BSS E 0
065A 0 065E    WRITE DC IOARA      WRITE IOCC
065B 0 0902    DC /0902
065C 0 0000    SENSE DC /0000      SENSE IOCC
065D 0 0F03    DC /0F03
065E 0 0000    IOARA DC 0      OUTPUT AREA
065F 0 0000    WRDSW DC 0      1/2 WORD SWITCH
0660 0 0D92    ADRS DC PRA4      MESSAGE ADDRESS
0661 0 2121    K2121 DC /2121
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0662 0 0000    END DC 0      SE
0663 0 C400 02E1 LD L SWO      GET SW FNC 0
0665 0 100B    SLA 11
0666 0 4410 03B2 BSI L HALT,-   BRANCH # NOT LOOP
0668 0 4C00 02EF BSC L MONT     LOOP PROGRAM
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
066A 0000      BSS E 0
066A 0 0021    CONV DC 33      2 MIC MEM CON MULT
066B 0 0042    CONV1 DC 66     4 MIC MEM CON MULT
066C 0 002C    SPEC DC /002C   SPACE
066D 0 0308    CON DC 776     MIN RD TIME AT LD PT
066E 0 0712    DC 1810      MAX RD TIME AT LD PT
066F 0 0308    DC 776     MIN WT TIME AT LD PT
0670 0 048B    DC 1163     MAX WT TIME AT LD PT
0671 0 0080    CON1 DC 128   MIN RD TIME NOT LD
0672 0 012F    DC 303     MAX RD TIME NOT LD
0673 0 0080    DC 128     MIN WT TIME NOT LD
0674 0 00C3    DC 195     MAX WT TIME NOT LD
0675 0 00F8    MT5XA DC 248  UPPER GR LIMIT-9TR
0676 0 0131    DC 305     UPPER GR LIMIT-7TR
0677 0 00F5    DC 245     LOWER GR LIMIT-9TR
0678 0 012E    DC 302     LOWER GR LIMIT-7TR
0679 0 01E0    MT1XO DC 480   BCKWRD TO FORWRD TME
067A 0 0003    MT5XO DC 3     DELAY # 5 SEC
067B 0 0D40    DC 3392
067C 0 0002    DC 2
067D 0 7100    DC 28928
067E 0 0001    DC 1
067F 0 D4C0    DC 54464
0680 0 0001    DC 1
0681 0 3880    DC 14464
0682 0 0000    DC 0
0683 0 9C40    DC 40000
0684 0 0000    DC 0      500 MILLISEC
0685 0 4E20    DC 20000
0686 0 0000    DC 0
0687 0 3E80    DC 16000
0688 0 0000    DC 0
0689 0 2EE0    DC 12000
068A 0 0000    DC 0
068B 0 1F40    DC 8000
068C 0 0000    DC 0
068D 0 0FA0    DC 4000
068E 0 0000    DC 0      80.00
068F 0 0C80    DC 3200
0690 0 0000    DC 0      60.00
0691 0 0960    DC 2400
0692 0 0000    DC 0      40.00
0693 0 0640    DC 1600
0694 0 0000    DC 0      35.00
0695 0 0578    DC 1400

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8B913470
8B913480
8B913490
8B913500
8B913510
8B913520
8B913530
8B913540
8B913550
8B913560
8B913570
8B913580
8B913590
8B913600
8B913610
8B913620
8B913630
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8B913650
8B913660
8B913670
8B913680
8B913690
8B913700
8B913710
8B913720
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8B914010
8B914020
8B914030
8B914040
8B914050
8B914060
8B914070
8B914080
8B914090
8B914100
8B914110
8B914120
8B914130
8B914140

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2400 TIMING TEST

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0696 0 0000    DC 0      30.00      8B914150
0697 0 0480    DC 1200
0698 0 0000    DC 0      25.00      8B914160
0699 0 03E8    DC 1000      8B914170
069A 0 0000    DC 0      22.50      8B914180
069B 0 0384    DC 900      8B914190
069C 0 0000    DC 0      20.00      8B914200
069D 0 0320    DC 800      8B914210
069E 0 0000    DC 0      17.50      8B914220
069F 0 02BC    DC 700      8B914230
06A0 0 0000    DC 0      15.00      8B914240
06A1 0 0258    DC 600      8B914250
06A2 0 0000    DC 0      12.50      8B914260
06A3 0 01F4    DC 500      8B914270
06A4 0 0000    DC 0      10.00      8B914280
06A5 0 0190    DC 400      8B914290
06A6 0 0000    DC 0      09.00      8B914300
06A7 0 0168    DC 360      8B914310
06A8 0 0000    DC 0      08.00      8B914320
06A9 0 0140    DC 320      8B914330
06AA 0 0000    DC 0      07.00      8B914340
06AB 0 0118    DC 280      8B914350
06AC 0 0000    DC 0      06.00      8B914360
06AD 0 00F0    DC 240      8B914370
06AE 0 0000    DC 0      05.50      8B914380
06AF 0 00DC    DC 220      8B914390
06B0 0 0000    DC 0      05.00      8B914400
06B1 0 00C8    DC 200      8B914410
06B2 0 0000    DC 0      04.50      8B914420
06B3 0 00B4    DC 180      8B914430
06B4 0 0000    DC 0      04.00      8B914440
06B5 0 00A0    DC 160      8B914450
06B6 0 0000    DC 0      03.50      8B914460
06B7 0 008C    DC 140      8B914470
06B8 0 0000    DC 0      03.00      8B914480
06B9 0 0078    DC 120      8B914490
06BA 0 0000    DC 0      02.75      8B914500
06BB 0 006E    DC 110      8B914510
06BC 0 0000    DC 0      02.50      8B914520
06BD 0 0064    DC 100      8B914530
06BE 0 0000    DC 0      02.25      8B914540
06BF 0 005A    DC 90      8B914550
06C0 0 0000    DC 0      02.00      8B914560
06C1 0 0050    DC 80      8B914570
06C2 0 0000    DC 0      01.75      8B914580
06C3 0 0046    DC 70      8B914590
06C4 0 0000    DC 0      01.50      8B914600
06C5 0 003C    DC 60      8B914610
06C6 0 0000    DC 0      01.25      8B914620
06C7 0 0032    DC 50      8B914630
06C8 0 0000    DC 0      8B914640
06C9 0 002C    DC 44      01.10      8B914650
06CA 0 0000    DC 0      8B914660
06CB 0 0028    DC 40      01.00      8B914670
06CC 0 0000    DC 0      8B914680
06CD 0 0024    DC 36      8B914690
06CE 0 0000    DC 0      8B914700
06CF 0 0020    DC 32      8B914710
06D0 0 0000    DC 0      8B914720
06D1 0 001C    DC 28      00.70      8B914730
06D2 0 0000    DC 0      8B914740
06D3 0 0018    DC 24      00.60      8B914750
06D4 0 0000    DC 0      8B914760
06D5 0 0016    DC 22      8B914770
06D6 0 0000    DC 0      00.55      8B914780
06D7 0 0014    DC 20      8B914790
06D8 0 03C0    DC 960      8B914800
06D9 0 0780    DC 1920      8B914810

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2400 TIMING TEST

06DA 0 4D92 MD1LM DC 19858 MODEL 1 RD LIMIT 88914830
06DB 0 0003 GPHLM DC 3 LINE LIMIT MODIFIER 88914840
06DC 0 0000 MLGX7 DC 0 CONVERT MULTIPLIER 88914850
06DD 0 0025 MOD1S DC 37 MODEL 1 TAPE SPEED 88914860
06DE 0 0048 MOD2S DC 75 MODEL 2 TAPE SPEED 88914870
06DF 0 0070 INPSE DC 112 MODEL 3 TAPE SPEED 88914880
XXX*
XXX*
XXX*
DST DC 0 0 MAX TIME FOR WRT 88914900
DC 0 1 MIN TIME FOR WRT 88914910
DC 0 2 MAX TIME FOR RD 88914920
DC 0 3 MIN TIME FOR RD 88914930
DC 0 4 ACTUAL WRT TIME 88914940
DC 0 5 ACTUAL RD TIME 88914950
DC 0 6 WD CT 88914960
DC 0 7 LAST DSW 88914970
DC 0 8 AREA CODE 88914980
DC 0 9 FUNCTION 88914990
DC 0 10 MODIFIER 88915000
DC DST&8 11 ADRS OF AREA COD 88915010
DC DST&9 12 ADRS OF FUNCTION 88915020
DC DST&10 13 ADRS OF MODIFIER 88915030
DC IOA 14 ADRS OF I/O AREA 88915040
DC 0 15 NUMBER OF TRACKS 88915050
XXX*
XXX*
XXX*
CHECK FOR DRIVE READY
MON4 LDX 1 0 SET IXING FOR DR 0 88915100
LDX L2 DST 88915110
LD L SWO GET CONTROL SWS 88915120
BSC &Z IS DR 0 TO BE RUN 88915130
MDX MON25 NO 88915140
BSI L DSWO SENSE DRIVE SRC 88915150
BSC E IS DRIVE READY 88915160
MDX MON11 NO 88915170
BSI L BEGIN SET CONSTANTS SRC 88915180
SET DST0 TO DRIVE 0 88915190
BSI L RST RESTORE PRINTER 88915200
SLA 16 88915210
STO L RID ZERO RTN NO 88915220
LD L EDIT&4 GET NO TRACKS 88915230
STO 2 15 SET IN DST 88915240
LD L EDIT GET AREA CODE 88915250
STO 2 8 SET IN DST 88915260
EOR L1 MONX0 SET DR SELECTION 88915270
STO L ACTI SET 88915280
XXX*
XXX*
XXX*
CHECK PROG HALT SW-BIT 15
MON25 LD L SWO GET SW FNC 0 88915290
BSC E IS PROG HALT SW ON 88915300
MDX MON23 YES 88915310
MDX MON22 NO 88915320
MON25 LD L PGSW GET PROG SW 88915330
BSC Z IS PROGRAM COMPLETE 88915340
MDX MON24 YES 88915350
MDX MONTC NO 88915360
0711 0 C400 0938 88915370
0713 0 4820 88915380
0714 0 7070 88915390
0715 0 7027 88915400

2400 TIMING TEST

0716 0 4400 03B2 MON23 BSI L HALT 88915510
XXX*
XXX*
XXX*
CHECK FOR ROUTINE SELECTED IN BIT SWITCHES
MON22 LD L SW1 GET SW FNC 1 88915520
BSC Z IS A RTN SELECTED 88915530
MDX MONTF YES 88915540
MONT6 LD L RID GET RTN NO 88915550
MON10 A L MTTX1 ADD 1 88915560
MONTF STO MONT7&1 SAVE 88915570
STO L RID GO REWIND SRC 88915580
BSI L RWD SET UP I/O AREA 88915590
LDX 3 10 88915600
LD L MTTX6 88915610
MONT8 STO L3 IOA 88915620
MDX 3 -1 88915630
MONT7 LDX L3 0 IX 3 # RTN NUMBER 88915640
LD L3 MONT9 GET ROUTINE ADRS 88915650
STO L RAD SAVE FOR PRINT 88915660
BSC I3 MONT9 TRANSFER TO RTN 88915670
XXX*
XXX*
XXX*
TABLE OF ROUTINE ADDRESSES
MON9 DC MONR1 ERROR 88915680
DC MTT01 ROUTINE NUMBER 1 88915690
DC MTT02 2 88915700
DC MTT03 3 88915710
DC MTT04 4 88915720
DC MTT05 5 88915730
DC MTT07 6 88915740
DC MONR1 PROGRAM COMPLETE 88915750
PGCM DC PGCM-MONT9-1 NO OF ROUTINES 88915760
XXX*
XXX*
XXX*
SET UP TO CK DR 1
MONTC LDX 1 1 SET IXING FOR DR 1 88915770
LDX L2 DST 88915780
LD L SWO GET CONTROL SWS 88915790
SLA 1 88915800
BSC &Z IS DR 1 TO BE RUN 88915810
MDX MON25 NO 88915820
LD L EDIT&5 GET NO TRKS-DR 1 88915830
BSC L MON25,&Z BRANCH # NOT AVAIL 88915840
BSI L DSWO GO SENSE DR SRC 88915850
BSC E IS DRIVE READY 88915860
MDX MONTE NO 88915870
BSI L BEGIN SET CONSTANTS SRC 88915880
SET DST0 TO DRIVE 1 88915890
BSI L RST RESTORE PRINTER 88915900
LD L EDIT&5 GET NO TRACKS 88915910
STO 2 15 SET IN DST 88915920
LD L EDIT GET AREA CODE 88915930
EOR L1 MONX0 SET DR SEL 88915940
STO L ACTI 88915950
SLA 16 88915960
STO L RID SAVE 88915970
MDX MONTD MONTD GO TEST DR 1 88915980
MONXO DC 0 DR 0 SELECTION 88915990
DC /0020 DR 1 SELECTION 88916000
XXX*
XXX*
XXX*



2400 TIMING TEST

```

*
* DRIVE 1 IS NOT READY
0761 0 4400 083C MONTE BSI L MLG DR 1 NOT READY 88916190
0763 0 0F1D DC MSG5&4 88916200
0764 0 0E4B DC NOTE1&26 88916210
0765 0 C001 COO1 DC /C001 ID C1 88916220
0766 0 0000 DC /0000 LINE 0-FORM 0 88916230
0767 0 7401 093B MDX L PGSW,1 88916240
0769 0 70A7 MDX MON25 88916250
*
* DRIVE 0 IS NOT READY
076A 0 4400 083C MON11 BSI L MLG DR 0 NOT READY 88916260
076C 0 0F1D DC MSG5&4 88916270
076D 0 0E4B DC NOTE1&26 88916280
076E 0 C000 COO0 DC /C000 ID C0 88916290
076F 0 0000 DC /0000 LINE 0-FORM 0 88916300
0770 0 70A0 MDX MON25 88916310
*
* RETURN FROM ROUTINES
* CHECK FOR ALL RTNS RUN
0771 0 4400 0306 MONR1 BSI L RDSWS READ SWS 88916320
0773 0 C400 02DF LD L RID GET RTN NO 88916330
0775 0 90C6 S PGCM SUB TOTAL RTNS 88916340
0776 0 4820 BSC Z ARE ALL RTNS RUN 88916350
0777 0 7094 MDX MONTD NO 88916360
*
* ALL ROUTINES COMPLETE-CK
* FOR PROGRAM COMPLETE
0778 0 4400 07F0 BSI L RWD REWIND DRIVE 88916370
077A 0 7401 093B MDX L PGSW,1 88916380
077C 0 4400 083C BSI L MLG ALL RTNS RUN 88916390
077E 0 0F21 DC MSG6&4 88916400
077F 0 0F49 DC MSG15&8 88916410
0780 0 A000 A000 DC /A000 ID A0 88916420
0781 0 0000 DC /0000 LINE 0-FORM 0 88916430
0782 0 7100 MDX 1 0 WAS RUN ON DRIVE 0 88916440
0783 0 708D MDX MON25 88916450
0784 0 7088 MDX MONTC YES 88916460
*
* PROGRAM IS COMPLETE
0785 0 4400 083C MON24 BSI L MLG PROGRAM COMPLETE 88916470
0787 0 0F21 DC MSG6&4 88916480
0788 0 0F51 DC MSG16&8 88916490
0789 0 A001 A001 DC /A001 ID A1 88916500
078A 0 0000 DC /0000 LINE 0-FORM 0 88916510
*
* *****
* BSI L END TERMINATE *
* *****
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
078D 0 0000 TMWRT DC 0 SE
078E 0 C030 LD TWRX3 MODIFY TIME RTN/WRT
078F 0 D00F STO TMWR9
0790 0 C500 07BD TMRD1 LD L1 TWRX1 GET SENSE WD CTR
0792 0 F400 02E4 EOR L EDIT SET AREA CODE
0794 0 D026 STO SNWC1 SET IN IOCC
0795 0 6B10 STX 3 TMWR0&1 SAVE IX 3
0796 0 1010 SLA 16

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2400 TIMING TEST

```

0797 0 D024 STO TWRX0 CLEAR COUNT 88916870
0798 0 4400 0373 BSI L DCC GO WRITE * 88916880
079A 0 06EB TMWR1 DC DST&11 ADR OF STRING * 88916890
*
*
* TIME A DOUBLE CHANGE IN
* THE WORD COUNTER
079B 0 0C00 07BA TMWR2 XIO L SNWC SENSE WD CTR 88916900
079D 0 8400 07C7 A L TMRX2 ADD DESIRED 88916910
079F 0 4C30 07A5 TMWR9 BSC L TMWR0,-Z HAS CTR CHANGED 88916920
07A1 0 7401 07BC TMWR3 MDX L TWRX0,1 NO-STEP COUNT 88916930
07A3 0 4C00 079B BSC L TMWR2 LOOP 88916940
07A5 0 6700 0000 TMWR0 LDX L3 0 RESTURE IX 3 88916950
07A7 0 3005 WAIT5 WAIT 5 WAIT FOR RD OR WRT 88916960
07A8 0 4C00 082A BSC L INTR 88916970
*
* INTERRUPT RETURN
07AA 0 C207 TMWR4 LD 2 7 GET LAST DSW 88916980
07AB 0 E400 092B AND L MTTY8 CK FOR CORRECT 88916990
07AD 0 4820 BSC Z IS IT CORRECT 88917000
07AE 0 7002 MDX TMWR6 NO 88917010
07AF 0 4C80 078D TMWR8 BSC I TMWRT RETURN SX 88917020
*
* NOT CORRECT-ABORT THE TEST
07B1 0 4400 083C TMWR6 BSI L MLG TEST ABORTED 88917030
07B3 0 0F41 DC MSG14&4 88917040
07B4 0 0F09 DC MSG2&8 88917050
07B5 0 E004 E004 DC /E004 ID E4 88917060
07B6 0 0002 DC /0002 LINE 0-FORM 2 88917070
07B7 0 4C00 071C TMWR7 BSC L MONT6 GO RESTART THE RTN 88917080
*
* SENSE WORD CTR IOCC
07BA 0000 BSS E 0 88917090
07BA 0 0000 SNWC DC 0 88917100
07BB 0 0000 SNWC1 DC 0 88917110
*
* WRITE TIME
07BC 0 0000 TWRX0 DC 0 88917120
07BD 0 0710 TWRX1 DC /0710 SENSE WD CTR-DR 0 88917130
07BE 0 0730 TWRX2 DC /0730 1 88917140
07BF 0 4C30 TWRX3 DC /4C30 88917150
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
07C0 0 0000 TMRDT DC 0 SE
07C1 0 C006 LD TMRX3 MODIFY TIME RTN/RD
07C2 0 D0DC STO TMWR9
07C3 0 70CC MDX TMRD1 GO TIME A READ
*
* INTERRUPT RETURN
07C4 0 C0FB TMRD4 LD TMRDT GET RETURN 88917450
07C5 0 D0C7 STO TMWRT SET 88917460
07C6 0 70E3 MDX TMWR4 88917470
07C7 0 000A TMWR2 DC /000A 88917480
07C8 0 4C10 TMWR3 DC /4C10 88917490
*
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX

```

2400 TIMING TEST

```

XXXXXXXXXXXXXXXXXXXX COMMON BACKSPACE ROUTINE XX      8B917550
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL                      XX      8B917560
XXXXXXXXXXXXXXXXXXXX BSI L BSP                          XX      8B917570
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX    8B917580
*                                                       8B917590
07C9 0 0000      BSP   DC      0                               SE      8B917600
07CA 0 6B08      STX    3  BSP2&1   SAVE IX 3                8B917610
07CB 0 403B      BSP3  BSI    DSWO    SENSE DRIVE           SRC      8B917620
07CC 0 4C04 07CB BSC    L  BSP3,E   BRANCH # NOT READY        8B917630
07CE 0 1803      SRA    3                               8B917640
07CF 0 4C04 07D2 BSC    L  BSP2,E   IS DR AT LD PT                          8B917650
07D1 0 7004      MDX    BSP4    NO                          8B917660
07D2 0 6700 0000 BSP2  LDX   L3  0   RESTORE IX 3            8B917670
07D4 0 4C80 07C9 BSC    I  BSP      EXIT                                SX      8B917680
*                                                       8B917690
*                                                       8B917700
* DRIVE IS NOT READY                                     8B917710
*                                                       8B917720
*                                                       8B917730
* DRIVE IS NOT AT LOAD POINT                            8B917740
*                                                       8B917750
07D6 0 C500 07E0 BSP4  LD    L1  BSPX1   GET MODIFIER        8B917760
07D8 0 D20A      STO    2  10    SET IN DST                8B917770
07D9 0 C02A      LD     RWDX0   GET FUNCTION              8B917780
07DA 0 D209      STO    2  9     SET IN DST                8B917790
*****
07DB 0 4400 0373 BSI    L  DCC      GO BACKSPACE *                       8B917800
07DD 0 06EB      BSP6  DC    DST&11  ADRS OF STRING *        8B917810
*****
07DE 0 3006      WAIT6 WAIT 6      WAIT FOR BSP INTRPT     8B917820
07DF 0 7002      MDX    BSP12                                8B917830
*                                                       8B917840
*                                                       8B917850
* BACKSPACE CONSTANTS                                   8B917860
*                                                       8B917870
07E0 0 000B      BSPX1 DC    /000B                                8B917880
07E1 0 002B      DC     /002B                                8B917890
*                                                       8B917900
* CHECK DRIVE                                           8B917910
*                                                       8B917920
07E2 0 C207      BSP12 LD    2  7     GET LAST DSW         8B917930
07E3 0 4804      BSC    E          IS DRIVE READY          8B917940
07E4 0 7003      MDX    BSP13   NO                          8B917950
07E5 0 1806      SRA    6                               8B917960
07E6 0 4804      BSC    E          IS OP COMPLETE ON       8B917970
07E7 0 70EA      MDX    BSP2    NO                          8B917980
07E8 0 4400 083C BSP13 BSI    L  MLG    BACKSPACE ERROR      8B917990
07EA 0 0F3D      DC     MSG13&4                                8B918000
07EB 0 0F09      DC     MSG2&8                                8B918010
07EC 0 E003      E003  DC    /E003   ID E3                8B918020
07ED 0 0002      DC     /0002   LINE 0-FORM 2              8B918030
*****
07EE 0 4400 0662 BSI    L  END      TERMINATE *                       8B918040
*****
XXXXXXXXXXXXXXXXXXXX COMMON REWIND ROUTINE XXXXX      8B918050
XXXXXXXXXXXXXXXXXXXX ROUTINE CALL                      XXXXX    8B918060
XXXXXXXXXXXXXXXXXXXX BSI L RWD                          XXXXX    8B918070
XXXXXXXXXXXXXXXXXXXX XXXXX                              XXXXX    8B918080
*                                                       8B918090
07F0 0 0000      RWD   DC      0                               SE      8B918100
07F1 0 4015      RWD3  BSI    DSWO    SENSE DRIVE           SRC      8B918110
07F2 0 C011      LD     RWDX0   GET FUNCTION              8B918120
07F3 0 D209      STO    2  9     SET IN DST                8B918130
07F4 0 C500 0805 LD     L1  RWDX1   GET MODIFIER              8B918140
07F6 0 D20A      STO    2  10    SET IN DST                8B918150
07F7 0 C207      LD     2  7     GET LAST DSW         8B918160
07F8 0 4C04 07F1 BSC    L  RWD3,E   BRANCH-DR NOT READY        8B918170
07FA 0 1803      SRA    3          YES                                8B918180

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2400 TIMING TEST

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07FB 0 4C04 07FE BSC    L  RWD2,E   IS DR AT LOAD PT      8B918230
07FD 0 7002      MDX    RWD4    NO                          8B918240
07FE 0 4C80 07F0 RWD2  BSC    I  RWD      RETURN TO PROG     SX      8B918250
*                                                       8B918260
* DRIVE IS NOT AT LD                                    8B918270
*                                                       8B918280
*****
0800 0 4400 0373 RWD4  BSI    L  DCC      GO REWIND $                8B918300
0802 0 06EB      DC     DST&11  ADRS OF STRING *                8B918310
*****
0803 0 70ED      MDX    RWD3                                8B918320
*                                                       8B918330
* REWIND CONSTANTS                                     8B918340
*                                                       8B918350
0804 0 0400      RWDX0 DC    /0400   FUNCTION CNTRL        8B918360
0805 0 0004      RWDX1 DC    /0004   RWD DR 0              8B918370
0806 0 0024      DC     /0024   RWD DR 1              8B918380
*                                                       8B918390
*                                                       8B918400
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918410
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918420
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918430
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918440
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918450
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918460
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918470
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918480
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918490
DSWO  DC      0                               SE      8B918500
LDX   3  2      SET FOR DOUBLE SENSE                8B918510
DSW5  LD    L1  DSWX1   GET MODIFIER                  8B918520
STO   2  10     SET MOD                               8B918530
LD    2  11     GET ADR AREA CODE                     8B918540
STO   DSW1     SET IN CALL                            8B918550
LD    2  13     GET MOD ADR                           8B918560
STO   DSW2     SET IN CALL                            8B918570
DSW7  STX   3  DSW8&1  SAVE INDEX 3                  8B918580
*****
0811 0 4400 03A2 BSI    L  DIND   GO SENSE *                       8B918600
0813 0 0000      DC     0          AREA CODE ADRS *          8B918610
0814 0 0000      DC     0          MODIFIER *              8B918620
0815 0 0000      DC     0          LOAD A RETURN *         8B918630
*****
0816 0 6700 0000 DSW8  LDX   L3  0   RESTORE INDEX 3                8B918640
0818 0 73FF      MDX    3  -1    IS THIS THE SECOND     8B918650
0819 0 70F6      MDX    DSW7   NO-GO SENSE AGAIN       8B918660
081A 0 D207      STO    2  7     SET IN DST              8B918670
081B 0 4C80 0807 BSC    I  DSWO    RETURN                                SX      8B918680
*                                                       8B918690
*                                                       8B918700
* CONSTANTS USED BY DSW                                 8B918710
*                                                       8B918720
DSWX1 DC    0          MODIFIER FOR DRIVE 0            8B918730
DC     /0020   MODIFIER FOR DRIVE 1                    8B918740
*                                                       8B918750
*                                                       8B918760
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918770
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918780
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918790
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918800
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918810
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918820
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 8B918830
*                                                       8B918840
INTRT DC    0                               SE      8B918850
STX   L1  INTR&1   SAVE IX 1                          8B918860
LD    I  INTR1     GET FINAL RETURN                    8B918870
STO   INTR2       SAVE                                8B918880
MDX   L  INTRT,1  ADD 1 TO RETURN                       8B918890
BSC   I  INTRT    EXIT                                SX      8B918900

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2400 TIMING TEST

```

0829 0 0000      INTR2 DC      0      DRIVE 0 FINAL RETURN      8B918910
082A 0 6500 0000  INTR  LDX  L1 0      RESTORE IXING          8B918920
082C 0 6600 06E0  LDX  L2 DST          8B918930
082E 0 4C80 0829  BSC  I  INTR2      8B918940
~
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0830 0 0000      PRDWT DC      0      SE      8B919040
0831 0 D209      STO  2 9      SET FUNC          8B919050
0832 0 C500 081D  LD  L1 DSWX1     GET MODIFIER      8B919060
0834 0 D20A      STO  2 10     SET IN DST        8B919070
0835 0 C400 0929  LD  L  MTTY4     GET WORD COUNT OF 5 8B919080
0837 0 D206      STO  2 6      SET WD CT         8B919090
0838 0 D400 0E9C  STO  L  IOA      SET IN IO AREA    8B919100
083A 0 4C80 0830  BSC  I  PRDWT     SX      8B919110
~
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
083C 0 0000      MLG  DC      0      8B919120
083D 0 4400 0306  BSI  L  RDSWS     READ SWS          8B919130
083F 0 C400 02E1  LD  L  SWO        GET SWS           8B919140
0841 0 1802      SRA  2           CK FOR BYPASS    8B919150
0842 0 4C04 08C7  BSC  L  MLG18,E   BRANCH # BYPASS  8B919160
0844 0 4400 0EC9  BSI  L  LDSP      PRINT HEADING     8B919170
0846 0 6780 083C  LDX  I3 MLG      IX # ADRS CALL  8B919180
0848 0 C300      LD  3 0          GET MSG ID        8B919190
0849 0 D400 08FB  STO  L  MOD4     SET IN MSG        8B919200
084B 0 C301      LD  3 1          GET LINE AND FORM NO 8B919210
084C 0 D400 0901  STO  L  MLGX0    SAVE              8B919220
084E 0 6D00 08B8  STX  L1 MLG05&1  SAVE IX 1        8B919230
0850 0 C400 02DF  LD  L  RID       GET RTN NO       8B919240
0852 0 D400 08FA  STO  L  MOD3     SET IN MSG        8B919250
0854 0 6D00 08FC  STX  L1 MOD5     SET DR NO IN MSG  8B919260
0856 0 C400 02DE  LD  L  PID       GET PRG NUMBER    8B919270
0858 0 D400 08F9  STO  L  MOD2     SET IN MSG        8B919280
085A 0 C400 0901  LD  L  MLGX0    GET LINE/FORM NO 8B919290
085C 0 1008      SLA  8           SAVE FORM NO     8B919300
085D 0 1808      SRA  8           8B919310
085E 0 D001      STO  MLG00&1    8B919320
085F 0 6700 0000  MLG00 LDX  L3 0   IX 3 # FORM NUMBER 8B919330
0861 0 4F80 0863  BSC  I3 MLG02    GO TO SET UP MSG  8B919340
0863 0 0869      DC  FORM0        ADRS OF FORM 0 SETUP 8B919350
0864 0 08D1      DC  FORM1        1      8B919360
0865 0 08D9      DC  FORM2        2      8B919370
0866 0 08DF      DC  FORM3        3      8B919380
0867 0 08E5      DC  FORM4        4      8B919390
0868 0 08EB      DC  FORM5        5      8B919400
~
*
*          FORM IS 0
*
0869 0 6300      FORM0 LDX  3 0   8B919410
*
*          SET LINE NO AND WD CT
*
086A 0 C500 02EA  MLG03 LD  L1 EDIT&6  GET DR MODEL      8B919420
086C 0 4820      BSC  Z           IS THIS MODEL 3 8B919430

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2400 TIMING TEST

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086D 0 701F      MDX  MLG11      NO      8B919590
086E 0 6104      LDX  1 4          8B919600
086F 0 10A0      MLG10 SLT  32     CLEAR A AND Q     8B919610
0870 0 C500 08FC  LD  L1 MOD00-1   GET MSG WD        8B919620
0872 0 A400 06DC  M  L  MLGX7      CALCULATE TIME    8B919630
0874 0 AC00 0D84  D  L  MT7X0     SCALE BY 10       8B919640
0876 0 D500 08FC  STO  L1 MOD00-1  SAVE THE TIME     8B919650
0878 0 71FF      MDX  1 -1       DECR IX 1         8B919660
0879 0 70F5      MDX  MLG10     LOOP             8B919670
087A 0 C400 02DF  LD  L  RID       GET RTN NO        8B919680
087C 0 9400 0902  S  L  MLGX1     SUB 4             8B919690
087E 0 4808      BSC  &         IS THIS RTN 5, 6, 7 8B919700
087F 0 702F      MDX  MLG07     NO-PRINT TIME    8B919710
0880 0 6104      LDX  1 4          8B919720
0881 0 10A0      MLG16 SLT  32     CLEAR A AND Q     8B919730
0882 0 C500 08FC  LD  L1 MOD00-1   GET TIME          8B919740
0884 0 A400 06DF  M  L  INPSE     CALCULATE GAP     8B919750
0886 0 AC00 0907  D  L  MLGX9     SCALE BY 1000    8B919760
0888 0 D500 08FC  STO  L1 MOD00-1  SAVE GAP          8B919770
088A 0 71FF      MDX  1 -1       DECR IX 1         8B919780
088B 0 70F5      MDX  MLG16     LOOP             8B919790
088C 0 7022      MDX  MLG07     GO PRINT          8B919800
088D 0 6104      MLG11 LDX  1 4          8B919810
088E 0 10A0      MLG12 SLT  32     CLEAR A AND Q     8B919820
088F 0 C500 08FC  LD  L1 MOD00-1   GET MSG WD        8B919830
0891 0 18D0      RTE  16         SET IN Q          8B919840
0892 0 AC00 0D84  D  L  MT7X0     SCALE BY 10       8B919850
0894 0 18D0      RTE  16         SET IN Q          8B919860
0895 0 1090      SLT  16         CLEAR Q/SET Q IN A 8B919870
0896 0 A400 06DC  M  L  MLGX7     CALCULATE TIME    8B919880
0898 0 1010      SLA  16         CLEAR ACCUM       8B919890
0899 0 AC00 0D84  D  L  MT7X0     SCALE BY 10       8B919900
089B 0 D500 08FC  STO  L1 MOD00-1  SAVE THE TIME     8B919910
089D 0 71FF      MDX  1 -1       DECR IX 1         8B919920
089E 0 70EF      MDX  MLG12     LOOP             8B919930
089F 0 C400 02DF  LD  L  RID       GET RTN NO        8B919940
08A1 0 9060      S  L  MLGX1     SUB 4             8B919950
08A2 0 4808      BSC  &         IS THIS RTN 5,6, OR7 8B919960
08A3 0 700B      MDX  MLG07     NO-PRINT TIME    8B919970
08A4 0 6104      LDX  1 4          8B919980
08A5 0 10A0      MLG15 SLT  32     CLEAR A AND Q     8B919990
08A6 0 C500 08FC  LD  L1 MOD00-1   GET TIME          8B920000
08A8 0 A400 06DF  M  L  INPSE     CALCULATE GAP     8B920010
08AA 0 A85D      D  L  MLGX9&1   SCALE BY 100      8B920020
08AB 0 D500 08FC  STO  L1 MOD00-1  SAVE GAP          8B920030
08AD 0 71FF      MDX  1 -1       DECR IX 1         8B920040
08AE 0 70F6      MDX  MLG15     LOOP             8B920050
08AF 0 C051      MLG07 LD  MLGX0   GET LINE AND FORM NO 8B920060
08B0 0 1808      SRA  8           SAVE LINE NUMBER  8B920070
08B1 0 1008      SLA  8           8B920080
08B2 0 EF00 0902  OR  L3 MLGX1    INSERT WD CT      8B920090
08B4 0 D042      STO  MOD0       SET IN MSG        8B920100
~
*****
MLG04 BSI  L  LOGC  GO PRINT          *
*****
08B5 0 4400 03BF  MLG04 BSI  L  LOGC  GO PRINT          *
*****
08B7 0 6500 0000  MLG05 LDX  L1 0   RESTORE IX 1     8B920140
08B9 0 6600 06E0  MLG06 LDX  L2 DST  8B920150
08BB 0 7402 083C  MLGOA MDX  L  MLG,2 8B920160
08BD 0 C400 036E  LD  L  ONE      GET 0001         8B920170
08BF 0 D038      STO  MOD1      RESTORE HEX/DEC SW 8B920180
08C0 0 C03A      CKERR LD  MOD4  GET MSG ID         8B920190
08C1 0 180C      SRA  12       SAVE MSG TYPE     8B920200
08C2 0 F00D      EOR  K000E    CK FOR E          8B920210
08C3 0 4818      BSC  &-       SKIP # NOT E     8B920220
08C4 0 7005      MDX  CKHLT    BRANCH           8B920230
08C5 0 4C80 083C  MLGE  BSC  I  MLG  RETURN          SX  8B920240
08C7 0 7402 083C  MLG18 MDX  L  MLG,2 INCR RETURN      8B920250
08C9 0 70F1      MDX  MLGOA    BRANCH           8B920260

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2400 TIMING TEST

```

08CA 0 C400 02E1 CKHLT LD L SWO GET SWS 88920270
08CC 0 1801 SRA 1 88920280
08CD 0 4404 03B2 BSI L HALT,E BRANCH IF HLT ON ERR 88920290
08CF 0 70F5 MDX MLGE CONTINUE 88920300
08D0 0 000E K000E DC /000E 88920310
* * *
* FORM IS ONE
*
08D1 0 C201 FORM1 LD 2 1 GET MIN WRT TIME 88920340
08D2 0 D02A STO MOD00 SET IN MSG 88920350
08D3 0 C204 LD 2 4 GET ACTUAL WRT TIME 88920360
08D4 0 D029 STO MOD01 SET IN MSG 88920370
08D5 0 C200 LD 2 0 GET MAX WRT TIME 88920380
08D6 0 D028 COM00 STO MOD02 SET IN MSG 88920390
08D7 0 6303 LDX 3 3 SET IX FDR WD CT 88920400
08D8 0 7091 MDX MLG03 GO COMPLETE MSG 88920410
* * *
* FORM IS 2
*
08D9 0 C207 FORM2 LD 2 7 GET DSX 88920430
08DA 0 D022 STO MOD00 SET IN MSG 88920440
08DB 0 1010 SLA 16 ZERO ACCUM 88920450
08DC 0 D01B STO MOD1 SET TO HEX 88920460
08DD 0 6301 COM01 LDX 3 1 SET IX FOR WD CT 88920470
08DE 0 70D0 MDX MLG07 GO COMPLETE MESSAGE 88920480
* * *
* FORM IS 3
*
08DF 0 C203 FORM3 LD 2 3 GET MIN RD TIME 88920490
08E0 0 D01C STO MOD00 SFT IN MSG 88920500
08E1 0 C205 LD 2 5 GET ACTUAL RD TIME 88920510
08E2 0 D01B STO MOD01 SET IN MSG 88920520
08E3 0 C202 LD 2 2 GET MAX RD TIME 88920530
08E4 0 70F1 MDX COM00 GO COMPLETE MSG 88920540
* * *
* FORM IS 4
*
08E5 0 C048 FORM4 LD MTTYC GET 10 MSEC GDT AVG 88920550
08E6 0 D016 STO MOD00 SET IN MSG 88920560
08E7 0 C048 LD MTTYD GET VAR GDT AVG 88920570
08E8 0 D015 STO MOD01 SET IN MSG 88920580
08E9 0 C048 LD MTTYE GET MIN GDT AVG 88920590
08EA 0 70EB MDX COM00 GO COMPLETE MSG 88920600
* * *
* FORM IS 5
*
08EB 0 C400 0D89 FORM5 LD L MT7X5 GET AVERAGE CREEP 88920610
08ED 0 D010 STO MOD01 SET IN MSG 88920620
08EE 0 C400 0D90 LD L MT7Y1 GET LOW VALUE 88920630
08F0 0 D00C STO MOD00 SET IN MSG 88920640
08F1 0 C400 0D91 LD L MT7Y2 GET HIGH VALUE 88920650
08F3 0 D00B STO MOD02 SET IN MSG 88920660
08F4 0 6303 LDX 3 3 SET TO WORD COUNT 88920670
08F5 0 4C00 086A BSC L MLG03 GO COMPLETE MSG 88920680
* * *
* LOG MESSAGE STORAGE
*
08F7 0 0000 MOD0 DC 0 LINE NO/WORD CT 88920690
08F8 0 0001 MOD1 DC 1 HEX/DEC 88920700
08F9 0 0000 MOD2 DC 0 PROGRAM NUMBER 88920710
08FA 0 0000 MOD3 DC 0 ROUTINE NUMBER 88920720
08FB 0 0000 MOD4 DC 0 MESSAGE ID 88920730
08FC 0 0000 MOD5 DC 0 DRIVE NUMBER 88920740
08FD 0 0000 MOD00 DC 0 DATA WORD 0 88920750
08FE 0 0000 MOD01 DC 0 1 88920760
08FF 0 0000 MOD02 DC 0 2 88920770
0900 0 0000 MOD03 DC 0 3 88920780
* 88920790
88920800
88920810
88920820
88920830
88920840
88920850
88920860
88920870
88920880
88920890
88920900
88920910
88920920
88920930
88920940

```

2400 TIMING TEST

```

* * *
* ROUTINE CONSTANTS
*
0901 0 0000 MLGX0 DC 0 LINE AND FORM TEMP 88920950
0902 0 0004 MLGX1 DC 4 WD CT # 4 88920960
0903 0 0005 MLGX2 DC 5 5 88920970
0904 0 0006 MLGX3 DC 6 6 88920980
0905 0 0007 MLGX4 DC 7 7 88920990
0906 0 0002 MLGX8 DC 2 88921000
0907 0 03E8 MLGX9 DC 1000 88921010
0908 0 0064 DC 100 88921020
* * *
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921050
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921060
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921070
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921080
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921090
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921100
*
CN1 DC 0 SE 88921110
LD RTNIX GET ADRS 88921120
STO STCN1&1 SET 88921130
BSI STCN SET DST SRC 88921140
BSC I CN1 EXIT SX 88921150
RTNIX DC CON-1 DR 0 ADRS 88921160
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921170
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921180
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921190
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921200
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921210
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921220
*
CN2 DC 0 SE 88921230
LD RTN2X GET ADRS 88921240
STO STCN1&1 SET 88921250
BSI STCN SET DST SRC 88921260
BSC I CN2 EXIT SX 88921270
RTN2X DC CON1-1 88921280
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921290
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921300
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921310
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921320
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921330
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921340
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921350
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921360
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921370
*
STCN DC 0 SE 88921380
LDX 3 4 IX 3 # NUMBER WDS 88921390
STCN1 LD L3 0 GET CONSTANT 88921400
STO 2 0 SET IN DST 88921410
MDX 3 -1 DECR IX 3 88921420
MDX STCN2 88921430
MDX STCN3 FINISHED 88921440
STCN2 MDX 2 1 INCR IX 2 88921450
MDX STCN1 LOOP 88921460
STCN3 LDX L2 DST RESTORE IX 2 88921470
BSC I STCN EXIT SX 88921480
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921490
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921500
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921510
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921520
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921530
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX 88921540
*
MTTX1 DC /0001 88921550
MTTX3 DC 0 TEMP 88921560
MTTX6 DC /FFFF 88921570
MTTX9 DC /0500 FUNCTION # WRT 88921580
MTTY4 DC /400A 88921590
MTTY5 DC 47 CONSTANT 88921600
MTTY8 DC /2003 DSW CK 88921610
MTTY9 DC /0600 FUNCTION # READ 88921620
0909 0 0000 88920950
090A 0 C004 88920960
090B 0 D00E 88920970
090C 0 400A 88920980
090D 0 4C80 0909 88920990
090F 0 066C 88921000
0910 0 0000 88921010
0911 0 C004 88921020
0912 0 D007 88921050
0913 0 4003 88921060
0914 0 4C80 0910 88921070
0916 0 0670 88921080
0917 0 0000 88921090
0918 0 6304 88921100
0919 0 C700 0000 88921110
091B 0 D200 88921120
091C 0 73FF 88921130
091D 0 7001 88921140
091E 0 7002 88921150
091F 0 7201 88921160
0920 0 70F8 88921170
0921 0 6600 06E0 88921180
0923 0 4C80 0917 88921190
0925 0 0001 88921200
0926 0 0000 88921210
0927 0 FFFF 88921220
0928 0 0500 88921230
0929 0 400A 88921240
092A 0 002F 88921250
092B 0 2003 88921260
092C 0 0600 88921270

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2400 TIMING TEST

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092D 0 0E6C      MTTYB DC      TOTA&3      ADRS OF COUNTS      88921630
092E 0 0000      BSS E 0      88921640
092E 0 0000      MTTYC DC      0      10 MSEC AVG      88921650
092F 0 0000      DC 0      88921660
0930 0 0000      MTTYD DC      0      VARIABLE AVG      88921670
0931 0 0000      DC 0      88921680
0932 0 0000      MTTYE DC      0      MIN AVG      88921690
0933 0 0000      DC 0      88921700
0934 0 0000      MTTYA DC      0      88921710
0935 0 0000      DC 0      88921720
0936 0 4C00 0B2E MTTYF BSC L MT51E  PROG MODIFIER      88921730
0938 0 00EB      MTTZO DC      235      CONSTANT      88921740
0939 0 9400 0C43 MTTZ1 S L MT5XE      88921750
093B 0 0000      PGSW DC      0      88921760
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
093C 0 COE8      MTT01 LD      MTTX1      GET 0001      88921820
093D 0 DOE8      STO MTTX3      SET RTN 1 SW      88921830
093E 0 40CA      BSI CN1      GO SET CONSTANTS      SRC 88921840
*
*
*          PORTION COMMON TO RTNS 1-3
*          PREPARE TO WRITE
*
093F 0 COE8      MT101 LD      MTTX9      GET WRT FNC      88921880
0940 0 4400 0830 BSI L PRDWT      GO SET UP      SRC 88921890
*
*
*          DETERMINE RTN BEING RUN
*
0942 0 COE3      LD MTTX3      GET RTN 1 SW      88921940
0943 0 4820      BSC Z      IS THIS RTN 1      88921950
0944 0 7005      MDX MT102      YES-SKIP WRT      88921960
*
*
*          ROUTINE 3 RUNNING
*
0945 0 4400 081F BSI L INTRT      GO SET INTR RETURN      SRC 88922000
0947 0 07AA      DC TMWR4      88922010
0948 0 4400 078D BSI L TMWRT      WRITE      88922020
*
*
*          COMMON TO RTNS 1 AND 3
*
*
*          TIME A WRITE
*
094A 0 4400 081F MT102 BSI L INTRT      GO SET RETURN      SRC 88922090
094C 0 07AA      DC TMWR4      88922100
094D 0 4400 078D BSI L TMWRT      GO TIME A WRT      SRC 88922110
*
*
*          CHECK THE TIMING
*
094F 0 C0D6      LD MTTX3      GET RTN 1 SW      88922150
0950 0 4820      BSC Z      IS THIS RTN 1      88922160
0951 0 7011      MDX MT105      YES      88922170
0952 0 C400 07BC LD L TWRX0      GET TIME      88922180
0954 0 D204      MT108 STO 2 4      SAVE      88922190
0955 0 9200      S 2 0      SUB MAX TIME      88922200
0956 0 4830      BSC -Z      IS TIME TOO LONG      88922210
0957 0 7010      MDX MT104      YES      88922220
0958 0 C201      LD 2 1      GET MIN TIME      88922230
0959 0 9204      S 2 4      SUB ACTUAL TIME      88922240
095A 0 4830      BSC -Z      IS TIME TOO SHORT      88922250
095B 0 700C      MDX MT104      YES      88922260
*
*
*          CHECK PRINT REQUEST SW
*
095C 0 C400 02E1 LD L SW0      GET SW FNC 0      88922270

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2400 TIMING TEST

```

095E 0 1809      SRA 9      88922310
095F 0 4804      BSC E      IS DATA PRINT REQ. 88922320
0960 0 700E      MDX MT107      YES      88922330
*
*          ROUTINE IS COMPLETE
*
0961 0 4C00 0771 MT103 BSC L MONR1      EXIT      88922360
0963 0 C400 07BC MT105 LD L TWRX0      88922370
0965 0 9400 0679 S L MT1X0      SUB TURNAROUND      88922380
0967 0 70EC      MDX MT108      88922390
*
*          ERROR FOUND IN TIMING
*
0968 0 4400 083C MT104 BSI L MLG      PRINT ERROR      88922420
096A 0 OF25      DC MSG7&4      88922440
096B 0 OF11      DC MSG3&8      88922450
096C 0 E005      E005 DC /E005      ID E5      88922470
096D 0 0001      DC /0001      LINE 0-FORM 1      88922480
096E 0 70F2      MDX MT103      88922490
*
*          DATA PRINT IS REQUESTED
*
096F 0 4400 083C MT107 BSI L MLG      PRINT DATA      88922520
0971 0 OF25      DC MSG7&4      88922530
0972 0 OF11      DC MSG3&8      88922540
0973 0 A002      A002 DC /A002      ID A2      88922550
0974 0 0001      DC /0001      LINE 0-FORM 1      88922570
0975 0 70EB      MDX MT103      88922580
*
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
0976 0 COAE      MTT02 LD MTTX1      GET 0001      88922630
0977 0 DOAE      STO MTTX3      SET RTN 2 SW      88922640
0978 0 4400 0909 BSI L CN1      GO SET CONSTANTS      SRC 88922660
*
*
*          COMMON TO RTNS 2 AND 4
*
097A 0 COAD      MT200 LD MTTX9      GET WRT FNC      88922690
097B 0 4400 0830 BSI L PRDWT      GO SET UP      SRC 88922700
097D 0 4400 081F BSI L INTRT      SET INTRPT RETURN      SRC 88922710
097F 0 07AA      DC TMWR4      88922720
0980 0 4400 078D BSI L TMWRT      WRITE      88922730
*
*
*          DETERMINE RTN BEING RUN
*
0982 0 COA3      LD MTTX3      GET RTN 2 SW      88922770
0983 0 4820      BSC Z      IS THIS RTN 2      88922780
0984 0 702C      MDX MT203      YES      88922790
*
*
*          THIS IS RTN 4
*
0985 0 4400 078D BSI L TMWRT      WRITE      88922830
*
*
*          WRITE IS COMPLETE
*
0987 0 4400 07F0 BSI L RWD      GO REWIND      SRC 88922870
0989 0 COA2      MT208 LD MTTY9      GET READ FNCT      88922890
098A 0 4400 0830 BSI L PRDWT      GO SET UP      SRC 88922900
098C 0 4400 081F BSI L INTRT      GO SET TIME INT RET      SRC 88922910
098E 0 07C4      DC TMRD4      88922920
098F 0 C400 0926 LD L MTTX3      GET RTN 2 SW      88922930
0991 0 4820      BSC Z      IS THIS RTN 2      88922940
0992 0 7002      MDX MT209      YES      88922950
0993 0 4400 07C0 BSI L TMRDT      GO READ      SRC 88922960
0995 0 4400 07C0 MT209 BSI L TMRDT      GO READ      SRC 88922970
*

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2400 TIMING TEST

2400 TIMING TEST

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* READ IS COMPLETE
*
0997 0 C400 0926 LD L MTTX3 GET RTN 2 SW 8B922990
0999 0 4820 BSC Z IS THIS RTN 2 8B923000
099A 0 7011 MDX MT201 YES 8B923010
099B 0 C400 07BC LD L TWRX0 GET TIME 8B923020
099D 0 D205 MT202 STO 2 5 SAVE 8B923030
099E 0 9202 S 2 2 SUB MAX TIME 8B923040
099F 0 4830 BSC -Z IS TIME TOO LONG 8B923050
09A0 0 7013 MDX MT204 YES 8B923060
09A1 0 C203 LD 2 3 GET MIN TIME 8B923070
09A2 0 9205 S 2 5 SUB ACTUAL 8B923080
09A3 0 4830 BSC -Z IS TIME TOO SHORT 8B923090
09A4 0 700F MDX MT204 YES 8B923100
*
* CHECK PRINT REQ SW
*
09A5 0 C400 02E1 LD L SW0 GET SW FNC 0 8B923110
09A7 0 1809 SRA 9 8B923120
09A8 0 4804 BSC E IS SW SET 8B923130
09A9 0 7011 MDX MT205 YES 8B923140
*
* PRINT NOT REQUESTED
*
09AA 0 C400 0771 MT206 BSC L MONR1 EXIT 8B923150
09AC 0 C400 07BC MT201 LD L TWRX0 GET TIME 8B923160
09AE 0 9400 0679 S L MT1X0 8B923170
09B0 0 70EC MDX MT202 8B923180
*
* THIS IS RTN 2
*
09B1 0 4400 07F0 MT203 BSI L RWD REWIND DRIVE SRC 8B923190
09B3 0 70D5 MDX MT208 8B923200
*
* TIMING ERROR
*
09B4 0 4400 083C MT204 BSI L MLG PRINT ERROR 8B923210
09B6 0 0F29 DC MSG8&4 8B923220
09B7 0 0F11 DC MSG3&8 8B923230
09B8 0 E006 DC /E006 ID E6 8B923240
09B9 0 0003 DC /0003 LINE 0-FORM 3 8B923250
09BA 0 70EF MDX MT206 8B923260
*
* PRINT REQUEST SW IS ON
*
09BB 0 4400 083C MT205 BSI L MLG PRINT DATA 8B923270
09BD 0 0F29 DC MSG8&4 8B923280
09BE 0 0F11 DC MSG3&8 8B923290
09BF 0 A003 DC /A003 ID A3 8B923300
09C0 0 0003 DC /0003 LINE 0-FORM 3 8B923310
09C1 0 70E8 MDX MT206 8B923320
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER THREE XXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
09C2 0 1010 MTT03 SLA 16 ZERO ACCUM SE 8B923330
09C3 0 D400 0926 STO L MTTX3 CLEAR RTN 1 SW 8B923340
09C5 0 4400 0910 BSI L CN2 GO SET CONSTANTS SRC 8B923350
09C7 0 4C00 093F BSC L MT101 GO TO COMMON RTN 8B923360
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER FOUR XXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
09C9 0 1010 MTT04 SLA 16 ZERO ACCUM SE 8B923370
09CA 0 D400 0926 STO L MTTX3 CLEAR RTN 2 SW 8B923380
09CC 0 4400 0910 BSI L CN2 GO SET CONSTANTS SRC 8B923390

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```

09CE 0 70AB MDX MT200 GO TO COMMON RTN 8B923670
*
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
XXXXXXXXXXXXXXXXXXXX ROUTINE NUMBER FIVE XXXXXXXX
XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX
*
* SET UP FOR WRITE
*
MTT05 SLA 16 ZERO ACCUM 8B923740
STO L MTTX3 8B923750
RTE 16 CLEAR Q REG 8B923760
SLA 16 CLEAR ACCUM 8B923770
STO L MTTYC CLEAR ALL TOTALS 8B923780
STO L MTTYD 8B923790
STO L MTTYE 8B923800
STO L GRL1&1 8B923810
STO L MT526&1 8B923820
STO L LNSW 8B923830
LDX 3 -47 8B923840
MT573 STO L3 TOTA&50 8B923850
MDX 3 1 DECR IX 3 8B923860
MDX MT573 LOOP 8B923870
LD L MT730 RESTORE RTN CONSTANT 8B923880
STO L PRSW 8B923890
LDD L MT506-1 8B923900
STO L DLYC 8B923910
LD L ADRS1 8B923920
STO L MT50A&1 8B923930
LD L K0086 8B923940
STO L MT509&1 8B923950
LDD L MT506 8B923960
STO L MT507 8B923970
LD L MT5X3 8B923980
STO L SW 8B923990
SLA 16 8B924000
STO L LINE 8B924010
STO L MTTX3 8B924020
STO L MT5X4 8B924030
LD L MTTZ1 8B924040
STO L MT510 8B924050
LD L MTTZ1&1 8B924060
STO L MT510&1 8B924070
LD L MTTX9 8B924080
BSI L PRDWT GO SET UP SRC 8B924090
BSI L INTRT GO SET TIME INT RET SRC 8B924100
DC TMWR4 8B924110
*
* WRITE FIRST RECORD
*
MT502 BSI L TMWRT WRITE 8B924120
*
* DELAY 10 MILLISEC
*
LD L MT540 GET DELAY CT 8B924130
STO L DLY1 SET 8B924140
***** 8B924150
BSI L DELAY GO DELAY * 8B924160
DC DLY1-1 ADRS OF COUNT * 8B924170
***** 8B924180
*
* WRITE SECOND RECORD
*
MT503 BSI L TMWRT WRITE 8B924190
MDX MT507 BRANCH 8B924200
*
* VARIABLE DELAY .5 MILLISEC
*
TO 5 SECS. 8B924210

```



2400 TIMING TEST

```

*
OA1C 0000      BSS E 0          8B924350
OA1C 0 1000    MT5Q7 NOP 0          8B924360
OA1D 0 1000    NUP 0          8B924370
OA1E 0 4400 078D BSI L TMWRT 8B924380
OA20 0 CC00 0C36 LDD L DLYC 8B924390
OA22 0 DC00 0C34 STD L DLY2-1 8B924400
                   SET 8B924410
*****
OA24 0 4400 038C MT505 BSI L DELAY GO DELAY * 8B924420
OA26 0 0C34      DC DLY2-1 ADRS OF COUNT * 8B924430
                   8B924440
*****
OA27 0 6600 06E0 MT508 LDX L2 DST 8B924450
                   8B924460
                   8B924470
                   8B924480
                   8B924490
                   8B924500
                   8B924510
                   8B924520
                   8B924530
                   8B924540
                   8B924550
                   8B924560
                   8B924570
                   8B924580
                   8B924590
                   8B924600
                   8B924610
                   8B924620
                   8B924630
                   8B924640
                   8B924650
                   8B924660
                   8B924670
                   8B924680
                   8B924690
                   8B924700
                   8B924710
                   8B924720
                   8B924730
                   8B924740
                   8B924750
                   8B924760
                   8B924770
                   8B924780
                   8B924790
                   8B924800
                   8B924810
                   8B924820
                   8B924830
                   8B924840
                   8B924850
                   8B924860
                   8B924870
                   8B924880
                   8B924890
                   8B924900
                   8B924910
                   8B924920
                   8B924930
                   8B924940
                   8B924950
                   8B924960
                   8B924970
                   8B924980
                   8B924990
                   8B925000
                   8B925010
                   8B925020

```

2400 TIMING TEST

```

OA6A 0 4400 07C0 BSI L TMRDT GO READ SRC 8B925030
                   8B925040
                   8B925050
                   8B925060
                   8B925070
                   8B925080
                   8B925090
                   8B925100
                   8B925110
                   8B925120
                   8B925130
                   8B925140
                   8B925150
                   8B925160
                   8B925170
                   8B925180
                   8B925190
                   8B925200
                   8B925210
                   8B925220
                   8B925230
                   8B925240
                   8B925250
                   8B925260
                   8B925270
                   8B925280
                   8B925290
                   8B925300
                   8B925310
                   8B925320
                   8B925330
                   8B925340
                   8B925350
                   8B925360
                   8B925370
                   8B925380
                   8B925390
                   8B925400
                   8B925410
                   8B925420
                   8B925430
                   8B925440
                   8B925450
                   8B925460
                   8B925470
                   8B925480
                   8B925490
                   8B925500
                   8B925510
                   8B925520
                   8B925530
                   8B925540
                   8B925550
                   8B925560
                   8B925570
                   8B925580
                   8B925590
                   8B925600
                   8B925610
                   8B925620
                   8B925630
                   8B925640
                   8B925650
                   8B925660
                   8B925670
                   8B925680
                   8B925690
                   8B925700

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2400 TIMING TEST

2400 TIMING TEST

OAC9 0 1890	SRT	16	88925710		
OACA 0 AC00 OC45	D	L MT5XF	AVG IT	88925720	
OACC 0 D700 OE9B	STD	L3 TOTA&50	SAVE	88925730	
OACE 0 7301	MDX	3 1		88925740	
OACF 0 70F7	MDX	MT525	NO-LOOP	88925750	
OADO 0 6D00 OBC4	STX	L1 MT520&1	SAVE IX 1	88925760	
OAD2 0 1010	SLA	16	PRINT BLANK LINE	88925770	
OAD3 0 4400 OC19	BSI	L LOADK	*	88925780	
OAD5 0 4400 044C	BSI	L PCC0	*	88925790	
OAD7 0 6700 ODB1	LDX	L3 PR3-1	IX # MSG ADRS	88925800	
OAD9 0 4400 OC21	BSI	L LOADV	SET MSG - PRINT	88925810	
OADB 0 62D4	LDX	2 -44		88925820	
OADC 0 6580 06EF	LDX	I1 DST&15	IX 1 # NO TRACKS	88925830	
OADE 0 C500 0675	LD	L1 MT5XA	SET LINE LIMITS	88925840	
OAE0 0 D400 OC43	STO	L MT5XE		88925850	
OAE2 0 C500 0677	LD	L1 MT5XA&2		88925860	
OAE4 0 D400 OC44	STO	L MT5XE&1		88925870	
OAE6 0 C400 OC43	LD	L MT5XE		88925880	
OAE8 0 9400 06DB	S	L GPHLM	SET TO NEXT LINE	88925890	
OAEA 0 D400 OC43	STO	L MT5XE		88925900	
OAEC 0 C400 OC44	LD	L MT5XE&1		88925910	
OAE8 0 9400 06DB	S	L GPHLM	SET TO NEXT LINE	88925920	
OAF0 0 D400 OC44	STO	L MT5XE&1		88925930	
OAF2 0 7401 OC42	MDX	L LINE,1	INCR LINE COUNT	88925940	
OAF4 0 C400 06EF	LD	L DST&15	GET NO TRACKS	88925950	
OAF6 0 4C18 0B04	BSC	L TRK9,&-	BCH IF 9 TRK	88925960	
OAF8 0 C400 OC42	LD	L LINE	GET LINE COUNT	88925970	
OAF9 0 9400 0461	S	L PCCX1	SUB 9	88925980	
OAF0 0 4C18 0B0B	BSC	L MT517,&-	BCH IF 9	88925990	
OAFE 0 9400 OC3C	S	L MT5X2	SUB 30	88926000	
OB00 0 4C18 0B0B	BSC	L MT517,&-	BCH IF 39	88926010	
OB02 0 1010	SLA	16	CLEAR ACC	88926020	
OB03 0 7009	MDX	MT517&2	BRANCH	88926030	
OB04 0 C400 OC42	TRK9	LD	L LINE	GET LINE COUNT	88926040
OB06 0 9400 0463	S	L K007	SUB 7	88926050	
OB08 0 4C18 0B0B	BSC	L MT517,&-	BCH IF 7	88926060	
OB0A 0 70F3	MDX	NTDSH		88926070	
OB0B 0 C400 OC41	MT517	LD	L KDASH	GET DASH	88926080
OB0D 0 4400 OC19	BSI	L LOADK	SET I/O AREA	88926090	
OB0F 0 1010	SLA	16	CLEAR ACC	88926100	
OB10 0 D400 OD93	STO	L PRA3	*	88926110	
OB12 0 D400 OD94	STO	L PRA1	*	88926120	
OB14 0 C400 OC41	LD	L KDASH	SET FINAL DASH	88926130	
OB16 0 1008	SLA	8		88926140	
OB17 0 D400 ODB0	STO	L PRA&26		88926150	
OB19 0 C400 036E	LD	L ONE	GET ONE	88926160	
OB1B 0 D400 OBA0	STO	L CKHLW	SET SW # ODD	88926170	
OB1D 0 C400 066C	LD	L SPEC	GET *	88926180	
OB1F 0 D400 OD95	STO	L PRA4&3	SET IN I/O	88926190	
OB21 0 63E5	LDX	3 -27	IX # MSG	88926200	
OB22 0 61D1	LDX	1 -47		88926210	
OB23 0 C500 OE9B	MT519	LD	L1 TOTA&50	GET A COUNT	88926220
OB25 0 4C18 0B59	BSC	L MT51B,&-	IS IT ZERO	88926230	
OB27 0 9400 OC43	MT51D	S	L MT5XE	SUB GRAPH LIMIT	88926240
OB29 0 4C28 0B3C	BSC	L MT51A,&Z	IS COUNT LESS	88926250	
OB2B 0 1010	SLA	16	ZERO ACCUM	88926260	
OB2C 0 D500 OE9B	STO	L1 TOTA&50	ZERO THE COUNT	88926270	
OB2E 0 C071	MT51E	LD	L CKHLW	GET HALF WD SW	88926280
OB2F 0 4804	BSC	E	IS THIS FIRST HALF	88926290	
OB30 0 7008	MDX	MT51F	YES	88926300	
OB31 0 C400 OC3F	LD	L MT5X8	GET AN E	88926310	
OB33 0 1808	SRA	8	SET TO SECOND HALF	88926320	
OB34 0 EF00 ODB1	OR	L3 PRA&27	SET IN MSG	88926330	
OB36 0 D700 ODB1	MTAAA	STO	L3 PRA&27	*	88926340
OB38 0 7020	MDX	MT51B		88926350	
OB39 0 C400 OC3F	MT51F	LD	L MT5X8	GET AN E	88926360
OB3B 0 70F8	MDX	MTAAA-2		88926370	
	*			88926380	

OB3C 0 C500 OE9B	MT51A	LD	L1 TOTA&50	GET A COUNT	88926390	
OB3E 0 9400 OC43	S	L	MT5XE		88926400	
OB40 0 4830	BSC	-Z		IS COUNT LESS/EQUAL	88926410	
OB41 0 7017	MDX	MT51B		NO	88926420	
OB42 0 C500 OE9B	LD	L1 TOTA&50		GET A COUNT	88926430	
OB44 0 9400 OC44	S	L	MT5XE&1		88926440	
OB46 0 4828	BSC	&Z		IS COUNT FOR THIS LN	88926450	
OB47 0 7011	MDX	MT51B		NO	88926460	
OB48 0 C057	LD	L	CKHLW	GET HALF WD SW	88926470	
OB49 0 4804	BSC	E		IS THIS FIRST HALF	88926480	
OB4A 0 7008	MDX	MTAAB		YES	88926490	
OB4B 0 C400 OC40	LD	L	MT5X9	GET A ZERO	88926500	
OB4D 0 1808	SRA	8			88926510	
OB4E 0 EF00 ODB1	OR	L3	PRA&27	SET IN MSG	88926520	
OB50 0 D700 ODB1	MTAAC	STO	L3	PRA&27	88926530	
OB52 0 7003	MDX	MTAAD			88926540	
OB53 0 C400 OC40	MTAAB	LD	L	MT5X9	GET A ZERO	88926550
OB55 0 70F8	MDX	MTAAC-2			88926560	
OB56 0 1010	MTAAD	SLA	16	ZERO ACCUM	88926570	
OB57 0 D500 OE9B	STO	L1	TOTA&50	ZERO THE COUNT	88926580	
OB59 0 7401 OBA0	MT51B	MDX	L	CKHLW,1	INCR HALF WD SW	88926590
OB5B 0 C044	LD	L	CKHLW	GET HALF WD SW	88926600	
OB5C 0 4804	BSC	E		IS IT EVEN	88926610	
OB5D 0 7301	MDX	3	1	NO-DECR IX 3	88926620	
OB5E 0 1000	NOP				88926630	
OB5F 0 7101	MDX	1	1	DECR IX 1	88926640	
OB60 0 70C2	MDX	MT519		LOOP	88926650	
	*				88926660	
	*			A LINE IS SET UP-PRINT	88926670	
	*				88926680	
OB61 0 7401 0B9E	MDX	L	LNSW,1	ADD ONE TO LINE SW	88926690	
OB63 0 6A25	STX	2	MT51C&1		88926700	
OB64 0 C039	LD	L	LNSW	GET LINE SW	88926710	
OB65 0 9039	S		PRSW	SUB PRINT SW	88926720	
OB66 0 4820	BSC	Z		ARE THEY EQUAL	88926730	
OB67 0 7011	MDX	MT526		NO	88926740	
OB68 0 740A 0B9F	MDX	L	PRSW,10	ADD TEN TO PRINT SW	88926750	
OB6A 0 6600 0000	GRL1	LDX	L2	0	IX 2 # CHARACTER	88926760
OB6C 0 C400 06EF	LD	L	DST&15	GET NO TRACKS	88926770	
OB6E 0 4820	BSC	Z		IS THIS 9 TRACK	88926780	
OB6F 0 7003	MDX	MT518		NO	88926790	
OB70 0 C600 OBA1	MT572	LD	L2	PR6	GET 9 TRACK CHAR	88926800
OB72 0 7002	MDX	MT50B			88926810	
OB73 0 C600 OBA5	MT518	LD	L2	PR7	GET 7 TRACK CHAR	88926820
OB75 0 D400 OD94	MT50B	STO	L	PRA1	SET IN MSG	88926830
OB77 0 7401 0B6B	MDX	L	GRL1&1,1		SELECT NEXT CHAR	88926840
OB79 0 6600 0000	MT526	LDX	L2	0	IX 1 # CHARACTER	88926850
OB7B 0 C600 OE06	LD	L2	PR8		GET CHARACTER	88926860
OB7D 0 D400 OD93	STO	L	PRA3		SET IN MSG	88926870
OB7F 0 7401 0B7A	MDX	L	MT526&1,1		INCR IX 2	88926880
	*****				88926890	
OB81 0 4400 044C	BSI	L	PCC0	GO PRINT	88926900	
	*****				88926910	
OB83 0 1010	MT50C	SLA	16		88926920	
OB84 0 D400 OD93	STO	L	PRA3	RESTORE	88926930	
OB86 0 D400 OD94	STO	L	PRA1	SET IN MSG	88926940	
OB88 0 6600 0000	MT51C	LDX	L2	0	RESTORE IX 2	88926950
OB8A 0 7201	MDX	2	1		ARE ALL LINES DONE	88926960
OB8B 0 7010	MDX	MT584		LOOP	88926970	
OB8C 0 C400 0926	LD	L	MTTX3	GET LAST LINE SW	88926980	
OB8E 0 F400 0925	EOR	L	MTTX1	EOR WITH 0001	88926990	
OB90 0 D400 0926	STO	L	MTTX3	SAVE	88927000	
OB92 0 4C18 OBA9	BSC	L	MT562,&-	BRANCH IF DONE	88927010	
OB94 0 C400 0936	LD	L	MTTYF	NO	88927020	
OB96 0 D090	STO	MT51D		MODIFY PROG	88927030	
OB97 0 C400 0937	LD	L	MTTYF&1		88927040	
OB99 0 D08E	STO	MT51D&1		MODIFY PROG	88927050	
OB9A 0 6600 FFFF	LDX	L2	-1	IX 2 # FFFF	88927060	



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OB9C 0 4C00 0AE6 MT584 BSC L MT516 LOOP 8B927070
OB9E 0 0000 LNSW DC 0 8B927080
OB9F 0 0007 PRSW DC 7 8B927090
OBA0 0 0000 CKHLW DC 0 HALF WD SW 8B927100
OBA1 0 3B08 PR6 DC /3B08 PRINTER .8 8B927110
OBA2 0 3B07 DC /3B07 PRINTER .7 8B927120
OBA3 0 3B06 DC /3B06 PRINTER .6 8B927130
OBA4 0 3B05 DC /3B05 PRINTER .5 8B927140
* 8B927150
OBA5 0 013B PR7 DC /013B PRINTER 1. 8B927160
OBA6 0 3B09 DC /3B09 PRINTER .9 8B927170
OBA7 0 3B08 DC /3B08 PRINTER .8 8B927180
OBA8 0 3B07 DC /3B07 PRINTER .7 8B927190
* 8B927200
* GRAPH COMPLETE-PRINT AVGS 8B927210
* 8B927220
* 8B927230
***** 8B927240
MT562 LDX L3 PR3-1 LOAD MSG ADRS 8B927250
OBA9 0 6700 ODB1 BSI L LOADV SET MSG -PRINT 8B927260
OBAB 0 4400 OC21 LDX L3 PR4-1 LOAD MSG ADRS 8B927270
OBAD 0 6700 ODCD BSI L LOADV SET MSG - PRINT 8B927280
OBAF 0 4400 OC21 LD L SWO GET SWITCHES 8B927290
OBB1 0 C400 02E1 SLA 5 CK HEADING BYPASS 8B927300
OBB3 0 1005 BSC L BY,&Z BCH IF ON 8B927310
OBB4 0 4C28 OBBE LDX L3 NOTE-1 LOAD MSG ADRS 8B927320
OBB6 0 6700 ODE9 BSI L LOADV SET MSG - PRINT 8B927330
OBB8 0 4400 OC21 LDX L3 NOTE1-1 IX # MSG ADRS 8B927340
OBBA 0 6700 OE30 BSI L LOADV SET MSG - PRINT 8B927350
OBBC 0 4400 OC21 BY SLA 16 CLEAS ACC 8B927360
OBBE 0 1010 BSI L LOADK SET BLANK MSG 8B927370
OBBF 0 4400 OC19 BSI L PCCO PRINT 8B927380
OBC1 0 4400 044C MT520 LDX L1 *-# RESTORE IX 1 8B927390
OBC3 0 6500 0000 MT521 LDX L2 DST RESTORE IX 2 8B927400
OBC5 0 6600 06E0 SLA 16 CLEAR A REG 8B927410
OBC7 0 1010 STO L GRL1&1 8B927420
OBC8 0 D400 0B6B STO LNSW 8B927430
OBCA 0 D0D3 STO MT526&1 8B927440
OBCB 0 DOAE LD L MT730 GET 0007 8B927450
OBCD 0 C400 0D2D PRSW 8B927460
OBE0 0 D0D0 BSI L MLG PRINT AVERAGES 8B927470
OBCF 0 4400 083C DC MSG10&4 8B927480
OBD1 0 0F35 DC MSG4&8 8B927490
OBD2 0 0F19 A004 DC /A004 ID A4 8B927500
OBD3 0 A004 DC /0004 LINE 0-FORM 4 8B927510
OBD4 0 0004 * 8B927520
* REWIND THE DRIVE-RESTORE 8B927530
* 8B927540
OBD5 0 C400 0939 LD L MTTZ1 8B927550
OBD7 0 D400 0B27 STO L MT51D RESTORE PROG 8B927560
OBD9 0 C400 093A LD L MTTZ1&1 8B927570
OBD8 0 D400 0B28 STO L MT51D&1 RESTORE PROG 8B927580
OBD0 0 4400 07F0 MT581 BSI L RWD GO REWIND 8B927590
OBDF 0 C400 02E1 LD L SWO GET SW FNC 0 SRC 8B927600
OBE1 0 1008 SLA 8 8B927610
OBE2 0 4810 BSC - IS LOOP RD ON 8B927620
OBE3 0 702F MDX MT582 NO 8B927630
OBE4 0 1010 SLA 16 RESTORE NECESSARY 8B927640
OBE5 0 D400 0926 STO L MTTX3 *VALUES IN RTN 8B927650
OBE7 0 D05A STO LINE 8B927660
OBE8 0 D0B5 STO LNSW 8B927670
OBE9 0 18D0 RTE 16 8B927680
OBEA 0 1010 SLA 16 8B927690
OBEB 0 DC00 092E STD L MTTYC 8B927700
OBED 0 DC00 0930 STD L MTTYD 8B927710
OBEF 0 DC00 0932 STD L MTTYE 8B927720
OBF1 0 D400 0B6B STO L GRL1&1 8B927730
OBF3 0 D400 0B7A STO L MT526&1 8B927740

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OBF5 0 6700 FFD1 LDX L3 -47 8B927750
OBF7 0 D700 0E9B MT585 STO L3 TOTA&50 8B927760
OBF9 0 7301 MDX 3 1 8B927770
OBFA 0 70FC MDX MT585 8B927780
OBFB 0 D042 STO MT5X4 8B927790
OBF C 0 C400 0D2D LD L MT730 8B927800
OBFE 0 D400 0B9F STO L PRSW 8B927810
OC00 0 C400 0939 LD L MTTZ1 8B927820
OC02 0 D400 0B27 STO L MT51D 8B927830
OC04 0 C400 093A LD L MTTZ1&1 8B927840
OC06 0 D400 0B28 STO L MT51D&1 8B927850
OC08 0 C827 LDD MT5Q6 8B927860
OC09 0 DC00 0A1C STD L MT5Q7 8B927870
OC0B 0 C031 LD MT5X3 8B927880
OC0C 0 D400 0462 STO L SW 8B927890
OC0E 0 C029 LD ADRS1 8B927900
OC0F 0 D400 0A30 STO L MT50A&1 8B927910
OC11 0 4C00 0A61 BSC L MT583 LOOP READ 8B927920
OC13 0 4C00 0771 MT582 BSC L MONR1 EXIT 8B927930
* 8B927940
* INTRPT RETURN ON WRITE 8B927950
* 8B927960
* 8B927970
OC15 0 4C80 078D MT522 BSC I TMWRT GO TO CALLING RTN 8B927980
* 8B927990
* INTRPT RETURN ON TM READ 8B928000
* 8B928010
OC17 0 4C80 07C0 MT523 BSC I TMRDT 8B928020
* 8B928030
* LOAD I/O AREA WITH CONSTANT 8B928040
OC19 0 0000 LOADK DC *-# RETURN ADRS 8B928050
OC1A 0 631C LDX 3 28 8B928060
OC1B 0 D700 0D92 LOAD1 STO L3 PRA3-1 STORE IN MSG AREA 8B928070
OC1D 0 73FF MDX 3 -1 8B928080
OC1E 0 70FC MDX LOAD1 8B928090
OC1F 0 4C80 0C19 BSC I LOADK RETURN TO MAINLINE 8B928100
* 8B928110
* LOAD I/O AREA WITH MESSAGE 8B928120
* 8B928130
OC21 0 0000 LOADV DC *-# RETURN ADDR 8B928140
OC22 0 6802 STX 3 MT510&1 8B928150
OC23 0 631C LDX 3 28 8B928160
OC24 0 C700 0000 MT510 LD L3 *-# LOAD MESSAGE 8B928170
OC26 0 D700 0092 STO L3 PRA3-1 STORE IN MSG AREA 8B928180
OC28 0 73FF MDX 3 -1 8B928190
OC29 0 70FA MDX MT510 8B928200
OC2A 0 4400 044C BSI L PCCO PRINT MESSAGE 8B928210
OC2C 0 4C80 0C21 BSC I LOADV RETURN TO MAINLINE 8B928220
* 8B928230
* ROUTINE 5 CONSTANTS 8B928240
* 8B928250
OC2E 0 0000 BSS E 0 8B928260
OC2E 0 4400 0C46 KBSC BSI L LDFLT BRANCH TO LFT COL LD 8B928270
OC30 0 1000 MT5Q6 NOP 0 PROG RESTORE 8B928280
OC31 0 1000 NOP 0 * 8B928290
OC32 0 0000 DC 0 DELAY COUNT 1 8B928300
OC33 0 0000 DLY1 DC 0 * 8B928310
OC34 0 0000 DC 0 DELAY COUNT 2 8B928320
OC35 0 0000 DLY2 DC 0 * 8B928330
OC36 0 0000 DLYC DC 0 DELAY STORAGE 8B928340
OC37 0 0000 DC 0 * 8B928350
OC38 0 067E ADRS1 DC MT5X0&4 CONSTANTS 8B928360
OC39 0 067E ADRS2 DC MT5X0-4 * 8B928370
OC3A 0 0056 K0086 DC 86 * 8B928380
OC3B 0 000A K0010 DC 10 * 8B928390
OC3C 0 001E MT5X2 DC 30 * 8B928400
OC3D 0 0002 MT5X3 DC 2 * 8B928410

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OC3E 0 0000	MT5X4 DC	0	SERIES COUNT	88928430
OC3F 0 3500	MT5X8 DC	/3500	PRINTER E	88928440
OC40 0 2600	MT5X9 DC	/2600	PRINTER O	88928450
OC41 0 2020	KDASH DC	/2020	PRINTER DASH	88928460
OC42 0 0000	LINE DC	0	LINE COUNT	88928470
OC43 0 0000	MT5XE DC	0	UPPER LIMIT STORAGE	88928480
OC44 0 0000	DC	0	LOWER LIMIT STORAGE	88928490
OC45 0 0005	MT5XF DC	5	NUMBER PASSES	88928500
	*			88928510
	+			88928520
	*		ROUTINE TO LOAD THE LEFT	88928530
	*		* COLUMN	88928540
	*			88928550
OC46 0 0000	LDLFT DC	0		88928560
OC47 0 6B22	STX 3	LDLFE&1	SAVE INDEXING	88928570
OC48 0 6323	LDX 3	35	IX 3 # NUMBER OF REC	88928580
OC49 0 4400 078D	LDLFA BSI L	TMWRT	WRITE	88928590
OC4B 0 73FF	MDX 3	-1	DECR INDEX	88928600
OC4C 0 70FC	MDX	LDLFA	LOOP	88928610
OC4D 0 6323	LDX 3	35	SET BACKSPACE INDEX	88928620
OC4E 0 6600 06E0	LDLFB LDX L2	DST	RESTORE IX 2	88928630
OC50 0 4400 07C9	BSI L	BSP	BACKSPACE 1 REC	88928640
OC52 0 620A	LDX 2	10	SET DLY LOOP IX	88928650
OC53 0 4400 06B1	LD L	MT591	GET DLY CONSTANT	88928660
OC55 0 0019	STO	LDLFX	SET	88928670
OC56 0 6B06	STX 3	LDLFX&1	SAVE IX 3	88928680
OC57 0 4400 038C	LDLFC BSI L	DELAY	GO DELAY	88928690
OC59 0 0C6E	DC	LDLFX-1	ADRS OF DELAY COUNT	88928700
OC5A 0 72FF	MDX 2	-1	DECR INDEX	88928710
OC5B 0 70FB	MDX	LDLFC	LOOP	88928720
OC5C 0 6700 0000	LDLFX LDX L3	0	RESTORE IX 3	88928730
OC5E 0 73FF	MDX 3	-1	DECR BSP INDEX	88928740
OC5F 0 70EE	MDX	LDLFB	LOOP	88928750
OC60 0 6600 06E0	LDX L2	DST	RESTORE IX 2	88928760
OC62 0 4400 0928	LD L	MTTX9	GET WRT FUNCTION	88928770
OC64 0 4400 0830	BSI L	PRDWT	SET UP TO WRT	88928780
OC66 0 4400 081F	BSI L	INTRT	SET INTRPT RETURN	88928790
OC68 0 07AA	DC	TMWR4		88928800
OC69 0 6700 0000	LDLFE LDX L3	0	RESTORE IX 3	88928810
OC6B 0 4C80 0C46	BSC I	LDLFT	EXIT	88928820
OC6E 0 0000	BSS E	0		88928830
OC6E 0 0000	DC	0	DELAY CONSTANT	88928840
OC6F 0 0000	LDLFX DC	0	* STORAGE	88928850
	*			88928860
			XX	88928870
			XXXXXXXXXXXXXXXXXXXX ROUTINE SIX XXXXXXXXXXXXXXXX	88928880
			XX	88928890
				88928900
				88928910
OC70 0 7FFF	MT6X8 DC	/7FFF	CONSTANT	88928920
OC71 0 1010	MTT07 SLA	16	ZERO ACCUM	88928930
OC72 0 D400 0926	STO L	MTTX3	SET DELAY COND # 0	88928940
OC74 0 D400 0D87	STO L	MT7X3	CLEAR ALL TOTALS	88928950
OC76 0 D400 0D88	STO L	MT7X4		88928960
OC78 0 D400 0D89	STO L	MT7X5		88928970
OC7A 0 D400 0D8A	STO L	MT7X6		88928980
OC7C 0 D400 0D2E	STO L	LOW		88928990
OC7E 0 D400 0D2F	STO L	HI		88929000
OC80 0 D400 0D30	STO L	AVG		88929010
OC82 0 C400 0925	LD L	MTTX1	GET 1	88929020
OC84 0 D400 0D8B	STO L	MT7X7	SET FIRST PASS SW	88929030
OC86 0 D400 0D86	STO L	MT7X2	SET RD/RERD SW # RD	88929040
OC88 0 C0E7	LD	MT6X8	GET LOW CONSTANT	88929050
OC89 0 D400 0D90	STO L	MT7Y1	SET AS LOW	88929060
OC8B 0 1010	SLA	16	ZERO ACCUM	88929070
OC8C 0 D400 0D91	STO L	MT7Y2	SET AS HIGH	88929080
OC8E 0 C400 0D84	MT700 LD L	MT7X0		88929090
OC90 0 D400 0D85	STO L	MT7X1	SET IN LOOP CONTROL	88929100
	*			

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			PREPARE TO WRITE	88929110
				88929120
OC92 0 C400 0928	MT719 LD L	MTTX9	GET WRITE FUNCTION	88929130
OC94 0 4400 0830	BSI L	PRDWT	GO SET UP	88929140
OC96 0 4400 081F	BSI L	INTRT	GO SET INTR RETURN	88929150
OC98 0 0C15	DC	MT522		88929160
	*			88929170
	*		WRITE FIRST RECORD	88929180
	*			88929190
OC99 0 4400 078D	BSI L	TMWRT	WRITE	88929200
	*			88929210
	*		WRITE SECOND RECORD	88929220
	*			88929230
OC9B 0 4400 078D	BSI L	TMWRT	WRITE	88929240
	*			88929250
	*		WRITE THIRD RECORD	88929260
	*			88929270
OC9D 0 4400 078D	BSI L	TMWRT	WRITE	88929280
	*			88929290
	*		WRITE LONG RECORD	88929300
	*			88929310
OC9F 0 C400 0D8C	LD L	MT7XA		88929320
OCA1 0 D206	STO 2	6	SET WD CT	88929330
OCA2 0 D400 0E9C	STO L	IOA	SET IN I/O AREA	88929340
OCA4 0 4400 078D	BSI L	TMWRT	WRITE	88929350
OCA6 0 4400 0910	BSI L	CN2	GO SET CONSTANTS	88929360
	*			88929370
	*		POSITION TAPE	88929380
	*			88929390
OCA8 0 6304	MT702 LDX 3	4		88929400
OCA9 0 6B03	MT703 STX 3	MT704&1	SAVE IX 3	88929410
OCAA 0 4400 07C9	BSI L	BSP	GO BACKSPACE	88929420
OCAC 0 6700 0000	LDX L3	0	RESTORE IX 3	88929430
OCAE 0 73FF	MDX 3	-1	IS TAPE POSITIONED	88929440
OCAF 0 70F9	MDX	MT703	NO-LOOP	88929450
	*			88929460
	*		SET UP TO READ	88929470
	*			88929480
OCB0 0 C400 092C	LD L	MTTY9	GET READ FUNCTION	88929490
OCB2 0 4400 0830	BSI L	PRDWT	GO SET UP	88929500
OCB4 0 4400 081F	BSI L	INTRT	GO SET TIME INT RET	88929510
OCB6 0 07C4	DC	TMRD4		88929520
	*			88929530
	*		READ FIRST RECORD	88929540
	*			88929550
OCB7 0 4400 07C0	BSI L	TMRDT	GO READ	88929560
	*			88929570
	*		READ SECOND RECORD	88929580
	*			88929590
OCB9 0 4400 07C0	BSI L	TMRDT	GO READ	88929600
	*			88929610
	*		READ THIRD RECORD	88929620
	*			88929630
OCBB 0 4400 07C0	BSI L	TMRDT	GO READ	88929640
	*			88929650
	*		CHECK READ/REREAD AND	88929660
	*		FIRST PASS SWITCHES	88929670
	*			88929680
OCBD 0 C400 0D8B	LD L	MT7X7	GET FIRST PASS SW	88929690
OCBF 0 4C18 0CC3	BSC L	MT713,-&	IS THIS FIRST PASS	88929700
OCC1 0 4C00 0D3E	BSC L	MT711	YES	88929710
OCC3 0 C400 0D86	MT713 LD L	MT7X2	GET RD/RERD SW	88929720
OCC5 0 D001	STO	MT710&1	SET IN PROG	88929730
OCC6 0 6700 0000	MT710 LDX L3	0	IX 3 # RD/RERD SW	88929740
OCC8 0 C400 07BC	LD L	TWRX0	GET THE TIME	88929750
OCCA 0 D205	STO 2	5	SAVE	88929760
OCCB 0 D700 0D87	STO L3	MT7X3	SAVE	88929770
OCCD 0 7300	MDX 3	0	IS RD/RERD SW # RD	88929780



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OCCE 0 7001          MDX   MT705   YES      88929790
OCCE 0 7000          MDX   MT70F   NO       88929800
OCUO 0 4400 07C9    MT705 BSI  L  BSP      GO BACKSPACE   SRC  88929810
*
*                   PREPARE TO WRITE
*
OC02 0 C400 0928    MT70B LD  L  MTTX9   GET WRITE FUNCTION
OC04 0 4400 0830          BSI  L  PRDWT   GO SET UP      SRC  88929860
OC06 0 4400 07C0          BSI  L  TMRDT   GO WRT         SRC  88929870
OC08 0 1010          SLA  16        ZERO ACCUM     88929880
OC09 0 D400 0D86          STO  L  MT7X2   CLEAR RD/RERD SW  88929890
OC0B 0 6303          LDX  3 3       SET TO BACKSPACE 3 88929900
OC0C 0 70CC          MDX   MT703   88929910
*
*                   READ/REREAD SW # REREAD
*
OCDD 0 C400 0D8A    MT70F LD  L  MT7X6   GET REF TIME     88929920
OCDF 0 9400 0D87          S    L  MT7X3   SUB REREAD TIME  88929930
OCE1 0 D400 0D8F          STO  L  MT7Y0   SAVE             88929940
OCE3 0 8400 0D89          A    L  MT7X5   ADD TO TOTAL     88929950
OCE5 0 D400 0D89          STO  L  MT7X5   SAVE             88929960
OCE7 0 C400 0D87          LD   L  MT7X3   GET NEW COUNT    88929970
OCE9 0 9400 0D90          S    L  MT7Y1   SUB LOW VALUE    88929980
OCEB 0 4810          BSC  -         IS NEW COUNT LOW 88929990
OCEC 0 7004          MDX   MT750   NO               88930000
OCED 0 C400 0D87          LD   L  MT7X3   SET NEW CT AS LOW 88930010
OCEF 0 D400 0D90          STO  L  MT7Y1   88930020
OCF1 0 C400 0D87    MT750 LD  L  MT7X3   GET NEW CT       88930030
OCF3 0 9400 0D91          S    L  MT7Y2   SUB HIGH         88930040
OCF5 0 4808          BSC  &        IS NEW COUNT HIGH 88930050
OCF6 0 7004          MDX   MT751   NO               88930060
OCF7 0 C400 0D87          LD   L  MT7X3   SET NEW CT AS HIGH 88930070
OCF9 0 D400 0D91          STO  L  MT7Y2   *               88930080
OCFB 0 74FF 0D85    MT751 MDX  L  MT7X1,-1 DECR LOOP CONTROL 88930090
OCFD 0 7079          MDX   MT718   LOOP CNTRL NOT ZERO 88930100
*
*                   OUTPUT RESULTS
*
OCFE 0 1010          MT744 SLA  16        CLEAR SWITCH     88930110
OCFF 0 D400 0D88          STO  L  MT7X4   *               88930120
OD01 0 C400 0D8A          LD   L  MT7X6   GET REF TIME     88930130
OD03 0 9400 0D90          S    L  MT7Y1   SUB LOW COUNT    88930140
OD05 0 D400 0D90          STO  L  MT7Y1   SET AS LOW       88930150
OD07 0 4810          BSC  -         SKIP IF NEG      88930160
OD08 0 7029          MDX   ERR      BRANCH TO ERROR 88930170
OD09 0 1010          SLA  16        CLEAR A REG      88930180
OD0A 0 9400 0D90          S    L  MT7Y1   MAKE POSITIVE    88930190
OD0C 0 D400 0D90          STO  L  MT7Y1   SAVE             88930200
OD0E 0 C400 0D91    MT754 LD  L  MT7Y2   GET HIGH COUNT   88930210
OD10 0 9400 0D8A          S    L  MT7X6   SUB REG TIME     88930220
OD12 0 D400 0D91          STO  L  MT7Y2   SET AS HIGH      88930230
OD14 0 4C10 0D1A          BSC  L  MT755,- BRANCH IF POS    88930240
OD16 0 1010          SLA  16        CLEAR A REG      88930250
OD17 0 9079          S    MT7Y2     MAKE POSITIVE    88930260
OD18 0 D078          STO  MT7Y2     SAVE             88930270
OD19 0 701E          MDX   ERR1     BRANCH           88930280
OD1A 0 C06E          LD   L  MT7X5   GET TOTAL CREEP 88930290
OD1B 0 1890          SRT  16        CALCULATE AVG CREEP 88930300
OD1C 0 A871          D    MT7XE     *               88930310
OD1D 0 4820          BSC  Z         *               88930320
OD1E 0 7001          MDX   MT715   *               88930330
OD1F 0 18D0          RTE  16        *               88930340
OD20 0 D068          MT715 STO  L  MT7X5   *               88930350
OD21 0 4C28 0D45          BSC  L  MT714,&Z IS CREEP NEG     88930360
OD23 0 4820          BSC  Z         IS CREEP ZERO   88930370
OD24 0 7016          MDX   ERR2     NO               88930380
OD25 0 4400 083C          BSI  L  MLG    PRINT ZERO CREEP 88930390
OD27 0 0F39          DC   MSG11&4  88930400

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OD28 0 0F01          DC   MSG1&8      88930470
OD29 0 E007          E007 DC   /E007   ID E7           88930480
OD2A 0 0005          DC   /0005      LINE 0- FORM 5 88930490
OD2B 0 4C00 0771    MT712 BSC  L  MONR1  EXIT           88930500
OD2D 0 0007          MT730 DC   7      88930510
OD2E 0 0000          LOW  DC   0      NEG CREEP SWS 88930520
OD2F 0 0000          HI   DC   0      *               88930530
OD30 0 0000          AVG  DC   0      *               88930540
OD31 0 8000          K8000 DC  /8000  CONSTANT       88930550
*
*                   SET ERROR SWITCH
*
OD32 0 C0FE          ERR  LD   K8000   SET LOW SW      88930560
OD33 0 D0FA          STO  LOW        *               88930570
OD34 0 C400 036E          LD   L  ONE     GET ONE         88930580
OD36 0 D051          STO  MT7X4     SET NEG CREEP SW 88930590
OD37 0 70D6          MDX   MT754     BRANCH          88930600
OD38 0 C0F8          ERR1 LD  K8000   SET HI SW       88930610
OD39 0 D0F5          STO  HI        *               88930620
OD3A 0 70DF          MDX   MT755     *               88930630
OD3B 0 C0F5          ERR2 LD  K8000   SET AVG SW      88930640
OD3C 0 D0F3          STO  AVG        *               88930650
OD3D 0 7032          MDX   MT716     *               88930660
*
*                   FIRST PASS SW IS ON
*
OD3E 0 C400 07BC    MT711 LD  L  TWRX0  GET THE TIME    88930670
OD40 0 D049          STO  MT7X6     SAVE            88930680
OD41 0 1010          SLA  16        ZERO ACCUM     88930690
OD42 0 D048          STO  MT7X7     CLEAR 1ST PASS SW 88930700
OD43 0 4C00 0CC3    BSC  L  MT713   *               88930710
*
*                   CREEP IS FORWARD PRINT FFF
*
OD45 0 1010          MT714 SLA  16        *               88930720
OD46 0 9042          S    MT7X5     *               88930730
OD47 0 D041          STO  MT7X5     *               88930740
OD48 0 7400 0D88          MDX  L  MT7X4,0  CK NEG CREEP SW 88930750
OD4A 0 7025          MDX   MT716     BRANCH IF SET   88930760
OD4B 0 C500 02EA          LD   L1 EDIT&6  GET DR MODEL    88930770
OD4D 0 D005          STO  MT760&1   SET IN IX       88930780
OD4E 0 C400 02EC          LD   L  EDIT&8  GET MEM SPEED   88930790
OD50 0 4C30 0D60          BSC  L  MT761,Z- BRANCH IF 4 MIC 88930800
OD52 0 6700 0000    MT760 LDX  L3 0   IX # MODEL     $ 88930810
OD54 0 C700 0D63          LD   L3 MT762   GET CONSTANT    88930820
OD56 0 9039          S    MT7Y1     SUB LOW CREEP   88930830
OD57 0 4C30 0D69          BSC  L  MT763,-Z BRANCH IF TOO LOW 88930840
OD59 0 4400 083C          BSI  L  MLG     PRINT CREEP     88930850
DC   MSG11&4      88930860
DC   MSG1&8       88930870
A007 DC   /A007   LINE 0- FORM 5 88930880
DC   /0005      BRANCH          88930890
DC   MT712      BRANCH          88930900
MT760 MDX  L  MT760&1,3 INCR IX TO 4 MIC 88930910
MT761 MDX  L  MT760  BRANCH          88930920
DC   14          CONSTANTS       88930930
DC   42          *               88930940
DC   21          *               88930950
DC   7           *               88930960
DC   21          *               88930970
DC   10          *               88930980
MT763 BSI  L  MLG  CREEP TOO LOW 88930990
DC   MSG11&4     88931000
DC   MSG1&8      88931010
E001 DC   /E001   LINE 0- FORM 5 88931020
DC   /0005      BRANCH          88931030
MDX   MT712     *               88931040
*                   88931050
*                   88931060
*                   88931070
*                   88931080
*                   88931090
*                   88931100
*                   88931110
*                   88931120
*                   88931130
*                   88931140

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2400 TIMING TEST

2400 TIMING TEST

```

*          CREEP IS BCKWARD PRINT BBB      88931150
*
OD70 0 4400 083C MT716 BSI L MLG          BACKWARD CREEP      88931160
OD72 0 0F39      DC          MSG11&4      88931170
OD73 0 0F01      DC          MSG1&8       88931180
OD74 0 E008      DC          /E008        ID E8             88931190
OD75 0 0005      DC          /0005        LINE 0- FORM 5   88931200
OD76 0 70B4      MDX         MT712        88931210
OD77 0 C400 0925 MT718 LD L MTTX1        GET 0001         88931220
OD79 0 D00C      STD         MT7X2        SET RD/RERD # RD 88931230
OD7A 0 6303      LDX         3 3          88931240
OD7B 0 6B03      MT746 STX 3 MT745&1      88931250
OD7C 0 4400 07C9 BSI L BSP          GO BACKSPACE     SRC 88931260
OD7E 0 6700 0000 MT745 LDX L3 0          88931270
OD80 0 73FF      MDX         3 -1         88931280
OD81 0 70F9      MDX         MT746        88931290
OD82 0 4C00 0C92 BSC L MT719        88931300
*
*          ROUTINE CONSTANTS
*
OD84 0 000A      MT7X0 DC          10          CONSTANT         88931310
OD85 0 0000      MT7X1 DC          0          LOOP CONTROL    88931320
OD86 0 0000      MT7X2 DC          0          RD/RERD SW     88931330
OD87 0 0000      MT7X3 DC          0          2ND READ TIME  88931340
OD88 0 0000      MT7X4 DC          0          1ST READ TIME  88931350
OD89 0 0000      MT7X5 DC          0          TOTAL CREEP    88931360
OD8A 0 0000      MT7X6 DC          0          REFERENCE TIME 88931370
OD8B 0 0001      MT7X7 DC          1          FIRST PASS SW  88931380
OD8C 0 40C8      MT7XA DC         /40C8        WD CT # 200    88931390
OD8D 0 0003      MT7XC DC          3          88931400
OD8E 0 000A      MT7XE DC          10         88931410
OD8F 0 0000      MT7Y0 DC          0          TEMP STORAGE   88931420
OD90 0 7FFF      MT7Y1 DC         /7FFF        LOW VALUE STORAGE 88931430
OD91 0 0000      MT7Y2 DC          0          HIGH VALUE STORAGE 88931440
*
*          PRINT AREA
*
OD92 0 0000      BSS         E 0          88931450
OD92 0 0000      PRA4 DC          /0000      88931460
OD93 0 0000      PRA3 DC          /0000      88931470
OD94 0 0000      PRA1 DC          /0000      88931480
OD95 0 2C00      DC          /2C00        *                88931490
OD96 0 001B      PRA BSS        27          PRINT AREA     88931500
ODB1 0 FFFF      DC          /FFFF        88931510
*
*          SPECIAL MESSAGES
*
ODB2 0 0000      PR3 DC          /0000      88931520
ODB3 0 0000      DC          /0000        88931530
ODB4 0 002C      DC          /002C        *                88931540
ODB5 0 2020      DC          /2020        --              88931550
ODB6 0 2020      DC          /2020        --              88931560
ODB7 0 2020      DC          /2020        --              88931570
ODB8 0 0120      DC          /0120        1-              88931580
ODB9 0 2020      DC          /2020        --              88931590
ODBA 0 2002      DC          /2002        -2              88931600
ODBB 0 2020      DC          /2020        --              88931610
ODBC 0 2003      DC          /2003        -3              88931620
ODBD 0 2004      DC          /2004        -4              88931630
ODBE 0 2005      DC          /2005        -5              88931640
ODBF 0 2006      DC          /2006        -6              88931650
ODC0 0 2008      DC          /2008        -8              88931660
ODC1 0 2001      DC          /2001        -1              88931670
ODC2 0 0A20      DC          /0A20        0-              88931680
ODC3 0 2002      DC          /2002        -2              88931690

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ODC4 0 0A20      DC          /0A20        0-              88931830
ODC5 0 030A      DC          /030A        30              88931840
ODC6 0 2006      DC          /2006        -6              88931850
ODC7 0 0A20      DC          /0A20        0-              88931860
ODC8 0 3B02      DC          /3B02        .2              88931870
ODC9 0 2020      DC          /2020        --              88931880
ODCA 0 0120      DC          /0120        1-              88931890
ODCB 0 0320      DC          /0320        3-              88931900
ODCC 0 052C      DC          /052C        5*              88931910
ODCD 0 FFFF      DC          /FFFF        88931920
ODCE 0 0000      PR4 DC          /0000        88931930
ODCF 0 0000      DC          /0000        88931940
ODD0 0 002C      DC          /002C        *              88931950
ODD1 0 1629      DC          /1629        WR              88931960
ODD2 0 3913      DC          /3913        IT              88931970
ODD3 0 3500      DC          /3500        E               88931980
ODD4 0 3726      DC          /3726        GO              88931990
ODD5 0 0034      DC          /0034        D               88932000
ODD6 0 2616      DC          /2616        OW              88932010
ODD7 0 2500      DC          /2500        N               88932020
ODD8 0 1339      DC          /1339        TI              88932030
ODD9 0 2435      DC          /2435        ME              88932040
ODDA 0 0000      DC          /0000        88932050
ODDB 0 0000      DC          /0000        88932060
ODDC 0 0000      DC          /0000        88932070
ODDD 0 0024      DC          /0024        M               88932080
ODDE 0 3923      DC          /3923        IL              88932090
ODDF 0 2339      DC          /2339        LI              88932100
ODE0 0 1235      DC          /1235        SE              88932110
ODE1 0 3326      DC          /3326        CO              88932120
ODE2 0 2534      DC          /2534        ND              88932130
ODE3 0 1200      DC          /1200        S               88932140
ODE4 0 2C12      DC          /2C12        *S              88932150
ODE5 0 3533      DC          /3533        EC              88932160
ODE6 0 2625      DC          /2625        ON              88932170
ODE7 0 3412      DC          /3412        DS              88932180
ODE8 0 002C      DC          /002C        *              88932190
ODE9 0 FFFF      DC          /FFFF        88932200
ODEA 0 2526      NOTE DC         /2526        NO              88932210
ODEB 0 1335      DC          /1335        TE              88932220
ODEC 0 2023      DC          /2023        -L              88932230
ODED 0 3613      DC          /3613        FT              88932240
ODEE 0 0033      DC          /0033        C               88932250
ODEF 0 2623      DC          /2623        OL              88932260
ODF0 0 0039      DC          /0039        I               88932270
ODF1 0 1200      DC          /1200        S               88932280
ODF2 0 2326      DC          /2326        LO              88932290
ODF3 0 3134      DC          /3134        AD              88932300
ODF4 0 3534      DC          /3534        ED              88932310
ODF5 0 0034      DC          /0034        D               88932320
ODF6 0 1429      DC          /1429        UR              88932330
ODF7 0 3925      DC          /3925        IN              88932340
ODF8 0 3700      DC          /3700        G               88932350
ODF9 0 1338      DC          /1338        TH              88932360
ODFA 0 3500      DC          /3500        E               88932370
ODFB 0 2331      DC          /2331        LA              88932380
ODFC 0 1213      DC          /1213        ST              88932390
ODFD 0 0004      DC          /0004        4               88932400
ODFE 0 0037      DC          /0037        G               88932410
ODFF 0 2600      DC          /2600        0               88932420
OE00 0 3426      DC          /3426        DO              88932430
OE01 0 1625      DC          /1625        WN              88932440
OE02 0 0013      DC          /0013        T               88932450
OE03 0 3924      DC          /3924        IM              88932460
OE04 0 3512      DC          /3512        ES              88932470
OE05 0 FFFF      DC          /FFFF        88932480
OE06 0 0000      PR8 DC         /0000        88932490
OE07 0 0000      DC          /0000        88932500

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2400 TIMING TEST

OE08 0 0000 DC /0000
OE09 0 0000 DC /0000
OE0A 0 3900 DC /3900 I
OE0B 0 2500 DC /2500 N
OE0C 0 1300 DC /1300 T
OE0D 0 3500 DC /3500 E
OE0E 0 2900 DC /2900 R
OE0F 0 0000 DC /0000
OE10 0 2900 DC /2900 R
OE11 0 3500 DC /3500 E
OE12 0 3300 DC /3300 C
OE13 0 2600 DC /2600 D
OE14 0 2900 DC /2900 R
OE15 0 3400 DC /3400 D
OE16 0 0000 DC /0000
OE17 0 3700 DC /3700 G
OE18 0 3100 DC /3100 A
OE19 0 2700 DC /2700 P
OE1A 0 0000 DC /0000
OE1B 0 3900 DC /3900 I
OE1C 0 2500 DC /2500 N
OE1D 0 0000 DC /0000
OE1E 0 3800 DC /3800 H
OE1F 0 2500 DC /2500 N
OE20 0 3400 DC /3400 D
OE21 0 2900 DC /2900 R
OE22 0 1300 DC /1300 T
OE23 0 3800 DC /3800 H
OE24 0 1200 DC /1200 S
OE25 0 0000 DC /0000
OE26 0 2600 DC /2600 D
OE27 0 3600 DC /3600 F
OE28 0 0000 DC /0000
OE29 0 3900 DC /3900 I
OE2A 0 2500 DC /2500 N
OE2B 0 3300 DC /3300 C
OE2C 0 3800 DC /3800 H
OE2D 0 3500 DC /3500 E
OE2E 0 1200 DC /1200 S
OE2F 0 0000 DC /0000
OE30 0 0000 DC /0000
OE31 0 0000 DC /0000
OE32 0 0000 DC /0000
OE33 0 0020 DC /0020 -
OE34 0 2020 DC /2020 --
OE35 0 3431 DC /3431 DA
OE36 0 1238 DC /1238 SH
OE37 0 3512 DC /3512 ES
OE38 0 0039 DC /0039 I
OE39 0 2534 DC /2534 ND
OE3A 0 3933 DC /3933 IC
OE3B 0 3113 DC /3113 AT
OE3C 0 3500 DC /3500 E
OE3D 0 3929 DC /3929 IR
OE3E 0 3700 DC /3700 G
OE3F 0 2339 DC /2339 LI
OE40 0 2439 DC /2439 MI
OE41 0 1312 DC /1312 TS
OE42 0 0000 DC /0000
OE43 0 0000 DC /0000
OE44 0 0000 DC /0000
OE45 0 0000 DC /0000
OE46 0 0000 DC /0000
OE47 0 0000 DC /0000
OE48 0 0000 DC /0000
OE49 0 0000 DC /0000
OE4A 0 0000 DC /0000
OE4B 0 0000 DC /0000

NOTE1

2400 TIMING TEST

OE4C 0 FFFF DC /FFFF
OE4D 0 2C2C LN3 DC /2C2C **
OE4E 0 2426 DC /2426 MO
OE4F 0 3420 DC /3420 D-
OE50 0 0000 LN3A DC /0000 MODEL
OE51 0 0000 LN3B DC /0000 TRACK
OE52 0 2013 DC /2013 -T
OE53 0 2922 DC /2922 RK
OE54 0 0000 DC /0000
OE55 0 1218 DC /1218 SY
OE56 0 1200 DC /1200 S
OE57 0 3429 DC /3429 DR
OE58 0 0000 LN3D DC /0000 DRIVE NUMBER
OE59 0 0000 DC /0000
OE5A 0 0000 LN3C DC /0000 MEM SPEED
OE5B 0 0000 DC /0000 *
OE5C 0 0024 DC /0024 M \$
OE5D 0 3933 DC /3933 IC \$
OE5E 0 0012 DC /0012 S
OE5F 0 3533 DC /3533 EC
OE60 0 0024 DC /0024 M
OE61 0 3524 DC /3524 EM
OE62 0 0000 DC /0000
OE63 0 3431 DC /3431 DA
OE64 0 1335 DC /1335 TE
OE65 0 2000 DC /2000 -
OE66 0 0000 DC /0000
OE67 0 0000 DC /0000
OE68 0 FFFF DC /FFFF
OE69 0033
OE9C 002D

TOTA BSS 51 VAR TOTAL STORAGE
IDA BSS 45 INPUT/OUTPUT AREA

***** SET UP AND PRINT HDINGS XXX

LDSP DC 0
LD I MLG GET TYPE ADRS
STO LDSP1&1 SET
MDX L MLG,1 INCR RETURN
LD I MLG GET HDING ADRS
STO LDSP3&1 SET
MDX L MLG,1 INCR RETURN
STX 2 LDSP2&1 SAVE IX 2
LD L SWO GET SWS
SRA 10
OED8 0 4C04 0EF5 BSC L LDSP2,E BRANCH # BYPASS
OEDA 0 631C LDX 3 28 CLEAR MSG AREA
OEDB 0 1010 SLA 16 *
OEDC 0 D700 0D92 LDSP5 STO L3 PRA4 *
OEDE 0 73FF MDX 3 -1 *
OEDF 0 70FC MDX LDSP5 *
OEEO 0 63F8 LDX 3 -8 SET MESSAGE
OEE1 0 62FC LDX 2 -4 *
OEE2 0 C600 0000 LDSP1 LD L2 *- *
OEE4 0 D600 0D9A STO L2 PRA&4 *
OEE6 0 C700 0000 LDSP3 LD L3 *- *
OEE8 0 D700 0DAE STO L3 PRA&27 *
OEEA 0 C700 0F31 LD L3 MSG9&8 *
OEEC 0 D700 0DA4 STO L3 PRA&14 *
OEEF 0 7201 MDX 2 1 *
OEFF 0 7001 MDX LDSP4 *
OEF0 0 62FC LDX 2 -4 *
OEF1 0 7301 LDSP4 MDX 3 1 *
OEF2 0 70EF MDX LDSP1 *
OEF3 0 4400 044C BSI L PCCO PRINT MESSAGE
OEF5 0 6600 0000 LDSP2 LDX L2 *- RESTORE IX 2
OEF7 0 4C80 0EC9 BSC I LDSP EXIT

*
* HEADING MESSAGES

88932510
88932520
88932530
88932540
88932550
88932560
88932570
88932580
88932590
88932600
88932610
88932620
88932630
88932640
88932650
88932660
88932670
88932680
88932690
88932700
88932710
88932720
88932730
88932740
88932750
88932760
88932770
88932780
88932790
88932800
88932810
88932820
88932830
88932840
88932850
88932860
88932870
88932880
88932890
88932900
88932910
88932920
88932930
88932940
88932950
88932960
88932970
88932980
88932990
88933000
88933010
88933020
88933030
88933040
88933050
88933060
88933070
88933080
88933090
88933100
88933110
88933120
88933130
88933140
88933150
88933160
88933170
88933180
88933190
88933200
88933210
88933220
88933230
88933240
88933250
88933260
88933270
88933280
88933290
88933300
88933310
88933320
88933330
88933332
88933334
88933335
88933360
88933370
88933380
88933390
88933400
88933410
88933420
88933430
88933440
88933470
88933480
88933490
88933500
88933510
88933520
88933530
88933540
88933550
88933560
88933570
88933580
88933590
88933600
88933610
88933620
88933630
88933640
88933650
88933660
88933670
88933680
88933690
88933700
88933710
88933720
88933730
88933740
88933750
88933760
88933770
88933780
88933790
88933800
88933810
88933820
88933830
88933840
88933850
88933860

2400 TIMING TEST

```

*
MSG1 DC /2439 MI
      DC /2500 N
      DC /0000
      DC /3115 AV
      DC /3700 G
      DC /0000
      DC /2431 MA
      DC /1700 X
*
MSG2 DC /3412 DS
      DC /1600 W
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
      DC /0000
*
MSG3 DC /2439 MI
      DC /2500 N
      DC /0000
      DC /3133 AC
      DC /1323 TL
      DC /0000
      DC /2431 MA
      DC /1700 X
*
MSG4 DC /010A IO
      DC /2412 MS
      DC /0000
      DC /1531 VA
      DC /2900 R
      DC /0000
      DC /2439 MI
      DC /2500 N
*
MSG5 DC /2526 NO
      DC /1300 T
      DC /2934 RD
      DC /1800 Y
*
MSG6 DC /3326 CO
      DC /2427 MP
      DC /2335 LE
      DC /1335 TE
*
MSG7 DC /1629 WR
      DC /1300 T
      DC /3423 DL
      DC /1800 Y
*
MSG8 DC /2935 RE
      DC /3134 AD
      DC /0034 D
      DC /2318 LY
*
MSG9 DC /2939 RI
      DC /3400 D
      DC /0000
      DC /2931 RA
      DC /3400 D
      DC /0000
      DC /1425 UN
      DC /3913 IT
*
MSG10 DC /3115 AV

```

```

8B933870
8B933880
8B933890
8B933900
8B933910
8B933920
8B933930
8B933940
8B933950
8B933960
8B933970
8B933980
8B933990
8B934000
8B934010
8B934020
8B934030
8B934040
8B934050
8B934060
8B934070
8B934080
8B934090
8B934100
8B934110
8B934120
8B934130
8B934140
8B934150
8B934160
8B934170
8B934180
8B934190
8B934200
8B934210
8B934220
8B934230
8B934240
8B934250
8B934260
8B934270
8B934280
8B934290
8B934300
8B934310
8B934320
8B934330
8B934340
8B934350
8B934360
8B934370
8B934380
8B934390
8B934400
8B934410
8B934420
8B934430
8B934440
8B934450
8B934460
8B934470
8B934480
8B934490
8B934500
8B934510
8B934520
8B934530
8B934540

```

2400 TIMING TEST

```

OF32 0 3700 DC /3700 G
OF33 0 3929 DC /3929 IR
OF34 0 3700 DC /3700 G
*
MSG11 DC /3329 CR
      DC /3535 EE
      DC /2700 P
      DC /0000
*
MSG13 DC /3212 BS
      DC /2700 P
      DC /3529 ER
      DC /2900 R
*
MSG14 DC /3132 AB
      DC /2629 OR
      DC /1335 TE
      DC /3400 D
*
MSG15 DC /1425 UN
      DC /3913 IT
      DC /0033 C
      DC /2624 OM
      DC /2723 PL
      DC /3513 ET
      DC /3500 E
      DC /0000
*
MSG16 DC /2729 PR
      DC /2637 OG
      DC /2931 RA
      DC /2400 M
      DC /3326 CO
      DC /2427 MP
      DC /2335 LE
      DC /1335 TE
*
OF52 012D END BEGN
NO STATEMENTS FLAGGED IN THE ABOVE ASSEMBLY

```

```

8B934550
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8B934900
8B934910
8B934920

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ACTI 02ED 0147 02FA 031F 070A 0759
 ADRS 0660 0629
 ADRS1 0C38 09ED 0A39 0C0E
 ADRS2 0C39 0A5A
 ARIA1 01E1 01D6 01D7 01DD
 ARIA2 025C 024B 024E 0251 0254 0257
 ARIA3 0281 0270 0273 0276 0279 027C
 ARIA4 02CB 02BB 02BE 02C1 02C4 02C7
 ARIA5 01F8 01E6 01E9 01EC 01EF 01F2
 AVG 0D30 0428 0C80 0D3C
 A000 0780
 A001 0789
 A002 0973
 A003 09BF
 A004 0BD3
 A007 0D5D
 BAK 01D2 01DC
 BEGAC 01CB 0157
 BEGAD 0203 01CD 0209
 BEGAE 020F 01CF
 BEGAF 0213 01F5 020E
 BEGAG 022B 0236
 BEGAI 0291 021A
 BEGAJ 02A2 02A9
 BEGAK 02D5 0249 025A 0268 027F 0288 0290 0296 02B9 02CA
 BEGAM 0269 0216
 BEGAN 0151 0156
 BEGAP 013D 0138 02D8
 BEGAR 023C 0244
 BEGAS 02AB 02B3
 BEGBA 013F 0142
 BEGIN 014F 02D5 06FB 074D
 BEGN 012D 0F52
 BEGN1 0132 012D
 BEGN2 0133 0137
 BEGX3 014E 013E
 BEGX4 02D8 0143
 BEGX5 02DA 0307
 BEGX7 02DC 0308
 BEGX8 0158 0151
 BSP 07C9 07D4 0C50 0CAA 0CDO 0D7C
 BSP12 07E2 07DF
 BSP13 07E8 07E4
 BSPX1 07E0 07D6
 BSP2 07D2 07CA 07CF 07E7
 BSP3 07CB 07CC
 BSP4 07D6 07D1
 BSP6 07DD
 BY 0BBE 0BB4
 CKERR 08C0
 CKHLT 08CA 08C4
 CKHLW 0BA0 0B1B 0B2E 0B48 0B59 0B5B
 CN1 0909 090D 093E 0978
 CN2 0910 0914 09C5 09CC 0A68 0CA6
 CODE 05DA 03EF 0406 05D1 05D5
 CODEH 060E 0293 05B0 05F1
 COM00 08D6 08E4 08EA
 COM01 08DD
 CON 066D 0153 0203 0206 022C 0233 0239 02A3 02A6 090F
 CONV 066A 020F
 CONVO 05D9 05B3 05B5 05B8 05BA 05BC
 CONV1 066B 020A
 CON1 0671 023C 023F 0241 02AB 02AE 02B0 0916
 CVTBL 05DC 05AC
 C000 076E
 C001 0765
 DCC 0373 0375 0378 0379 047B 0798 07DB 0800
 DCC2 0384 0374

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DCC3 038A 0383 0386
 DELAY 038C 038D 039A 039C 0A16 0A24 0C57
 DIND 03A2 03A3 0811
 DLYC 0C36 09EB 0A20 0A31 0A3E
 DLY1 0C33 0A14 0A18
 DLY2 0C35 0A22 0A26
 UR9 04E4 04E0 04E3
 UST 06E0 02F1 02FB 0327 0335 06EB 06EC 06ED 06F1 073E 079A 07DD 0802 082C
 08B9 0921 0A27 0ADC 0AF4 0B6C 0BC5 0C4E 0C60
 DSW 036A 031E 0320 0321 0322 0324 0325
 DSWSP 0330
 DSWX1 081D 0809 0832
 DSW0 0807 06F7 0749 07CB 07F1 081B
 DSW1 0813 080D
 DSW2 0814 080F
 DSW5 0809
 DSW7 0810 0819
 DSW8 0816 0810
 EDIT 02E4 0134 0149 01CB 0213 0245 0269 0291 02B6 02F9 0319 0473 04DE 04E7
 04F0 05A0 0702 0705 0745 0751 0754 0792 086A 0D4B 0D4E
 END 0662 078B 07EE
 ERR 0D32 0D08
 ERR1 036F 032C
 ERR1 0D38 0D19
 ERR2 0D3B 0D24
 E001 0D6D
 E003 07EC
 E004 07B5
 E005 096C
 E006 09B8
 E007 0D29
 E008 0D74
 FNC 03AE 03A7
 FORM0 0869 0863
 FORM1 08D1 0864
 FORM2 08D9 0865
 FORM3 08DF 0866
 FORM4 08E5 0867
 FORM5 08EB 0868
 FWD1 0287 026F
 FWD2 02D1 02B7
 FWRD 0262 0247
 GPHLM 06DB 01F3 0223 0226 0228 0258 0266 026D 027D 028A 029F 02D3 0AE8 0AEE
 GRL1 0B6A 09DA 0B77 0BC8 0BF1
 HALT 03B2 03BC 0666 0716 08CD
 HEDEC 05A6 03ED 0404 05D6
 HEDE1 05B0 05C6
 HEDE2 05B4 05BE
 HEDE3 05BF 05B6
 HEDE4 05C8 05A7
 HEDE5 05CA 05A8
 HEDE6 05CC 05A9
 HERE 0436 0420 0432
 HERE1 0428 0424
 HERE2 042F 042B
 HEXCD 060C 0416 049E 04A7 04B4 04BE 04CA 05FB 05FF
 HEXCV 05E6 0414 049C 04A5 04B2 04BC 04C8 0604
 HEXC1 05ED 05EE 05F7
 HEXC2 0600 05E8
 HEXC3 0602 05E7
 HEXWD 0606 0412 049A 04A3 04B0 04BA 04C6 05EA
 HEX00 0607 05F3 05F8 05FA 05FC 05FE
 HI 0D2F 042F 0C7E 0D39
 H0205 0528 04FA
 H023B 0527 04F6
 H0400 0526 04FD
 H2020 0529 0503
 ILSW 0366 0316 0337

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INPSE 06DF 0221 029D 0884 08A8
 INTR 082A 07A8 0820
 INTRR 031E 031A
 INTRT 081F 0822 0825 0827 0945 094A 097D 098C 0A0D 0A65 0C66 0C96 0CB4
 INTR2 0829 0824 082E
 INTR3 0312 02EE
 IOA 0E9C 06EE 0728 0838 0CA2
 IOARA 065E 063C 0645 0647 065A
 IOCC1 0380 03AA 03AB
 JDLY2 0391 0399
 JDLY3 0398 0393
 JDLY4 039A 0396
 KBSC 0C2E 0A5D
 KDASH 0C41 0B0B 0B14
 K000E 08D0 08C2
 K0010 0C3B 0A57
 K0020 0465 0425 042C 0433
 K006 0464 041E
 K007 0463 03E5 0B06
 K0086 0C3A 09F0 0A36
 K0200 0525 0500
 K0300 0524 04ED
 K0700 0522 04E2
 K0900 0523 04E4
 K2121 0661 0634
 K8000 0D31 0D32 0D38 0D3B
 LDLEFA 0C49 0C4C
 LDLEFB 0C4E 0C5F
 LDLEFC 0C57 0C5B
 LDLEFE 0C69 0C47
 LDLEFT 0C46 0C2E 0C6B
 LDLEFX 0C6F 0C55 0C59
 LDLEFY 0C5C 0C56
 LDSP 0EC9 0844 0EF7
 LDSP1 0EE2 0ECC 0EF2
 LDSP2 0EF5 0ED4 0ED8
 LDSP3 0EE6 0ED1
 LDSP4 0EF1 0EEF
 LDSP5 0EDC 0EDF
 LINE 0C42 09FB 0AF2 0AF8 0B04 0BE7
 LNSW 0B9E 09DE 0B61 0B64 0BCA 0BE8
 LN3 0E4D 0511
 LN3A 0E50 04EE
 LN3B 0E51 04E5
 LN3C 0E5A 04F8 0501 0505
 LN3D 0E58 050F
 LOADK 0C19 0516 0AD3 0B0D 0BBF 0C1F
 LOADV 0C21 0513 0AD9 0BAB 0BAF 0BB8 0BBC 0C2C
 LOAD1 0C1B 0C1E
 LOG 061E 0561 0658
 LOGAB 047E 0466
 LOGAC 0466 0439 045D
 LOGAD 046C 046B
 LOGAE 0480 0483
 LOGAF 0488 048B
 LOGBC 052A 043B 045F
 LOGC 038F 044A 0454 08B5
 LOGCA 0556 053B
 LOGCB 055C
 LOGCC 0561 0539
 LOGC1 0534 0560
 LOGC2 053A 0555
 LOGC3 053F 053E
 LOGC4 0548 0547 0556 0559
 LOGC5 054A 0543 0558
 LOGC6 055A 054E
 LOGC7 0563 03C0 052C
 LOGC8 0565 03C2 052D

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LOGC9 0567 03C4 052E
 LOGD0 0499 03D4
 LOGIC 03CC 03CF
 LOGV1 03E0 03DE
 LOGV2 03EA 03F8 03FC
 LOGX3 05A0 047D
 LOGX8 05A4 05A1
 LOGX9 05A5 05A2
 LOGO1 063A 062A 0635 064B 0650 0651
 LOGO2 0654 0630
 LOGO3 064B 0644
 LOGO4 062C 062B 0632 0637 0639 064F
 LOGO5 0637 0653
 LOG2C 03D6 04D1
 LOG3C 03FE 03E7 040F
 LOG4C 0408 0418
 LOG5C 0411 03FF
 LOG6C 0419 03DF 03FD 0410
 LOG7C 0444 044D 0490 0569
 LOG8C 0446 044E
 LOG9C 0448 044F
 LOW 0D2E 0421 0C7C 0D33
 LUX00 056B 052B 054C 0550 055E
 LUX02 056C 0535 053A 0544 0552 0554
 LUX03 056D 054F 055B
 LOX04 056E 0541 0557
 MD1LM 06DA 0237
 MD3 04ED 04E9 04EC
 MK15 043E 03B3 0476 061F
 MK27 0440 03B5 0477 0621
 MLG 083C 0761 076A 077C 0785 07B1 07E8 0846 08BB 08C5 08C7 0968 096F 09B4
 09BB 08CF 0D25 0D59 0D69 0D70 0ECA 0ECD 0ECF 0ED2
 MLGE 08C5 08CF
 MLGX0 0901 084C 085A 08AF
 MLGX1 0902 087C 08A1 08B2
 MLGX2 0903
 MLGX3 0904
 MLGX4 0905
 MLGX7 06DC 01DE 020C 0211 0872 0896
 MLGX8 0906
 MLGX9 0907 0886 08AA
 MLGOA 08BB 08C9
 MLG00 085F 085E
 MLG02 0863 0861
 MLG03 086A 08D8 08F5
 MLG04 08B5
 MLG05 08B7 084E
 MLG06 08B9
 MLG07 08AF 087F 088C 08A3 08DE
 MLG10 086F 0879
 MLG11 088D 086D
 MLG12 088E 089E
 MLG15 08A5 08AE
 MLG16 0881 088B
 MLG18 08C7 0842
 MOD0 08F7 03C6 03D0 0451 0467 08B4
 MOD00 08FD 0870 0876 0882 0888 088F 089B 08A6 08AB 08D2 08DA 08E0 08E6 08F0
 MOD01 08FE 08D4 08E2 08E8 08ED
 MOD02 08FF 08D6 08F3
 MOD03 0900
 MOD1 08F8 08BF 08DC
 MOD1S 06DD 021F
 MOD2 08F9 0858
 MOD2S 06DE 029B
 MOD3 08FA 0852
 MOD4 08FB 0849 08C0
 MOD5 08FC 0854
 MONE 039E 0391



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MONR1 0771 0734 073B 0961 09AA 0C13 0D2B
 MONT 02EF 014C 0668
 MONTC 073D 0715 0784
 MONTD 070C 075E 0777
 MONTE 0761 074C
 MONTF 0720 071B
 MONT1 02F1 02F4
 MONT4 06F0 0300
 MONT6 071C 07B7
 MONT7 072C 0720
 MONT8 0728 072B
 MONT9 0734 072E 0732 073C
 MONX0 075F 0708 0757
 MON10 071E
 MON11 076A 06FA
 MON22 0718 0710
 MON23 0716 070F
 MON24 0785 0714
 MON25 0711 06F6 0744 0747 0769 0770 0783
 MSG1 0EF9 0D28 0D5C 0D6C 0D73
 MSG10 0F31 0BD1
 MSG11 0F35 0D27 0D5B 0D6B 0D72
 MSG13 0F39 07EA
 MSG14 0F3D 07B3
 MSG15 0F41 077F
 MSG16 0F49 0788
 MSG2 0F01 07B4 07EB
 MSG3 0F09 096B 0972 09B7 09BE
 MSG4 0F11 0BD2
 MSG5 0F19 0763 076C
 MSG6 0F1D 077E 0787
 MSG7 0F21 096A 0971
 MSG8 0F25 09B6 09BD
 MSG9 0F29 0EEA
 MST 03A0 0392 0398
 MTAAB 0B53 0B4A
 MTAAC 0B50 0B55
 MTAAD 0B56 0B52
 MTTX1 0925 071E 093C 0976 088E 0C82 0D77
 MTTX3 0926 093D 0942 094F 0977 0982 098F 0997 09C3 09CA 09D0 09FD 0A4A 0A4C
 0A52 0AAA 0AAC 0AB2 0B8C 0B90 0BE5 0C72
 MTTX6 0927 0726
 MTTX9 0928 093F 097A 0A09 0C62 0C92 0CD2
 MTTYA 0934
 MTTYB 092D 0AA7
 MTTYC 092E 08E5 09D4 0A83 0A85 0AB5 0AB9 0BEB
 MTTYD 0930 03E8 08E7 09D6 0A8A 0A8C 0BED
 MTTYE 0932 08E9 09D8 0A99 0A9B 0BEF
 MTTYF 0936 0894 0B97
 MTTY4 0929 0835
 MTTY5 092A 0AA1
 MTTY8 092B 07AB
 MTTY9 092C 0989 0A61 0CB0
 MTTZ0 0938 0AB7
 MTTZ1 0939 0A01 0A05 0BD5 0BD9 0C00 0C04
 MTT01 093C 0735
 MTT02 0976 0736
 MTT03 09C2 0737
 MTT04 09C9 0738
 MTT05 09CF 0739
 MTT07 0C71 073A
 MT1X0 0679 021D 0231 0299 0965 09AE
 MT101 093F 09C7
 MT102 094A 0944
 MT103 0961 096E 0975
 MT104 0968 0957 095B
 MT105 0963 0951

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MT107 096F 0960
 MT108 0954 0967
 MT200 097A 09CE
 MT201 09AC 099A
 MT202 099D 09B0
 MT203 09B1 0984
 MT204 09B4 09A0 09A4
 MT205 09BB 09A9
 MT206 09AA 09BA 09C1
 MT208 0989 09B3
 MT209 0995 0992
 MT5Q6 0C30 09F3 0A47 0C08
 MT5Q7 0A1C 09F5 0A1B 0A49 0A5F 0C09
 MT5XA 0675 024C 024F 0252 0255 0262 0264 0271 0274 0277 027A 028C 028E 02B4
 02BC 02BF 02C2 02C5 02C8 02D1 0ADE 0AE2
 MT5XE 0C43 0939 0AE0 0AE4 0AE6 0AEA 0AEC 0AFO 0B27 0B3E 0B44
 MT5XF 0C45 0A4E 0AAE 0ACA
 MT5X0 067A 01E7 01EA 01ED 01F0 0A2F 0C38 0C39
 MT5X2 0C3C 0AFE
 MT5X3 0C3D 09F6 0A43 0C0B
 MT5X4 0C3E 09FF 0A9D 0A9F 0AA5 0BFB
 MT5X8 0C3F 0B31 0B39
 MT5X9 0C40 0B4B 0B53
 MT50A 0A2F 09EF 0A3B 0A5C 0C0F
 MT50B 0B75 0B72
 MT50C 0B83
 MT50D 0A68 0AA4 0AB1
 MT50F 0A8E 0A95 0AA9
 MT502 0A10 0A35 0A51
 MT503 0A19
 MT505 0A24
 MT506 06D7 09E9 0A3C
 MT508 0A27
 MT509 0A2D 09F2 0A33 0A38 0A59 0A60
 MT51A 0B3C 0B29
 MT51B 0B59 0B25 0B38 0B41 0B47
 MT51C 0B88 0B63
 MT51D 0B27 0A03 0A07 0B96 0B99 0BD7 0BDB 0C02 0C06
 MT51E 0B2E 0936
 MT51F 0B39 0B30
 MT510 0C24 0C22 0C29
 MT516 0AE6 0B9C
 MT517 0B0B 0AFC 0B00 0B03 0B08
 MT518 0B73 0B6F
 MT519 0B23 0B60
 MT520 0BC3 0AD0
 MT521 0BC5 0AC3
 MT522 0C15 0C98
 MT523 0C17
 MT524 0AB5 0ABC
 MT525 0AC7 0AC2 0ACF
 MT526 0B79 09DC 0B67 0B7F 0BCB 0BF3
 MT540 06A5 0A12
 MT562 0BA9 0B92
 MT572 0B70
 MT573 09E1 09E4
 MT580 0AC3 0AC1
 MT581 0BDD
 MT582 0C13 0BE3
 MT583 0A61 0A56 0C11
 MT584 0B9C 0B8B
 MT585 0BF7 0BFA
 MT590 0A57 0A42
 MT591 06B1 0C53
 MT6X8 0C70 0C88
 MT7XA 0D8C 0C9F
 MT7XC 0D8D
 MT7XE 0D8E 0D1C

DATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
 cC NO. 415178 415233 411731 411857 411875 431319 431319A
 PROG ID 08B9-2
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ATE 01JUL66 01NOV66 15MAY67 01SEP67 01OCT67 14NOV69 30JAN70
 cC NO. 415178 415233 411731 411857 411875 431319 431319A
 PROG ID 08B9-2
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MT7X0 0D84 022E 0874 0892 0899 0C8E
MT7X1 0D85 0C90 0CFB
MT7X2 0D86 0C86 0CC3 0CD9 0D79
MT7X3 0D87 0C74 0CCB 0CDF 0CE7 0CED 0CF1 0CF7
MT7X4 0D88 0C76 0CFF 0D36 0D48
MT7X5 0D89 08EB 0C78 0CE3 0CE5 0D1A 0D20 0D46 0D47
MT7X6 0D8A 0C7A 0CDD 0D01 0D10 0D40
MT7X7 0D8B 0C84 0CBD 0D42
MT7Y0 0D8F 0CE1
MT7Y1 0D90 08EE 0C89 0CE9 0CEF 0D03 0D05 0D0A 0D0C 0D56
MT7Y2 0D91 08F1 0C8C 0CF3 0CF9 0D0E 0D12 0D17 0D18
MT70B 0CD2
MT70F 0CDD 0CCF
MT700 0C8E
MT702 0CA8
MT703 0CA9 0CAF 0CDC
MT704 0CAC 0CA9
MT705 0CDO 0CCE
MT710 0CC6 0CC5
MT711 0D3E 0CC1
MT712 0D2B 0D5F 0D6F 0D76
MT713 0CC3 0CBF 0D43
MT714 0D45 0D21
MT715 0D20 0D1E
MT716 0D70 0D3D 0D4A
MT718 0D77 0CFD
MT719 0C92 0D82
MT730 0D2D 09E5 0BCC 0BFC
MT744 0CFE
MT745 0D7E 0D7B
MT746 0D7B 0D81
MT750 0CF1 0CEC
MT751 0CFB 0CF6
MT754 0D0E 0D37
MT755 0D1A 0D14 0D3A
MT760 0D52 0D4D 0D60 0D62
MT761 0D60 0D50
MT762 0D63 0D54
MT763 0D69 0D57
NOTE 0DEA 0BB6
NOTE1 0E31 0764 076D 0BBA
NTDSH 0AFE 0B0A
ONE 036E 0217 0323 0485 08BD 0B19 0D34
OPARA 05E1 05AA 05CE 05D0 05D2 05D4
PART2 01E6 01E0
PCCO 044C 0453 0518 0AD5 0B81 0BC1 0C2A 0EF3
PCCX1 0461 0450 0AFA
PGCM 073C 073C 0775
PGSW 093B 02F7 0711 0767 077A
PID 02DE 0856
PRA 0D96 03C8 04A0 04A9 04AC 04B6 04C0 04C3 04CC 04CF 0B17 0B34 0B36 0B4E
0B50 0EE4 0EEC
PRA1 0D94 0B12 0B75 0B86
PRA3 0D93 0B10 0B7D 0B84 0C1B 0C26 0EE8
PRA4 0D92 03CC 0426 042D 0434 0458 0470 0530 0532 05A3 062C 063A 0660 0B1F
0EDC
PRDWT 0830 083A 0940 097B 098A 0A0B 0A63 0C64 0C94 0CB2 0CD4
PRO2 0587 0570 0572
PRSP 059F 052F
PRSW 0B9F 09E7 0B65 0B68 0BCE 0BFE
PRWC 0491 046E
PRO0 0573 056E
PRO1 057E 056F
PRO3 0594 0571
PR3 0DB2 0AD7 0BA9
PR4 0DCE 0BAD
PR6 0BA1 0B70
PR7 0BA5 0B73

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PRB 0E06 0B7B
RAD 02E0 0488 0730
RDSWS 0306 02FF 030D 0771 083D
RID 02DF 02F6 03E3 041C 04AE 0700 071C 0721 075C 0773 0850 087A 089F
RST 04D3 051C 06FD 074F
RSTX2 051E 04D9
RSTX3 0520 04DA
RST1 04DA 04DD
RST2 04DE 04D7
RTN1X 090F 090A
RTN2X 0916 0911
RWD 07F0 0723 0778 07FE 0987 09B1 0A54 0BDD
RWDX0 0804 07D9 07F2
RWDX1 0805 07F4
RWD2 07FE 07FB
RWD3 07F1 07F8 0803
RWD4 0800 07FD
SENSE 065C 0623 063E
SNSPR 0442 0472 0475 0478 0480 0484 0487 0488
SNWC 07BA 079B
SNWC1 07BB 0794
SPEC 066C 01D3 01D9 0B1D
STCN 0917 090C 0913 0923
STCN1 0919 090B 0912 0920
STCN2 091F 091D
STCN3 0921 091E
SVE 051A 0507
SVEXT 0359 0352
SVINT 0332 014E 031C 0359
SVINO 033E 034E 0357
SVINI 0340 0348
SVIO 0364 0333 0344 0345 0358
SVO 035B 034C
SV1 035C 033E
SV2 035D 033C
SV3 035E 0353
SV4 035F 033B 0340 0349 034B 0356
SV5 0360 033F 0342 0346
SV6 0361 033D 0343 0354
SV7 0362 0338 034F
SW 0462 03E1 03F4 03F6 09F8 0A40 0A45 0C0C
SWO 02E1 02DA 0436 045A 04D4 0663 06F3 070C 0740 083F 08CA 095C 09A5 0ABD
0BB1 0BDF 0ED5
SW1 02E2 02DC 0309 030C 0718
SYDR 050D 0509
SYDR1 050F 050C
TAAQ 0368 0311 0315
TADSW 036C 0326 0329 032D
TADWC 036D 032A
TAX1 030F 0314 031D 032F
TERM 02E3 0334 0419 0456 0536 062E
TERR 0371 0317
TMIC 0500 04F2 04FF
TMICA 04FD 04F4
TMICB 0505 04FC
TMRDT 07C0 07C4 0993 0995 0A6A 0A6C 0A72 0A74 0A7A 0C17 0CB7 0CB9 0CBB 0CD6
TMRD1 0790 07C3
TMRD4 07C4 098E 0A67 0CB6
TMRX2 07C7 079D
TMRX3 07C8 07C1
TMWRT 078D 07AF 07C5 0948 094D 0980 0985 0A10 0A19 0A1E 0A29 0A2B 0C15 0C49
0C99 0C9B 0C9D 0CA4
TMWR0 07A5 0795 079F
TMWR1 079A
TMWR2 079B 07A3
TMWR3 07A1
TMWR4 07AA 07C6 0947 094C 097F 0A0F 0C68
TMWR6 07B1 07AE



2400 TIMING TEST

TMWR7 07B7
TMWR8 07AF
TMWR9 079F 078F 07C2
TOTA 0E69 092D 09E1 0A70 0A78 0A7E 0A80 0A87 0A8E 0A90 0A96 0AC7 0ACC 0B23
0B2C 0B3C 0B42 0B57 0BF7
TRK9 0804 0AF6
TURA1 06D9 021B
TURA2 06D8 0297
TWRX0 07BC 0797 07A1 0952 0963 099B 09AC 0A6E 0A76 0A7C 0CC8 0D3E
TWRX1 07BD 0790
TWRX2 07BE
TWRX3 07BF 078E
UNMK3 0302 02FD 03B8 048C 0654
UNMK4 0304 02FE 03BA 048E 0656
WAITA 0372 300A
WAIT1 0148 3001
WAIT2 03B7 3002
WAIT3 047A 3003
WAIT4 0626 3004
WAIT5 07A7 3005
WAIT6 07DE 3006
WAIT9 0370 3009
WC 03BE 03DB 03DC 03FA 040D
WDCON 05E5 03EB 0402 05AE
WORD 05D8 05AF 05B4 05B9 05C0
WRDSW 065F 0628 0642 0648 064D
WRITE 065A 063D
XIOSN 063E 0641
XIOWR 063D 064A
END OF ASSEMBLY

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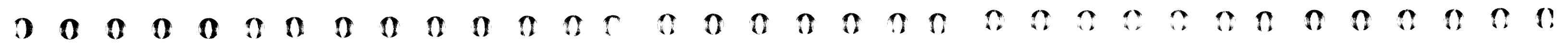


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3. USE PROCEDURE

- 3.1 PROGRAM LOADING
- 3.1.1 ON TAPE DRIVE(S) TO BE TESTED,
1. LOAD TAPE REEL.
2. DEPRESS LOAD-REWIND KEY.
3. DEPRESS START KEY. AFTER TAPE REWINDS TO LOAD POINT, DRIVE(S) SHOULD BECOME READY.
- 3.1.2 REFER TO RELOCATABLE DIAGNOSTIC LOADER DOCUMENTATION FOR LOADING PROCEDURE.
- 3.1.3 IF OPTIONS ARE DESIRED, GO TO 3.2.2.
- IF NO OPTIONS ARE DESIRED, GO TO 3.2.1.
- 3.2 OPERATION
- 3.2.1 TYPICAL OPERATING PROCEDURE
- IF NO OPTIONS ARE SET THE PROGRAM ASSUMES,
1. BOTH DRIVES ARE TO BE RUN.
2. OUTPUT DEVICE IS TO BE 1053/1816 TYPEWRITER.
- TO EXECUTE PROGRAM DEPRESS THE START BUTTON.
- 3.2.2 OPERATING OPTIONS
- IF OPTIONS ARE DESIRED, SET SWITCHES DESIRED FROM TABLES 1 AND 2 AND DEPRESS THE START BUTTON.

TABLE 1 GENERAL CONTROL

1. SWITCHES MAY BE SET PRIOR TO PROGRAM LOADING OR AT WAIT 1.
2. SWITCHES 0-1 MAY BE CHANGED ONLY BY A RESET-START OPERATION.
3. SWITCHES 5-15 MAY BE CHANGED ANYTIME.

* DATA ENTRY SWITCHES * DESCRIPTION *															
* 0	* 1	* 2	* 3	* 4	* 5	* 6	* 7	* 8	* 9	* 10	* 11	* 12	* 13	* 14	* 15
*
*
*
*
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1. PURPOSE

THE MAGNETIC TAPE TIMING TEST (MTTIM) IS DESIGNED TO TEST WRITE AND READ DELAY, INTER-RECORD GAP, CREEP AND ERASE HEAD ON THE 2400 MAGNETIC TAPE UNIT SERIES FOR COMPLIANCE WITH THE PRODUCT SPECIFICATIONS. THE PROGRAM IS ABLE TO TEST,

1. SYSTEMS WITH ONE OR TWO TAPE DRIVES.
2. DRIVES WITH 9 TRACK OR 7 TRACK READ-WRITE HEADS.
3. MODELS 1, 2, OR 3 WITH 2 OR 4 USEC STORAGE.

IF SYSTEM HAS TWO DRIVES, BOTH DRIVES MAY BE SEQUENTIALLY TESTED IN ONE CONTINUOUS RUN OF THE PROGRAM.

2. PREREQUISITES

THIS PROGRAM ASSUMES THAT THE 2400 MAGNETIC TAPE FUNCTION TEST RUNS AND NO TAPE CONTROL ERRORS EXIST. EQUIPMENT REQUIRED CONSISTS OF,

1. 1442 CARD READ/PUNCH OR 1054 PAPER TAPE READER.
2. 1053 OR 1816 TYPEWRITER, OR 1443 PRINTER.
 A. IF 1443 IS USED - A CARRIAGE TAPE WITH AT LEAST CHANNEL 1 PUNCHED SHOULD BE USED.
3. 1800 PROCESSOR CONTROLLER.
4. ONE OR TWO 2400 SERIES MAGNETIC TAPE DRIVES.
5. THIS PROGRAM REQUIRES THE RELOCATABLE DIAGNOSTIC LOADER.

TABLE 2 LOOP ROUTINE

1. THESE SWITCHES CAN BE CHANGED AT ANY TIME.
2. IF ZERO IS ENTERED, THE PROGRAM WILL NOT LOOP BUT WILL RUN ALL ROUTINES IN SEQUENCE.
3. IF IT IS DESIRED TO START ON A ROUTINE OTHER THAN ROUTINE 1, AND CONTINUE

THE TEST FROM THAT POINT,

- A. SET STARTING ROUTINE PER TABLE 2.
- B. START PROGRAM.
- C. WHILE PROGRAM IS RUNNING SELECT ROUTINE ZERO.
- D. PROGRAM WILL COMPLETE THE SELECTED ROUTINE AND THEN RUN THE REMAINING ROUTINES IN THEIR NORMAL SEQUENCE.

```

*****
* PROGRAM SWITCHES          * DESCRIPTION          *
* 5 6 7                    *                   *
* X X X.....ENTER A ROUTINE NUMBER FROM 0 TO 7. *
*                               *
*****

```

3.3 TERMINATING PROCEDURE

1. THE PROGRAM WILL TERMINATE WHEN ALL DRIVES WHICH ARE SELECTED HAVE BEEN TESTED.
2. THE PROGRAM WILL TERMINATE IF ERROR PRINTOUT E003 OCCURS. (SEE SEC. 4.3)

3.4 RESTART PROCEDURE

PRESS THE STOP, RESET AND START BUTTONS. THE PROGRAM SHOULD GO TO WAIT 1. IF THIS DOES NOT OCCUR, THE PROGRAM MUST BE RELOADED.

3.5 PROGRAM HALTS

PROGRAM WAITS ARE USED IN THIS PROGRAM, AND ARE IDENTIFIED BY REFERENCING THE B REG AND I REG.

A PROGRAM WAIT IS OF THE FORM,

30XX, (B REG).

A DESCRIPTION OF THE INDIVIDUAL PROGRAM WAITS CAN BE FOUND AT THE BEGINNING OF THE PROGRAM LISTING. A TYPICAL WAIT DESCRIPTION FOLLOWS. IT IS INCLUDED TO SHOW THE FORMAT OF THE LISTING, AND IT IS NOT NECESSARILY A DESCRIPTION OF AN ACTUAL WAIT.

```

*****
3001 0 014C          DC      WAIT1+1  WAIT FOR DATA ENTRY
*                               SWITCHES TO BE SET.
*                               PUSH START TO
*                               CONTINUE THE PROGRAM.
*****

```

B REG, (FIRST 4 DIGIT GROUP) CORRESPONDS TO B REG READING.

I REG, (SECOND 4 DIGIT GROUP) CORRESPONDS TO I REG READING.

4. PRINTOUTS
PID AND MID AS SEEN IN DESCRIPTIONS BELOW WILL NOT BE FOUND IN PRINTED HEADI INSTEAD, A DESCRIPTION OF THE MESSAGE WILL BE PRINTED.

4.1 COMMAND MESSAGES

```

PID MID RID RAD UNIT
          NO.
B900 C000 XXXX XXXX 000X
          DRIVE 0 IS NOT READY

B900 C001 XXXX XXXX 000X
          DRIVE 1 IS NOT READY

```

4.2 INFORMATION PRINTOUTS

```

PID MID RID RAD UNIT
          NO.
B900 A000 0008 XXXX 000X
          ALL ROUTINES ARE COMPLETE

B900 A001 0008 XXXX 000X
          PROGRAM IS COMPLETE.

```

```

          MIN TIME MAX
          LIM FND LIM
B900 A002 XXXX XXXX 000X XXXX XXXX XXXX
          WRITE DELAY TIMING, PRINTED IF BIT 6 OF DATA ENTRY SWITCHES IS ON. (
          IF RID IS 0001-TIME FND IS FOR A WRITE WHEN AT LOAD POINT.
          IF RID IS 0003-TIME FND IS FOR A WRITE WHEN NOT AT LOAD POINT.

```

```

          MIN TIME MAX
          LIM FND LIM
B900 A003 XXXX XXXX 000X XXXX XXXX XXXX
          READ DELAY TIMING, PRINTED IF BIT 6 OF DATA ENTRY SWITCHES IS ON. (M
          IF RID IS 0002-TIME FND IS FOR A READ WHEN AT LOAD POINT.
          IF RID IS 0004-TIME FND IS FOR A READ WHEN NOT AT LOAD POINT.

```

```

          IO
          MSEC VAR MIN
          AVG AVG AVG
B900 A004 0005 XXXX 000X XXXX XXXX XXXX
          INTERRECORD GAP AVERAGES FOUND BY ROUTINE 5. (INCHES)

```

```

          MIN AVG MAX
          CREEP CREEP CREEP
          FND FND
B900 A007 0006 XXXX 000X XXXX XXXX XXXX
          FORWARD CREEP FOUND. (INCHES).

```

4.3 ERROR PRINTOUTS

```

PID MID RID RAD UNIT MIN AVG MAX
          NO. CREEP CREEP CREEP
          FND FND
B900 E001 0006 XXXX 000X XXXX XXXX XXXX
          CREEP WAS LESS THAN .05 (INCHES).

```

```

          DSW
          RECEIVED
B900 E003 XXXX XXXX 000X XXXX
          DSW WRONG AFTER BACKSPACE, PUSH START TO RESTART PROGRAM.

```

DSW
RECEIVED



B900 E004 XXXX XXXX 000X XXXX
TEST ABORTED DUE TO DSW OR UNEXPECTED INTERRUPT.
IF DSW IS-FFFF-INTERRUPT OCCURRED ON A LEVEL OR ILSW
BIT OTHER THAN THAT EDITED FOR MAGNETIC TAPE.

MIN TIME MAX
LIM FND LIM

B900 E005 XXXX XXXX 000X XXXX XXXX XXXX
WRITE DELAY TIMING ERROR. (MSEC)
IF RID IS 0001-ERROR IS FOR A WRITE WHEN AT LOAD POINT.
IF RID IS 0003-ERROR IS FOR A WRITE WHEN NOT AT LOAD POINT.

MIN TIME MAX
LIM FND LIM

B900 E006 XXXX XXXX 000X XXXX XXXX XXXX
READ DELAY TIMING ERROR. (MSEC)
IF RID IS 0002-ERRCR IS FOR A READ WHEN AT LOAD POINT.
IF RID IS 0004-ERROR IS FOR A READ WHEN NOT AT LOAD POINT.

MIN AVG MAX
CREEP CREEP CREEP
FND FND

B900 E007 0006 XXXX 000X XXXX 0000 XXXX
CREEP WAS ZERO. (INCHES).
NOTE ANY NEGATIVE CREEP VALUE IS PRECEDED BY A MINUS SIGN.

MIN AVG MAX
CREEP CREEP CREEP
FND FND

B900 E008 0006 XXXX 000X XXXX XXXX XXXX
CREEP WAS NEGATIVE. (INCHES)
NOTE EACH NEGATIVE CREEP VALUE IS PRECEDED BY A MINUS SIGN.

NOTE

PROGRAM ID, ROUTINE NUMBER, ROUTINE ADDRESS, MESSAGE ID, UNIT
NUMBER AND DSW ARE ALWAYS PRINTED IN HEXADECIMAL. ALL
OTHER WORDS OF ANY MESSAGE ARE PRINTED IN DECIMAL. ON
DECIMAL PRINTOUTS, ASSUME A DECIMAL POINT AS SHOWN BELOW.
WHERE PRINTOUT IS IN INCHES XX.XX
WHERE PRINTOUTS ARE IN TIME MODEL 3 MODEL 1 AND 2
XX.XX XXX.X

IN ADDITION TO THE ABOVE PRINTOUTS, ROUTINE 5 PRINTS A PLOT
OF RECORD GAP VS WRITE GO DOWN TIME.

5. COMMENTS

A. MTTIM CONSISTS OF A MAGNETIC TAPE TIMING MONITOR ROUTINE, A SERIES OF
COMMON MAGNETIC TAPE SUBROUTINES, AND A SERIES OF TESTS. SECTION 5.B
GIVES A DESCRIPTION OF EACH OF THE COMMON SUBROUTINES AND THEIR
CALLING SEQUENCES. SECTION 5.C GIVES A DESCRIPTION OF EACH OF THE
TEST ROUTINES.

THERE IS ONE TABLE AROUND WHICH ALL ROUTINES ARE ORIENTED. THIS
TABLE IS THE DEVICE STATUS TABLE, CALLED DST. INDEX REGISTER 1
ALWAYS CONTAINS THE NUMBER OF THE TAPE DRIVE BEING USED AND INDEX
REGISTER 2, THE BASE ADDRESS OF THE DST TABLE. THE DST TABLE IS THE
BASIC MEANS OF COMMUNICATION BETWEEN ROUTINES.

B. COMMON SUBROUTINES

EACH SUBROUTINE ASSUMES THAT INDEX REGISTER 1 CONTAINS THE UNIT
IDENTIFICATION AND INDEX REGISTER 2 CONTAINS THE BASE ADDRESS OF THE
DST TABLE.

CALL NAME

BSI L BSP
USE- BACKSPACE ONE RECORD.

BSI L DCC
DC ADRS. OF CALL STRING
USE- BUILD THE PROPER IOCC WORDS FROM THE CALL STRING AND ISSUE THE XIO
COMMAND.

BSI L DELAY
DC NUMBER OF LOOPS.
USE- DELAY 25 USEC FOR EACH LOOP SPECIFIED.

BSI L DIND
DC ADRS. OF AREA CODE
DC ADRS. OF MODIFIER
USE- BUILD THE PROPER IOCC WORDS AND SENSE THE DEVICE.

BSI L DSWO
USE- CALLS ON SUBROUTINE DIND AND RETURNS WITH THE DSW STORED IN THE
DSW TABLE AND IN THE A REGISTER.

BSI L HALT
USE- MASKS ALL INTERRUPT LEVELS AND WAITS FOR OPERATOR ACTION.

BSI L INTRT
DC RETURN ADDRESS
USE- SAVES FOR USE AFTER THE NEXT INTERRUPT THE RETURN ADDRESS
SPECIFIED IN THE CONSTANT.

BSI L LOG
USE- DETERMINE THE DESIRED OUTPUT DEVICE, CONVERT AND PRINT THE MESSAGE
CODE SET UP BY SUBROUTINE MLG.

BSI L MLG
DC ADRS. OF LEFT HALF OF HEADING
DC ADRS. OF RIGHT HALF OF HEADING
DC MESSAGE ID
DC LINE AND FORMAT NUMBER.
USE-1. SETS UP THE HEADING TO BE PRINTED AND CALLS ON PCCO.
2. SETS UP THE MESSAGE TO BE PRINTED AND CALLS ON LOG.

BSI L PCCO
USE- DETERMINES THE DESIRED OUTPUT DEVICE AND PRINTS A PRESET MESSAGE
WITH NO CONVERSION.

BSI L RWD

2400 TIMING TEST

2400 TIMING TEST

USE- REWINDS THE DRIVE SPECIFIED BY XR1.

BSI L TMRDT
USE- READS THE UNIT SPECIFIED BY XR1 AND TIMES THE LENGTH OF TIME TO A CHANGE IN THE WORD COUNTER.

BSI L TMWRT
USE- WRITES ON THE UNIT SPECIFIED BY XR1 AND TIMES THE LENGTH OF TIME UNTIL THE WORD COUNTER HAS CHANGED TWICE.

C. TEST ROUTINES

RTN. NO. DESCRIPTION

1 THRU 4 THESE ROUTINES TIME READ AND WRITE DELAYS AND CHECK FOR TIME BEING WITHIN LIMITS. IF TIME IS OUTSIDE LIMITS AN ERROR PRINTOUT IS GIVEN. TIME WHICH IS WITHIN LIMITS IS PRINTED ONLY IF REQUESTED. (SEE TABLE 1)

1 WRITE DELAY AT LOAD POINT.

2 READ DELAY AT LOAD POINT.

3 WRITE DELAY NOT AT LOAD POINT.

4 READ DELAY NOT AT LOAD POINT.

5 INTERRECORD GAP TEST

THIS ROUTINE WRITES A SERIES OF RECORDS WITH CONTROLLED GO LINE DOWN TIME BETWEEN RECORDS. THE SEQUENCE IS --

- A. WRITE A RECORD
- B. DELAY 10 MILLISECONDS
- C. WRITE A RECORD
- D. LOAD THE LEFT COLUMN IF NEXT VARIABLE DELAY IS 2.0,3.0,4.0, OR 5.0 SECONDS.
- E. WRITE A RECORD
- F. DELAY A VARIABLE TIME (0.5MILLISECONDS TO 5 SECONDS)
- G. WRITE A RECORD
- H. WRITE A RECORD

THE ABOVE SERIES IS REPEATED 47 TIMES WITH THE VARIABLE DELAY INCREASING EACH TIME.

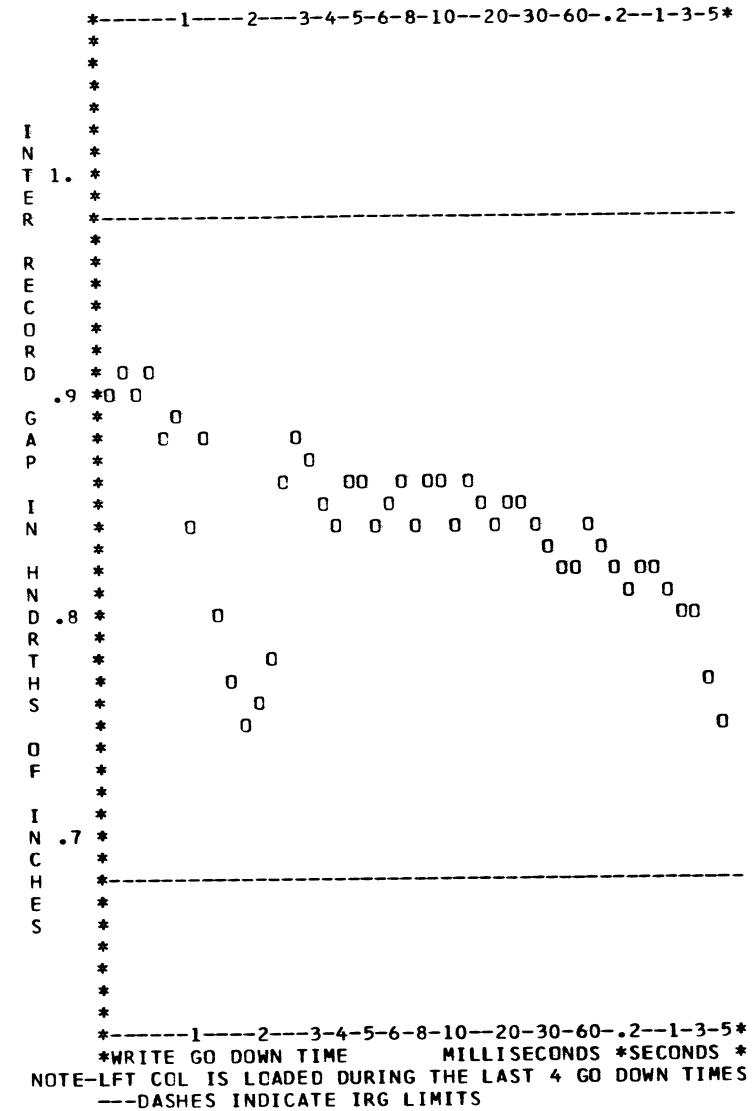
WHEN VARIABLE DELAY REACHES 5 SECONDS, THE SERIES IS RESTARTED FOR A TOTAL OF FIVE PASSES.

THE TAPE IS THEN REWOUND AND ALL RECORDS READ AND ALL GAPS CHECKED FOR LENGTH.

A GRAPH OF INTERRECORD GAP VERSUS VARIABLE GO LINE DOWN TIME IS THEN PRINTED. A SUMMARY IS ALSO PRINTED SHOWING AVERAGE GAP LENGTH WITH 10 MILLISECONDS DELAY, VARIABLE DELAY AND NO DELAY.

6 WRITE-BACKSPACE-WRITE CREEP TEST.

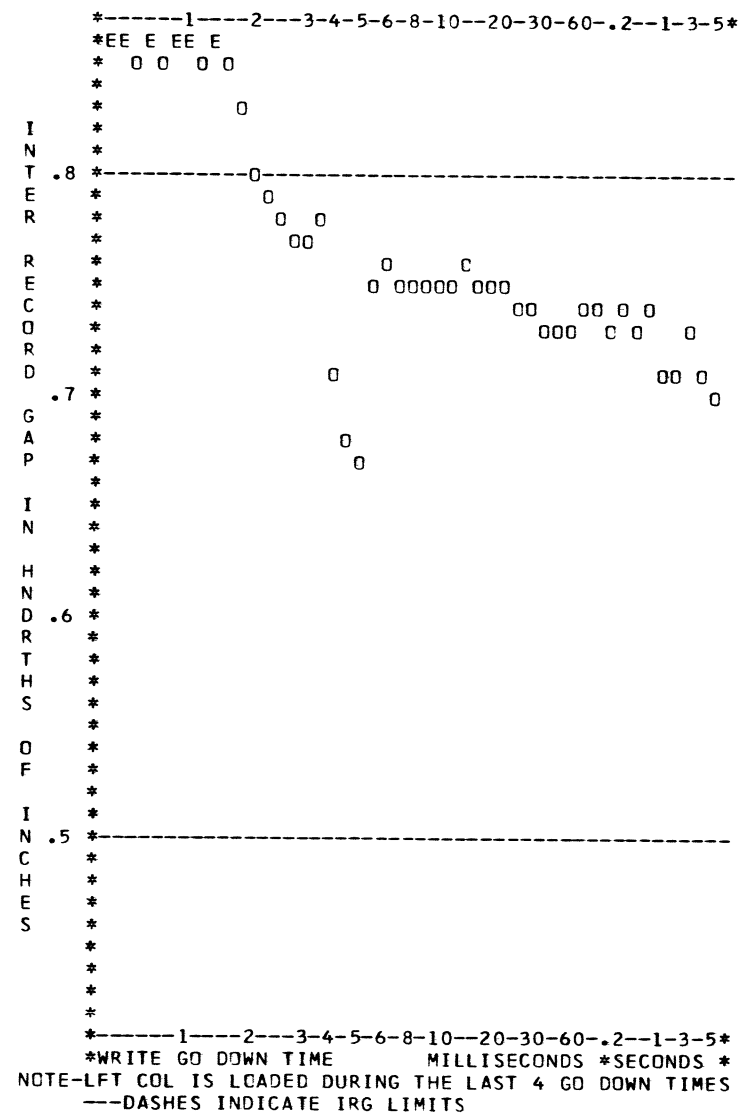
THIS ROUTINE CHECKS FOR TAPE CREEP BY WRITING SEVERAL RECORDS, BACKSPACING OVER THE LAST RECORD WRITTEN AND REWRITING IT. THE LENGTH OF THE RESULTING GAP IS THEN CHECKED AND COMPARED TO THE ORIGINAL GAP.



TYPICAL 7 TRACK GRAPH

DATE	28FEB66	01JUL66	04NOV66	15MAY67	01SEP67	01OCT67	14NOV69	PROG ID	08B9-*	DATE	28FEB66	01JUL66	04NOV66	15MAY67	01SEP67	01OCT67	14NOV69	PROG ID	08B9-*
EC NO.	415120	415178	415233	411731	411857	411875	431319	PAGE	4	EC NO.	415120	415178	415233	411731	411857	411875	431319	PAGE	4A





MISADJUSTED 9 TRACK DRIVE

NOTE-POINTS PLOTTED AS E ARE OUTSIDE
THE GRAPH LIMIT.

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6 APPENDIX

6.1 EDIT PROCEDURE

THE FOLLOWING EDIT PROCEDURE IS FOR CARD INPUT. THE EDIT PROCEDURE FOR PAPER TAPE INPUT IS LOCATED IN THE PAPER TAPE EDIT UTILITY PROGRAM DOCUMENTATION. THE PROPER EDIT CARDS MUST BE THE LAST CARDS IN THIS PROGRAM DECK. THE FOLLOWING FORMS ARE PROVIDED TO AID IN MANUALLY PREPARING THESE EDIT CARDS OR UPDATING EXISTING EDIT CARDS. IF IT IS NECESSARY TO PREPARE OR MODIFY EDIT CARDS, FILL IN THE NECESSARY DATA IN THE FORMS PRIOR TO PUNCHING THE CARDS. CARD COLUMNS THAT ARE SHADED SHOULD BE LEFT BLANK.

THE LAST EDIT CARD IS THE "END EDIT CARD." THE INFORMATION IN THIS CARD INCLUDES:

CARD 0 MUST CONTAIN ALL SEVEN ENTRIES. REFERENCE THE COLUMN HEADING FOR THE NECESSARY ENTRIES.

1. AN "E" IN COLUMN 1.
2. THE PID FOR THIS PROGRAM (COL 2-3).
3. A TERMINATOR WORD OF "FFFF" (COL. 7-10).

2040 C

	PROGRAM ID		CARD SEQUENCE NUMBER				NUMBER OF EDIT ENTRIES		ILSW BIT FOR TAPES EXAMPLE: BIT 0 = 8000 BIT 1 = 4000 ETC.		INTERRUPT ADDRS. FOR TAPE (HEX) EXAMPLE: LEVEL 7 WOULD BE 0012		NUMBER OF TRACKS DRIVE 0 0000 = 9 TRACK 0001 = 7 TRACK		NO. OF TRACKS DR 1. 0000 = 9 TRACK 0001 = 7 TRACK FFFF = NO DRIVE 1 ON SYSTEM.		DRIVE 0 MODEL. 0000 = MODEL 3 0001 = MODEL 1 0002 = MODEL 2		DRIVE 1 MODEL. 0000 = MODEL 3 OR NO DRIVE 1 ON SYS. 0001 = MODEL 1 0002 = MODEL 2		CORE STORAGE SPEED 0000=2.00 MICRO SEC 8000=2.25 MICRO SEC 0001=4.00 MICRO SEC														
COLUMN	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	26	31	35	41	45	51	55	61	66	71				
CARD 0	E	B	9	0	0	/	E	D	0	0	/	0	0	0	7	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/			
END	E	B	9	0	0	/	F	F	F	F	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/	/			
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DATE	28 FEB 66					01 JUL 66					04 NOV 66					15 MAY 67					01SEP67					01OCT67					14NOV69				
EC NO.	415120					415178					415233					411731					411857					411875					431319				

