

7030 DPS

SMFI-2A
File No. JB AX2A

SMFI- 2
I- Box 2 Program

JB AX2
April 5, 1961

1. Programs becoming obsolete: None
2. Used to provide a test of the Central Processor I-Box.

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1. PURPOSE

The I-Box 2 Program provides the maintenance engineer with a test of the following I-Box instruction categories:

- 1) Store Zero in Index Internal and External Memory.
- 2) Compare Value and Compare Count in Internal, External and Index Memories.
- 3) Proper Index Selection.

2. PROGRAM INTRODUCTION

- 2.1. This program has been designed for use after the I-Box 1 program has been run successfully.
- 2.2. The program operates under the control of the Sense Switch Interrogation Program (SSIP).

3. OPERATING PROCEDURES

- 3.1. The Sense Switch Interrogation Program must be in the machine.
- 3.2. Loading Procedures (PUNFUL Cards).
 - 3.2.1. At the Maintenance Console:
 - 1) Depress Master Reset
 - 2) Depress Start Clock
 - 3) Depress IPL
 - 4) Disable Interrupt and Time Clock
 - 5) Enable Maintenance Mode
 - 3.2.2. Place binary deck in card reader
 - 3.2.3. Depress Start on card reader, the program will start itself.

3.3. Error Indications

The program operates under Sense Switch Interrogation Program (SSIP) control, all error indication options of the SSIP Program apply to this program. Refer to the SSIP Program writeup.

3.4. Success Indications

All success indication options of the SSIP Program apply to this program. Refer to the SSIP Program writeup.

3.5. Operation Options

Refer to the SSIP Program writeup for all operation options.

4. PROGRAM PHILOSOPHY

This program is designed to test the I-Box instructions and associated hardware. The entire program is under control of SSIP. Below are listed all of the routines that are part of this program, and a brief description of what each tests:

- 1200 Basic test of LX control and basic data, value and count fields only. Tests BXCZ and BXVZ.
- 1202 More comprehensive test of LX data from external (main) memory. Also tests KVI, KC, BXE, BXL, and BXH. Data includes only value and count fields.
- 1204 Continuation of 1202 with the addition of KCI.
- 1206 Checks Z to all three memories.
- 1208 Checks J field index selection for validity and uniqueness.
- 1210 Checks LX from internal registers and index core storage, value and count fields only.
- 1212 Checks KV and KC using internal registers and index core storage as operands.
- 1214 Checks LX and SR, refill field only, for all memory combinations. (Internal, External, and Index Memories).

- 1216 Checks SX, all bits but 24-27, to all three memories. (Internal, External, and Index Memories.)
- 1218 Checks LX and SX, bits 24-27, from and to all three memories. (Internal, External, and Index Memories.)

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PROGRAM WRITEUP ADDENDUM

Program I-Box -2A
File No. JB AX2A

MAINTENANCE TAPE CONTROL CARD

Location/s of Exit Branches

1.	<u>1</u>	<u>7</u>	<u>3</u>	<u>0</u>	<u>0</u>	<u>0</u>
2.	_____	_____	_____	_____	_____	_____
3.	_____	_____	_____	_____	_____	_____
4.	_____	_____	_____	_____	_____	_____
5.	_____	_____	_____	_____	_____	_____
6.	_____	_____	_____	_____	_____	_____

Pre-Loading Manual Intervention Required? Yes _____ No x

Pre-Loading Procedure (If Any)

SLC,64.0

000100.00

PUNID,SMFI-2A

JA AX 2A

SMFI-2A

END,64.0

100.00

000100.00

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SLC,%8#6477.0

006477.00

PUNFUL

PRNS

SEM,6,C,G

-START SMFI-2A, MAKE DUMMY PASS TO
-SSIP FOR HOUSEKEEPING PURPOSES.

START	XW,%8#6500.0,BIT63+1.00-START,0,2	6500.00 20	216540.00 00	006477.00
	B,S+1.0	6501.10 00		006500.00
	BD,S+1.32	6502.04 00		006500.40
	SIC,SENO+.32	1311.40 80		006501.00
	B,SSW	1301.10 00		006501.40

----1200---LOAD INDEX CNTRL CHECK,EXT MEM.

1000	LX,\$X1,1001D	-UPDATE IDENT	6505.02 10	006502.00
	SX,\$X1,DPET13		1437.03 10	006502.40
	SIC,RET		1306.40 80	006503.00
	B,1DF1		1443.10 00	006503.40
	Z,1C200		6705.22 00	006504.00
	BD,1001		6506.04 00	006504.40
	CNOP			

1001D	%1QSZ#DD%BU,64,8#,1200	Z		006505.00
-------	------------------------	---	--	-----------

1001	LX,\$X0,100Z	-LOAD WITH ZERO.	17306.00 10	006506.00
	BXCZ,1002		6510.70 42	006506.40
	BXVZ,1002		6510.71 42	006507.00
	SIC,SEN		1310.00 80	006507.40
	B,SERS	-BOTH COUNT AND VALUE NOT ZERO.	1304.10 00	006510.00

1002	LX,\$X0,1000	-LOAD WITH ONES.	17307.00 10	006510.40
	BXCZ,1003		6512.30 42	006511.00
	B,1005		6514.10 00	006511.40

1003	BXVZ,1004		6513.31 42	006512.00
	B,1005		6514.10 00	006512.40

1004	SIC,SEN		1310.00 80	006513.00
	B,SERS	-BOTH COUNT AND VALUE ZERO.	1304.10 00	006513.40

1005	LX,\$XC,100Z	-LOAD WITH ZERO.	17306.00 10	006514.00
	BXCZ,1006		6516.70 42	006514.40
	BXVZ,1006		6516.71 42	006515.00
	SIC,SEN		1310.00 80	006515.40
	B,SERS	-BOTH COUNT AND VALUE NOT ZERO.	1304.10 00	006516.00

1006	LX,\$X0,100VO	-VALUE ALL ONES.	17310.00 10	006516.40
	SIC,SEN		1310.00 80	006517.00
	BXVZ,SERS	-NO BITS IN VALUE FIELD.	1304.31 42	006517.40

1007	LX,\$X0,100CO	-COUNT ALL ONES.	17311.00 10	006520.00
	SIC,SEN		1310.00 80	006520.40
	BXCZ,SERS	-NO BITS IN COUNT FIELD.	1304.30 42	006521.00

1008	SIC,10010	-GO CHK IX 0 IS 0.	6526.00 80	006521.40
	B,1007		6523.10 00	006522.00
	B,10011		6526.50 00	006522.40

1007	LX,\$X0,100Z	-SUBROUTINE TO LOAD INDEX	17306.00 10	006523.00
	BXCZ,1008	-REG 0 TO 0 AND CHK FOR	6524.70 42	006523.40
	B,1009	-SAME.	6525.10 00	006524.00

1008	BXVZ,10010		6526.31 42	006524.40
1009	SIC,SEN		1310.00 80	006525.00

1009	B,SERS	-COUNT OR VALUE NOT ZERO.	1304.10 00	006525.40
10010	\$B,0		0.10 00	006526.00

10011	LX,\$X0,BIT0	-START CHECK OF EACH BIT IN VALUE.	17326.00	10	006526.40
	SIC,SEN		1310.00	80	006527.00
	BXVZ,SERS	-NO BIT 0.	1304.31	42	006527.40
	-				
	SIC,10010	-CLR IX 0.	6526.00	80	006530.00
	B,1007		6523.10	00	006530.40
	LX,\$X0,BIT1		17327.00	10	006531.00
	SIC,SEN		1310.00	80	006531.40
	BXVZ,SERS	-NO BIT 1.	1304.31	42	006532.00
	-				
	SIC,10010		6526.00	80	006532.40
	B,1007		6523.10	00	006533.00
	LX,\$X0,BIT2		17330.00	10	006533.40
	SIC,SEN		1310.00	80	006534.00
	BXVZ,SERS	-NO BIT 2.	1304.31	42	006534.40
	-				
	SIC,10010		6526.00	80	006535.00
	B,1007		6523.10	00	006535.40
	LX,\$X0,BIT3		17331.00	10	006536.00
	SIC,SEN		1310.00	80	006536.40
	BXVZ,SERS	-NO BIT 3.	1304.31	42	006537.00
	-				
	SIC,10010		6526.00	80	006537.40
	B,1007		6523.10	00	006540.00
	LX,\$X0,BIT4		17332.00	10	006540.40
	SIC,SEN		1310.00	80	006541.00
	BXVZ,SERS	-NO BIT 4.	1304.31	42	006541.40
	-				
	SIC,10010		6526.00	80	006542.00
	B,1007		6523.10	00	006542.40
	LX,\$X0,BIT5		17333.00	10	006543.00
	SIC,SEN		1310.00	80	006543.40
	BXVZ,SERS	-NO BIT 5.	1304.31	42	006544.00
	-				
	SIC,10010		6526.00	80	006544.40
	B,1007		6523.10	00	006545.00
	LX,\$X0,BIT6		17334.00	10	006545.40
	SIC,SEN		1310.00	80	006546.00
	BXVZ,SERS	-NO BIT 6.	1304.31	42	006546.40
	-				
	SIC,10010		6526.00	80	006547.00
	B,1007		6523.10	00	006547.40
	LX,\$X0,BIT7		17335.00	10	006550.00
	SIC,SEN		1310.00	80	006550.40
	BXVZ,SERS	-NO BIT 7.	1304.31	42	006551.00

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SIC,10010	6526.00	80	006551.40
B,1007	6523.10	00	006552.00
LX,\$X0,BIT8	17336.00	10	006552.40
SIC,SEN	1310.00	80	006553.00
BXVZ,SERS	1304.31	42	006553.40
-			
SIC,10010	6526.00	80	006554.00
B,1007	6523.10	00	006554.40
LX,\$X0,BIT9	17337.00	10	006555.00
SIC,SEN	1310.00	80	006555.40
BXVZ,SERS	1304.31	42	006556.00
-			
SIC,10010	6526.00	80	006556.40
B,1007	6523.10	00	006557.00
LX,\$X0,BIT10	17340.00	10	006557.40
SIC,SEN	1310.00	80	006560.00
BXVZ,SERS	1304.31	42	006560.40
-			
SIC,10010	6526.00	80	006561.00
B,1007	6523.10	00	006561.40
LX,\$X0,BIT11	17341.00	10	006562.00
SIC,SEN	1310.00	80	006562.40
BXVZ,SERS	1304.31	42	006563.00
-			
SIC,10010	6526.00	80	006563.40
B,1007	6523.10	00	006564.00
LX,\$X0,BIT12	17342.00	10	006564.40
SIC,SEN	1310.00	80	006565.00
BXVZ,SERS	1304.31	42	006565.40
-			
SIC,10010	6526.00	80	006566.00
B,1007	6523.10	00	006566.40
LX,\$X0,BIT13	17343.00	10	006567.00
SIC,SEN	1310.00	80	006567.40
BXVZ,SERS	1304.31	42	006570.00
-			
SIC,10010	6526.00	80	006570.40
B,1007	6523.10	00	006571.00
LX,\$X0,BIT14	17344.00	10	006571.40
SIC,SEN	1310.00	80	006572.00
BXVZ,SERS	1304.31	42	006572.40
-			
SIC,10010	6526.00	80	006573.00
B,1007	6523.10	00	006573.40
LX,\$X0,BIT15	17345.00	10	006574.00
SIC,SEN	1310.00	80	006574.40
BXVZ,SERS	1304.31	42	006575.00

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	SIC,10010		6526.00	80	006575.40
	B,1007		6523.10	00	006576.00
	LX,\$X0,BIT16		17346.00	10	006576.40
	SIC,SEN		1310.00	80	006577.00
	BXVZ,SERS	-NO BIT 16.	1304.31	42	006577.40
	-				
	SIC,10010		6526.00	80	006600.00
	B,1007		6523.10	00	006600.40
	LX,\$X0,BIT17		17347.00	10	006601.00
	SIC,SEN		1310.00	80	006601.40
	BXVZ,SERS	-NO BIT 17.	1304.31	42	006602.00
	-				
	SIC,10010		6526.00	80	006602.40
	B,1007		6523.10	00	006603.00
	LX,\$X0,BIT18		17350.00	10	006603.40
	SIC,SEN		1310.00	80	006604.00
	BXVZ,SERS	-NO BIT 18.	1304.31	42	006604.40
	-				
	SIC,10010		6526.00	80	006605.00
	B,1007		6523.10	00	006605.40
	LX,\$X0,BIT19		17351.00	10	006606.00
	SIC,SEN		1310.00	80	006606.40
	BXVZ,SERS	-NO BIT 19.	1304.31	42	006607.00
	-				
	SIC,10010		6526.00	80	006607.40
	B,1007		6523.10	00	006610.00
	LX,\$X0,BIT20		17352.00	10	006610.40
	SIC,SEN		1310.00	80	006611.00
	BXVZ,SERS	-NO BIT 20.	1304.31	42	006611.40
	-				
	SIC,10010		6526.00	80	006612.00
	B,1007		6523.10	00	006612.40
	LX,\$X0,BIT21		17353.00	10	006613.00
	SIC,SEN		1310.00	80	006613.40
	BXVZ,SERS	-NO BIT 21.	1304.31	42	006614.00
	-				
	SIC,10010		6526.00	80	006614.40
	B,1007		6523.10	00	006615.00
	LX,\$X0,BIT22		17354.00	10	006615.40
	SIC,SEN		1310.00	80	006616.00
	BXVZ,SERS	-NO BIT 22.	1304.31	42	006616.40
	-				
	SIC,10010		6526.00	80	006617.00
	B,1007		6523.10	00	006617.40
	LX,\$X0,BIT23		17355.00	10	006620.00
	SIC,SEN		1310.00	80	006620.40
	BXVZ,SERS	-NO BIT 23.	1304.31	42	006621.00
18	SIC,10010		6526.00	80	006621.40
15	B,1007		6523.10	00	006622.00
14					
13					
12					
11					
10					
9					
8					
7					
6					
5					
4					
3					
2					
1					

LX,SX0,BIT28	-START CHK OF EACH BIT IN COUNT.	17362.00	10	006622.40
SIC,SEN		1310.00	80	006623.00
BXCZ,SERS	-NO BIT 28.	1304.30	42	006623.40
-				
SIC,10010		6526.00	80	006624.00
B,1007		6523.10	00	006624.40
LX,SX0,BIT29		17363.00	10	006625.00
SIC,SEN		1310.00	80	006625.40
BXCZ,SERS	-NO BIT 29.	1304.30	42	006626.00
-				
SIC,10010		6526.00	80	006626.40
B,1007		6523.10	00	006627.00
LX,SX0,BIT30		17364.00	10	006627.40
SIC,SEN		1310.00	80	006630.00
BXCZ,SERS	-NO BIT 30.	1304.30	42	006630.40
-				
SIC,10010		6526.00	80	006631.00
B,1007		6523.10	00	006631.40
LX,SX0,BIT31		17365.00	10	006632.00
SIC,SEN		1310.00	80	006632.40
BXCZ,SERS	-NO BIT 31.	1304.30	42	006633.00
-				
SIC,10010		6526.00	80	006633.40
B,1007		6523.10	00	006634.00
LX,SX0,BIT32		17366.00	10	006634.40
SIC,SEN		1310.00	80	006635.00
BXCZ,SERS	-NO BIT 32.	1304.30	42	006635.40
-				
SIC,10010		6526.00	80	006636.00
B,1007		6523.10	00	006636.40
LX,SX0,BIT33		17367.00	10	006637.00
SIC,SEN		1310.00	80	006637.40
BXCZ,SERS	-NO BIT 33.	1304.30	42	006640.00
-				
SIC,10010		6526.00	80	006640.40
B,1007		6523.10	00	006641.00
LX,SX0,BIT34		17370.00	10	006641.40
SIC,SEN		1310.00	80	006642.00
BXCZ,SERS	-NO BIT 34.	1304.30	42	006642.40
-				
SIC,10010		6526.00	80	006643.00
B,1007		6523.10	00	006643.40
LX,SX0,BIT35		17371.00	10	006644.00
SIC,SEN		1310.00	80	006644.40
BXCZ,SERS	-NO BIT 35.	1304.30	42	006645.00

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SIC,10010	6526.00	80	006645.40
B,1007	6523.10	00	006646.00
LX,\$X0,BIT36	17372.00	10	006646.40
SIC,SEN	1310.00	80	006647.00
BXCZ,SERS	1304.30	42	006647.40
-			
SIC,10010	6526.00	80	006650.00
B,1007	6523.10	00	006650.40
LX,\$XC,BIT37	17373.00	10	006651.00
SIC,SEN	1310.00	80	006651.40
BXCZ,SERS	1304.30	42	006652.00
-			
SIC,10010	6526.00	80	006652.40
B,1007	6523.10	00	006653.00
LX,\$X0,BIT38	17374.00	10	006653.40
SIC,SEN	1310.00	80	006654.00
BXCZ,SERS	1304.30	42	006654.40
-			
SIC,10010	6526.00	80	006655.00
B,1007	6523.10	00	006655.40
LX,\$X0,BIT39	17375.00	10	006656.00
SIC,SEN	1310.00	80	006656.40
BXCZ,SERS	1304.30	42	006657.00
-			
SIC,10010	6526.00	80	006657.40
B,1007	6523.10	00	006660.00
LX,\$X0,BIT40	17376.00	10	006660.40
SIC,SEN	1310.00	80	006661.00
BXCZ,SERS	1304.30	42	006661.40
-			
SIC,10010	6526.00	80	006662.00
B,1007	6523.10	00	006662.40
LX,\$X0,BIT41	17377.00	10	006663.00
SIC,SEN	1310.00	80	006663.40
BXCZ,SERS	1304.30	42	006664.00
-			
SIC,10010	6526.00	80	006664.40
B,1007	6523.10	00	006665.00
LX,\$XC,BIT42	17400.00	10	006665.40
SIC,SEN	1310.00	80	006666.00
BXCZ,SERS	1304.30	42	006666.40
-			
SIC,10010	6526.00	80	006667.00
B,1007	6523.10	00	006667.40
LX,\$X0,BIT43	17401.00	10	006670.00
SIC,SEN	1310.00	80	006670.40
BXCZ,SERS	1304.30	42	006671.00

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SIC,10010			6526.00	80	006671.40
B,1007			6523.10	00	006672.00
LX,\$X0,BIT44			17402.00	10	006672.40
SIC,SEN			1310.00	80	006673.00
BXCZ,SERS	-NO BIT 44.		1304.30	42	006673.40
-					
SIC,10010			6526.00	80	006674.00
B,1007			6523.10	00	006674.40
LX,\$X0,BIT45			17403.00	10	006675.00
SIC,SEN			1310.00	80	006675.40
BXCZ,SERS	-NO BIT 45.		1304.30	42	006676.00
-					
B,\$+1.0			6677.50	00	006676.40
BD,1001			6506.04	00	006677.00
SIC,SEN0+.32			1311.40	80	006677.40
B,SSW	-TO SSIP		1301.10	00	006700.00
BD,\$+.32			6701.04	00	006700.40
-					
LX,\$X13,IC200	-UPDATE CONTINUITY CHECK.		6705.32	10	006701.00
V+,\$X13,BIT0			17326.32	B0	006701.40
SX,\$X13,IC200			6705.33	10	006702.00
-					
LX,\$X13,IC200	-UPDATE CONTINUITY CHECK.		6705.32	10	006702.40
KV,\$X13,ICK200			6706.32	90	006703.00
SIC,SEN			1310.00	80	006703.40
BZXE,SERS	-CONTINUITY ERROR.		1304.32	C0	006704.00
B,102			6707.10	00	006704.40
-					
CNOP					
IC200	XW,0,0,0	-CONTINUITY REG 1200.	0.00	00 000000.00 00	006705.00
ICK200	XW,%8#400000.00,0,0		400000.00	00 000000.00 00	006706.00

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102	LX,\$X1,102ID	-UPDATE IDENT	6712.02 10	006707.00
	SX,\$X1,DPET13		1437.03 10	006707.40
	SIC,RET		1306.40 80	006710.00
	B,1DF1		1443.10 00	006710.40
	Z,1C202		10223.22 00	006711.00
	BD,1020		6713.04 00	006711.40
	CMOP			
102ID	%IQSZHDD%BU,64,8H,1202	Z		006712.00
1020	SIC,10010	-TEST BIT 0.	6526.00 80	006713.00
	B,1007		6523.10 00	006713.40
	LX,\$X0,BIT0		17326.00 10	006714.00
	KV,\$X0,BIT0	-EQUAL CONDITION,KV.	17326.00 90	006714.40
	SIC,SEN		1310.00 80	006715.00
	BXL,SERS		1304.32 42	006715.40
	SIC,SEN		1310.00 80	006716.00
	BXH,SERS		1304.33 42	006716.40
	BXE,1021		6720.72 C2	006717.00
	SIC,SEN		1310.00 80	006717.40
	B,SERS		1304.10 00	006720.00
1021	KVI,\$X0,%8H400000.0	-EQUAL CONDITION,KVI.	400000.01 04	006720.40
	SIC,SEN		1310.00 80	006721.00
	BXL,SERS		1304.32 42	006721.40
	SIC,SEN		1310.00 80	006722.00
	BXH,SERS		1304.33 42	006722.40
	BXE,1022		6724.72 C2	006723.00
	SIC,SEN		1310.00 80	006723.40
	B,SERS		1304.10 00	006724.00
1022	KV,\$X0,BIT1	-INDEX HI CONDITION,KV.	17327.00 90	006724.40
	SIC,SEN		1310.00 80	006725.00
	BXL,SERS		1304.32 42	006725.40
	SIC,SEN		1310.00 80	006726.00
	BXE,SERS		1304.32 C2	006726.40
	BXH,1023		6734.73 42	006727.00
	SIC,SEN		1310.00 80	006727.40
	B,SERS		1304.10 00	006730.00
	KVI,\$X0,%8H200000.0	-INDEX HI CONDITION,KVI.	200000.01 04	006730.40
	SIC,SEN		1310.00 80	006731.00
	BXL,SERS		1304.32 42	006731.40
	SIC,SEN		1310.00 80	006732.00
	BXE,SERS		1304.32 C2	006732.40
	BXH,1023		6734.73 42	006733.00
	SIC,SEN		1310.00 80	006733.40
	B,SERS		1304.10 00	006734.00
1023	B,\$+1.0		6735.50 00	006734.40
	BD,1020		6713.04 00	006735.00
	SIC,SEN0+,32		1311.40 80	006735.40
	B,SSW	-TO SSIP.	1301.10 00	006736.00
	BD,\$+,32		6737.04 00	006736.40
	LX,\$X13,1C202	-UPDATE CONTINUITY CHECK.	10223.32 10	006737.00
	V+,\$X13,BIT0		17326.32 B0	006737.40
	SX,\$X13,1C202		10223.33 10	006740.00

1024	SIC, I0010	-TEST BIT 1.	6526.00	80	006740.40
	B, I007		6523.10	00	006741.00
	LX, SX0, BIT1		17327.00	10	006741.40
	KV, SX0, BIT1	-EQUAL CONDITION, KV.	17327.00	90	006742.00
	SIC, SEN		1310.00	80	006742.40
	BXL, SERS		1304.32	42	006743.00
	SIC, SEN		1310.00	80	006743.40
	BXH, SERS		1304.33	42	006744.00
	BXE, I025		6746.32	C2	006744.40
	SIC, SEN		1310.00	80	006745.00
	B, SERS		1304.10	00	006745.40
1025	KVI, SX0, %8#200000.0	-EQUAL CONDITION, KVI.	200000.01	04	006746.00
	SIC, SEN		1310.00	80	006746.40
	BXL, SERS		1304.32	42	006747.00
	SIC, SEN		1310.00	80	006747.40
	BXH, SERS		1304.33	42	006750.00
	BXE, I026		6752.32	C2	006750.40
	SIC, SEN		1310.00	80	006751.00
	B, SERS		1304.10	00	006751.40
1026	KV, SX0, BIT2	-INDEX HI CONDITION, KV.	17330.00	90	006752.00
	SIC, SEN		1310.00	80	006752.40
	BXL, SERS		1304.32	42	006753.00
	SIC, SEN		1310.00	80	006753.40
	BXE, SERS		1304.32	C2	006754.00
	BXH, I027		6756.33	42	006754.40
	SIC, SEN		1310.00	80	006755.00
	B, SERS		1304.10	00	006755.40
1027	KVI, SX0, %8#100000.0	-INDEX HI CONDITION, KVI.	100000.01	04	006756.00
	SIC, SEN		1310.00	80	006756.40
	BXL, SERS		1304.32	42	006757.00
	SIC, SEN		1310.00	80	006757.40
	BXE, SERS		1304.32	C2	006760.00
	BXH, I028		6762.33	42	006760.40
	SIC, SEN		1310.00	80	006761.00
	B, SERS		1304.10	00	006761.40
1028	KV, SX0, BIT0	-INDEX LOW CONDITION, KV.	17326.00	90	006762.00
	SIC, SEN		1310.00	80	006762.40
	BXH, SERS		1304.33	42	006763.00
	SIC, SEN		1310.00	80	006763.40
	BXE, SERS		1304.32	C2	006764.00
	BXL, I029		6766.32	42	006764.40
	SIC, SEN		1310.00	80	006765.00
	B, SERS		1304.10	00	006765.40
1029	KVI, SX0, %8#400000.0	-INDEX LOW CONDITION, KVI.	400000.01	04	006766.00
	SIC, SEN		1310.00	80	006766.40
	BXH, SERS		1304.33	42	006767.00
	SIC, SEN		1310.00	80	006767.40
	BXE, SERS		1304.32	C2	006770.00
	BXL, I0210		6772.32	42	006770.40
	SIC, SEN		1310.00	80	006771.00
	B, SERS		1304.10	00	006771.40
10210	B, \$+1.0		6773.10	00	006772.00
	BD, I024		6740.44	00	006772.40
	SIC, SEN0+.32		1311.40	80	006773.00
	B, SSW	-TO SSIP.	1301.10	00	006773.40
	BD, \$+.32		6774.44	00	006774.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	006774.40
	V+, \$X13, BIT1		17327.32	B0	006775.00
	SX, \$X13, IC202		10223.33	10	006775.40

10211	SIC, I0010	-TEST BIT 2.	6526.00	80	006776.00
	B, I007		6523.10	00	006776.40
	LX, \$X0, BIT2		17330.00	10	006777.00
	KV, \$X0, BIT2	-EQUAL CONDITION, KV.	17330.00	90	006777.40
	SIC, SEN		1310.00	80	007000.00
	BXL, SERS		1304.32	42	007000.40
	SIC, SEN		1310.00	80	007001.00
	BXH, SERS		1304.33	42	007001.40
	BXE, I0212		7003.72	C2	007002.00
	SIC, SEN		1310.00	80	007002.40
	B, SERS		1304.10	00	007003.00
10212	KVI, \$X0, %8#100000.0	-EQUAL CONDITION, KVI.	100000.01	04	007003.40
	SIC, SEN		1310.00	80	007004.00
	BXL, SERS		1304.32	42	007004.40
	SIC, SEN		1310.00	80	007005.00
	BXH, SERS		1304.33	42	007005.40
	BXE, I0213		7007.72	C2	007006.00
	SIC, SEN		1310.00	80	007006.40
	B, SERS		1304.10	00	007007.00
10213	KV, \$X0, BIT3	-INDEX HI CONDITION, KV.	17331.00	90	007007.40
	SIC, SEN		1310.00	80	007010.00
	BXL, SERS		1304.32	42	007010.40
	SIC, SEN		1310.00	80	007011.00
	BXE, SERS		1304.32	C2	007011.40
	BXH, I0214		7013.73	42	007012.00
	SIC, SEN		1310.00	80	007012.40
	B, SERS		1304.10	00	007013.00
10214	KVI, \$X0, %8#40000.0	-INDEX HI CONDITION, KVI.	40000.01	04	007013.40
	SIC, SEN		1310.00	80	007014.00
	BXL, SERS		1304.32	42	007014.40
	SIC, SEN		1310.00	80	007015.00
	BXE, SERS		1304.32	C2	007015.40
	BXH, I0215		7017.73	42	007016.00
	SIC, SEN		1310.00	80	007016.40
	B, SERS		1304.10	00	007017.00
10215	KV, \$X0, BIT1	-INDEX LOW CONDITION, KV.	17327.00	90	007017.40
	SIC, SEN		1310.00	80	007020.00
	BXH, SERS		1304.33	42	007020.40
	SIC, SEN		1310.00	80	007021.00
	BXE, SERS		1304.32	C2	007021.40
	BXL, I0216		7023.72	42	007022.00
	SIC, SEN		1310.00	80	007022.40
	B, SERS		1304.10	00	007023.00
10216	KVI, \$X0, %8#200000.0	-INDEX LOW CONDITION, KVI.	200000.01	04	007023.40
	SIC, SEN		1310.00	80	007024.00
	BXH, SERS		1304.33	42	007024.40
	SIC, SEN		1310.00	80	007025.00
	BXE, SERS		1304.32	C2	007025.40
	BXL, I0217		7027.72	42	007026.00
	SIC, SEN		1310.00	80	007026.40
	B, SERS		1304.10	00	007027.00
10217	B, \$+1.0		7030.50	00	007027.40
	BD, I0211		6776.04	00	007030.00
	SIC, SEN0+.32		1311.40	80	007030.40
	B, SSW	-TO SSIP.	1301.10	00	007031.00
	BD, \$+.32		7032.04	00	007031.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007032.00
	V+, \$X13, BIT2		17330.32	80	007032.40
	SX, \$X13, IC202		10223.33	10	007033.00

10218	SIC, I0010	-TEST BIT 3.	6526.00	80	007033.40
	B, I007		6523.10	00	007034.00
	LX, SX0, BIT3		17331.00	10	007034.40
	KV, SX0, BIT3	-EQUAL CONDITION, KV.	17331.00	90	007035.00
	SIC, SEN		1310.00	80	007035.40
	BXL, SERS		1304.32	42	007036.00
	SIC, SEN		1310.00	80	007036.40
	BXH, SERS		1304.33	42	007037.00
	BXE, I0219		7041.32	C2	007037.40
	SIC, SEN		1310.00	80	007040.00
	B, SERS		1304.10	00	007040.40
10219	KVI, SX0, %8#40000.0	-EQUAL CONDITION, KVI.	40000.01	04	007041.00
	SIC, SEN		1310.00	80	007041.40
	BXL, SERS		1304.32	42	007042.00
	SIC, SEN		1310.00	80	007042.40
	BXH, SERS		1304.33	42	007043.00
	BXE, I0220		7045.32	C2	007043.40
	SIC, SEN		1310.00	80	007044.00
	B, SERS		1304.10	00	007044.40
10220	KV, SX0, BIT4	-INDEX HI CONDITION, KV.	17332.00	90	007045.00
	SIC, SEN		1310.00	80	007045.40
	BXL, SERS		1304.32	42	007046.00
	SIC, SEN		1310.00	80	007046.40
	BXE, SERS		1304.32	C2	007047.00
	BXH, I0221		7051.33	42	007047.40
	SIC, SEN		1310.00	80	007050.00
	B, SERS		1304.10	00	007050.40
10221	KVI, SX0, %8#20000.0	-INDEX HI CONDITION, KVI.	20000.01	04	007051.00
	SIC, SEN		1310.00	80	007051.40
	BXL, SERS		1304.32	42	007052.00
	SIC, SEN		1310.00	80	007052.40
	BXE, SERS		1304.32	C2	007053.00
	BXH, I0222		7055.33	42	007053.40
	SIC, SEN		1310.00	80	007054.00
	B, SERS		1304.10	00	007054.40
10222	KV, SX0, BIT2	-INDEX LOW CONDITION, KV.	17330.00	90	007055.00
	SIC, SEN		1310.00	80	007055.40
	BXH, SERS		1304.33	42	007056.00
	SIC, SEN		1310.00	80	007056.40
	BXE, SERS		1304.32	C2	007057.00
	BXL, I0223		7061.32	42	007057.40
	SIC, SEN		1310.00	80	007060.00
	B, SERS		1304.10	00	007060.40
10223	KVI, SX0, %8#100000.0	-INDEX LOW CONDITION, KVI.	100000.01	04	007061.00
	SIC, SEN		1310.00	80	007061.40
	BXH, SERS		1304.33	42	007062.00
	SIC, SEN		1310.00	80	007062.40
	BXE, SERS		1304.32	C2	007063.00
	BXL, I0224		7065.32	42	007063.40
	SIC, SEN		1310.00	80	007064.00
	B, SERS		1304.10	00	007064.40
10224	B, \$+1.0		7066.10	00	007065.00
	BD, I0218		7033.44	00	007065.40
	SIC, SEN0+.32		1311.40	80	007066.00
	B, SSW	-TO SSIP.	1301.10	00	007066.40
	BD, \$+.32		7067.44	00	007067.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007067.40
	V+, \$X13, BIT3		17331.32	80	007070.00
	SX, \$X13, IC202		10223.33	10	007070.40

10225	SIC, I0010	-TEST BIT 4.	6526.00	80	007071.00
	B, I007		6523.10	00	007071.40
	LX, SX0, BIT4		17332.00	10	007072.00
	KV, SX0, BIT4	-EQUAL CONDITION, KV.	17332.00	90	007072.40
	SIC, SEN		1310.00	80	007073.00
	BXL, SERS		1304.32	42	007073.40
	SIC, SEN		1310.00	80	007074.00
	BXH, SERS		1304.33	42	007074.40
	BXE, I0226		7076.72	C2	007075.00
	SIC, SEN		1310.00	80	007075.40
	B, SERS		1304.10	00	007076.00
10226	KVI, SX0, %8#20000.0	-EQUAL CONDITION, KVI.	20000.01	04	007076.40
	SIC, SEN		1310.00	80	007077.00
	BXL, SERS		1304.32	42	007077.40
	SIC, SEN		1310.00	80	007100.00
	BXH, SERS		1304.33	42	007100.40
	BXE, I0227		7102.72	C2	007101.00
	SIC, SEN		1310.00	80	007101.40
	B, SERS		1304.10	00	007102.00
10227	KV, SX0, BIT5	-INDEX HI CONDITION, KV.	17333.00	90	007102.40
	SIC, SEN		1310.00	80	007103.00
	BXL, SERS		1304.32	42	007103.40
	SIC, SEN		1310.00	80	007104.00
	BXE, SERS		1304.32	C2	007104.40
	BXH, I0228		7106.73	42	007105.00
	SIC, SEN		1310.00	80	007105.40
	B, SERS		1304.10	00	007106.00
10228	KVI, SX0, %8#10000.0	-INDEX HI CONDITION, KVI.	10000.01	04	007106.40
	SIC, SEN		1310.00	80	007107.00
	BXL, SERS		1304.32	42	007107.40
	SIC, SEN		1310.00	80	007110.00
	BXE, SERS		1304.32	C2	007110.40
	BXH, I0229		7112.73	42	007111.00
	SIC, SEN		1310.00	80	007111.40
	B, SERS		1304.10	00	007112.00
10229	KV, SX0, BIT3	-INDEX LOW CONDITION, KV.	17331.00	90	007112.40
	SIC, SEN		1310.00	80	007113.00
	BXH, SERS		1304.33	42	007113.40
	SIC, SEN		1310.00	80	007114.00
	BXE, SERS		1304.32	C2	007114.40
	BXL, I0230		7116.72	42	007115.00
	SIC, SEN		1310.00	80	007115.40
	B, SERS		1304.10	00	007116.00
10230	KVI, SX0, %8#40000.0	-INDEX LOW CONDITION, KVI.	40000.01	04	007116.40
	SIC, SEN		1310.00	80	007117.00
	BXH, SERS		1304.33	42	007117.40
	SIC, SEN		1310.00	80	007120.00
	BXE, SERS		1304.32	C2	007120.40
	BXL, I0231		7122.72	42	007121.00
	SIC, SEN		1310.00	80	007121.40
	B, SERS		1304.10	00	007122.00
10231	B, \$+1.0		7123.50	00	007122.40
	BD, I0225		7071.04	00	007123.00
	SIC, SEN0+.32		1311.40	80	007123.40
	B, SSW	-TO SSIP.	1301.10	00	007124.00
	BD, \$+.32		7125.04	00	007124.40
	LX, SX13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007125.00
	V+, SX13, BIT4		17332.32	80	007125.40
	SX, SX13, IC202		10223.33	10	007126.00

10232	SIC, I0010	-TEST BIT 5.	6526.00 80	007126.40
	B, I007		6523.10 00	007127.00
	LX, SX0, BIT5		17333.00 10	007127.40
	KV, SX0, BIT5	-EQUAL CONDITION, KV.	17333.00 90	007130.00
	SIC, SEN		1310.00 80	007130.40
	BXL, SERS		1304.32 42	007131.00
	SIC, SEN		1310.00 80	007131.40
	BXH, SERS		1304.33 42	007132.00
	BXE, I0233		7134.32 C2	007132.40
	SIC, SEN		1310.00 80	007133.00
	B, SERS		1304.10 00	007133.40
10232	KVI, SX0, %8#10000.0	-EQUAL CONDITION, KVI.	10000.01 04	007134.00
	SIC, SEN		1310.00 80	007134.40
	BXL, SERS		1304.32 42	007135.00
	SIC, SEN		1310.00 80	007135.40
	BXH, SERS		1304.33 42	007136.00
	BXE, I0234		7140.32 C2	007136.40
	SIC, SEN		1310.00 80	007137.00
	B, SERS		1304.10 00	007137.40
10234	KV, SX0, BIT6	-INDEX HI CONDITION, KV.	17334.00 90	007140.00
	SIC, SEN		1310.00 80	007140.40
	BXL, SERS		1304.32 42	007141.00
	SIC, SEN		1310.00 80	007141.40
	BXE, SERS		1304.32 C2	007142.00
	BXH, I0235		7144.33 42	007142.40
	SIC, SEN		1310.00 80	007143.00
	B, SERS		1304.10 00	007143.40
10235	KVI, SX0, %8#4000.0	-INDEX HI CONDITION, KVI.	4000.01 04	007144.00
	SIC, SEN		1310.00 80	007144.40
	BXL, SERS		1304.32 42	007145.00
	SIC, SEN		1310.00 80	007145.40
	BXE, SERS		1304.32 C2	007146.00
	BXH, I0236		7150.33 42	007146.40
	SIC, SEN		1310.00 80	007147.00
	B, SERS		1304.10 00	007147.40
10236	KV, SX0, BIT4	-INDEX LOW CONDITION, KV.	17332.00 90	007150.00
	SIC, SEN		1310.00 80	007150.40
	BXH, SERS		1304.33 42	007151.00
	SIC, SEN		1310.00 80	007151.40
	BXE, SERS		1304.32 C2	007152.00
	BXL, I0237		7154.32 42	007152.40
	SIC, SEN		1310.00 80	007153.00
	B, SERS		1304.10 00	007153.40
10237	KVI, SX0, %8#20000.0	-INDEX LOW CONDITION, KVI.	20000.01 04	007154.00
	SIC, SEN		1310.00 80	007154.40
	BXH, SERS		1304.33 42	007155.00
	SIC, SEN		1310.00 80	007155.40
	BXE, SERS		1304.32 C2	007156.00
	BXL, I0238		7160.32 42	007156.40
	SIC, SEN		1310.00 80	007157.00
	B, SERS		1304.10 00	007157.40
10238	B, \$+1.0		7161.10 00	007160.00
	BD, I0232		7126.44 00	007160.40
	SIC, SEN0+.32		1311.40 80	007161.00
	B, SSW	-TO SSIP.	1301.10 00	007161.40
	BD, \$+.32		7162.44 00	007162.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	007162.40
	V+, \$X13, BIT5		17333.32 B0.	007163.00
	SX, \$X13, IC202		10223.33 10	007163.40

10239	SIC,IC010	-TEST BIT 6.	6526.00 80	007164.00
	B,1007		6523.10 00	007164.40
	LX,\$X0,BIT6		17334.00 10	007165.00
	KV,\$X0,BIT6	-EQUAL CONDITION,KV.	17334.00 90	007165.40
	SIC,SEN		1310.00 80	007166.00
	BXL,SERS		1304.32 42	007166.40
	SIC,SEN		1310.00 80	007167.00
	BXH,SERS		1304.33 42	007167.40
	BXE,10240		7171.72 C2	007170.00
	SIC,SEN		1310.00 80	007170.40
	B,SERS		1304.10 00	007171.00
10240	KVI,\$X0,%8#4000.0	-EQUAL CONDITION,KVI.	4000.01 04	007171.40
	SIC,SEN		1310.00 80	007172.00
	BXL,SERS		1304.32 42	007172.40
	SIC,SEN		1310.00 80	007173.00
	BXH,SERS		1304.33 42	007173.40
	BXE,10241		7175.72 C2	007174.00
	SIC,SEN		1310.00 80	007174.40
	B,SERS		1304.10 00	007175.00
10241	KV,\$X0,BIT7	-INDEX HI CONDITION,KV.	17335.00 90	007175.40
	SIC,SEN		1310.00 80	007176.00
	BXL,SERS		1304.32 42	007176.40
	SIC,SEN		1310.00 80	007177.00
	BXE,SERS		1304.32 C2	007177.40
	BXH,10242		7201.73 42	007200.00
	SIC,SEN		1310.00 80	007200.40
	B,SERS		1304.10 00	007201.00
10242	KVI,\$X0,%8#2000.0	-INDEX HI CONDITION,KVI.	2000.01 04	007201.40
	SIC,SEN		1310.00 80	007202.00
	BXL,SERS		1304.32 42	007202.40
	SIC,SEN		1310.00 80	007203.00
	BXE,SERS		1304.32 C2	007203.40
	BXH,10243		7205.73 42	007204.00
	SIC,SEN		1310.00 80	007204.40
	B,SERS		1304.10 00	007205.00
10243	KV,\$X0,BIT5	-INDEX LOW CONDITION,KV.	17333.00 90	007205.40
	SIC,SEN		1310.00 80	007206.00
	BXH,SERS		1304.33 42	007206.40
	SIC,SEN		1310.00 80	007207.00
	BXE,SERS		1304.32 C2	007207.40
	BXL,10244		7211.72 42	007210.00
	SIC,SEN		1310.00 80	007210.40
	B,SERS		1304.10 00	007211.00
10244	KVI,\$X0,%8#10000.0	-INDEX LOW CONDITION,KVI.	10000.01 04	007211.40
	SIC,SEN		1310.00 80	007212.00
	BXH,SERS		1304.33 42	007212.40
	SIC,SEN		1310.00 80	007213.00
	BXE,SERS		1304.32 C2	007213.40
	BXL,10245		7215.72 42	007214.00
	SIC,SEN		1310.00 80	007214.40
	B,SERS		1304.10 00	007215.00
10245	B,\$+1.0		7216.50 00	007215.40
	BD,10239		7164.04 00	007216.00
	SIC,SEN0+.32		1311.40 80	007216.40
	B,SSW	-TO SSIP.	1301.10 00	007217.00
	BD,\$+.32		7220.04 00	007217.40
	LX,\$X13,IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	007220.00
	V+,\$X13,BIT6		17334.32 80	007220.40
	SX,\$X13,IC202		10223.33 10	007221.00

10246	SIC, I0010	-TEST BIT 7.	6526.00	80	007221.40
	B, I007		6523.10	00	007222.00
	LX, \$X0, BIT7		17335.00	10	007222.40
	KV, \$X0, BIT7	-EQUAL CONDITION, KV.	17335.00	90	007223.00
	SIC, SEN		1310.00	80	007223.40
	BXL, SERS		1304.32	42	007224.00
	SIC, SEN		1310.00	80	007224.40
	BXH, SERS		1304.33	42	007225.00
	BXE, I0247		7227.32	C2	007225.40
	SIC, SEN		1310.00	80	007226.00
	B, SERS		1304.10	00	007226.40
10247	KVI, \$X0, %8#2000.0	-EQUAL CONDITION, KVI.	2000.01	04	007227.00
	SIC, SEN		1310.00	80	007227.40
	BXL, SERS		1304.32	42	007230.00
	SIC, SEN		1310.00	80	007230.40
	BXH, SERS		1304.33	42	007231.00
	BXE, I0248		7233.32	C2	007231.40
	SIC, SEN		1310.00	80	007232.00
	B, SERS		1304.10	00	007232.40
10248	KV, \$X0, BIT8	-INDEX HI CONDITION, KV.	17336.00	90	007233.00
	SIC, SEN		1310.00	80	007233.40
	BXL, SERS		1304.32	42	007234.00
	SIC, SEN		1310.00	80	007234.40
	BXE, SERS		1304.32	C2	007235.00
	BXH, I0249		7237.33	42	007235.40
	SIC, SEN		1310.00	80	007236.00
	B, SERS		1304.10	00	007236.40
10249	KVI, \$X0, %8#1000.0	-INDEX HI CONDITION, KVI.	1000.01	04	007237.00
	SIC, SEN		1310.00	80	007237.40
	BXL, SERS		1304.32	42	007240.00
	SIC, SEN		1310.00	80	007240.40
	BXE, SERS		1304.32	C2	007241.00
	BXH, I0250		7243.33	42	007241.40
	SIC, SEN		1310.00	80	007242.00
	B, SERS		1304.10	00	007242.40
10250	KV, \$X0, BIT6	-INDEX LOW CONDITION, KV.	17334.00	90	007243.00
	SIC, SEN		1310.00	80	007243.40
	BXH, SERS		1304.33	42	007244.00
	SIC, SEN		1310.00	80	007244.40
	BXE, SERS		1304.32	C2	007245.00
	BXL, I0251		7247.32	42	007245.40
	SIC, SEN		1310.00	80	007246.00
	B, SERS		1304.10	00	007246.40
10251	KVI, \$X0, %8#4000.0	-INDEX LOW CONDITION, KVI.	4000.01	04	007247.00
	SIC, SEN		1310.00	80	007247.40
	BXH, SERS		1304.33	42	007250.00
	SIC, SEN		1310.00	80	007250.40
	BXE, SERS		1304.32	C2	007251.00
	BXL, I0252		7253.32	42	007251.40
	SIC, SEN		1310.00	80	007252.00
	B, SERS		1304.10	00	007252.40
10252	B, \$+1.0		7254.10	00	007253.00
	BD, I0246		7221.44	00	007253.40
	SIC, SEN0+.32		1311.40	80	007254.00
	B, SSW	-TO SSIP.	1301.10	00	007254.40
	BD, \$+.32		7255.44	00	007255.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007255.40
	V+, \$X13, BIT7		17335.32	B0	007256.00
	SX, \$X13, IC202		10223.33	10	007256.40

10253	SIC, I0010	-TEST BIT 3.	6526.00 80	007257.00
	B, I007		6523.10 00	007257.40
	LX, SX0, BIT8		17336.00 10	007260.00
	KV, SX0, BIT8	-EQUAL CONDITION, KV.	17336.00 90	007260.40
	SIC, SEN		1310.00 80	007261.00
	BXL, SERS		1304.32 42	007261.40
	SIC, SEN		1310.00 80	007262.00
	BXH, SERS		1304.32 42	007262.40
	BXE, I0254		7264.72 C2	007263.00
	SIC, SEN		1310.00 80	007263.40
	B, SERS		1304.10 00	007264.00
10254	KVI, SX0, %8#1000.0	-EQUAL CONDITION, KVI.	1000.01 04	007264.40
	SIC, SEN		1310.00 80	007265.00
	BXL, SERS		1304.32 42	007265.40
	SIC, SEN		1310.00 80	007266.00
	BXH, SERS		1304.32 42	007266.40
	BXE, I0255		7270.72 C2	007267.00
	SIC, SEN		1310.00 80	007267.40
	B, SERS		1304.10 00	007270.00
10255	KV, SX0, BIT9	-INDEX HI CONDITION, KV.	17337.00 90	007270.40
	SIC, SEN		1310.00 80	007271.00
	BXL, SERS		1304.32 42	007271.40
	SIC, SEN		1310.00 80	007272.00
	BXE, SERS		1304.32 C2	007272.40
	BXH, I0256		7274.73 42	007273.00
	SIC, SEN		1310.00 80	007273.40
	B, SERS		1304.10 00	007274.00
10256	KVI, SX0, %8#400.0	-INDEX HI CONDITION, KVI.	400.01 04	007274.40
	SIC, SEN		1310.00 80	007275.00
	BXL, SERS		1304.32 42	007275.40
	SIC, SEN		1310.00 80	007276.00
	BXE, SERS		1304.32 C2	007276.40
	BXH, I0257		7300.73 42	007277.00
	SIC, SEN		1310.00 80	007277.40
	B, SERS		1304.10 00	007300.00
10257	KV, SX0, BIT7	-INDEX LOW CONDITION, KV.	17335.00 90	007300.40
	SIC, SEN		1310.00 80	007301.00
	BXH, SERS		1304.32 42	007301.40
	SIC, SEN		1310.00 80	007302.00
	BXE, SERS		1304.32 C2	007302.40
	BXL, I0258		7304.72 42	007303.00
	SIC, SEN		1310.00 80	007303.40
	B, SERS		1304.10 00	007304.00
10258	KVI, SX0, %8#2000.0	-INDEX LOW CONDITION, KVI.	2000.01 04	007304.40
	SIC, SEN		1310.00 80	007305.00
	BXH, SERS		1304.32 42	007305.40
	SIC, SEN		1310.00 80	007306.00
	BXE, SERS		1304.32 C2	007306.40
	BXL, I0259		7310.72 42	007307.00
	SIC, SEN		1310.00 80	007307.40
	B, SERS		1304.10 00	007310.00
10259	B, \$+1.0		7311.50 00	007310.40
	BD, I0253		7257.04 00	007311.00
	SIC, SEN0+.32		1311.40 80	007311.40
	B, SSW	-TO SSIP.	1301.10 00	007312.00
	BD, \$+.32		7313.04 00	007312.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	007313.00
	V+, \$X13, BIT8		17336.32 80	007313.40
	SX, \$X13, IC202		10223.33 10	007314.00

10260	SIC, I0010	-TEST BIT 9.	6526.00	80	007314.40
	B, I007		6523.10	00	007315.00
	LX, \$X0, BIT9		17337.00	10	007315.40
	KV, \$X0, BIT9	-EQUAL CONDITION, KV.	17337.00	90	007316.00
	SIC, SEN		1310.00	80	007316.40
	BXL, SERS		1304.32	42	007317.00
	SIC, SEN		1310.00	80	007317.40
	BXH, SERS		1304.33	42	007320.00
	BXE, I0261		7322.32	C2	007320.40
	SIC, SEN		1310.00	80	007321.00
	B, SERS		1304.10	00	007321.40
10261	KVI, \$X0, %8#400.0	-EQUAL CONDITION, KVI.	400.01	04	007322.00
	SIC, SEN		1310.00	80	007322.40
	BXL, SERS		1304.32	42	007323.00
	SIC, SEN		1310.00	80	007323.40
	BXH, SERS		1304.33	42	007324.00
	BXE, I0262		7326.32	C2	007324.40
	SIC, SEN		1310.00	80	007325.00
	B, SERS		1304.10	00	007325.40
10262	KV, \$X0, BIT10	-INDEX HI CONDITION, KV.	17340.00	90	007326.00
	SIC, SEN		1310.00	80	007326.40
	BXL, SERS		1304.32	42	007327.00
	SIC, SEN		1310.00	80	007327.40
	BXE, SERS		1304.32	C2	007330.00
	BXH, I0263		7332.33	42	007330.40
	SIC, SEN		1310.00	80	007331.00
	B, SERS		1304.10	00	007331.40
10263	KVI, \$XC, %8#200.0	-INDEX HI CONDITION, KVI.	200.01	04	007332.00
	SIC, SEN		1310.00	80	007332.40
	BXL, SERS		1304.32	42	007333.00
	SIC, SEN		1310.00	80	007333.40
	BXE, SERS		1304.32	C2	007334.00
	BXH, I0264		7336.33	42	007334.40
	SIC, SEN		1310.00	80	007335.00
	B, SERS		1304.10	00	007335.40
10264	KV, \$XC, BIT8	-INDEX LOW CONDITION, KV.	17336.00	90	007336.00
	SIC, SEN		1310.00	80	007336.40
	BXH, SERS		1304.33	42	007337.00
	SIC, SEN		1310.00	80	007337.40
	BXE, SERS		1304.32	C2	007340.00
	BXL, I0265		7242.32	42	007340.40
	SIC, SEN		1310.00	80	007341.00
	B, SERS		1304.10	00	007341.40
10265	KVI, \$X0, %8#1000.0	-INDEX LOW CONDITION, KVI.	1000.01	04	007342.00
	SIC, SEN		1310.00	80	007342.40
	BXH, SERS		1304.33	42	007343.00
	SIC, SEN		1310.00	80	007343.40
	BXE, SERS		1304.32	C2	007344.00
	BXL, I0266		7346.32	42	007344.40
	SIC, SEN		1310.00	80	007345.00
	B, SERS		1304.10	00	007345.40
10266	B, \$+1.0		7347.10	00	007346.00
	BD, I0260		7314.44	00	007346.40
	SIC, SEN0+.32		1311.40	80	007347.00
	B, SSW	-TO SSIP.	1301.10	00	007347.40
	BD, \$+.32		7350.44	00	007350.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007350.40
	V+, \$X13, BIT9		17337.32	B0	007351.00
	SX, \$X13, IC202		10223.33	10	007351.40

10267	SIC, I0010	-TEST BIT 10.	6526.00	80	007352.00
	B, I007		6523.10	00	007352.40
	LX, \$X0, BIT10		17340.00	10	007353.00
	KV, \$X0, BIT10	-EQUAL CONDITION, KV.	17340.00	90	007353.40
	SIC, SEN		1310.00	80	007354.00
	BXL, SERS		1304.32	42	007354.40
	SIC, SEN		1310.00	80	007355.00
	BXH, SERS		1304.33	42	007355.40
	BXE, I0268		7357.72	C2	007356.00
	SIC, SEN		1310.00	80	007356.40
	B, SERS		1304.10	00	007357.00
10268	KVI, \$X0, %8#200.0	-EQUAL CONDITION, KVI.	200.01	04	007357.40
	SIC, SEN		1310.00	80	007360.00
	BXL, SERS		1304.32	42	007360.40
	SIC, SEN		1310.00	80	007361.00
	BXH, SERS		1304.33	42	007361.40
	BXE, I0269		7363.72	C2	007362.00
	SIC, SEN		1310.00	80	007362.40
	B, SERS		1304.10	00	007363.00
10269	KV, \$X0, BIT11	-INDEX HI CONDITION, KV.	17341.00	90	007363.40
	SIC, SEN		1310.00	80	007364.00
	BXL, SERS		1304.32	42	007364.40
	SIC, SEN		1310.00	80	007365.00
	BXE, SERS		1304.32	C2	007365.40
	BXH, I0270		7367.73	42	007366.00
	SIC, SEN		1310.00	80	007366.40
	B, SERS		1304.10	00	007367.00
10270	KVI, \$X0, %8#100.0	-INDEX HI CONDITION, KVI.	100.01	04	007367.40
	SIC, SEN		1310.00	80	007370.00
	BXL, SERS		1304.32	42	007370.40
	SIC, SEN		1310.00	80	007371.00
	BXE, SERS		1304.32	C2	007371.40
	BXH, I0271		7373.73	42	007372.00
	SIC, SEN		1310.00	80	007372.40
	B, SERS		1304.10	00	007373.00
10271	KV, \$X0, BIT9	-INDEX LOW CONDITION, KV.	17337.00	90	007373.40
	SIC, SEN		1310.00	80	007374.00
	BXH, SERS		1304.33	42	007374.40
	SIC, SEN		1310.00	80	007375.00
	BXE, SERS		1304.32	C2	007375.40
	BXL, I0272		7377.72	42	007376.00
	SIC, SEN		1310.00	80	007376.40
	B, SERS		1304.10	00	007377.00
10272	KVI, \$X0, %8#400.0	-INDEX LOW CONDITION, KVI.	400.01	04	007377.40
	SIC, SEN		1310.00	80	007400.00
	BXH, SERS		1304.33	42	007400.40
	SIC, SEN		1310.00	80	007401.00
	BXE, SERS		1304.32	C2	007401.40
	BXL, I0273		7403.72	42	007402.00
	SIC, SEN		1310.00	80	007402.40
	B, SERS		1304.10	00	007403.00
10273	B, \$+1.0		7404.50	00	007403.40
	BD, I0267		7352.04	00	007404.00
	SIC, SEN0+.32		1311.40	80	007404.40
	B, SSW	-TO SSIP.	1301.10	00	007405.00
	BD, \$+.32		7406.04	00	007405.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007406.00
	V+, \$X13, BIT10		17340.32	B0	007406.40
	SX, \$X13, IC202		10223.33	10	007407.00

10274	SIC, I0010	-TEST BIT 11.	6526.00	80	007407.40
	B, I007		6523.10	00	007410.00
	LX, SX0, BIT11		17341.00	10	007410.40
	KV, SX0, BIT11	-EQUAL CONDITION, KV.	17341.00	90	007411.00
	SIC, SEN		1310.00	80	007411.40
	BXL, SERS		1304.32	42	007412.00
	SIC, SEN		1310.00	80	007412.40
	BXH, SERS		1304.33	42	007413.00
	BXE, I0275		7415.32	C2	007413.40
	SIC, SEN		1310.00	80	007414.00
	B, SERS		1304.10	00	007414.40
10275	KVI, SX0, %8#100.0	-EQUAL CONDITION, KVI.	100.01	04	007415.00
	SIC, SEN		1310.00	80	007415.40
	BXL, SERS		1304.32	42	007416.00
	SIC, SEN		1310.00	80	007416.40
	BXH, SERS		1304.33	42	007417.00
	BXE, I0276		7421.32	C2	007417.40
	SIC, SEN		1310.00	80	007420.00
	B, SERS		1304.10	00	007420.40
10276	KV, SX0, BIT12	-INDEX HI CONDITION, KV.	17342.00	90	007421.00
	SIC, SEN		1310.00	80	007421.40
	BXL, SERS		1304.32	42	007422.00
	SIC, SEN		1310.00	80	007422.40
	BXE, SERS		1304.32	C2	007423.00
	BXH, I0277		7425.33	42	007423.40
	SIC, SEN		1310.00	80	007424.00
	B, SERS		1304.10	00	007424.40
10277	KVI, SX0, %8#40.0	-INDEX HI CONDITION, KVI.	40.01	04	007425.00
	SIC, SEN		1310.00	80	007425.40
	BXL, SERS		1304.32	42	007426.00
	SIC, SEN		1310.00	80	007426.40
	BXE, SERS		1304.32	C2	007427.00
	BXH, I0278		7431.33	42	007427.40
	SIC, SEN		1310.00	80	007430.00
	B, SERS		1304.10	00	007430.40
10278	KV, SX0, BIT10	-INDEX LOW CONDITION, KV.	17340.00	90	007431.00
	SIC, SEN		1310.00	80	007431.40
	BXH, SERS		1304.33	42	007432.00
	SIC, SEN		1310.00	80	007432.40
	BXE, SERS		1304.32	C2	007433.00
	BXL, I0279		7435.32	42	007433.40
	SIC, SEN		1310.00	80	007434.00
	B, SERS		1304.10	00	007434.40
10279	KVI, SX0, %8#200.0	-INDEX LOW CONDITION, KVI.	200.01	04	007435.00
	SIC, SEN		1310.00	80	007435.40
	BXH, SERS		1304.33	42	007436.00
	SIC, SEN		1310.00	80	007436.40
	BXE, SERS		1304.32	C2	007437.00
	BXL, I0280		7441.32	42	007437.40
	SIC, SEN		1310.00	80	007440.00
	B, SERS		1304.10	00	007440.40
10280	B, \$+1.0		7442.10	00	007441.00
	BD, I0274		7407.44	00	007441.40
	SIC, SEN0+.32		1311.40	80	007442.00
	B, SSW	-TO SSIP.	1301.10	00	007442.40
	BD, \$+.32		7443.44	00	007443.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007443.40
	V+, \$X13, BIT11		17341.32	B0	007444.00
	SX, \$X13, IC202		10223.33	10	007444.40

10281	SIC, I0010	-TEST BIT 12.	6526.00	80	007445.00
	B, I007		6523.10	00	007445.40
	LX, SX0, BIT12		17342.00	10	007446.00
	KV, SX0, BIT12	-EQUAL CONDITION, KV.	17342.00	90	007446.40
	SIC, SEN		1310.00	80	007447.00
	BXL, SERS		1304.32	42	007447.40
	SIC, SEN		1310.00	80	007450.00
	BXH, SERS		1304.33	42	007450.40
	BXE, I0282		7452.72	C2	007451.00
	SIC, SEN		1310.00	80	007451.40
	B, SERS		1304.10	00	007452.00
10282	KVI, SX0, %8#40.0	-EQUAL CONDITION, KVI.	40.01	04	007452.40
	SIC, SEN		1310.00	80	007453.00
	BXL, SERS		1304.32	42	007453.40
	SIC, SEN		1310.00	80	007454.00
	BXH, SERS		1304.33	42	007454.40
	BXE, I0283		7456.72	C2	007455.00
	SIC, SEN		1310.00	80	007455.40
	B, SERS		1304.10	00	007456.00
10283	KV, SX0, BIT13	-INDEX HI CONDITION, KV.	17343.00	90	007456.40
	SIC, SEN		1310.00	80	007457.00
	BXL, SERS		1304.32	42	007457.40
	SIC, SEN		1310.00	80	007460.00
	BXE, SERS		1304.32	C2	007460.40
	BXH, I0284		7462.73	42	007461.00
	SIC, SEN		1310.00	80	007461.40
	B, SERS		1304.10	00	007462.00
10284	KVI, SX0, %8#20.0	-INDEX HI CONDITION, KVI.	20.01	04	007462.40
	SIC, SEN		1310.00	80	007463.00
	BXL, SERS		1304.32	42	007463.40
	SIC, SEN		1310.00	80	007464.00
	BXE, SERS		1304.32	C2	007464.40
	BXH, I0285		7466.73	42	007465.00
	SIC, SEN		1310.00	80	007465.40
	B, SERS		1304.10	00	007466.00
10285	KV, SX0, BIT11	-INDEX LOW CONDITION, KV.	17341.00	90	007466.40
	SIC, SEN		1310.00	80	007467.00
	BXH, SERS		1304.33	42	007467.40
	SIC, SEN		1310.00	80	007470.00
	BXE, SERS		1304.32	C2	007470.40
	BXL, I0286		7472.72	42	007471.00
	SIC, SEN		1310.00	80	007471.40
	B, SERS		1304.10	00	007472.00
10286	KVI, SX0, %8#100.0	-INDEX LOW CONDITION, KVI.	100.01	04	007472.40
	SIC, SEN		1310.00	80	007473.00
	BXH, SERS		1304.33	42	007473.40
	SIC, SEN		1310.00	80	007474.00
	BXE, SERS		1304.32	C2	007474.40
	BXL, I0287		7476.72	42	007475.00
	SIC, SEN		1310.00	80	007475.40
	B, SERS		1304.10	00	007476.00
10287	B, \$+1.0		7477.50	00	007476.40
	BD, I0281		7445.04	00	007477.00
	SIC, SEN0+.32		1311.40	80	007477.40
	B, SSW	-TO SSIP.	1301.10	00	007500.00
	BD, \$+.32		7501.04	00	007500.40
	LX, SX13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007501.00
	V+, SX13, BIT12		17342.32	B0	007501.40
	SX, SX13, IC202		10223.33	10	007502.00

10288	SIC, I0010	-TEST BIT 13.	6526.00	80	007502.40
	B, I007		6523.10	00	007503.00
	LX, \$X0, BIT13		17343.00	10	007503.40
	KV, \$X0, BIT13	-EQUAL CONDITION, KV.	17343.00	90	007504.00
	SIC, SEN		1310.00	80	007504.40
	BXL, SERS		1304.32	42	007505.00
	SIC, SEN		1310.00	80	007505.40
	BXH, SERS		1304.33	42	007506.00
	BXE, I0289		7510.32	C2	007506.40
	SIC, SEN		1310.00	80	007507.00
	B, SERS		1304.10	00	007507.40
10289	KVI, \$X0, %8#20.0	-EQUAL CONDITION, KVI.	20.01	04	007510.00
	SIC, SEN		1310.00	80	007510.40
	BXL, SERS		1304.32	42	007511.00
	SIC, SEN		1310.00	80	007511.40
	BXH, SERS		1304.33	42	007512.00
	BXE, I0290		7514.32	C2	007512.40
	SIC, SEN		1310.00	80	007513.00
	B, SERS		1304.10	00	007513.40
10290	KV, \$X0, BIT14	-INDEX HI CONDITION, KV.	17344.00	90	007514.00
	SIC, SEN		1310.00	80	007514.40
	BXL, SERS		1304.32	42	007515.00
	SIC, SEN		1310.00	80	007515.40
	BXE, SERS		1304.32	C2	007516.00
	BXH, I0291		7520.33	42	007516.40
	SIC, SEN		1310.00	80	007517.00
	B, SERS		1304.10	00	007517.40
10291	KVI, \$X0, %8#10.0	-INDEX HI CONDITION, KVI.	10.01	04	007520.00
	SIC, SEN		1310.00	80	007520.40
	BXL, SERS		1304.32	42	007521.00
	SIC, SEN		1310.00	80	007521.40
	BXE, SERS		1304.32	C2	007522.00
	BXH, I0292		7524.33	42	007522.40
	SIC, SEN		1310.00	80	007523.00
	B, SERS		1304.10	00	007523.40
10292	KV, \$X0, BIT12	-INDEX LOW CONDITION, KV.	17342.00	90	007524.00
	SIC, SEN		1310.00	80	007524.40
	BXH, SERS		1304.33	42	007525.00
	SIC, SEN		1310.00	80	007525.40
	BXE, SERS		1304.32	C2	007526.00
	BXL, I0293		7530.32	42	007526.40
	SIC, SEN		1310.00	80	007527.00
	B, SERS		1304.10	00	007527.40
10293	KVI, \$X0, %8#40.0	-INDEX LOW CONDITION, KVI.	40.01	04	007530.00
	SIC, SEN		1310.00	80	007530.40
	BXH, SERS		1304.33	42	007531.00
	SIC, SEN		1310.00	80	007531.40
	BXE, SERS		1304.32	C2	007532.00
	BXL, I0294		7534.32	42	007532.40
	SIC, SEN		1310.00	80	007533.00
	B, SERS		1304.10	00	007533.40
10294	B, \$+1.0		7535.10	00	007534.00
	BD, I0288		7502.44	00	007534.40
	SIC, SEN0+.32		1311.40	80	007535.00
	B, SSW	-TO SSIP.	1301.10	00	007535.40
	BD, \$+.32		7536.44	00	007536.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007536.40
	V+, \$X13, BIT13		17343.32	B0	007537.00
	SX, \$X13, IC202		10223.33	10	007537.40

10295	SIC,IC010	-TEST BIT 14.	6526.00	20	007540.00
	B,1007		6523.10	00	007540.40
	LX,\$X0,BIT14		17344.00	10	007541.00
	KV,\$X0,BIT14	-EQUAL CONDITION,KV.	17344.00	90	007541.40
	SIC,SEN		1310.00	80	007542.00
	BXL,SERS		1304.32	42	007542.40
	SIC,SEN		1310.00	80	007543.00
	BXH,SERS		1304.33	42	007543.40
	BXE,10296		7545.72	C2	007544.00
	SIC,SEN		1310.00	80	007544.40
	B,SERS		1304.10	00	007545.00
10296	KVI,\$X0,%8#10.0	-EQUAL CONDITION,KVI.	10.01	04	007545.40
	SIC,SEN		1310.00	80	007546.00
	BXL,SERS		1304.32	42	007546.40
	SIC,SEN		1310.00	80	007547.00
	BXH,SERS		1304.33	42	007547.40
	BXE,10297		7551.72	C2	007550.00
	SIC,SEN		1310.00	80	007550.40
	B,SERS		1304.10	00	007551.00
10297	KV,\$X0,BIT15	-INDEX HI CONDITION,KV.	17345.00	90	007551.40
	SIC,SEN		1310.00	80	007552.00
	BXL,SERS		1304.32	42	007552.40
	SIC,SEN		1310.00	80	007553.00
	BXE,SERS		1304.32	C2	007553.40
	BXH,10298		7555.73	42	007554.00
	SIC,SEN		1310.00	80	007554.40
	B,SERS		1304.10	00	007555.00
10298	KVI,\$X0,%8#4.0	-INDEX HI CONDITION,KVI.	4.01	04	007555.40
	SIC,SEN		1310.00	80	007556.00
	BXL,SERS		1304.32	42	007556.40
	SIC,SEN		1310.00	80	007557.00
	BXE,SERS		1304.32	C2	007557.40
	BXH,10299		7561.73	42	007560.00
	SIC,SEN		1310.00	80	007560.40
	B,SERS		1304.10	00	007561.00
10299	KV,\$X0,BIT13	-INDEX LOW CONDITION,KV.	17343.00	90	007561.40
	SIC,SEN		1310.00	80	007562.00
	BXH,SERS		1304.33	42	007562.40
	SIC,SEN		1310.00	80	007563.00
	BXE,SERS		1304.32	C2	007563.40
	BXL,102100		7565.72	42	007564.00
	SIC,SEN		1310.00	80	007564.40
	B,SERS		1304.10	00	007565.00
102100	KVI,\$X0,%8#20.0	-INDEX LOW CONDITION,KVI.	20.01	04	007565.40
	SIC,SEN		1310.00	80	007566.00
	BXH,SERS		1304.33	42	007566.40
	SIC,SEN		1310.00	80	007567.00
	BXE,SERS		1304.32	C2	007567.40
	BXL,102101		7571.72	42	007570.00
	SIC,SEN		1310.00	80	007570.40
	B,SERS		1304.10	00	007571.00
102101	B,\$+1.0		7572.50	00	007571.40
	BD,10295		7540.04	00	007572.00
	SIC,SEN0+.32		1311.40	80	007572.40
	B,SSW	-TO SSIP.	1301.10	00	007573.00
	BD,\$+.32		7574.04	00	007573.40
	LX,\$X13,IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007574.00
	V+,\$X13,BIT14		17344.32	B0	007574.40
	SX,\$X13,IC202		10223.33	10	007575.00

102102	SIC, I0010	-TEST BIT 15.	6526.00	80	007575.40
	B, I007		6523.10	00	007576.00
	LX, \$X0, BIT15		17345.00	10	007576.40
	KV, \$X0, BIT15	-EQUAL CONDITION, KV.	17345.00	90	007577.00
	SIC, SEN		1310.00	80	007577.40
	BXL, SERS		1304.32	42	007600.00
	SIC, SEN		1310.00	80	007600.40
	BXH, SERS		1304.33	42	007601.00
	BXE, I02103		7603.32	C2	007601.40
	SIC, SEN		1310.00	80	007602.00
	B, SERS		1304.10	00	007602.40
102103	KVI, \$X0, %8#4.0	-EQUAL CONDITION, KVI.	4.01	04	007603.00
	SIC, SEN		1310.00	80	007603.40
	BXL, SERS		1304.32	42	007604.00
	SIC, SEN		1310.00	80	007604.40
	BXH, SERS		1304.33	42	007605.00
	BXE, I02104		7607.32	C2	007605.40
	SIC, SEN		1310.00	80	007606.00
	B, SERS		1304.10	00	007606.40
102104	KV, \$X0, BIT16	-INDEX HI CONDITION, KV.	17346.00	90	007607.00
	SIC, SEN		1310.00	80	007607.40
	BXL, SERS		1304.32	42	007610.00
	SIC, SEN		1310.00	80	007610.40
	BXE, SERS		1304.32	C2	007611.00
	BXH, I02105		7613.33	42	007611.40
	SIC, SEN		1310.00	80	007612.00
	B, SERS		1304.10	00	007612.40
102105	KVI, \$X0, %8#2.0	-INDEX HI CONDITION, KVI.	2.01	04	007613.00
	SIC, SEN		1310.00	80	007613.40
	BXL, SERS		1304.32	42	007614.00
	SIC, SEN		1310.00	80	007614.40
	BXE, SERS		1304.32	C2	007615.00
	BXH, I02106		7617.33	42	007615.40
	SIC, SEN		1310.00	80	007616.00
	B, SERS		1304.10	00	007616.40
102106	KV, \$X0, BIT14	-INDEX LOW CONDITION, KV.	17344.00	90	007617.00
	SIC, SEN		1310.00	80	007617.40
	BXH, SERS		1304.33	42	007620.00
	SIC, SEN		1310.00	80	007620.40
	BXE, SERS		1304.32	C2	007621.00
	BXL, I02107		7623.32	42	007621.40
	SIC, SEN		1310.00	80	007622.00
	B, SERS		1304.10	00	007622.40
102107	KVI, \$X0, %8#10.0	-INDEX LOW CONDITION, KVI.	10.01	04	007623.00
	SIC, SEN		1310.00	80	007623.40
	BXH, SERS		1304.33	42	007624.00
	SIC, SEN		1310.00	80	007624.40
	BXE, SERS		1304.32	C2	007625.00
	BXL, I02108		7627.32	42	007625.40
	SIC, SEN		1310.00	80	007626.00
	B, SERS		1304.10	00	007626.40
102108	B, \$+1.0		7630.10	00	007627.00
	BD, I02102		7575.44	00	007627.40
	SIC, SEN0+.32		1311.40	80	007630.00
	B, SSW	-TO SSIP.	1301.10	00	007630.40
	BD, \$+.32		7631.44	00	007631.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007631.40
	V+, \$X13, BIT15		17345.32	B0	007632.00
	SX, \$X13, IC202		10223.33	10	007632.40

102109	SIC, I0010	-TEST BIT 16.	6526.00	80	007633.00
	B, I007		6523.10	00	007633.40
	LX, SX0, BIT16		17346.00	10	007634.00
	KV, SX0, BIT16	-EQUAL CONDITION, KV.	17346.00	90	007634.40
	SIC, SEN		1310.00	80	007635.00
	BXL, SERS		1304.32	42	007635.40
	SIC, SEN		1310.00	80	007636.00
	BXH, SERS		1304.33	42	007636.40
	BXE, I02110		7640.72	C2	007637.00
	SIC, SEN		1310.00	80	007637.40
	B, SERS		1304.10	00	007640.00
102110	KVI, SX0, %8#2.0	-EQUAL CONDITION, KVI.	2.01	04	007640.40
	SIC, SEN		1310.00	80	007641.00
	BXL, SERS		1304.32	42	007641.40
	SIC, SEN		1310.00	80	007642.00
	BXH, SERS		1304.33	42	007642.40
	BXE, I02111		7644.72	C2	007643.00
	SIC, SEN		1310.00	80	007643.40
	B, SERS		1304.10	00	007644.00
102111	KV, SX0, BIT17	-INDEX HI CONDITION, KV.	17347.00	90	007644.40
	SIC, SEN		1310.00	80	007645.00
	BXL, SERS		1304.32	42	007645.40
	SIC, SEN		1310.00	80	007646.00
	BXE, SERS		1304.32	C2	007646.40
	BXH, I02112		7650.73	42	007647.00
	SIC, SEN		1310.00	80	007647.40
	B, SERS		1304.10	00	007650.00
102112	KVI, SX0, %8#1.0	-INDEX HI CONDITION, KVI.	1.01	04	007650.40
	SIC, SEN		1310.00	80	007651.00
	BXL, SERS		1304.32	42	007651.40
	SIC, SEN		1310.00	80	007652.00
	BXE, SERS		1304.32	C2	007652.40
	BXH, I02113		7654.73	42	007653.00
	SIC, SEN		1310.00	80	007653.40
	B, SERS		1304.10	00	007654.00
102113	KV, SX0, BIT15	-INDEX LOW CONDITION, KV.	17345.00	90	007654.40
	SIC, SEN		1310.00	80	007655.00
	BXH, SERS		1304.33	42	007655.40
	SIC, SEN		1310.00	80	007656.00
	BXE, SERS		1304.32	C2	007656.40
	BXL, I02114		7660.72	42	007657.00
	SIC, SEN		1310.00	80	007657.40
	B, SERS		1304.10	00	007660.00
102114	KVI, SX0, %8#4.0	-INDEX LOW CONDITION, KVI.	4.01	04	007660.40
	SIC, SEN		1310.00	80	007661.00
	BXH, SERS		1304.33	42	007661.40
	SIC, SEN		1310.00	80	007662.00
	BXE, SERS		1304.32	C2	007662.40
	BXL, I02115		7664.72	42	007663.00
	SIC, SEN		1310.00	80	007663.40
	B, SERS		1304.10	00	007664.00
102115	B, \$+1.0		7665.50	00	007664.40
	BD, I02109		7633.04	00	007665.00
	SIC, SEN0+.32		1311.40	80	007665.40
	B, SSW	-TO SSIP.	1301.10	00	007666.00
	BD, \$+.32		7667.04	00	007666.40
	LX, SX13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007667.00
	V+, SX13, BIT16		17346.32	B0	007667.40
	SX, SX13, IC202		10223.33	10	007670.00

102116	SIC, I0010	-TEST BIT 17.	6526.00	80	007670.40
	B, I007		6523.10	00	007671.00
	LX, \$X0, BIT17		17347.00	10	007671.40
	KV, \$X0, BIT17	-EQUAL CONDITION, KV.	17347.00	90	007672.00
	SIC, SEN		1310.00	80	007672.40
	BXL, SERS		1304.32	42	007673.00
	SIC, SEN		1310.00	80	007673.40
	BXH, SERS		1304.33	42	007674.00
	BXE, I02117		7676.32	C2	007674.40
	SIC, SEN		1310.00	80	007675.00
	B, SERS		1304.10	00	007675.40
102117	KVI, \$X0, %8#1.0	-EQUAL CONDITION, KVI.	1.01	04	007676.00
	SIC, SEN		1310.00	80	007676.40
	BXL, SERS		1304.32	42	007677.00
	SIC, SEN		1310.00	80	007677.40
	BXH, SERS		1304.33	42	007700.00
	BXE, I02118		7702.32	C2	007700.40
	SIC, SEN		1310.00	80	007701.00
	B, SERS		1304.10	00	007701.40
102118	KV, \$X0, BIT18	-INDEX HI CONDITION, KV.	17350.00	90	007702.00
	SIC, SEN		1310.00	80	007702.40
	BXL, SERS		1304.32	42	007703.00
	SIC, SEN		1310.00	80	007703.40
	BXE, SERS		1304.32	C2	007704.00
	BXH, I02119		7706.33	42	007704.40
	SIC, SEN		1310.00	80	007705.00
	B, SERS		1304.10	00	007705.40
102119	KVI, \$X0, %8#0.40	-INDEX HI CONDITION, KVI.	0.41	04	007706.00
	SIC, SEN		1310.00	80	007706.40
	BXL, SERS		1304.32	42	007707.00
	SIC, SEN		1310.00	80	007707.40
	BXE, SERS		1304.32	C2	007710.00
	BXH, I02120		7712.33	42	007710.40
	SIC, SEN		1310.00	80	007711.00
	B, SERS		1304.10	00	007711.40
102120	KV, \$X0, BIT16	-INDEX LOW CONDITION, KV.	17346.00	90	007712.00
	SIC, SEN		1310.00	80	007712.40
	BXH, SERS		1304.33	42	007713.00
	SIC, SEN		1310.00	80	007713.40
	BXE, SERS		1304.32	C2	007714.00
	BXL, I02121		7716.32	42	007714.40
	SIC, SEN		1310.00	80	007715.00
	B, SERS		1304.10	00	007715.40
102121	KVI, \$X0, %8#2.0	-INDEX LOW CONDITION, KVI.	2.01	04	007716.00
	SIC, SEN		1310.00	80	007716.40
	BXH, SERS		1304.33	42	007717.00
	SIC, SEN		1310.00	80	007717.40
	BXE, SERS		1304.32	C2	007720.00
	BXL, I02122		7722.32	42	007720.40
	SIC, SEN		1310.00	80	007721.00
	B, SERS		1304.10	00	007721.40
102122	B, \$+1.0		7723.10	00	007722.00
	BD, I02116		7670.44	00	007722.40
	SIC, SEN0+.32		1311.40	80	007723.00
	B, SSW	-TO SSIP.	1301.10	00	007723.40
	BD, \$+.32		7724.44	00	007724.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007724.40
	V+, \$X13, BIT17		17347.32	B0	007725.00
	SX, \$X13, IC202		10223.33	10	007725.40

102123	SIC, I0010	-TEST BIT 18.	6526.00	80	007726.00
	B, I007		6523.10	00	007726.40
	LX, SX0, BIT18		17350.00	10	007727.00
	KV, SX0, BIT18	-EQUAL CONDITION, KV.	17350.00	90	007727.40
	SIC, SEN		1310.00	80	007730.00
	BXL, SERS		1304.32	42	007730.40
	SIC, SEN		1310.00	80	007731.00
	BXH, SERS		1304.33	42	007731.40
	BXE, I02124		7733.72	C2	007732.00
	SIC, SEN		1310.00	80	007732.40
	B, SERS		1304.10	00	007733.00
102124	KVI, SX0, %8#0.40	-EQUAL CONDITION, KVI.	0.41	04	007733.40
	SIC, SEN		1310.00	80	007734.00
	BXL, SERS		1304.32	42	007734.40
	SIC, SEN		1310.00	80	007735.00
	BXH, SERS		1304.33	42	007735.40
	BXE, I02125		7737.72	C2	007736.00
	SIC, SEN		1310.00	80	007736.40
	B, SERS		1304.10	00	007737.00
102125	KV, SX0, BIT19	-INDEX HI CONDITION, KV.	17351.00	90	007737.40
	SIC, SEN		1310.00	80	007740.00
	BXL, SERS		1304.32	42	007740.40
	SIC, SEN		1310.00	80	007741.00
	BXE, SERS		1304.32	C2	007741.40
	BXH, I02126		7743.73	42	007742.00
	SIC, SEN		1310.00	80	007742.40
	B, SERS		1304.10	00	007743.00
102126	KVI, SX0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01	04	007743.40
	SIC, SEN		1310.00	80	007744.00
	BXL, SERS		1304.32	42	007744.40
	SIC, SEN		1310.00	80	007745.00
	BXE, SERS		1304.32	C2	007745.40
	BXH, I02127		7747.73	42	007746.00
	SIC, SEN		1310.00	80	007746.40
	B, SERS		1304.10	00	007747.00
102127	KV, SX0, BIT17	-INDEX LOW CONDITION, KV.	17347.00	90	007747.40
	SIC, SEN		1310.00	80	007750.00
	BXH, SERS		1304.33	42	007750.40
	SIC, SEN		1310.00	80	007751.00
	BXE, SERS		1304.32	C2	007751.40
	BXL, I02128		7753.72	42	007752.00
	SIC, SEN		1310.00	80	007752.40
	B, SERS		1304.10	00	007753.00
102128	KVI, SX0, %8#1.0	-INDEX LOW CONDITION, KVI.	1.01	04	007753.40
	SIC, SEN		1310.00	80	007754.00
	BXH, SERS		1304.33	42	007754.40
	SIC, SEN		1310.00	80	007755.00
	BXE, SERS		1304.32	C2	007755.40
	BXL, I02129		7757.72	42	007756.00
	SIC, SEN		1310.00	80	007756.40
	B, SERS		1304.10	00	007757.00
102129	B, \$+1.0		7760.50	00	007757.40
	BD, I02123		7726.04	00	007760.00
	SIC, SEN0+.32		1311.40	80	007760.40
	B, SSW	-TO SSIP.	1301.10	00	007761.00
	BD, \$+.32		7762.04	00	007761.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	007762.00
	V+, \$X13, BIT18		17350.32	B0	007762.40
	SX, \$X13, IC202		10223.33	10	007763.00

102130	SIC, I0010	-TEST BIT 19.	6526.00	80	007763.40
	B, I007		6523.10	00	007764.00
	LX, SX0, BIT19		17351.00	10	007764.40
	KV, SX0, BIT19	-EQUAL CONDITION, KV.	17351.00	90	007765.00
	SIC, SEN		1310.00	80	007765.40
	BXL, SERS		1304.32	42	007766.00
	SIC, SEN		1310.00	80	007766.40
	BXH, SERS		1304.32	42	007767.00
	BXE, I02131		7771.32	C2	007767.40
	SIC, SEN		1310.00	80	007770.00
	B, SERS		1304.10	00	007770.40
102131	KV, SX0, BIT20	-INDEX HI CONDITION, KV.	17352.00	90	007771.00
	SIC, SEN		1310.00	80	007771.40
	BXL, SERS		1304.32	42	007772.00
	SIC, SEN		1310.00	80	007772.40
	BXE, SERS		1304.32	C2	007773.00
	BXH, I02132		7775.33	42	007773.40
	SIC, SEN		1310.00	80	007774.00
	B, SERS		1304.10	00	007774.40
102132	KVI, SX0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01	04	007775.00
	SIC, SEN		1310.00	80	007775.40
	BXL, SERS		1304.32	42	007776.00
	SIC, SEN		1310.00	80	007776.40
	BXE, SERS		1304.32	C2	007777.00
	BXH, I02133		10001.33	42	007777.40
	SIC, SEN		1310.00	80	010000.00
	B, SERS		1304.10	00	010000.40
102133	KV, SX0, BIT18	-INDEX LOW CONDITION, KV.	17350.00	90	010001.00
	SIC, SEN		1310.00	80	010001.40
	BXH, SERS		1304.32	42	010002.00
	SIC, SEN		1310.00	80	010002.40
	BXE, SERS		1304.32	C2	010003.00
	BXL, I02134		10005.32	42	010003.40
	SIC, SEN		1310.00	80	010004.00
	B, SERS		1304.10	00	010004.40
102134	KVI, SX0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41	04	010005.00
	SIC, SEN		1310.00	80	010005.40
	BXH, SERS		1304.32	42	010006.00
	SIC, SEN		1310.00	80	010006.40
	BXE, SERS		1304.32	C2	010007.00
	BXL, I02135		10011.32	42	010007.40
	SIC, SEN		1310.00	80	010010.00
	B, SERS		1304.10	00	010010.40
102135	B, \$+1.0		10012.10	00	010011.00
	BD, I02130		7763.44	00	010011.40
	SIC, SEN0+.32		1311.40	80	010012.00
	B, SSW	-TO SSIP.	1301.10	00	010012.40
	BD, \$+.32		10013.44	00	010013.00
	LX, SX13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	010013.40
	V+, SX13, BIT19		17351.32	80	010014.00
	SX, SX13, IC202		10223.33	10	010014.40

102135	SIC, I0010	-TEST BIT 20.	6526.00 80	010015.00
	B, I007		6523.10 00	010015.40
	LX, \$X0, BIT20		17352.00 10	010016.00
	KV, \$X0, BIT20	-EQUAL CONDITION, KV.	17352.00 90	010016.40
	SIC, SEN		1310.00 80	010017.00
	BXL, SERS		1304.32 42	010017.40
	SIC, SEN		1310.00 80	010020.00
	BXH, SERS		1304.33 42	010020.40
	BXE, I02137		10022.72 C2	010021.00
	SIC, SEN		1310.00 80	010021.40
	B, SERS		1304.10 00	010022.00
102137	KV, \$X0, BIT21	-INDEX HI CONDITION, KV.	17353.00 90	010022.40
	SIC, SEN		1310.00 80	010023.00
	BXL, SERS		1304.32 42	010023.40
	SIC, SEN		1310.00 80	010024.00
	BXE, SERS		1304.32 C2	010024.40
	BXH, I02138		10026.73 42	010025.00
	SIC, SEN		1310.00 80	010025.40
	B, SERS		1304.10 00	010026.00
102138	KVI, \$X0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01 04	010026.40
	SIC, SEN		1310.00 80	010027.00
	BXL, SERS		1304.32 42	010027.40
	SIC, SEN		1310.00 80	010030.00
	BXE, SERS		1304.32 C2	010030.40
	BXH, I02139		10032.73 42	010031.00
	SIC, SEN		1310.00 80	010031.40
	B, SERS		1304.10 00	010032.00
102139	KV, \$X0, BIT19	-INDEX LOW CONDITION, KV.	17351.00 90	010032.40
	SIC, SEN		1310.00 80	010033.00
	BXH, SERS		1304.33 42	010033.40
	SIC, SEN		1310.00 80	010034.00
	BXE, SERS		1304.32 C2	010034.40
	BXL, I02140		10036.72 42	010035.00
	SIC, SEN		1310.00 80	010035.40
	B, SERS		1304.10 00	010036.00
102140	KVI, \$X0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41 04	010036.40
	SIC, SEN		1310.00 80	010037.00
	BXH, SERS		1304.33 42	010037.40
	SIC, SEN		1310.00 80	010040.00
	BXE, SERS		1304.32 C2	010040.40
	BXL, I02141		10042.72 42	010041.00
	SIC, SEN		1310.00 80	010041.40
	B, SERS		1304.10 00	010042.00
102141	B, \$+1.0		10043.50 00	010042.40
	BD, I02136		10015.04 00	010043.00
	SIC, SEN0+.32		1311.40 80	010043.40
	B, SSW	-TO SSIP.	1301.10 00	010044.00
	BD, \$+.32		10045.04 00	010044.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	010045.00
	V+, \$X13, BIT20		17352.32 80	010045.40
	SX, \$X13, IC202		10223.33 10	010046.00

102142	SIC, I0010	-TEST BIT 21.	6526.00	80	010046.40
	B, I007		6523.10	00	010047.00
	LX, SX0, BIT21		17353.00	10	010047.40
	KV, SX0, BIT21	-EQUAL CONDITION, KV.	17353.00	90	010050.00
	SIC, SEN		1310.00	80	010050.40
	BXL, SERS		1304.32	42	010051.00
	SIC, SEN		1310.00	80	010051.40
	BXH, SERS		1304.33	42	010052.00
	BXE, I02143		10054.32	C2	010052.40
	SIC, SEN		1310.00	80	010053.00
	B, SERS		1304.10	00	010053.40
102143	KV, SX0, BIT22	-INDEX HI CONDITION, KV.	17354.00	90	010054.00
	SIC, SEN		1310.00	80	010054.40
	BXL, SERS		1304.32	42	010055.00
	SIC, SEN		1310.00	80	010055.40
	BXE, SERS		1304.32	C2	010056.00
	BXH, I02144		10060.33	42	010056.40
	SIC, SEN		1310.00	80	010057.00
	B, SERS		1304.10	00	010057.40
102144	KVI, SX0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01	04	010060.00
	SIC, SEN		1310.00	80	010060.40
	BXL, SERS		1304.32	42	010061.00
	SIC, SEN		1310.00	80	010061.40
	BXE, SERS		1304.32	C2	010062.00
	BXH, I02145		10064.33	42	010062.40
	SIC, SEN		1310.00	80	010063.00
	B, SERS		1304.10	00	010063.40
102145	KV, SX0, BIT20	-INDEX LOW CONDITION, KV.	17352.00	90	010064.00
	SIC, SEN		1310.00	80	010064.40
	BXH, SERS		1304.33	42	010065.00
	SIC, SEN		1310.00	80	010065.40
	BXE, SERS		1304.32	C2	010066.00
	BXL, I02146		10070.32	42	010066.40
	SIC, SEN		1310.00	80	010067.00
	B, SERS		1304.10	00	010067.40
102146	KVI, SX0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41	04	010070.00
	SIC, SEN		1310.00	80	010070.40
	BXH, SERS		1304.33	42	010071.00
	SIC, SEN		1310.00	80	010071.40
	BXE, SERS		1304.32	C2	010072.00
	BXL, I02147		10074.32	42	010072.40
	SIC, SEN		1310.00	80	010073.00
	B, SERS		1304.10	00	010073.40
102147	B, \$+1.0		10075.10	00	010074.00
	BD, I02142		10046.44	00	010074.40
	SIC, SEN0+.32		1311.40	80	010075.00
	B, SSW	-TO SSIP.	1301.10	00	010075.40
	BD, \$+.32		10076.44	00	010076.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	010076.40
	V+, \$X13, BIT21		17353.32	B0	010077.00
	SX, \$X13, IC202		10223.33	10	010077.40

102148	SIC, I0010	-TEST BIT 22.	6526.00 80	010100.00
	B, I007		6523.10 00	010100.40
	LX, SX0, BIT22		17354.00 10	010101.00
	KV, SX0, BIT22	-EQUAL CONDITION, KV.	17354.00 90	010101.40
	SIC, SEN		1310.00 80	010102.00
	BXL, SERS		1304.32 42	010102.40
	SIC, SEN		1310.00 80	010103.00
	BXH, SERS		1304.33 42	010103.40
	BXE, I02149		10105.72 C2	010104.00
	SIC, SEN		1310.00 80	010104.40
	B, SERS		1304.10 00	010105.00
102149	KV, SX0, BIT23	-INDEX HI CONDITION, KV.	17355.00 90	010105.40
	SIC, SEN		1310.00 80	010106.00
	BXL, SERS		1304.32 42	010106.40
	SIC, SEN		1310.00 80	010107.00
	BXE, SERS		1304.32 C2	010107.40
	BXH, I02150		10111.73 42	010110.00
	SIC, SEN		1310.00 80	010110.40
	B, SERS		1304.10 00	010111.00
102150	KVI, SX0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01 04	010111.40
	SIC, SEN		1310.00 80	010112.00
	BXL, SERS		1304.32 42	010112.40
	SIC, SEN		1310.00 80	010113.00
	BXE, SERS		1304.32 C2	010113.40
	BXH, I02151		10115.73 42	010114.00
	SIC, SEN		1310.00 80	010114.40
	B, SERS		1304.10 00	010115.00
102151	KV, SX0, BIT21	-INDEX LOW CONDITION, KV.	17353.00 90	010115.40
	SIC, SEN		1310.00 80	010116.00
	BXH, SERS		1304.33 42	010116.40
	SIC, SEN		1310.00 80	010117.00
	BXE, SERS		1304.32 C2	010117.40
	BXL, I02152		10121.72 42	010120.00
	SIC, SEN		1310.00 80	010120.40
	B, SERS		1304.10 00	010121.00
102152	KVI, SX0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41 04	010121.40
	SIC, SEN		1310.00 80	010122.00
	BXH, SERS		1304.33 42	010122.40
	SIC, SEN		1310.00 80	010123.00
	BXE, SERS		1304.32 C2	010123.40
	BXL, I02153		10125.72 42	010124.00
	SIC, SEN		1310.00 80	010124.40
	B, SERS		1304.10 00	010125.00
102153	B, \$+1.0		10126.50 00	010125.40
	BD, I02148		10100.04 00	010126.00
	SIC, SEN0+.32		1311.40 80	010126.40
	B, SSW	-TO SSIP.	1301.10 00	010127.00
	BD, \$+.32		10130.04 00	010127.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	010130.00
	V+, \$X13, BIT22		17354.32 B0	010130.40
	SX, \$X13, IC202		10223.33 10	010131.00

102154	SIC, I0010	-TEST BIT 23.	6526.00	80	010131.40
	B, I007		6523.10	00	010132.00
	LX, \$X0, BIT23		17355.00	10	010132.40
	KV, \$X0, BIT23	-EQUAL CONDITION, KV.	17355.00	90	010133.00
	SIC, SEN		1310.00	80	010133.40
	BXL, SERS		1304.32	42	010134.00
	SIC, SEN		1310.00	80	010134.40
	BXH, SERS		1304.33	42	010135.00
	BXE, I02155		10137.32	C2	010135.40
	SIC, SEN		1310.00	80	010136.00
	B, SERS		1304.10	00	010136.40
102155	KV, \$X0, BIT24	-INDEX HI CONDITION, KV.	17356.00	90	010137.00
	SIC, SEN		1310.00	80	010137.40
	BXL, SERS		1304.32	42	010140.00
	SIC, SEN		1310.00	80	010140.40
	BXE, SERS		1304.32	C2	010141.00
	BXH, I02156		10143.33	42	010141.40
	SIC, SEN		1310.00	80	010142.00
	B, SERS		1304.10	00	010142.40
102156	KVI, \$X0, %8#0.0	-INDEX HI CONDITION, KVI.	0.01	04	010143.00
	SIC, SEN		1310.00	80	010143.40
	BXL, SERS		1304.32	42	010144.00
	SIC, SEN		1310.00	80	010144.40
	BXE, SERS		1304.32	C2	010145.00
	BXH, I02157		10147.33	42	010145.40
	SIC, SEN		1310.00	80	010146.00
	B, SERS		1304.10	00	010146.40
102157	KV, \$X0, BIT22	-INDEX LOW CONDITION, KV.	17354.00	90	010147.00
	SIC, SEN		1310.00	80	010147.40
	BXH, SERS		1304.33	42	010150.00
	SIC, SEN		1310.00	80	010150.40
	BXE, SERS		1304.32	C2	010151.00
	BXL, I02158		10153.32	42	010151.40
	SIC, SEN		1310.00	80	010152.00
	B, SERS		1304.10	00	010152.40
102158	KVI, \$X0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41	04	010153.00
	SIC, SEN		1310.00	80	010153.40
	BXH, SERS		1304.33	42	010154.00
	SIC, SEN		1310.00	80	010154.40
	BXE, SERS		1304.32	C2	010155.00
	BXL, I02159		10157.32	42	010155.40
	SIC, SEN		1310.00	80	010156.00
	B, SERS		1304.10	00	010156.40
102159	B, \$+1.0		10160.10	00	010157.00
	BD, I02154		10131.44	00	010157.40
	SIC, SEN0+.32		1311.40	80	010160.00
	B, SSW	-TO SSIP.	1301.10	00	010160.40
	BD, \$+.32		10161.44	00	010161.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32	10	010161.40
	V+, \$X13, BIT23		17355.32	80	010162.00
	SX, \$X13, IC202		10223.33	10	010162.40

102160	SIC, I0010	-TEST BIT 24.	6526.00 80	010163.00
	B, I007		6523.10 00	010163.40
	LX, SX0, BIT24		17356.00 10	010164.00
	KV, SX0, BIT24	-EQUAL CONDITION, KV.	17356.00 90	010164.40
	SIC, SEN		1310.00 80	010165.00
	BXL, SERS		1304.32 42	010165.40
	SIC, SEN		1310.00 80	010166.00
	BXH, SERS		1304.33 42	010166.40
	BXE, I02161		10170.72 C2	010167.00
	SIC, SEN		1310.00 80	010167.40
	B, SERS		1304.10 00	010170.00
102161	KVI, SX0, %8#0.0	-EQUAL CONDITION, KVI.	0.01 04	010170.40
	SIC, SEN		1310.00 80	010171.00
	BXL, SERS		1304.32 42	010171.40
	SIC, SEN		1310.00 80	010172.00
	BXH, SERS		1304.33 42	010172.40
	BXE, I02162		10174.72 C2	010173.00
	SIC, SEN		1310.00 80	010173.40
	B, SERS		1304.10 00	010174.00
102162	KV, SX0, I000	-INDEX HI CONDITION, KV.	17307.00 90	010174.40
	SIC, SEN		1310.00 80	010175.00
	BXL, SERS		1304.32 42	010175.40
	SIC, SEN		1310.00 80	010176.00
	BXE, SERS		1304.32 C2	010176.40
	BXH, I02163		10200.73 42	010177.00
	SIC, SEN		1310.00 80	010177.40
	B, SERS		1304.10 00	010200.00
102163	KV, SX0, BIT23	-INDEX LOW CONDITION, KV.	17355.00 90	010200.40
	SIC, SEN		1310.00 80	010201.00
	BXH, SERS		1304.33 42	010201.40
	SIC, SEN		1310.00 80	010202.00
	BXE, SERS		1304.32 C2	010202.40
	BXL, I02164		10204.72 42	010203.00
	SIC, SEN		1310.00 80	010203.40
	B, SERS		1304.10 00	010204.00
102164	KVI, SX0, %8#0.40	-INDEX LOW CONDITION, KVI.	0.41 04	010204.40
	SIC, SEN		1310.00 80	010205.00
	BXH, SERS		1304.33 42	010205.40
	SIC, SEN		1310.00 80	010206.00
	BXE, SERS		1304.32 C2	010206.40
	BXL, I02165		10210.72 42	010207.00
	SIC, SEN		1310.00 80	010207.40
	B, SERS		1304.10 00	010210.00
102165	B, \$+1.0		10211.50 00	010210.40
	BD, I02160		10163.04 00	010211.00
	SIC, SEN0+.32		1311.40 80	010211.40
	B, SSW	-TO SSIP.	1301.10 00	010212.00
	BD, \$+.32		10213.04 00	010212.40
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	010213.00
	SC, \$X13, \$X12		34.33 50	010213.40
	V+, \$X12, BIT0		17326.30 80	010214.00
	IC, \$X13, \$X12		34.32 50	010214.40
	SX, \$X13, IC202		10223.33 10	010215.00
	LX, \$X13, IC202	-UPDATE CONTINUITY CHECK.	10223.32 10	010215.40
	KV, \$X13, ICK202		10224.32 90	010216.00
	SIC, SEN		1310.00 80	010216.40
	BZXE, SERS	-CONTINUITY ERROR.	1304.32 C0	010217.00
	SC, \$X13, \$X13		35.33 50	010217.40
	LX, \$X12, ICK202		10224.30 10	010220.00
	SC, \$X12, \$X12		34.31 50	010220.40
	KV, \$X13, \$X12		34.32 90	010221.00
	SIC, SEN		1310.00 80	010221.40
	BZXE, SERS	-CONTINUITY ERROR.	1304.32 C0	010222.00

IC202 CMOP
 XW,0,0,0 -CONTINUITY REG 1202.
 ICK202 XW,%8#777777.77,%8#400000,0

0.00 00 000000.00 00 010223.00
 777777.77 08 000000.00 00 010224.00

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104	LX,\$X1,104ID	-UPDATE IDENT	10230.02 10	010225.00
	SX,\$X1,DPET13		1437.03 10	010225.40
	SIC,RET		1306.40 80	010226.00
	B,1DF1		1443.10 00	010226.40
	Z,1C204		11301.22 00	010227.00
	BD,1040		10232.04 00	010227.40
	CNOP			
104ID	%10SZ=DD%BU,64,8,1204	Z		010230.00
	SIC,10010	-TEST BIT 28.	6526.00 80	010231.00
	B,1007		6523.10 00	010231.40
1040	LX,\$X0,BIT28		17362.00 10	010232.00
	KC,\$X0,BIT0	-EQUAL CONDITION, KC.	17326.01 90	010232.40
	SIC,SEN		1310.00 80	010233.00
	BXH,SERS		1304.33 42	010233.40
	SIC,SEN		1310.00 80	010234.00
	BXL,SERS		1304.32 42	010234.40
	BXE,1041		10236.72 C2	010235.00
	SIC,SEN		1310.00 80	010235.40
	B,SERS		1304.10 00	010236.00
1041	KCI,\$X0,%8=400000	-EQUAL CONDITION,KCI.	400000.01 0A	010236.40
	SIC,SEN		1310.00 80	010237.00
	BXH,SERS		1304.33 42	010237.40
	SIC,SEN		1310.00 80	010240.00
	BXL,SERS		1304.32 42	010240.40
	BXE,1042		10242.72 C2	010241.00
	SIC,SEN		1310.00 80	010241.40
	B,SERS		1304.10 00	010242.00
1042	KC,\$X0,BIT1	-INDEX HIGH CONDITION, KC.	17327.01 90	010242.40
	SIC,SEN		1310.00 80	010243.00
	BXE,SERS		1304.32 C2	010243.40
	SIC,SEN		1310.00 80	010244.00
	BXL,SERS		1304.32 42	010244.40
	BXH,1043		10246.73 42	010245.00
	SIC,SEN		1310.00 80	010245.40
	B,SERS		1304.10 00	010246.00
1043	KCI,\$X0,%8=200000	-INDEX HIGH CONDITION KCI.	200000.01 0A	010246.40
	SIC,SEN		1310.00 80	010247.00
	BXL,SERS		1304.32 42	010247.40
	SIC,SEN		1310.00 80	010250.00
	BXE,SERS		1304.32 C2	010250.40
	BXH,1044		10252.73 42	010251.00
	SIC,SEN		1310.00 80	010251.40
	B,SERS		1304.10 00	010252.00
1044	KC,\$X0,BIT24	-CHECK NO VALUE FLD USED,KC.	17356.01 90	010252.40
	SIC,SEN		1310.00 80	010253.00
	BXL,SERS		1304.32 42	010253.40
	SIC,SEN		1310.00 80	010254.00
	BXE,SERS		1304.32 C2	010254.40
	BXH,1045		10256.73 42	010255.00
	SIC,SEN		1310.00 80	010255.40
	B,SERS		1304.10 00	010256.00
1045	B,\$+1.0		10257.50 00	010256.40
	BD,1040		10232.04 00	010257.00
	SIC,SEN0+.32		1311.40 80	010257.40
	B,SSW	-TO SSIP.	1301.10 00	010260.00
	BD,\$+.32		10261.04 00	010260.40
	LX,\$X13,1C204	-UPDATE CONTINUITY CHECK.	11301.32 10	010261.00
	V+,\$X13,BIT0		17326.32 80	010261.40
	SX,\$X13,1C204		11301.33 10	010262.00

102170	SIC, I0010	-TEST BIT 29.	6526.00	80	010262.40
	B, I007		6523.10	00	010263.00
	LX, SX0, BIT29		17363.00	10	010263.40
	KC, SX0, BIT1	-EQUAL CONDITION, KC.	17327.01	90	010264.00
	SIC, SEN		1310.00	80	010264.40
	BXL, SERS		1304.32	42	010265.00
	SIC, SEN		1310.00	80	010265.40
	BXH, SERS		1304.33	42	010266.00
	BXE, I02171		10270.32	C2	010266.40
	SIC, SEN		1310.00	80	010267.00
	B, SERS		1304.10	00	010267.40
102171	KCI, SX0, %8#200000	-EQUAL CONDITION, KCI.	200000.01	0A	010270.00
	SIC, SEN		1310.00	80	010270.40
	BXL, SERS		1304.32	42	010271.00
	SIC, SEN		1310.00	80	010271.40
	BXH, SERS		1304.33	42	010272.00
	BXE, I02172		10274.32	C2	010272.40
	SIC, SEN		1310.00	80	010273.00
	B, SERS		1304.10	00	010273.40
102172	KC, SX0, BIT2	-INDEX HIGH CONDITION, KC.	17330.01	90	010274.00
	SIC, SEN		1310.00	80	010274.40
	BXL, SERS		1304.32	42	010275.00
	SIC, SEN		1310.00	80	010275.40
	BXE, SERS		1304.32	C2	010276.00
	BXH, I02173		10300.33	42	010276.40
	SIC, SEN		1310.00	80	010277.00
	B, SERS		1304.10	00	010277.40
102173	KCI, SX0, %8#100000	-INDEX HI CONDITION, KCI.	100000.01	0A	010300.00
	SIC, SEN		1310.00	80	010300.40
	BXL, SERS		1304.32	42	010301.00
	SIC, SEN		1310.00	80	010301.40
	BXE, SERS		1304.32	C2	010302.00
	BXH, I02174		10304.33	42	010302.40
	SIC, SEN		1310.00	80	010303.00
	B, SERS		1304.10	00	010303.40
102174	KC, SX0, BIT0	-INDEX LOW CONDITION, KC.	17326.01	90	010304.00
	SIC, SEN		1310.00	80	010304.40
	BXH, SERS		1304.33	42	010305.00
	SIC, SEN		1310.00	80	010305.40
	BXE, SERS		1304.32	C2	010306.00
	BXL, I02175		10310.32	42	010306.40
	SIC, SEN		1310.00	80	010307.00
	B, SERS		1304.10	00	010307.40
102175	KCI, SX0, %8#400000	-INDEX LOW CONDITION, KCI.	400000.01	0A	010310.00
	SIC, SEN		1310.00	80	010310.40
	BXH, SERS		1304.33	42	010311.00
	SIC, SEN		1310.00	80	010311.40
	BXE, SERS		1304.32	C2	010312.00
	BXL, I02176		10314.32	42	010312.40
	SIC, SEN		1310.00	80	010313.00
	B, SERS		1304.10	00	010313.40
102176	B, \$+1.0		10315.10	00	010314.00
	BD, I02170		10262.44	00	010314.40
	SIC, SEN0+.32		1311.40	80	010315.00
	B, SSW	-TO SSIP.	1301.10	00	010315.40
	BD, \$+.32		10316.44	00	010316.00
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	010316.40
	V+, \$X13, BIT1		17327.32	B0	010317.00
	SX, \$X13, IC204		11301.33	10	010317.40

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102177	SIC,10010	-TEST BIT 30.	6528.00	80	010320.00
	B,1007		6523.10	00	010320.40
	LX,\$X0,BIT30		17364.00	10	010321.00
	KC,\$X0,BIT2	-EQUAL CONDITION, KC.	17330.01	90	010321.40
	SIC,SEN		1310.00	80	010322.00
	BXL,SERS		1304.32	42	010322.40
	SIC,SEN		1310.00	80	010323.00
	BXH,SERS		1304.33	42	010323.40
	BXE,102178		10325.72	C2	010324.00
	SIC,SEN		1310.00	80	010324.40
	B,SERS		1304.10	00	010325.00
102178	KCI,\$X0,%8H100000	-EQUAL CONDITION,KCI.	100000.01	0A	010325.40
	SIC,SEN		1310.00	80	010326.00
	BXL,SERS		1304.32	42	010326.40
	SIC,SEN		1310.00	80	010327.00
	BXH,SERS		1304.33	42	010327.40
	BXE,102179		10331.72	C2	010330.00
	SIC,SEN		1310.00	80	010330.40
	B,SERS		1304.10	00	010331.00
102179	KC,\$X0,BIT3	-INDEX HIGH CONDITION, KC.	17331.01	90	010331.40
	SIC,SEN		1310.00	80	010332.00
	BXL,SERS		1304.32	42	010332.40
	SIC,SEN		1310.00	80	010333.00
	BXE,SERS		1304.32	C2	010333.40
	BXH,102180		10335.73	42	010334.00
	SIC,SEN		1310.00	80	010334.40
	B,SERS		1304.10	00	010335.00
102180	KCI,\$X0,%8H40000	-INDEX HI CONDITION,KCI.	40000.01	0A	010335.40
	SIC,SEN		1310.00	80	010336.00
	BXL,SERS		1304.32	42	010336.40
	SIC,SEN		1310.00	80	010337.00
	BXE,SERS		1304.32	C2	010337.40
	BXH,102181		10341.72	42	010340.00
	SIC,SEN		1310.00	80	010340.40
	B,SERS		1304.10	00	010341.00
102181	KC,\$X0,BIT1	-INDEX LOW CONDITION, KC.	17327.01	90	010341.40
	SIC,SEN		1310.00	80	010342.00
	BXH,SERS		1304.33	42	010342.40
	SIC,SEN		1310.00	80	010343.00
	BXE,SERS		1304.32	C2	010343.40
	BXL,102182		10345.72	42	010344.00
	SIC,SEN		1310.00	80	010344.40
	B,SERS		1304.10	00	010345.00
102182	KCI,\$X0,%8H200000	-INDEX LOW CONDITION,KCI.	200000.01	0A	010345.40
	SIC,SEN		1310.00	80	010346.00
	BXH,SERS		1304.33	42	010346.40
	SIC,SEN		1310.00	80	010347.00
	BXE,SERS		1304.32	C2	010347.40
	BXL,102183		10351.72	42	010350.00
	SIC,SEN		1310.00	80	010350.40
	B,SERS		1304.10	00	010351.00
102183	B,\$+1.0		10352.50	00	010351.40
	BD,102177		143620.44	00	010352.00
	SIC,SEN0+.32		1311.40	80	010352.40
	B,SSW	-TO SSIP.	1301.10	00	010353.00
	BD,\$+.32		10354.04	00	010353.40
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	010354.00
	V+,\$X13,BIT2		17330.32	80	010354.40
	SX,\$X13,IC204		11301.33	10	010355.00

102184	SIC, I0010	-TEST BIT 31.	6526.00 80	010355.40
	B, I007		6523.10 00	010356.00
	LX, SX0, BIT31		17365.00 10	010356.40
	KC, SX0, BIT3	-EQUAL CONDITION, KC.	17331.01 90	010357.00
	SIC, SEN		1310.00 80	010357.40
	BXL, SERS		1304.32 42	010360.00
	SIC, SEN		1310.00 80	010360.40
	BXH, SERS		1304.33 42	010361.00
	BXE, I02185		10363.32 C2	010361.40
	SIC, SEN		1310.00 80	010362.00
	B, SERS		1304.10 00	010362.40
102185	KCI, SX0, %8#40000	-EQUAL CONDITION, KCI.	40000.01 0A	010363.00
	SIC, SEN		1310.00 80	010363.40
	BXL, SERS		1304.32 42	010364.00
	SIC, SEN		1310.00 80	010364.40
	BXH, SERS		1304.33 42	010365.00
	BXE, I02186		10367.32 C2	010365.40
	SIC, SEN		1310.00 80	010366.00
	B, SERS		1304.10 00	010366.40
102186	KC, SX0, BIT4	-INDEX HIGH CONDITION, KC.	17332.01 90	010367.00
	SIC, SEN		1310.00 80	010367.40
	BXL, SERS		1304.32 42	010370.00
	SIC, SEN		1310.00 80	010370.40
	BXE, SERS		1304.32 C2	010371.00
	BXH, I02187		10373.33 42	010371.40
	SIC, SEN		1310.00 80	010372.00
	B, SERS		1304.10 00	010372.40
102187	KCI, SX0, %8#20000	-INDEX HI CONDITION, KCI.	20000.01 0A	010373.00
	SIC, SEN		1310.00 80	010373.40
	BXL, SERS		1304.32 42	010374.00
	SIC, SEN		1310.00 80	010374.40
	BXE, SERS		1304.32 C2	010375.00
	BXH, I02188		10377.33 42	010375.40
	SIC, SEN		1310.00 80	010376.00
	B, SERS		1304.10 00	010376.40
102188	KC, SX0, BIT2	-INDEX LOW CONDITION, KC.	17330.01 90	010377.00
	SIC, SEN		1310.00 80	010377.40
	BXH, SERS		1304.33 42	010400.00
	SIC, SEN		1310.00 80	010400.40
	BXE, SERS		1304.32 C2	010401.00
	BXL, I02189		10403.32 42	010401.40
	SIC, SEN		1310.00 80	010402.00
	B, SERS		1304.10 00	010402.40
102189	KCI, SX0, %8#100000	-INDEX LOW CONDITION, KCI.	100000.01 0A	010403.00
	SIC, SEN		1310.00 80	010403.40
	BXH, SERS		1304.33 42	010404.00
	SIC, SEN		1310.00 80	010404.40
	BXE, SERS		1304.32 C2	010405.00
	BXL, I02190		10407.32 42	010405.40
	SIC, SEN		1310.00 80	010406.00
	B, SERS		1304.10 00	010406.40
102190	B, s+1.0		10410.10 00	010407.00
	BD, I02184		10355.44 00	010407.40
	SIC, SEN0+.32		1311.40 80	010410.00
	B, SSW	-TO SSIP.	1301.10 00	010410.40
	BD, s+.32		10411.44 00	010411.00
	LX, SX13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	010411.40
	V+, SX13, BIT3		17331.32 B0	010412.00
	SX, SX13, IC204		11301.33 10	010412.40

102191	SIC, I0010	-TEST BIT 32.	6526.00 80	010413.00
	B, I007		6523.10 00	010413.40
	LX, SX0, BIT32		17366.00 10	010414.00
	KC, SX0, BIT4	-EQUAL CONDITION, KC.	17332.01 90	010414.40
	SIC, SEN		1310.00 80	010415.00
	BXL, SERS		1304.32 42	010415.40
	SIC, SEN		1310.00 80	010416.00
	BXH, SERS		1304.33 42	010416.40
	BXE, I02192		10420.72 C2	010417.00
	SIC, SEN		1310.00 80	010417.40
	B, SERS		1304.10 00	010420.00
102192	KCI, SX0, %8=20000	-EQUAL CONDITION, KCI.	20000.01 0A	010420.40
	SIC, SEN		1310.00 80	010421.00
	BXL, SERS		1304.32 42	010421.40
	SIC, SEN		1310.00 80	010422.00
	BXH, SERS		1304.33 42	010422.40
	BXE, I02193		10424.72 C2	010423.00
	SIC, SEN		1310.00 80	010423.40
	B, SERS		1304.10 00	010424.00
102193	KC, SX0, BIT5	-INDEX HIGH CONDITION, KC.	17333.01 90	010424.40
	SIC, SEN		1310.00 80	010425.00
	BXL, SERS		1304.32 42	010425.40
	SIC, SEN		1310.00 80	010426.00
	BXE, SERS		1304.32 C2	010426.40
	BXH, I02194		10430.73 42	010427.00
	SIC, SEN		1310.00 80	010427.40
	B, SERS		1304.10 00	010430.00
102194	KCI, SX0, %8=10000	-INDEX HI CONDITION, KCI.	10000.01 0A	010430.40
	SIC, SEN		1310.00 80	010431.00
	BXL, SERS		1304.32 42	010431.40
	SIC, SEN		1310.00 80	010432.00
	BXE, SERS		1304.32 C2	010432.40
	BXH, I02195		10434.73 42	010433.00
	SIC, SEN		1310.00 80	010433.40
	B, SERS		1304.10 00	010434.00
102195	KC, SX0, BIT3	-INDEX LOW CONDITION, KC.	17331.01 90	010434.40
	SIC, SEN		1310.00 80	010435.00
	BXH, SERS		1304.33 42	010435.40
	SIC, SEN		1310.00 80	010436.00
	BXE, SERS		1304.32 C2	010436.40
	BXL, I02196		10440.72 42	010437.00
	SIC, SEN		1310.00 80	010437.40
	B, SERS		1304.10 00	010440.00
102196	KCI, SX0, %8=40000	-INDEX LOW CONDITION, KCI.	40000.01 0A	010440.40
	SIC, SEN		1310.00 80	010441.00
	BXH, SERS		1304.33 42	010441.40
	SIC, SEN		1310.00 80	010442.00
	BXE, SERS		1304.32 C2	010442.40
	BXL, I02197		10444.72 42	010443.00
	SIC, SEN		1310.00 80	010443.40
	B, SERS		1304.10 00	010444.00
102197	B, \$+1.0		10445.50 00	010444.40
	BD, I02191		10413.04 00	010445.00
	SIC, SEN0+.32		1311.40 80	010445.40
	B, SSW	-TO SSIP.	1301.10 00	010446.00
	BD, \$+.32		10447.04 00	010446.40
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	010447.00
	V+, \$X13, BIT4		17332.32 B0	010447.40
	SX, \$X13, IC204		11301.33 10	010450.00

102198	SIC, I0010	-TEST BIT 33.	6526.00	80	010450.40
	B, I007		6523.10	00	010451.00
	LX, \$X0, BIT33		17367.00	10	010451.40
	KC, \$X0, BIT5	-EQUAL CONDITION, KC.	17333.01	90	010452.00
	SIC, SEN		1310.00	80	010452.40
	BXL, SERS		1304.32	42	010453.00
	SIC, SEN		1310.00	80	010453.40
	BXH, SERS		1304.33	42	010454.00
	BXE, I02199		10456.32	C2	010454.40
	SIC, SEN		1310.00	80	010455.00
	B, SERS		1304.10	00	010455.40
102199	KCI, \$X0, %8#10000	-EQUAL CONDITION, KCI.	10000.01	0A	010456.00
	SIC, SEN		1310.00	80	010456.40
	BXL, SERS		1304.32	42	010457.00
	SIC, SEN		1310.00	80	010457.40
	BXH, SERS		1304.33	42	010460.00
	BXE, I02200		10462.32	C2	010460.40
	SIC, SEN		1310.00	80	010461.00
	B, SERS		1304.10	00	010461.40
102200	KC, \$X0, BIT6	-INDEX HIGH CONDITION, KC.	17334.01	90	010462.00
	SIC, SEN		1310.00	80	010462.40
	BXL, SERS		1304.32	42	010463.00
	SIC, SEN		1310.00	80	010463.40
	BXE, SERS		1304.32	C2	010464.00
	BXH, I02201		10466.33	42	010464.40
	SIC, SEN		1310.00	80	010465.00
	B, SERS		1304.10	00	010465.40
102201	KCI, \$X0, %8#4000	-INDEX HI CONDITION, KCI.	4000.01	0A	010466.00
	SIC, SEN		1310.00	80	010466.40
	BXL, SERS		1304.32	42	010467.00
	SIC, SEN		1310.00	80	010467.40
	BXE, SERS		1304.32	C2	010470.00
	BXH, I02202		10472.33	42	010470.40
	SIC, SEN		1310.00	80	010471.00
	B, SERS		1304.10	00	010471.40
102202	KC, \$X0, BIT4	-INDEX LOW CONDITION, KC.	17332.01	90	010472.00
	SIC, SEN		1310.00	80	010472.40
	BXH, SERS		1304.33	42	010473.00
	SIC, SEN		1310.00	80	010473.40
	BXE, SERS		1304.32	C2	010474.00
	BXL, I02203		10476.32	42	010474.40
	SIC, SEN		1310.00	80	010475.00
	B, SERS		1304.10	00	010475.40
102203	KCI, \$X0, %8#20000	-INDEX LOW CONDITION, KCI.	20000.01	0A	010476.00
	SIC, SEN		1310.00	80	010476.40
	BXH, SERS		1304.33	42	010477.00
	SIC, SEN		1310.00	80	010477.40
	BXE, SERS		1304.32	C2	010500.00
	BXL, I02204		10502.32	42	010500.40
	SIC, SEN		1310.00	80	010501.00
	B, SERS		1304.10	00	010501.40
102204	B, \$+1.0		10503.10	00	010502.00
	BD, I02198		10450.44	00	010502.40
	SIC, SEN0+.32		1311.40	80	010503.00
	B, SSW	-TO SSIP.	1301.10	00	010503.40
	BD, \$+.32		10504.44	00	010504.00
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	010504.40
	V+, \$X13, BIT5		17333.32	B0	010505.00
	SX, \$X13, IC204		11301.33	10	010505.40

102205	SIC,10010	-TEST BIT 34.	6526.00 80	010506.00
	B,1007		6523.10 00	010506.40
	LX,\$X0,BIT34		17370.00 10	010507.00
	KC,\$X0,BIT6	-EQUAL CONDITION, KC.	17334.01 90	010507.40
	SIC,SEN		1310.00 80	010510.00
	BXL,SERS		1304.32 42	010510.40
	SIC,SEN		1310.00 80	010511.00
	BXH,SERS		1304.33 42	010511.40
	BXE,102206		10513.72 C2	010512.00
	SIC,SEN		1310.00 80	010512.40
	B,SERS		1304.10 00	010513.00
102206	KCI,\$X0,%8#4000	-EQUAL CONDITION,KCI.	4000.01 0A	010513.40
	SIC,SEN		1310.00 80	010514.00
	BXL,SERS		1304.32 42	010514.40
	SIC,SEN		1310.00 80	010515.00
	BXH,SERS		1304.33 42	010515.40
	BXE,102207		10517.72 C2	010516.00
	SIC,SEN		1310.00 80	010516.40
	B,SERS		1304.10 00	010517.00
102207	KC,\$X0,BIT7	-INDEX HIGH CONDITION, KC.	17335.01 90	010517.40
	SIC,SEN		1310.00 80	010520.00
	BXL,SERS		1304.32 42	010520.40
	SIC,SEN		1310.00 80	010521.00
	BXE,SERS		1304.32 C2	010521.40
	BXH,102208		10523.73 42	010522.00
	SIC,SEN		1310.00 80	010522.40
	B,SERS		1304.10 00	010523.00
102208	KCI,\$X0,%8#2000	-INDEX HI CONDITION,KCI.	2000.01 0A	010523.40
	SIC,SEN		1310.00 80	010524.00
	BXL,SERS		1304.32 42	010524.40
	SIC,SEN		1310.00 80	010525.00
	BXE,SERS		1304.32 C2	010525.40
	BXH,102209		10527.73 42	010526.00
	SIC,SEN		1310.00 80	010526.40
	B,SERS		1304.10 00	010527.00
102209	KC,\$X0,BIT5	-INDEX LOW CONDITION, KC.	17333.01 90	010527.40
	SIC,SEN		1310.00 80	010530.00
	BXH,SERS		1304.33 42	010530.40
	SIC,SEN		1310.00 80	010531.00
	BXE,SERS		1304.32 C2	010531.40
	BXL,102210		10533.72 42	010532.00
	SIC,SEN		1310.00 80	010532.40
	B,SERS		1304.10 00	010533.00
102210	KCI,\$X0,%8#10000	-INDEX LOW CONDITION,KCI.	10000.01 0A	010533.40
	SIC,SEN		1310.00 80	010534.00
	BXH,SERS		1304.33 42	010534.40
	SIC,SEN		1310.00 80	010535.00
	BXE,SERS		1304.32 C2	010535.40
	BXL,102211		10537.72 42	010536.00
	SIC,SEN		1310.00 80	010536.40
	B,SERS		1304.10 00	010537.00
102211	B,\$+1.0		10540.50 00	010537.40
	BD,102205		10506.04 00	010540.00
	SIC,SEN0+.32		1311.40 80	010540.40
	B,SSW	-TO SSIP.	1301.10 00	010541.00
	BD,\$+.32		10542.04 00	010541.40
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	010542.00
	V+,\$X13,BIT6		17334.32 B0	010542.40
	SX,\$X13,IC204		11301.33 10	010543.00

102212	SIC, I0010	-TEST BIT 35.	6526.00	80	010543.40
	B, I007		6523.10	00	010544.00
	LX, SX0, BIT35		17371.00	10	010544.40
	KC, SX0, BIT7	-EQUAL CONDITION, KC.	17335.01	90	010545.00
	SIC, SEN		1310.00	80	010545.40
	BXL, SERS		1304.32	42	010546.00
	SIC, SEN		1310.00	80	010546.40
	BXH, SERS		1304.33	42	010547.00
	BXE, I02213		10551.32	C2	010547.40
	SIC, SEN		1310.00	80	010550.00
	B, SERS		1304.10	00	010550.40
102213	KCI, SX0, %8#2000	-EQUAL CONDITION, KCI.	2000.01	0A	010551.00
	SIC, SEN		1310.00	80	010551.40
	BXL, SERS		1304.32	42	010552.00
	SIC, SEN		1310.00	80	010552.40
	BXH, SERS		1304.33	42	010553.00
	BXE, I02214		10555.32	C2	010553.40
	SIC, SEN		1310.00	80	010554.00
	B, SERS		1304.10	00	010554.40
102214	KC, SX0, BIT8	-INDEX HIGH CONDITION, KC.	17336.01	90	010555.00
	SIC, SEN		1310.00	80	010555.40
	BXL, SERS		1304.32	42	010556.00
	SIC, SEN		1310.00	80	010556.40
	BXE, SERS		1304.32	C2	010557.00
	BXH, I02215		10561.33	42	010557.40
	SIC, SEN		1310.00	80	010560.00
	B, SERS		1304.10	00	010560.40
102215	KCI, SX0, %8#1000	-INDEX HI CONDITION, KCI.	1000.01	0A	010561.00
	SIC, SEN		1310.00	80	010561.40
	BXL, SERS		1304.32	42	010562.00
	SIC, SEN		1310.00	80	010562.40
	BXE, SERS		1304.32	C2	010563.00
	BXH, I02216		10565.33	42	010563.40
	SIC, SEN		1310.00	80	010564.00
	B, SERS		1304.10	00	010564.40
102216	KC, SX0, BIT6	-INDEX LOW CONDITION, KC.	17334.01	90	010565.00
	SIC, SEN		1310.00	80	010565.40
	BXH, SERS		1304.33	42	010566.00
	SIC, SEN		1310.00	80	010566.40
	BXE, SERS		1304.32	C2	010567.00
	BXL, I02217		10571.32	42	010567.40
	SIC, SEN		1310.00	80	010570.00
	B, SERS		1304.10	00	010570.40
102217	KCI, SX0, %8#4000	-INDEX LOW CONDITION, KCI.	4000.01	0A	010571.00
	SIC, SEN		1310.00	80	010571.40
	BXH, SERS		1304.33	42	010572.00
	SIC, SEN		1310.00	80	010572.40
	BXE, SERS		1304.32	C2	010573.00
	BXL, I02218		10575.32	42	010573.40
	SIC, SEN		1310.00	80	010574.00
	B, SERS		1304.10	00	010574.40
102218	B, \$+1.0		10576.10	00	010575.00
	BD, I02212		10543.44	00	010575.40
	SIC, SEN0+.32		1311.40	80	010576.00
	B, SSW	-TO SSIP.	1301.10	00	010576.40
	BD, \$+.32		10577.44	00	010577.00
	LX, SX13, IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	010577.40
	V+, SX13, BIT7		17335.32	B0	010600.00
	SX, SX13, IC204		11301.33	10	010600.40

102219	SIC, I0010	-TEST BIT 36.	6526.00	80	010601.00
	B, I007		6523.10	00	010601.40
	LX, SX0, BIT36		17372.00	10	010602.00
	KC, SX0, BIT8	-EQUAL CONDITION, KC.	17336.01	90	010602.40
	SIC, SEN		1310.00	80	010603.00
	BXL, SERS		1304.32	42	010603.40
	SIC, SEN		1310.00	80	010604.00
	BXH, SERS		1304.33	42	010604.40
	BXE, I02220		10606.72	C2	010605.00
	SIC, SEN		1310.00	80	010605.40
	B, SERS		1304.10	00	010606.00
102220	KCI, SX0, %8#1000	-EQUAL CONDITION, KCI.	1000.01	0A	010606.40
	SIC, SEN		1310.00	80	010607.00
	BXL, SERS		1304.32	42	010607.40
	SIC, SEN		1310.00	80	010610.00
	BXH, SERS		1304.33	42	010610.40
	BXE, I02221		10612.72	C2	010611.00
	SIC, SEN		1310.00	80	010611.40
	B, SERS		1304.10	00	010612.00
102221	KC, SX0, BIT9	-INDEX HIGH CONDITION, KC.	17337.01	90	010612.40
	SIC, SEN		1310.00	80	010613.00
	BXL, SERS		1304.32	42	010613.40
	SIC, SEN		1310.00	80	010614.00
	BXE, SERS		1304.32	C2	010614.40
	BXH, I02222		10616.73	42	010615.00
	SIC, SEN		1310.00	80	010615.40
	B, SERS		1304.10	00	010616.00
102222	KCI, SX0, %8#400	-INDEX HI CONDITION, KCI.	400.01	0A	010616.40
	SIC, SEN		1310.00	80	010617.00
	BXL, SERS		1304.32	42	010617.40
	SIC, SEN		1310.00	80	010620.00
	BXE, SERS		1304.32	C2	010620.40
	BXH, I02223		10622.73	42	010621.00
	SIC, SEN		1310.00	80	010621.40
	B, SERS		1304.10	00	010622.00
102223	KC, SX0, BIT7	-INDEX LOW CONDITION, KC.	17335.01	90	010622.40
	SIC, SEN		1310.00	80	010623.00
	BXH, SERS		1304.33	42	010623.40
	SIC, SEN		1310.00	80	010624.00
	BXE, SERS		1304.32	C2	010624.40
	BXL, I02224		10626.72	42	010625.00
	SIC, SEN		1310.00	80	010625.40
	B, SERS		1304.10	00	010626.00
102224	KCI, SX0, %8#2000	-INDEX LOW CONDITION, KCI.	2000.01	0A	010626.40
	SIC, SEN		1310.00	80	010627.00
	BXH, SERS		1304.33	42	010627.40
	SIC, SEN		1310.00	80	010630.00
	BXE, SERS		1304.32	C2	010630.40
	BXL, I02225		10632.72	42	010631.00
	SIC, SEN		1310.00	80	010631.40
	B, SERS		1304.10	00	010632.00
102225	B, \$+1.0		10633.50	00	010632.40
	BD, I02219		10601.04	00	010633.00
	SIC, SEN0+.32		1311.40	80	010633.40
	B, SSW	-TO SSIP.	1301.10	00	010634.00
	BD, \$+.32		10635.04	00	010634.40
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	010635.00
	V+, \$X13, BIT8		17336.32	80	010635.40
	SX, \$X13, IC204		11301.33	10	010636.00

102226	SIC,10010	-TEST BIT 37.	6526.00 80	010636.40
	B,1007		6523.10 00	010637.00
	LX,SX0,BIT37		17373.00 10	010637.40
	KC,SX0,BIT9	-EQUAL CONDITION, KC.	17337.01 90	010640.00
	SIC,SEN		1310.00 80	010640.40
	BXL,SERS		1304.32 42	010641.00
	SIC,SEN		1310.00 80	010641.40
	BXH,SERS		1304.33 42	010642.00
	BXE,102227		10644.32 C2	010642.40
	SIC,SEN		1310.00 80	010643.00
	B,SERS		1304.10 00	010643.40
102227	KCI,SX0,%8#400	-EQUAL CONDITION,KCI.	400.01 0A	010644.00
	SIC,SEN		1310.00 80	010644.40
	BXL,SERS		1304.32 42	010645.00
	SIC,SEN		1310.00 80	010645.40
	BXH,SERS		1304.33 42	010646.00
	BXE,102228		10650.32 C2	010646.40
	SIC,SEN		1310.00 80	010647.00
	B,SERS		1304.10 00	010647.40
102228	KC,SX0,BIT10	-INDEX HIGH CONDITION, KC.	17240.01 90	010650.00
	SIC,SEN		1310.00 80	010650.40
	BXL,SERS		1304.32 42	010651.00
	SIC,SEN		1310.00 80	010651.40
	BXE,SERS		1304.32 C2	010652.00
	BXH,102229		10654.33 42	010652.40
	SIC,SEN		1310.00 80	010653.00
	B,SERS		1304.10 00	010653.40
102229	KCI,SX0,%8#200	-INDEX HI CONDITION,KCI.	200.01 0A	010654.00
	SIC,SEN		1310.00 80	010654.40
	BXL,SERS		1304.32 42	010655.00
	SIC,SEN		1310.00 80	010655.40
	BXE,SERS		1304.32 C2	010656.00
	BXH,102230		10660.33 42	010656.40
	SIC,SEN		1310.00 80	010657.00
	B,SERS		1304.10 00	010657.40
102230	KC,SX0,BIT8	-INDEX LOW CONDITION, KC.	17336.01 90	010660.00
	SIC,SEN		1310.00 80	010660.40
	BXH,SERS		1304.33 42	010661.00
	SIC,SEN		1310.00 80	010661.40
	BXE,SERS		1304.32 C2	010662.00
	BXL,102231		10664.32 42	010662.40
	SIC,SEN		1310.00 80	010663.00
	B,SERS		1304.10 00	010663.40
102231	KCI,SX0,%8#1000	-INDEX LOW CONDITION,KCI.	1000.01 0A	010664.00
	SIC,SEN		1310.00 80	010664.40
	BXH,SERS		1304.33 42	010665.00
	SIC,SEN		1310.00 80	010665.40
	BXE,SERS		1304.32 C2	010666.00
	BXL,102232		10670.32 42	010666.40
	SIC,SEN		1310.00 80	010667.00
	B,SERS		1304.10 00	010667.40
102232	B,S+1.0		10671.10 00	010670.00
	B,102226		10636.44 00	010670.40
	SIC,SEN0+.32		1311.40 80	010671.00
	B,SEN	-TO SSIP.	1301.10 00	010671.40
	B,SEN.32		10672.44 00	010672.00
	LX,SEN13,IC304	-UPDATE CONTINUITY CHECK.	11301.32 10	010672.40
	V+,SEN13,BIT9		17337.32 80	010673.00
	SX,SEN13,IC204		11301.33 10	010673.40

102220	KCI,\$X0,%8#1000	-INDEX LOW CONDITION,KCI.	1000.01	0A	010664.00
	SIC,SEN		1310.00	80	010664.40
	BXH,SERS		1304.33	42	010665.00
	SIC,SEN		1310.00	80	010665.40
	BXE,SERS		1304.32	C2	010666.00
	BXL,102231		10664.32	42	010666.40
	SIC,SEN		1310.00	80	010667.00
102231	KCI,\$X0,%8#1000	-INDEX LOW CONDITION,KCI.	1000.01	0A	010664.00
	SIC,SEN		1310.00	80	010664.40
	BXH,SERS		1304.33	42	010665.00
	SIC,SEN		1310.00	80	010665.40
	BXE,SERS		1304.32	C2	010666.00
	BXL,102232		10670.32	42	010666.40
	SIC,SEN		1310.00	80	010667.00
102232	B,\$+1.0		1304.10	00	010667.40
	BD,102226		10671.10	00	010670.00
	SIC,SEN0+.32		10636.44	00	010670.40
	B,SSW	-TO SSIP.	1311.40	80	010671.00
	BD,\$+.32		1301.10	00	010671.40
			10672.44	00	010672.00
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.			
	V+,\$X13,BIT9		11301.32	10	010672.40
	SX,\$X13,IC204		17337.32	80	010673.00
			11301.33	10	010673.40

102233	SIC,10010	-TEST BIT 38.	6526.00 80	010674.00
	B,1007		6523.10 00	010674.40
	LX,\$X0,BIT38		17374.00 10	010675.00
	KC,\$X0,BIT10	-EQUAL CONDITION, KC.	17340.01 90	010675.40
	SIC,SEN		1310.00 80	010676.00
	BXL,SERS		1304.32 42	010676.40
	SIC,SEN		1310.00 80	010677.00
	BXH,SERS		1304.33 42	010677.40
	BXE,102234		10701.72 C2	010700.00
	SIC,SEN		1310.00 80	010700.40
	B,SERS		1304.10 00	010701.00
102234	KCI,\$X0,%8=200	-EQUAL CONDITION,KCI.	200.01 0A	010701.40
	SIC,SEN		1310.00 80	010702.00
	BXL,SERS		1304.32 42	010702.40
	SIC,SEN		1310.00 80	010703.00
	BXH,SERS		1304.33 42	010703.40
	BXE,102235		10705.72 C2	010704.00
	SIC,SEN		1310.00 80	010704.40
	B,SERS		1304.10 00	010705.00
102235	KC,\$X0,BIT11	-INDEX HIGH CONDITION, KC.	17341.01 90	010705.40
	SIC,SEN		1310.00 80	010706.00
	BXL,SERS		1304.32 42	010706.40
	SIC,SEN		1310.00 80	010707.00
	BXE,SERS		1304.32 C2	010707.40
	BXH,102236		10711.73 42	010710.00
	SIC,SEN		1310.00 80	010710.40
	B,SERS		1304.10 00	010711.00
102236	KCI,\$X0,%8=100	-INDEX HI CONDITION,KCI.	100.01 0A	010711.40
	SIC,SEN		1310.00 80	010712.00
	BXL,SERS		1304.32 42	010712.40
	SIC,SEN		1310.00 80	010713.00
	BXE,SERS		1304.32 C2	010713.40
	BXH,102237		10715.73 42	010714.00
	SIC,SEN		1310.00 80	010714.40
	B,SERS		1304.10 00	010715.00
102237	KC,\$X0,BIT9	-INDEX LOW CONDITION, KC.	17337.01 90	010715.40
	SIC,SEN		1310.00 80	010716.00
	BXH,SERS		1304.33 42	010716.40
	SIC,SEN		1310.00 80	010717.00
	BXE,SERS		1304.32 C2	010717.40
	BXL,102238		10721.72 42	010720.00
	SIC,SEN		1310.00 80	010720.40
	B,SERS		1304.10 00	010721.00
102238	KCI,\$X0,%8=400	-INDEX LOW CONDITION,KCI.	400.01 0A	010721.40
	SIC,SEN		1310.00 80	010722.00
	BXH,SERS		1304.33 42	010722.40
	SIC,SEN		1310.00 80	010723.00
	BXE,SERS		1304.32 C2	010723.40
	BXL,102239		10725.72 42	010724.00
	SIC,SEN		1310.00 80	010724.40
	B,SERS		1304.10 00	010725.00
102239	B,\$+1.0		10726.50 00	010725.40
	BD,102233		10674.04 00	010726.00
	SIC,SEN0+.32		1311.40 80	010726.40
	B,SSW	-TO SSIP.	1301.10 00	010727.00
	BD,\$+.32		10730.04 00	010727.40
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	010730.00
	V+,\$X13,BIT10		17340.32 80	010730.40
	SX,\$X13,IC204		11301.33 10	010731.00

102240	SIC, I0010	-TEST BIT 39.	6526.00 80	010731.40
	B, I007		6523.10 00	010732.00
	LX, SX0, BIT39		17375.00 10	010732.40
	KC, SX0, BIT11	-EQUAL CONDITION, KC.	17341.01 90	010733.00
	SIC, SEN		1310.00 80	010733.40
	BXL, SERS		1304.32 42	010734.00
	SIC, SEN		1310.00 80	010734.40
	BXH, SERS		1304.33 42	010735.00
	BXE, I02241		10737.32 C2	010735.40
	SIC, SEN		1310.00 80	010736.00
	B, SERS		1304.10 00	010736.40
102241	KCI, SX0, %8=100	-EQUAL CONDITION, KCI.	100.01 0A	010737.00
	SIC, SEN		1310.00 80	010737.40
	BXL, SERS		1304.32 42	010740.00
	SIC, SEN		1310.00 80	010740.40
	BXH, SERS		1304.33 42	010741.00
	BXE, I02242		10743.32 C2	010741.40
	SIC, SEN		1310.00 80	010742.00
	B, SERS		1304.10 00	010742.40
102242	KC, SX0, BIT12	-INDEX HIGH CONDITION, KC.	17342.01 90	010743.00
	SIC, SEN		1310.00 80	010743.40
	BXL, SERS		1304.32 42	010744.00
	SIC, SEN		1310.00 80	010744.40
	BXE, SERS		1304.32 C2	010745.00
	BXH, I02243		10747.33 42	010745.40
	SIC, SEN		1310.00 80	010746.00
	B, SERS		1304.10 00	010746.40
102243	KCI, SX0, %8=40	-INDEX HI CONDITION, KCI.	40.01 0A	010747.00
	SIC, SEN		1310.00 80	010747.40
	BXL, SERS		1304.32 42	010750.00
	SIC, SEN		1310.00 80	010750.40
	BXE, SERS		1304.32 C2	010751.00
	BXH, I02244		10753.33 42	010751.40
	SIC, SEN		1310.00 80	010752.00
	B, SERS		1304.10 00	010752.40
102244	KC, SX0, BIT10	-INDEX LOW CONDITION, KC.	17340.01 90	010753.00
	SIC, SEN		1310.00 80	010753.40
	BXH, SERS		1304.33 42	010754.00
	SIC, SEN		1310.00 80	010754.40
	BXE, SERS		1304.32 C2	010755.00
	BXL, I02245		10757.32 42	010755.40
	SIC, SEN		1310.00 80	010756.00
	B, SERS		1304.10 00	010756.40
102245	KCI, SX0, %8=200	-INDEX LOW CONDITION, KCI.	200.01 0A	010757.00
	SIC, SEN		1310.00 80	010757.40
	BXH, SERS		1304.33 42	010760.00
	SIC, SEN		1310.00 80	010760.40
	BXE, SERS		1304.32 C2	010761.00
	BXL, I02246		10763.32 42	010761.40
	SIC, SEN		1310.00 80	010762.00
	B, SERS		1304.10 00	010762.40
102246	B, \$+1.0		10764.10 00	010763.00
	BD, I02240		10731.44 00	010763.40
	SIC, SEN0+.32		1311.40 80	010764.00
	B, SSW	-TO SSIP.	1301.10 00	010764.40
	BD, \$+.32		10765.44 00	010765.00
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	010765.40
	V+, \$X13, BIT11		17341.32 80	010766.00
	SX, \$X13, IC204		11301.33 10	010766.40

102247	SIC, I0010	-TEST BIT 40.	6526.00 80	010767.00
	B, I007		6523.10 00	010767.40
	LX, SX0, BIT40		17376.00 10	010770.00
	KC, SX0, BIT12	-EQUAL CONDITION, KC.	17342.01 90	010770.40
	SIC, SEN		1310.00 80	010771.00
	BXL, SERS		1304.32 42	010771.40
	SIC, SEN		1310.00 80	010772.00
	BXH, SERS		1304.33 42	010772.40
	BXE, I02248		10774.72 C2	010773.00
	SIC, SEN		1310.00 80	010773.40
	B, SERS		1304.10 00	010774.00
102248	KCI, SX0, %8#40	-EQUAL CONDITION, KCI.	40.01 0A	010774.40
	SIC, SEN		1310.00 80	010775.00
	BXL, SERS		1304.32 42	010775.40
	SIC, SEN		1310.00 80	010776.00
	BXH, SERS		1304.33 42	010776.40
	BXE, I02249		11000.72 C2	010777.00
	SIC, SEN		1310.00 80	010777.40
	B, SERS		1304.10 00	011000.00
102249	KC, SX0, BIT13	-INDEX HIGH CONDITION, KC.	17343.01 90	011000.40
	SIC, SEN		1310.00 80	011001.00
	BXL, SERS		1304.32 42	011001.40
	SIC, SEN		1310.00 80	011002.00
	BXE, SERS		1304.32 C2	011002.40
	BXH, I02250		11004.73 42	011003.00
	SIC, SEN		1310.00 80	011003.40
	B, SERS		1304.10 00	011004.00
102250	KCI, SX0, %8#20	-INDEX HI CONDITION, KCI	20.01 0A	011004.40
	SIC, SEN		1310.00 80	011005.00
	BXL, SERS		1304.32 42	011005.40
	SIC, SEN		1310.00 80	011006.00
	BXE, SERS		1304.32 C2	011006.40
	BXH, I02251		11010.73 42	011007.00
	SIC, SEN		1310.00 80	011007.40
	B, SERS		1304.10 00	011010.00
102251	KC, SX0, BIT11	-INDEX LOW CONDITION, KC.	17341.01 90	011010.40
	SIC, SEN		1310.00 80	011011.00
	BXH, SERS		1304.33 42	011011.40
	SIC, SEN		1310.00 80	011012.00
	BXE, SERS		1304.32 C2	011012.40
	BXL, I02252		11014.72 42	011013.00
	SIC, SEN		1310.00 80	011013.40
	B, SERS		1304.10 00	011014.00
102252	KCI, SX0, %8#100	-INDEX LOW CONDITION, KCI.	100.01 0A	011014.40
	SIC, SEN		1310.00 80	011015.00
	BXH, SERS		1304.33 42	011015.40
	SIC, SEN		1310.00 80	011016.00
	BXE, SERS		1304.32 C2	011016.40
	BXL, I02253		11020.72 42	011017.00
	SIC, SEN		1310.00 80	011017.40
	B, SERS		1304.10 00	011020.00
102253	B, \$+1.0		11021.50 00	011020.40
	BD, I02247		10767.04 00	011021.00
	SIC, SEN0+.32		1311.40 80	011021.40
	B, SSW	-TO SSIP.	1301.10 00	011022.00
	BD, \$+.32		11023.04 00	011022.40
	LX, SX13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011023.00
	V+, SX13, BIT12		17342.32 80	011023.40
	SX, SX13, IC204		11301.33 10	011024.00

102254	SIC,10010	-TEST BIT 41.	6526.00 80	011024.40
	B,1007		6523.10 00	011025.00
	LX,\$X0,BIT41		17377.00 10	011025.40
	KC,\$X0,BIT13	-EQUAL CONDITION, KC.	17343.01 90	011026.00
	SIC,SEN		1310.00 80	011026.40
	BXL,SERS		1304.32 42	011027.00
	SIC,SEN		1310.00 80	011027.40
	BXH,SERS		1304.33 42	011030.00
	BXE,102255		11032.32 C2	011030.40
	SIC,SEN		1310.00 80	011031.00
	B,SERS		1304.10 00	011031.40
102255	KCI,\$X0,%8=20	-EQUAL CONDITION,KCI.	20.01 0A	011032.00
	SIC,SEN		1310.00 80	011032.40
	BXL,SERS		1304.32 42	011033.00
	SIC,SEN		1310.00 80	011033.40
	BXH,SERS		1304.33 42	011034.00
	BXE,102256		11036.32 C2	011034.40
	SIC,SEN		1310.00 80	011035.00
	B,SERS		1304.10 00	011035.40
102256	KC,\$X0,BIT14	-INDEX HIGH CONDITION, KC.	17344.01 90	011036.00
	SIC,SEN		1310.00 80	011036.40
	BXL,SERS		1304.32 42	011037.00
	SIC,SEN		1310.00 80	011037.40
	BXE,SERS		1304.32 C2	011040.00
	BXH,102257		11042.33 42	011040.40
	SIC,SEN		1310.00 80	011041.00
	B,SERS		1304.10 00	011041.40
102257	KCI,\$X0,%8=10	-INDEX HI CONDITION,KCI.	10.01 0A	011042.00
	SIC,SEN		1310.00 80	011042.40
	BXL,SERS		1304.32 42	011043.00
	SIC,SEN		1310.00 80	011043.40
	BXE,SERS		1304.32 C2	011044.00
	BXH,102258		11046.33 42	011044.40
	SIC,SEN		1310.00 80	011045.00
	B,SERS		1304.10 00	011045.40
102258	KC,\$X0,BIT12	-INDEX LOW CONDITION, KC.	17342.01 90	011046.00
	SIC,SEN		1310.00 80	011046.40
	BXH,SERS		1304.33 42	011047.00
	SIC,SEN		1310.00 80	011047.40
	BXE,SERS		1304.32 C2	011050.00
	BXL,102259		11052.32 42	011050.40
	SIC,SEN		1310.00 80	011051.00
	B,SERS		1304.10 00	011051.40
102259	KCI,\$X0,%8=40	-INDEX LOW CONDITION,KCI.	40.01 0A	011052.00
	SIC,SEN		1310.00 80	011052.40
	BXH,SERS		1304.33 42	011053.00
	SIC,SEN		1310.00 80	011053.40
	BXE,SERS		1304.32 C2	011054.00
	BXL,102260		11056.32 42	011054.40
	SIC,SEN		1310.00 80	011055.00
	B,SERS		1304.10 00	011055.40
102260	B,\$+1.0		11057.10 00	011056.00
	BD,102254		11024.44 00	011056.40
	SIC,SEN0+.32		1311.40 80	011057.00
	B,SSW	-TO SSIP.	1301.10 00	011057.40
	BD,\$+.32		11060.44 00	011060.00
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011060.40
	V+,\$X13,BIT13		17343.32 80	011061.00
	SX,\$X13,IC204		11301.33 10	011061.40

102261	SIC, I0010	-TEST BIT 42.	6526.00 80	011062.00
	B, I007		6523.10 00	011062.40
	LX, SX0, BIT42		17400.00 10	011063.00
	KC, SX0, BIT14	-EQUAL CONDITION, KC.	17344.01 90	011063.40
	SIC, SEN		1310.00 80	011064.00
	BXL, SERS		1304.32 42	011064.40
	SIC, SEN		1310.00 80	011065.00
	BXH, SERS		1304.33 42	011065.40
	BXE, I02262		11067.72 C2	011066.00
	SIC, SEN		1310.00 80	011066.40
	B, SERS		1304.10 00	011067.00
102262	KCI, SX0, %8#10	-EQUAL CONDITION, KCI.	10.01 0A	011067.40
	SIC, SEN		1310.00 80	011070.00
	BXL, SERS		1304.32 42	011070.40
	SIC, SEN		1310.00 80	011071.00
	BXH, SERS		1304.33 42	011071.40
	BXE, I02263		11073.72 C2	011072.00
	SIC, SEN		1310.00 80	011072.40
	B, SERS		1304.10 00	011073.00
102263	KC, SX0, BIT15	-INDEX HIGH CONDITION, KC.	17345.01 90	011073.40
	SIC, SEN		1310.00 80	011074.00
	BXL, SERS		1304.32 42	011074.40
	SIC, SEN		1310.00 80	011075.00
	BXE, SERS		1304.32 C2	011075.40
	BXH, I02264		11077.73 42	011076.00
	SIC, SEN		1310.00 80	011076.40
	B, SERS		1304.10 00	011077.00
102264	KCI, SX0, %8#4	-INDEX HI CONDITION, KCI.	4.01 0A	011077.40
	SIC, SEN		1310.00 80	011100.00
	BXL, SERS		1304.32 42	011100.40
	SIC, SEN		1310.00 80	011101.00
	BXE, SERS		1304.32 C2	011101.40
	BXH, I02265		11103.73 42	011102.00
	SIC, SEN		1310.00 80	011102.40
	B, SERS		1304.10 00	011103.00
102265	KC, SX0, BIT13	-INDEX LOW CONDITION, KC.	17343.01 90	011103.40
	SIC, SEN		1310.00 80	011104.00
	BXH, SERS		1304.33 42	011104.40
	SIC, SEN		1310.00 80	011105.00
	BXE, SERS		1304.32 C2	011105.40
	BXL, I02266		11107.72 42	011106.00
	SIC, SEN		1310.00 80	011106.40
	B, SERS		1304.10 00	011107.00
102266	KCI, SX0, %8#20	-INDEX LOW CONDITION, KCI.	20.01 0A	011107.40
	SIC, SEN		1310.00 80	011110.00
	BXH, SERS		1304.33 42	011110.40
	SIC, SEN		1310.00 80	011111.00
	BXE, SERS		1304.32 C2	011111.40
	BXL, I02267		11113.72 42	011112.00
	SIC, SEN		1310.00 80	011112.40
	B, SERS		1304.10 00	011113.00
102267	B, \$+1.0		11114.50 00	011113.40
	BD, I02261		11062.04 00	011114.00
	SIC, SEN0+.32		1311.40 80	011114.40
	B, SSW	-TO SSIP.	1301.10 00	011115.00
	BD, \$+.32		11116.04 00	011115.40
	LX, \$X13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011116.00
	V+, \$X13, BIT14		17344.32 B0	011116.40
	SX, \$X13, IC204		11301.33 10	011117.00

102268	SIC, I0010	-TEST BIT 43.	6526.00	80	011117.40
	B, I007		6523.10	00	011120.00
	LX, SX0, BIT43		17401.00	10	011120.40
	KC, SX0, BIT15	-EQUAL CONDITION, KC.	17345.01	90	011121.00
	SIC, SEN		1310.00	80	011121.40
	BXL, SERS		1304.32	42	011122.00
	SIC, SEN		1310.00	80	011122.40
	BXH, SERS		1304.33	42	011123.00
	BXE, I02269		11125.32	C2	011123.40
	SIC, SEN		1310.00	80	011124.00
	B, SERS		1304.10	00	011124.40
102269	KCI, SX0, %8#4	-EQUAL CONDITION, KCI.	4.01	0A	011125.00
	SIC, SEN		1310.00	80	011125.40
	BXL, SERS		1304.32	42	011126.00
	SIC, SEN		1310.00	80	011126.40
	BXH, SERS		1304.33	42	011127.00
	BXE, I02270		11131.32	C2	011127.40
	SIC, SEN		1310.00	80	011130.00
	B, SERS		1304.10	00	011130.40
102270	KC, SX0, BIT16	-INDEX HIGH CONDITION, KC.	17346.01	90	011131.00
	SIC, SEN		1310.00	80	011131.40
	BXL, SERS		1304.32	42	011132.00
	SIC, SEN		1310.00	80	011132.40
	BXE, SERS		1304.32	C2	011133.00
	BXH, I02271		11135.33	42	011133.40
	SIC, SEN		1310.00	80	011134.00
	B, SERS		1304.10	00	011134.40
102271	KCI, SX0, %8#2	-INDEX HI CONDITION, KCI.	2.01	0A	011135.00
	SIC, SEN		1310.00	80	011135.40
	BXL, SERS		1304.32	42	011136.00
	SIC, SEN		1310.00	80	011136.40
	BXE, SERS		1304.32	C2	011137.00
	BXH, I02272		11141.33	42	011137.40
	SIC, SEN		1310.00	80	011140.00
	B, SERS		1304.10	00	011140.40
102272	KC, SX0, BIT14	-INDEX LOW CONDITION, KC.	17344.01	90	011141.00
	SIC, SEN		1310.00	80	011141.40
	BXH, SERS		1304.33	42	011142.00
	SIC, SEN		1310.00	80	011142.40
	BXE, SERS		1304.32	C2	011143.00
	BXL, I02273		11145.32	42	011143.40
	SIC, SEN		1310.00	80	011144.00
	B, SERS		1304.10	00	011144.40
102273	KCI, SX0, %8#10	-INDEX LOW CONDITION, KCI.	10.01	0A	011145.00
	SIC, SEN		1310.00	80	011145.40
	BXH, SERS		1304.33	42	011146.00
	SIC, SEN		1310.00	80	011146.40
	BXE, SERS		1304.32	C2	011147.00
	BXL, I02274		11151.32	42	011147.40
	SIC, SEN		1310.00	80	011150.00
	B, SERS		1304.10	00	011150.40
102274	B, S+1.0		11152.10	00	011151.00
	BD, I02268		11117.44	00	011151.40
	SIC, SEN0+.32		1311.40	80	011152.00
	B, SSW	-TO SSIP.	1301.10	00	011152.40
	BD, S+.32		11153.44	00	011153.00
	LX, SX13, IC204	-UPDATE CONTINUITY CHECK.	11301.32	10	011153.40
	V+, SX13, BIT15		17345.32	B0	011154.00
	SX, SX13, IC204		11301.33	10	011154.40

102275	SIC,10010	-TEST BIT 44.	6526.00 80	011155.00
	B,1007		6523.10 00	011155.40
	LX,\$X0,BIT44		17402.00 10	011156.00
	KC,\$X0,BIT16	-EQUAL CONDITION, KC.	17346.01 90	011156.40
	SIC,SEN		1310.00 80	011157.00
	BXL,SERS		1304.32 42	011157.40
	SIC,SEN		1310.00 80	011160.00
	BXH,SERS		1304.33 42	011160.40
	BXE,102276		11162.72 C2	011161.00
	SIC,SEN		1310.00 80	011161.40
	B,SERS		1304.10 00	011162.00
102276	KCI,\$X0,%8#2	-EQUAL CONDITION,KCI.	2.01 0A	011162.40
	SIC,SEN		1310.00 80	011163.00
	BXL,SERS		1304.32 42	011163.40
	SIC,SEN		1310.00 80	011164.00
	BXH,SERS		1304.33 42	011164.40
	BXE,102277		11166.72 C2	011165.00
	SIC,SEN		1310.00 80	011165.40
	B,SERS		1304.10 00	011166.00
102277	KC,\$X0,BIT17	-INDEX HIGH CONDITION, KC.	17347.01 90	011166.40
	SIC,SEN		1310.00 80	011167.00
	BXL,SERS		1304.32 42	011167.40
	SIC,SEN		1310.00 80	011170.00
	BXE,SERS		1304.32 C2	011170.40
	BXH,102278		11172.73 42	011171.00
	SIC,SEN		1310.00 80	011171.40
	B,SERS		1304.10 00	011172.00
102278	KCI,\$X0,%8#1	-INDEX HI CONDITION,KCI.	1.01 0A	011172.40
	SIC,SEN		1310.00 80	011173.00
	BXL,SERS		1304.32 42	011173.40
	SIC,SEN		1310.00 80	011174.00
	BXE,SERS		1304.32 C2	011174.40
	BXH,102279		11176.73 42	011175.00
	SIC,SEN		1310.00 80	011175.40
	B,SERS		1304.10 00	011176.00
102279	KC,\$X0,BIT15	-INDEX LOW CONDITION, KC.	17345.01 90	011176.40
	SIC,SEN		1310.00 80	011177.00
	BXH,SERS		1304.33 42	011177.40
	SIC,SEN		1310.00 80	011200.00
	BXE,SERS		1304.32 C2	011200.40
	BXL,102280		11202.72 42	011201.00
	SIC,SEN		1310.00 80	011201.40
	B,SERS		1304.10 00	011202.00
102280	KCI,\$X0,%8#4	-INDEX LOW CONDITION,KCI.	4.01 0A	011202.40
	SIC,SEN		1310.00 80	011203.00
	BXH,SERS		1304.33 42	011203.40
	SIC,SEN		1310.00 80	011204.00
	BXE,SERS		1304.32 C2	011204.40
	BXL,102281		11206.72 42	011205.00
	SIC,SEN		1310.00 80	011205.40
	B,SERS		1304.10 00	011206.00
102281	B,\$+1.0		11207.50 00	011206.40
	BD,102275		11155.04 00	011207.00
	SIC,SEN0+.32		1311.40 80	011207.40
	B,SSW	-TO SSIP.	1301.10 00	011210.00
	BD,\$+.32		11211.04 00	011210.40
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011211.00
	V+,\$X13,BIT16		17346.32 B0	011211.40
	SX,\$X13,IC204		11301.33 10	011212.00

102282	SIC, I0010	-TEST BIT 45.	6526.00 80	011212.40
	B, I007		6523.10 00	011213.00
	LX, SX0, BIT45		17403.00 10	011213.40
	KC, SX0, BIT17	-EQUAL CONDITION, KC.	17347.01 90	011214.00
	SIC, SEN		1310.00 80	011214.40
	BXL, SERS		1304.32 42	011215.00
	SIC, SEN		1310.00 80	011215.40
	BXH, SERS		1304.33 42	011216.00
	BXE, I02283		11220.32 C2	011216.40
	SIC, SEN		1310.00 80	011217.00
	B, SERS		1304.10 00	011217.40
102283	KCI, SX0, %8#1	-EQUAL CONDITION, KCI.	1.01 0A	011220.00
	SIC, SEN		1310.00 80	011220.40
	BXL, SERS		1304.32 42	011221.00
	SIC, SEN		1310.00 80	011221.40
	BXH, SERS		1304.33 42	011222.00
	BXE, I02284		11224.32 C2	011222.40
	SIC, SEN		1310.00 80	011223.00
	B, SERS		1304.10 00	011223.40
102284	KC, SX0, BIT18	-INDEX HIGH CONDITION, KC.	17350.01 90	011224.00
	SIC, SEN		1310.00 80	011224.40
	BXL, SERS		1304.32 42	011225.00
	SIC, SEN		1310.00 80	011225.40
	BXE, SERS		1304.32 C2	011226.00
	BXH, I02285		11230.33 42	011226.40
	SIC, SEN		1310.00 80	011227.00
	B, SERS		1304.10 00	011227.40
102285	KCI, SX0, %8#0	-INDEX HI CONDITION, KCI.	0.01 0A	011230.00
	SIC, SEN		1310.00 80	011230.40
	BXL, SERS		1304.32 42	011231.00
	SIC, SEN		1310.00 80	011231.40
	BXE, SERS		1304.32 C2	011232.00
	BXH, I02286		11234.33 42	011232.40
	SIC, SEN		1310.00 80	011233.00
	B, SERS		1304.10 00	011233.40
102286	KC, SX0, BIT16	-INDEX LOW CONDITION, KC.	17346.01 90	011234.00
	SIC, SEN		1310.00 80	011234.40
	BXH, SERS		1304.33 42	011235.00
	SIC, SEN		1310.00 80	011235.40
	BXE, SERS		1304.32 C2	011236.00
	BXL, I02288		11240.32 42	011236.40
	SIC, SEN		1310.00 80	011237.00
	B, SERS		1304.10 00	011237.40
102288	KCI, SX0, %8#2	-INDEX LOW CONDITION, KCI.	2.01 0A	011240.00
	SIC, SEN		1310.00 80	011240.40
	BXH, SERS		1304.33 42	011241.00
	SIC, SEN		1310.00 80	011241.40
	BXE, SERS		1304.32 C2	011242.00
	BXL, I02289		11244.32 42	011242.40
	SIC, SEN		1310.00 80	011243.00
	B, SERS		1304.10 00	011243.40
102289	B, S+1.0		11245.10 00	011244.00
	BD, I02282		11212.44 00	011244.40
	SIC, SEN0+.32		1311.40 80	011245.00
	B, SSW	-TO SSIP.	1301.10 00	011245.40
	BD, S+.32		11246.44 00	011246.00
	LX, SX13, IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011246.40
	V+, SX13, BIT17		17347.32 80	011247.00
	SX, SX13, IC204		11301.33 10	011247.40

102290	SIC,10010	-TEST BITS ALL ZERO.	6526.00 80	011250.00
	B,1007		6523.10 00	011250.40
	LX,\$X0,100Z		17306.00 10	011251.00
	KC,\$X0,100Z	-EQUAL CONDITION,KC.	17306.01 90	011251.40
	SIC,SEN		1310.00 80	011252.00
	BXL,SERS		1304.32 42	011252.40
	SIC,SEN		1310.00 80	011253.00
	BXH,SERS		1304.33 42	011253.40
	BXE,102291		11255.72 C2	011254.00
	SIC,SEN		1310.00 80	011254.40
	B,SERS		1304.10 00	011255.00
102291	KCI,\$X0,%8=0	-EQUAL CONDITION,KCI.	0.01 0A	011255.40
	SIC,SEN		1310.00 80	011256.00
	BXL,SERS		1304.32 42	011256.40
	SIC,SEN		1310.00 80	011257.00
	BXH,SERS		1304.33 42	011257.40
	BXE,102292		11262.72 C2	011260.00
	SIC,SEN		1310.00 80	011260.40
	B,SERS		1304.10 00	011261.00
	SIC,SEN		1310.00 80	011261.40
	B,SERS		1304.10 00	011262.00
102292	KC,\$X0,BIT17	-INDEX LOW CONDITION, KC.	17347.01 90	011262.40
	SIC,SEN		1310.00 80	011263.00
	BXH,SERS		1304.33 42	011263.40
	SIC,SEN		1310.00 80	011264.00
	BXE,SERS		1304.32 C2	011264.40
	BXL,102293		11266.72 42	011265.00
	SIC,SEN		1310.00 80	011265.40
	B,SERS		1304.10 00	011266.00
102293	KCI,\$X0,%8=1	-INDEX LOW CONDITION,KCI.	1.01 0A	011266.40
	SIC,SEN		1310.00 80	011267.00
	BXH,SERS		1304.33 42	011267.40
	SIC,SEN		1310.00 80	011270.00
	BXE,SERS		1304.32 C2	011270.40
	BXL,102294		11272.72 42	011271.00
	SIC,SEN		1310.00 80	011271.40
	B,SERS		1304.10 00	011272.00
102294	B,\$+1.0		11273.50 00	011272.40
	BD,102290		11250.04 00	011273.00
	SIC,SEN0+.32		1311.40 80	011273.40
	B,SSW	-TO SSIP.	1301.10 00	011274.00
	BD,\$+.32		11275.04 00	011274.40
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011275.00
	V+,\$X13,BIT18		17350.32 80	011275.40
	SX,\$X13,IC204		11301.33 10	011276.00
	LX,\$X13,IC204	-UPDATE CONTINUITY CHECK.	11301.32 10	011276.40
	KV,\$X13,ICK204		11302.32 90	011277.00
	SIC,SEN		1310.00 80	011277.40
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	011300.00
	B,106		11303.10 00	011300.40
	CNOP			
IC204	XW,0,0,0	-CONTINUITY REG 1204.	0.00 00 000000.00 00	011301.00
ICK204	XW,%8=777777.40,0,0		777777.40 00 000000.00 00	011302.00

---1206---STORE ZERO CHECK.

106	LX,\$X1,1061D	-UPDATE IDENT	11306.02 10	011303.00
	SX,\$X1,DPET13		1437.03 10	011303.40
	SIC,RET		1306.40 80	011304.00
	B,1DF1		1443.10 00	011304.40
	Z,1C206		11343.22 00	011305.00
	BD,1060		11307.04 00	011305.40
	CNOP			
1061D	%1QSZDD%BU,64,8,1206	Z		011306.00
1060	LX,\$X0,1000	-CHECK STORE INTO EXT. MEM.	17307.00 10	011307.00
	SX,\$X0,106DMP		11345.01 10	011307.40
	L%BU,64,8,106DMP		11345.00 80 000000.20 50	011310.00
	BRZ,1061		11312.34 C2	011311.00
	B,1062	-OK TO TEST.	11313.50 00	011311.40
1061	SIC,SEN	-SX STORES NO BITS.	1310.00 80	011312.00
	B,SERS	-MUST DELETE THIS TEST.	1304.10 00	011312.40
	B,1063	-BYPASS	11316.50 00	011313.00
1062	Z,106DMP		11345.22 00	011313.40
	L%BU,64,8,106DMP		11345.00 80 000000.20 50	011314.00
	BRZ,1063		11316.74 C2	011315.00
	SIC,SEN		1310.00 80	011315.40
	B,SERS	-Z IN EXT MEM FAILS.	1304.10 00	011316.00
1063	L%BU,64,8,1000	-CHECK STORE INTO INT. MEM.	17307.00 80 000000.20 50	011316.40
	BRZ,1064		11320.74 C2	011317.40
	B,1065	-OK TO TEST.	11322.10 00	011320.00
1064	SIC,SEN	-VFL LOAD FAILS ON ALL BITS.	1310.00 80	011320.40
	B,SERS	-MUST DELETE TEST.	1304.10 00	011321.00
	B,1066	-DELETE.	11325.10 00	011321.40
1065	Z,\$R		11.22 00	011322.00
	L,\$R		11.00 80 000000.20 50	011322.40
	BRZ,1066		11325.34 C2	011323.40
	SIC,SEN		1310.00 80	011324.00
	B,SERS	-Z IN INT MEM. FAILS.	1304.10 00	011324.40
1066	LX,\$X0,1000	-CHECK STORE INTO IX CORE.	17307.00 10	011325.00
	L%BU,\$X0		20.00 80 000000.20 50	011325.40
	BRZ,1067		11327.74 C2	011326.40
	B,1068		11331.10 00	011327.00
1067	SIC,SEN		1310.00 80	011327.40
	B,SERS	-LX FAILS ON ALL BITS.	1304.10 00	011330.00
	B,1069	-BYPASS TEST.	11334.10 00	011330.40
1068	Z,16		20.22 00	011331.00
	L%BU,64,8,16.0		20.00 80 000000.20 50	011331.40
	BRZ,1069		11334.34 C2	011332.40
	SIC,SEN		1310.00 80	011333.00
	B,SERS	-Z IN IX CORE FAILS.	1304.10 00	011333.40
1069	B,\$+1.0		11335.10 00	011334.00
	BD,1060		11307.04 00	011334.40
	SIC,SEN0+.32		1311.40 80	011335.00
	B,SSW	-TO SSIP.	1301.10 00	011335.40
	BD,\$+.32		11336.44 00	011336.00
	LX,\$X13,1C206	-UPDATE CONTINUITY CHECK.	11343.32 10	011336.40
	V+,\$X13,BIT0		17326.32 80	011337.00
	SX,\$X13,1C206		11343.33 10	011337.40
	LX,\$X13,1C206	-UPDATE CONTINUITY CHECK.	11343.32 10	011340.00
	KV,\$X13,1CK206		11344.32 90	011340.40

	SIC,SEN		1310.00 80	011341.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	011341.40
	B,108		11346.10 00	011342.00
	CNOP		0.30 00	011342.40
	-			
IC206	XW,0,0,0	-CONTINUITY REG 1206.	0.00 00 000000.00 00	011343.00
ICK206	XW,%8#400000.00,0,0		400000.00 00 000000.00 00	011344.00
106DMP	NOP		0.30 00	011345.00
	NOP		0.30 00	011345.40

---1208---INDEX SELECTION TEST.

108	LX,\$X1,108ID	-UPDATE IDENT.	11351.02 10	011346.00
	SX,\$X1,DPET13		1437.03 10	011346.40
	SIC,RET		1306.40 80	011347.00
	B,1DF1		1443.10 00	011347.40
	Z,1C208		12675.22 00	011350.00
	BD,1081		11352.04 00	011350.40
	CNOP			
108ID	%IQSZ=DD%BU,64,8,1208	Z		011351.00
1081	SIC,108S17	-LOAD INDEX 0 AND CHK NO OTHERS	12757.00 80	011352.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10 00	011352.40
	LX,\$X0,1000		17307.00 10	011353.00
	L%BU,64,8,16.0		20.00 80 000000.20 50	011353.40
	SIC,SEN		1310.00 80	011354.40
	BRZ,SERS	-NO BITS IN IX 0.	1304.34 C2	011355.00
	L%BU,64,8,17.0		21.00 80 000000.20 50	011355.40
	BRZ,\$+1.32		11360.34 C2	011356.40
	SIC,SEN		1310.00 80	011357.00
	B,SERS	-BITS IN IX 1.	1304.10 00	011357.40
	L%BU,64,8,18.0		22.00 80 000000.20 50	011360.00
	BRZ,\$+1.32		11362.74 C2	011361.00
	SIC,SEN		1310.00 80	011361.40
	B,SERS	-BITS IN IX 2.	1304.10 00	011362.00
	L%BU,64,8,19.0		23.00 80 000000.20 50	011362.40
	BRZ,\$+1.32		11365.34 C2	011363.40
	SIC,SEN		1310.00 80	011364.00
	B,SERS	-BITS IN IX 3.	1304.10 00	011364.40
	L%BU,64,8,20.0		24.00 80 000000.20 50	011365.00
	BRZ,\$+1.32		11367.74 C2	011366.00
	SIC,SEN		1310.00 80	011366.40
	B,SERS	-BITS IN IX 4.	1304.10 00	011367.00
	L%BU,64,8,21.0		25.00 80 000000.20 50	011367.40
	BRZ,\$+1.32		11372.34 C2	011370.40
	SIC,SEN		1310.00 80	011371.00
	B,SERS	-BITS IN IX 5.	1304.10 00	011371.40
	L%BU,64,8,22.0		26.00 80 000000.20 50	011372.00
	BRZ,\$+1.32		11374.74 C2	011373.00
	SIC,SEN		1310.00 80	011373.40
	B,SERS	-BITS IN IX 6.	1304.10 00	011374.00
	L%BU,64,8,23.0		27.00 80 000000.20 50	011374.40
	BRZ,\$+1.32		11377.34 C2	011375.40
	SIC,SEN		1310.00 80	011376.00
	B,SERS	-BITS IN IX 7.	1304.10 00	011376.40
	L%BU,64,8,24.0		30.00 80 000000.20 50	011377.00
	BRZ,\$+1.32		11401.74 C2	011400.00
	SIC,SEN		1310.00 80	011400.40
	B,SERS	-BITS IN IX 8.	1304.10 00	011401.00
	L%BU,64,8,25.0		31.00 80 000000.20 50	011401.40
	BRZ,\$+1.32		11404.34 C2	011402.40
	SIC,SEN		1310.00 80	011403.00
	B,SERS	-BITS IN IX 9.	1304.10 00	011403.40
	L%BU,64,8,26.0		32.00 80 000000.20 50	011404.00
	BRZ,\$+1.32		11406.74 C2	011405.00
	SIC,SEN		1310.00 80	011405.40
	B,SERS	-BITS IN IX 10.	1304.10 00	011406.00
	L%BU,64,8,27.0		33.00 80 000000.20 50	011406.40
	BRZ,\$+1.32		11411.34 C2	011407.40
	SIC,SEN		1310.00 80	011410.00
	B,SERS	-BITS IN IX 11.	1304.10 00	011410.40
	L%BU,64,8,28.0		34.00 80 000000.20 50	011411.00
	BRZ,\$+1.32		11413.74 C2	011412.00
	SIC,SEN		1310.00 80	011412.40
	B,SERS	-BITS IN IX 12.	1304.10 00	011413.00

L%BU,64,8□,29.0		35.00 80 000000.20 50	011413.40
BRZ,\$+1.32		11416.34 C2	011414.40
SIC,SEN		1310.00 80	011415.00
B,SERS	-BITS IN IX 13.	1304.10 00	011415.40
L%BU,64,8□,30.0		36.00 80 000000.20 50	011416.00
BRZ,\$+1.32		11420.74 C2	011417.00
SIC,SEN		1310.00 80	011417.40
B,SERS	-BITS IN IX 14.	1304.10 00	011420.00
L%BU,64,8□,31.0		37.00 80 000000.20 50	011420.40
BRZ,\$+1.32		11423.34 C2	011421.40
SIC,SEN		1310.00 80	011422.00
B,SERS	-BITS IN IX 15.	1304.10 00	011422.40
B,\$+1.0		11424.10 00	011423.00
BD,1081		11352.04 00	011423.40
SIC,SEN0+.32		1311.40 80	011424.00
B,SSW	-TO SSIP	1301.10 00	011424.40
BD,\$+.32		11425.44 00	011425.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	011425.40
V+,\$X13,BIT0		17326.32 B0	011426.00
SX,\$X13,IC208		12675.33 10	011426.40

1082	SIC,108S17	-LOAD INDEX 1 AND CHK NO OTHERS	12757.00	80		011427.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00		011427.40
	LX,\$X1,1000		17307.02	10		011430.00
	L%BU,64,8□,17.0		21.00	80	000000.20 50	011430.40
	SIC,SEN		1310.00	80		011431.40
	BRZ,SERS	-NO BITS IN IX 1.	1304.34	C2		011432.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	011432.40
	BRZ,\$+1.32		11435.34	C2		011433.40
	SIC,SEN		1310.00	80		011434.00
	B,SERS	-BITS IN IX 0.	1304.10	00		011434.40
	L%BU,64,8□,18.0		22.00	80	000000.20 50	011435.00
	BRZ,\$+1.32		11437.74	C2		011436.00
	SIC,SEN		1310.00	80		011436.40
	B,SERS	-BITS IN IX 2.	1304.10	00		011437.00
	L%BU,64,8□,19.0		23.00	80	000000.20 50	011437.40
	BRZ,\$+1.32		11442.34	C2		011440.40
	SIC,SEN		1310.00	80		011441.00
	B,SERS	-BITS IN IX 3.	1304.10	00		011441.40
	L%BU,64,8□,20.0		24.00	80	000000.20 50	011442.00
	BRZ,\$+1.32		11444.74	C2		011443.00
	SIC,SEN		1310.00	80		011443.40
	B,SERS	-BITS IN IX 4.	1304.10	00		011444.00
	L%BU,64,8□,21.0		25.00	80	000000.20 50	011444.40
	BRZ,\$+1.32		11447.34	C2		011445.40
	SIC,SEN		1310.00	80		011446.00
	B,SERS	-BITS IN IX 5.	1304.10	00		011446.40
	L%BU,64,8□,22.0		26.00	80	000000.20 50	011447.00
	BRZ,\$+1.32		11451.74	C2		011450.00
	SIC,SEN		1310.00	80		011450.40
	B,SERS	-BITS IN IX 6.	1304.10	00		011451.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	011451.40
	BRZ,\$+1.32		11454.34	C2		011452.40
	SIC,SEN		1310.00	80		011453.00
	B,SERS	-BITS IN IX 7.	1304.10	00		011453.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	011454.00
	BRZ,\$+1.32		11456.74	C2		011455.00
	SIC,SEN		1310.00	80		011455.40
	B,SERS	-BITS IN IX 8.	1304.10	00		011456.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	011456.40
	BRZ,\$+1.32		11461.34	C2		011457.40
	SIC,SEN		1310.00	80		011460.00
	B,SERS	-BITS IN IX 9.	1304.10	00		011460.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	011461.00
	BRZ,\$+1.32		11463.74	C2		011462.00
	SIC,SEN		1310.00	80		011462.40
	B,SERS	-BITS IN IX 10.	1304.10	00		011463.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	011463.40
	BRZ,\$+1.32		11466.34	C2		011464.40
	SIC,SEN		1310.00	80		011465.00
	B,SERS	-BITS IN IX 11.	1304.10	00		011465.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	011466.00
	BRZ,\$+1.32		11470.74	C2		011467.00
	SIC,SEN		1310.00	80		011467.40
	B,SERS	-BITS IN IX 12.	1304.10	00		011470.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	011470.40
	BRZ,\$+1.32		11473.34	C2		011471.40
	SIC,SEN		1310.00	80		011472.00
	B,SERS	-BITS IN IX 13.	1304.10	00		011472.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	011473.00
	BRZ,\$+1.32		11475.74	C2		011474.00
	SIC,SEN		1310.00	80		011474.40
	B,SERS	-BITS IN IX 14.	1304.10	00		011475.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	011475.40
	BRZ,\$+1.32		11500.34	C2		011476.40
	SIC,SEN		1310.00	80		011477.00

	B,SERS	-BITS IN IX 15.	1304.10 00	011477.40
	B,\$+1.0		11501.10 00	011500.00
	BD,I082		11427.04 00	011500.40
	SIC,SEN0+.32		1311.40 80	011501.00
	B,SSW	-TO SSIP	1301.10 00	011501.40
	BD,\$+.32		11502.44 00	011502.00
	LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	011502.40
	V+,\$X13,BIT1		17327.32 80	011503.00
	SX,\$X13,IC208		12675.33 10	011503.40

1083	SIC,108S17	-LOAD INDEX 2 AND CHK NO OTHERS	12757.00	80		011504.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00		011504.40
	LX,\$X2,1000		17307.04	10		011505.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	011505.40
	SIC,SEN		1310.00	80		011506.40
	BRZ,SERS	-NO BITS IN IX 2.	1304.34	C2		011507.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	011507.40
	BRZ,\$+1.32		11512.34	C2		011510.40
	SIC,SEN		1310.00	80		011511.00
	B,SERS	-BITS IN IX 0.	1304.10	00		011511.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	011512.00
	BRZ,\$+1.32		11514.74	C2		011513.00
	SIC,SEN		1310.00	80		011513.40
	B,SERS	-BITS IN IX 1.	1304.10	00		011514.00
	L%BU,64,8□,19.0		23.00	80	000000.20 50	011514.40
	BRZ,\$+1.32		11517.34	C2		011515.40
	SIC,SEN		1310.00	80		011516.00
	B,SERS	-BITS IN IX 3.	1304.10	00		011516.40
	L%BU,64,8□,20.0		24.00	80	000000.20 50	011517.00
	BRZ,\$+1.32		11521.74	C2		011520.00
	SIC,SEN		1310.00	80		011520.40
	B,SERS	-BITS IN IX 4.	1304.10	00		011521.00
	L%BU,64,8□,21.0		25.00	80	000000.20 50	011521.40
	BRZ,\$+1.32		11524.34	C2		011522.40
	SIC,SEN		1310.00	80		011523.00
	B,SERS	-BITS IN IX 5.	1304.10	00		011523.40
	L%BU,64,8□,22.0		26.00	80	000000.20 50	011524.00
	BRZ,\$+1.32		11526.74	C2		011525.00
	SIC,SEN		1310.00	80		011525.40
	B,SERS	-BITS IN IX 6.	1304.10	00		011526.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	011526.40
	BRZ,\$+1.32		11531.34	C2		011527.40
	SIC,SEN		1310.00	80		011530.00
	B,SERS	-BITS IN IX 7.	1304.10	00		011530.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	011531.00
	BRZ,\$+1.32		11533.74	C2		011532.00
	SIC,SEN		1310.00	80		011532.40
	B,SERS	-BITS IN IX 8.	1304.10	00		011533.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	011533.40
	BRZ,\$+1.32		11536.34	C2		011534.40
	SIC,SEN		1310.00	80		011535.00
	B,SERS	-BITS IN IX 9.	1304.10	00		011535.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	011536.00
	BRZ,\$+1.32		11540.74	C2		011537.00
	SIC,SEN		1310.00	80		011537.40
	B,SERS	-BITS IN IX 10.	1304.10	00		011540.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	011540.40
	BRZ,\$+1.32		11543.34	C2		011541.40
	SIC,SEN		1310.00	80		011542.00
	B,SERS	-BITS IN IX 11.	1304.10	00		011542.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	011543.00
	BRZ,\$+1.32		11545.74	C2		011544.00
	SIC,SEN		1310.00	80		011544.40
	B,SERS	-BITS IN IX 12.	1304.10	00		011545.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	011545.40
	BRZ,\$+1.32		11550.34	C2		011546.40
	SIC,SEN		1310.00	80		011547.00
	B,SERS	-BITS IN IX 13.	1304.10	00		011547.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	011550.00
	BRZ,\$+1.32		11552.74	C2		011551.00
	SIC,SEN		1310.00	80		011551.40
	B,SERS	-BITS IN IX 14.	1304.10	00		011552.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	011552.40
	BRZ,\$+1.32		11555.34	C2		011553.40
	SIC,SEN		1310.00	80		011554.00

B,\$+1.0	B,SERS	-BITS IN IX 15.	1304.10 00	011554.40
BD,1083			11556.10 00	011555.00
SIC,SEN0+.32			1035.44 00	011555.40
B,SSW			1311.40 80	011556.00
BD,\$+.32		-TO SSIP	1301.10 00	011556.40
			11557.44 00	011557.00
LX,\$X13,IC208		-UPDATE CONTINUITY CHECK.	12675.32 10	011557.40
V+,\$X13,BIT2			17330.32 80	011560.00
SX,\$X13,IC208			12675.33 10	011560.40

1084	SIC,108S17	-LOAD INDEX 3 AND CHK NO OTHERS	12757.00	80	011561.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00	011561.40
	LX,\$X3,1000		17307.06	10	011562.00
	L%BU,64,8□,19.0		23.00	80 000000.20 50	011562.40
	SIC,SEN		1310.00	80	011563.40
	BRZ,SERS	-NO BITS IN IX 3.	1304.34	C2	011564.00
	L%BU,64,8□,16.0		20.00	80 000000.20 50	011564.40
	BRZ,\$+1.32		11567.34	C2	011565.40
	SIC,SEN		1310.00	80	011566.00
	B,SERS	-BITS IN IX 0.	1304.10	00	011566.40
	L%BU,64,8□,17.0		21.00	80 000000.20 50	011567.00
	BRZ,\$+1.32		11571.74	C2	011570.00
	SIC,SEN		1310.00	80	011570.40
	B,SERS	-BITS IN IX 1.	1304.10	00	011571.00
	L%BU,64,8□,18.0		22.00	80 000000.20 50	011571.40
	BRZ,\$+1.32		11574.34	C2	011572.40
	SIC,SEN		1310.00	80	011573.00
	B,SERS	-BITS IN IX 2.	1304.10	00	011573.40
	L%BU,64,8□,20.0		24.00	80 000000.20 50	011574.00
	BRZ,\$+1.32		11576.74	C2	011575.00
	SIC,SEN		1310.00	80	011575.40
	B,SERS	-BITS IN IX 4.	1304.10	00	011576.00
	L%BU,64,8□,21.0		25.00	80 000000.20 50	011576.40
	BRZ,\$+1.32		11601.34	C2	011577.40
	SIC,SEN		1310.00	80	011600.00
	B,SERS	-BITS IN IX 5.	1304.10	00	011600.40
	L%BU,64,8□,22.0		26.00	80 000000.20 50	011601.00
	BRZ,\$+1.32		11603.74	C2	011602.00
	SIC,SEN		1310.00	80	011602.40
	B,SERS	-BITS IN IX 6.	1304.10	00	011603.00
	L%BU,64,8□,23.0		27.00	80 000000.20 50	011603.40
	BRZ,\$+1.32		11606.34	C2	011604.40
	SIC,SEN		1310.00	80	011605.00
	B,SERS	-BITS IN IX 7.	1304.10	00	011605.40
	L%BU,64,8□,24.0		30.00	80 000000.20 50	011606.00
	BRZ,\$+1.32		11610.74	C2	011607.00
	SIC,SEN		1310.00	80	011607.40
	B,SERS	-BITS IN IX 8.	1304.10	00	011610.00
	L%BU,64,8□,25.0		31.00	80 000000.20 50	011610.40
	BRZ,\$+1.32		11613.34	C2	011611.40
	SIC,SEN		1310.00	80	011612.00
	B,SERS	-BITS IN IX 9.	1304.10	00	011612.40
	L%BU,64,8□,26.0		32.00	80 000000.20 50	011613.00
	BRZ,\$+1.32		11615.74	C2	011614.00
	SIC,SEN		1310.00	80	011614.40
	B,SERS	-BITS IN IX 10.	1304.10	00	011615.00
	L%BU,64,8□,27.0		33.00	80 000000.20 50	011615.40
	BRZ,\$+1.32		11620.34	C2	011616.40
	SIC,SEN		1310.00	80	011617.00
	B,SERS	-BITS IN IX 11.	1304.10	00	011617.40
	L%BU,64,8□,28.0		34.00	80 000000.20 50	011620.00
	BRZ,\$+1.32		11622.74	C2	011621.00
	SIC,SEN		1310.00	80	011621.40
	B,SERS	-BITS IN IX 12.	1304.10	00	011622.00
	L%BU,64,8□,29.0		35.00	80 000000.20 50	011622.40
	BRZ,\$+1.32		11625.34	C2	011623.40
	SIC,SEN		1310.00	80	011624.00
	B,SERS	-BITS IN IX 13.	1304.10	00	011624.40
	L%BU,64,8□,30.0		36.00	80 000000.20 50	011625.00
	BRZ,\$+1.32		11627.74	C2	011626.00
	SIC,SEN		1310.00	80	011626.40
	B,SERS	-BITS IN IX 14.	1304.10	00	011627.00
	L%BU,64,8□,31.0		37.00	80 000000.20 50	011627.40
	BRZ,\$+1.32		11632.34	C2	011630.40
	SIC,SEN		1310.00	80	011631.00

B,\$+1.0	B,SERS	-BITS IN IX 15.	1304.10 00	011631.40
BD,1084			11633.10 00	011632.00
SIC,SEN0+.32			11561.04 00	011632.40
B,SSW			1311.40 80	011633.00
BD,\$+.32		-TO SSIP	1301.10 00	011633.40
			11634.44 00	011634.00
LX,\$X13,IC208		-UPDATE CONTINUITY CHECK.	12675.32 10	011634.40
V+,\$X13,BIT3			17331.32 80	011635.00
SX,\$X13,IC208			12675.33 10	011635.40

1085	SIC,108S17	-LOAD INDEX 4 AND CHK NO OTHERS	12757.00	80		011636.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00		011636.40
	LX,\$X4,1000		17307.10	10		011637.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	011637.40
	SIC,SEN		1310.00	80		011640.40
	BRZ,SERS	-NO BITS IN IX 4.	1304.34	C2		011641.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	011641.40
	BRZ,\$+1.32		11644.34	C2		011642.40
	SIC,SEN		1310.00	80		011643.00
	B,SERS	-BITS IN IX 0.	1304.10	00		011643.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	011644.00
	BRZ,\$+1.32		11646.74	C2		011645.00
	SIC,SEN		1310.00	80		011645.40
	B,SERS	-BITS IN IX 1.	1304.10	00		011646.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	011646.40
	BRZ,\$+1.32		11651.34	C2		011647.40
	SIC,SEN		1310.00	80		011650.00
	B,SERS	-BITS IN IX 2.	1304.10	00		011650.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	011651.00
	BRZ,\$+1.32		11653.74	C2		011652.00
	SIC,SEN		1310.00	80		011652.40
	B,SERS	-BITS IN IX 3.	1304.10	00		011653.00
	L%BU,64,8□,21.0		25.00	80	000000.20 50	011653.40
	BRZ,\$+1.32		11656.34	C2		011654.40
	SIC,SEN		1310.00	80		011655.00
	B,SERS	-BITS IN IX 5.	1304.10	00		011655.40
	L%BU,64,8□,22.0		26.00	80	000000.20 50	011656.00
	BRZ,\$+1.32		11660.74	C2		011657.00
	SIC,SEN		1310.00	80		011657.40
	B,SERS	-BITS IN IX 6.	1304.10	00		011660.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	011660.40
	BRZ,\$+1.32		11663.34	C2		011661.40
	SIC,SEN		1310.00	80		011662.00
	B,SERS	-BITS IN IX 7.	1304.10	00		011662.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	011663.00
	BRZ,\$+1.32		11665.74	C2		011664.00
	SIC,SEN		1310.00	80		011664.40
	B,SERS	-BITS IN IX 8.	1304.10	00		011665.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	011665.40
	BRZ,\$+1.32		11670.34	C2		011666.40
	SIC,SEN		1310.00	80		011667.00
	B,SERS	-BITS IN IX 9.	1304.10	00		011667.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	011670.00
	BRZ,\$+1.32		11672.74	C2		011671.00
	SIC,SEN		1310.00	80		011671.40
	B,SERS	-BITS IN IX 10.	1304.10	00		011672.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	011672.40
	BRZ,\$+1.32		11675.34	C2		011673.40
	SIC,SEN		1310.00	80		011674.00
	B,SERS	-BITS IN IX 11.	1304.10	00		011674.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	011675.00
	BRZ,\$+1.32		11677.74	C2		011676.00
	SIC,SEN		1310.00	80		011676.40
	B,SERS	-BITS IN IX 12.	1304.10	00		011677.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	011677.40
	BRZ,\$+1.32		11702.34	C2		011700.40
	SIC,SEN		1310.00	80		011701.00
	B,SERS	-BITS IN IX 13.	1304.10	00		011701.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	011702.00
	BRZ,\$+1.32		11704.74	C2		011703.00
	SIC,SEN		1310.00	80		011703.40
	B,SERS	-BITS IN IX 14.	1304.10	00		011704.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	011704.40
	BRZ,\$+1.32		11707.34	C2		011705.40
	SIC,SEN		1310.00	80		011706.00

B,SERS	-BITS IN IX 15.	1304.10 00	011706.40
B,\$+1.0		11710.10 00	011707.00
BD,1085		11636.04 00	011707.40
SIC,SEN0+.32		1311.40 80	011710.00
B,SSW	-TO SSIP	1301.10 00	011710.40
BD,\$+.32		11711.44 00	011711.00
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LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	011711.40
V+,\$X13,BIT4		17332.32 B0	011712.00
SX,\$X13,IC208		12675.33 10	011712.40

1086	SIC,108S17	-LOAD INDEX 5 AND CHK NO OTHERS	12757.00	80		011713.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		011713.40
	LX,\$X5,1000		17307.12	10		011714.00
	L%BU,64,8□,21.0		25.00	80	000000.20 50	011714.40
	SIC,SEN		1310.00	80		011715.40
	BRZ,SERS	-NO BITS IN IX 5.	1304.34	C2		011716.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	011716.40
	BRZ,\$+1.32		11721.34	C2		011717.40
	SIC,SEN		1310.00	80		011720.00
	B,SERS	-BITS IN IX 0.	1304.10	00		011720.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	011721.00
	BRZ,\$+1.32		11723.74	C2		011722.00
	SIC,SEN		1310.00	80		011722.40
	B,SERS	-BITS IN IX 1.	1304.10	00		011723.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	011723.40
	BRZ,\$+1.32		11726.34	C2		011724.40
	SIC,SEN		1310.00	80		011725.00
	B,SERS	-BITS IN IX 2.	1304.10	00		011725.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	011726.00
	BRZ,\$+1.32		11730.74	C2		011727.00
	SIC,SEN		1310.00	80		011727.40
	B,SERS	-BITS IN IX 3.	1304.10	00		011730.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	011730.40
	BRZ,\$+1.32		11733.34	C2		011731.40
	SIC,SEN		1310.00	80		011732.00
	B,SERS	-BITS IN IX 4.	1304.10	00		011732.40
	L%BU,64,8□,22.0		26.00	80	000000.20 50	011733.00
	BRZ,\$+1.32		11735.74	C2		011734.00
	SIC,SEN		1310.00	80		011734.40
	B,SERS	-BITS IN IX 6.	1304.10	00		011735.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	011735.40
	BRZ,\$+1.32		11740.34	C2		011736.40
	SIC,SEN		1310.00	80		011737.00
	B,SERS	-BITS IN IX 7.	1304.10	00		011737.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	011740.00
	BRZ,\$+1.32		11742.74	C2		011741.00
	SIC,SEN		1310.00	80		011741.40
	B,SERS	-BITS IN IX 8.	1304.10	00		011742.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	011742.40
	BRZ,\$+1.32		11745.34	C2		011743.40
	SIC,SEN		1310.00	80		011744.00
	B,SERS	-BITS IN IX 9.	1304.10	00		011744.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	011745.00
	BRZ,\$+1.32		11747.74	C2		011746.00
	SIC,SEN		1310.00	80		011746.40
	B,SERS	-BITS IN IX 10.	1304.10	00		011747.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	011747.40
	BRZ,\$+1.32		11752.34	C2		011750.40
	SIC,SEN		1310.00	80		011751.00
	B,SERS	-BITS IN IX 11.	1304.10	00		011751.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	011752.00
	BRZ,\$+1.32		11754.74	C2		011753.00
	SIC,SEN		1310.00	80		011753.40
	B,SERS	-BITS IN IX 12.	1304.10	00		011754.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	011754.40
	BRZ,\$+1.32		11757.34	C2		011755.40
	SIC,SEN		1310.00	80		011756.00
	B,SERS	-BITS IN IX 13.	1304.10	00		011756.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	011757.00
	BRZ,\$+1.32		11761.74	C2		011760.00
	SIC,SEN		1310.00	80		011760.40
	B,SERS	-BITS IN IX 14.	1304.10	00		011761.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	011761.40
	BRZ,\$+1.32		11764.34	C2		011762.40
	SIC,SEN		1310.00	80		011763.00

B,SERS	-BITS IN IX 15.	1304.10 00	011763.40
B,\$+1.0		11765.10 00	011764.00
BD,1086		11713.04 00	011764.40
SIC,SENO+.32		1311.40 80	011765.00
B,SSW	-TO SSIP	1301.10 00	011765.40
BD,\$+.32		11766.44 00	011766.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	011766.40
V+,\$X13,BIT5		17333.32 80	011767.00
SX,\$X13,IC208		12675.33 10	011767.40

1087	SIC,108517	-LOAD INDEX 6 AND CHK NO OTHERS	12757.00	80		011770.00
	B,10851	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00		011770.40
	LX,SX6,1000		17307.14	10		011771.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	011771.40
	SIC,SEN		1310.00	80		011772.40
	BRZ,SERS	-NO BITS IN IX 6.	1304.34	C2		011773.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	011773.40
	BRZ,S+1.32		11776.34	C2		011774.40
	SIC,SEN		1310.00	80		011775.00
	B,SERS	-BITS IN IX 0.	1304.10	00		011775.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	011776.00
	BRZ,S+1.32		12000.74	C2		011777.00
	SIC,SEN		1310.00	80		011777.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012000.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012000.40
	BRZ,S+1.32		12003.34	C2		012001.40
	SIC,SEN		1310.00	80		012002.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012002.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012003.00
	BRZ,S+1.32		12005.74	C2		012004.00
	SIC,SEN		1310.00	80		012004.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012005.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012005.40
	BRZ,S+1.32		12010.34	C2		012006.40
	SIC,SEN		1310.00	80		012007.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012007.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012010.00
	BRZ,S+1.32		12012.74	C2		012011.00
	SIC,SEN		1310.00	80		012011.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012012.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012012.40
	BRZ,S+1.32		12015.34	C2		012013.40
	SIC,SEN		1310.00	80		012014.00
	B,SERS	-BITS IN IX 7.	1304.10	00		012014.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012015.00
	BRZ,S+1.32		12017.74	C2		012016.00
	SIC,SEN		1310.00	80		012016.40
	B,SERS	-BITS IN IX 8.	1304.10	00		012017.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012017.40
	BRZ,S+1.32		12022.34	C2		012020.40
	SIC,SEN		1310.00	80		012021.00
	B,SERS	-BITS IN IX 9.	1304.10	00		012021.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012022.00
	BRZ,S+1.32		12024.74	C2		012023.00
	SIC,SEN		1310.00	80		012023.40
	B,SERS	-BITS IN IX 10.	1304.10	00		012024.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012024.40
	BRZ,S+1.32		12027.34	C2		012025.40
	SIC,SEN		1310.00	80		012026.00
	B,SERS	-BITS IN IX 11.	1304.10	00		012026.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012027.00
	BRZ,S+1.32		12031.74	C2		012030.00
	SIC,SEN		1310.00	80		012030.40
	B,SERS	-BITS IN IX 12.	1304.10	00		012031.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012031.40
	BRZ,S+1.32		12034.34	C2		012032.40
	SIC,SEN		1310.00	80		012033.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012033.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012034.00
	BRZ,S+1.32		12036.74	C2		012035.00
	SIC,SEN		1310.00	80		012035.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012036.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012036.40
	BRZ,S+1.32		12041.34	C2		012037.40
	SIC,SEN		1310.00	80		012040.00

B,S+1.0	B,SERS	-BITS IN IX 15.	1304.10 00	012040.40
BD,I087			12042.10 00	012041.00
SIC,SEN0+.32			11770.04 00	012041.40
B,SSW			1311.40 80	012042.00
BD,S+.32		-TO SSIP	1301.10 00	012042.40
			12043.44 00	012043.00
LX,\$X13,IC208		-UPDATE CONTINUITY CHECK.	12675.32 10	012043.40
V+,\$X13,BIT6			17334.32 80	012044.00
SX,\$X13,IC208			12675.33 10	012044.40

1088	SIC,108S17	-LOAD INDEX 7 AND CHK NO OTHERS	12757.00	80		012045.00
	B,108S1	-GET DATA. CLR ALL IX REGS FIRST.	12677.10	00		012045.40
	LX,\$X7,1000		17307.16	10		012046.00
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012046.40
	SIC,SEN		1310.00	80		012047.40
	BRZ,SERS	-NO BITS IN IX 7.	1304.34	C2		012050.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012050.40
	BRZ,\$+1.32		12053.34	C2		012051.40
	SIC,SEN		1310.00	80		012052.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012052.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012053.00
	BRZ,\$+1.32		12055.74	C2		012054.00
	SIC,SEN		1310.00	80		012054.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012055.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012055.40
	BRZ,\$+1.32		12060.34	C2		012056.40
	SIC,SEN		1310.00	80		012057.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012057.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012060.00
	BRZ,\$+1.32		12062.74	C2		012061.00
	SIC,SEN		1310.00	80		012061.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012062.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012062.40
	BRZ,\$+1.32		12065.34	C2		012063.40
	SIC,SEN		1310.00	80		012064.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012064.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012065.00
	BRZ,\$+1.32		12067.74	C2		012066.00
	SIC,SEN		1310.00	80		012066.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012067.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012067.40
	BRZ,\$+1.32		12072.34	C2		012070.40
	SIC,SEN		1310.00	80		012071.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012071.40
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012072.00
	BRZ,\$+1.32		12074.74	C2		012073.00
	SIC,SEN		1310.00	80		012073.40
	B,SERS	-BITS IN IX 8.	1304.10	00		012074.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012074.40
	BRZ,\$+1.32		12077.34	C2		012075.40
	SIC,SEN		1310.00	80		012076.00
	B,SERS	-BITS IN IX 9.	1304.10	00		012076.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012077.00
	BRZ,\$+1.32		12101.74	C2		012100.00
	SIC,SEN		1310.00	80		012100.40
	B,SERS	-BITS IN IX 10.	1304.10	00		012101.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012101.40
	BRZ,\$+1.32		12104.34	C2		012102.40
	SIC,SEN		1310.00	80		012103.00
	B,SERS	-BITS IN IX 11.	1304.10	00		012103.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012104.00
	BRZ,\$+1.32		12106.74	C2		012105.00
	SIC,SEN		1310.00	80		012105.40
	B,SERS	-BITS IN IX 12.	1304.10	00		012106.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012106.40
	BRZ,\$+1.32		12111.34	C2		012107.40
	SIC,SEN		1310.00	80		012110.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012110.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012111.00
	BRZ,\$+1.32		12113.74	C2		012112.00
	SIC,SEN		1310.00	80		012112.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012113.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012113.40
	BRZ,\$+1.32		12116.34	C2		012114.40
	SIC,SEN		1310.00	80		012115.00

	B,SERS	-BITS IN IX 15.	1304.10 00	012115.40
	B,\$+1.0		12117.10 00	012116.00
	BD,1088		12045.04 00	012116.40
	SIC,SEN0+.32		1311.40 80	012117.00
	B,SSW	-TO SSIP	1301.10 00	012117.40
	BD,\$+.32		12120.44 00	012120.00
	LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012120.40
	V+,\$X13,BIT7		17335.32 B0	012121.00
	SX,\$X13,IC208		12675.33 10	012121.40

1089	SIC,108S17	-LOAD INDEX 8 AND CHK NO OTHERS	12757.00	80	012122.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00	012122.40
	LX,\$X8,1000		17307.20	10	012123.00
	L%BU,64,8□,24.0		30.00	80 000000.20 50	012123.40
	SIC,SEN		1310.00	80	012124.40
	BRZ,SERS	-NO BITS IN IX 8.	1304.34	C2	012125.00
	L%BU,64,8□,16.0		20.00	80 000000.20 50	012125.40
	BRZ,\$+1.32		12130.34	C2	012126.40
	SIC,SEN		1310.00	80	012127.00
	B,SERS	-BITS IN IX 0.	1304.10	00	012127.40
	L%BU,64,8□,17.0		21.00	80 000000.20 50	012130.00
	BRZ,\$+1.32		12132.74	C2	012131.00
	SIC,SEN		1310.00	80	012131.40
	B,SERS	-BITS IN IX 1.	1304.10	00	012132.00
	L%BU,64,8□,18.0		22.00	80 000000.20 50	012132.40
	BRZ,\$+1.32		12135.34	C2	012133.40
	SIC,SEN		1310.00	80	012134.00
	B,SERS	-BITS IN IX 2.	1304.10	00	012134.40
	L%BU,64,8□,19.0		23.00	80 000000.20 50	012135.00
	BRZ,\$+1.32		12137.74	C2	012136.00
	SIC,SEN		1310.00	80	012136.40
	B,SERS	-BITS IN IX 3.	1304.10	00	012137.00
	L%BU,64,8□,20.0		24.00	80 000000.20 50	012137.40
	BRZ,\$+1.32		12142.34	C2	012140.40
	SIC,SEN		1310.00	80	012141.00
	B,SERS	-BITS IN IX 4.	1304.10	00	012141.40
	L%BU,64,8□,21.0		25.00	80 000000.20 50	012142.00
	BRZ,\$+1.32		12144.74	C2	012143.00
	SIC,SEN		1310.00	80	012143.40
	B,SERS	-BITS IN IX 5.	1304.10	00	012144.00
	L%BU,64,8□,22.0		26.00	80 000000.20 50	012144.40
	BRZ,\$+1.32		12147.34	C2	012145.40
	SIC,SEN		1310.00	80	012146.00
	B,SERS	-BITS IN IX 6.	1304.10	00	012146.40
	L%BU,64,8□,23.0		27.00	80 000000.20 50	012147.00
	BRZ,\$+1.32		12151.74	C2	012150.00
	SIC,SEN		1310.00	80	012150.40
	B,SERS	-BITS IN IX 7.	1304.10	00	012151.00
	L%BU,64,8□,25.0		31.00	80 000000.20 50	012151.40
	BRZ,\$+1.32		12154.34	C2	012152.40
	SIC,SEN		1310.00	80	012153.00
	B,SERS	-BITS IN IX 9.	1304.10	00	012153.40
	L%BU,64,8□,26.0		32.00	80 000000.20 50	012154.00
	BRZ,\$+1.32		12156.74	C2	012155.00
	SIC,SEN		1310.00	80	012155.40
	B,SERS	-BITS IN IX 10.	1304.10	00	012156.00
	L%BU,64,8□,27.0		33.00	80 000000.20 50	012156.40
	BRZ,\$+1.32		12161.34	C2	012157.40
	SIC,SEN		1310.00	80	012160.00
	B,SERS	-BITS IN IX 11.	1304.10	00	012160.40
	L%BU,64,8□,28.0		34.00	80 000000.20 50	012161.00
	BRZ,\$+1.32		12163.74	C2	012162.00
	SIC,SEN		1310.00	80	012162.40
	B,SERS	-BITS IN IX 12.	1304.10	00	012163.00
	L%BU,64,8□,29.0		35.00	80 000000.20 50	012163.40
	BRZ,\$+1.32		12166.34	C2	012164.40
	SIC,SEN		1310.00	80	012165.00
	B,SERS	-BITS IN IX 13.	1304.10	00	012165.40
	L%BU,64,8□,30.0		36.00	80 000000.20 50	012166.00
	BRZ,\$+1.32		12170.74	C2	012167.00
	SIC,SEN		1310.00	80	012167.40
	B,SERS	-BITS IN IX 14.	1304.10	00	012170.00
	L%BU,64,8□,31.0		37.00	80 000000.20 50	012170.40
	BRZ,\$+1.32		12173.34	C2	012171.40
	SIC,SEN		1310.00	80	012172.00

B,SERS	-BITS IN IX 15.	1304.10 00	012172.40
B,\$+1.0		12174.10 00	012173.00
BD,I089		12122.04 00	012173.40
SIC,SENO+.32		1311.40 80	012174.00
B,SSW	-TO SSIP	1301.10 00	012174.40
BD,\$+.32		12175.44 00	012175.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012175.40
V+,\$X13,BIT8		17336.32 80	012176.00
SX,\$X13,IC208		12675.33 10	012176.40

10810	SIC,108S17	-LOAD INDEX 9 AND CHK NO OTHERS	12757.00	80		012177.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		012177.40
	LX,\$X9,1000		17307.22	10		012200.00
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012200.40
	SIC,SEN		1310.00	80		012201.40
	BRZ,SERS	-NO BITS IN IX 9.	1304.34	C2		012202.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012202.40
	BRZ,\$+1.32		12205.34	C2		012203.40
	SIC,SEN		1310.00	80		012204.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012204.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012205.00
	BRZ,\$+1.32		12207.74	C2		012206.00
	SIC,SEN		1310.00	80		012206.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012207.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012207.40
	BRZ,\$+1.32		12212.34	C2		012210.40
	SIC,SEN		1310.00	80		012211.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012211.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012212.00
	BRZ,\$+1.32		12214.74	C2		012213.00
	SIC,SEN		1310.00	80		012213.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012214.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012214.40
	BRZ,\$+1.32		12217.34	C2		012215.40
	SIC,SEN		1310.00	80		012216.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012216.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012217.00
	BRZ,\$+1.32		12221.74	C2		012220.00
	SIC,SEN		1310.00	80		012220.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012221.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012221.40
	BRZ,\$+1.32		12224.34	C2		012222.40
	SIC,SEN		1310.00	80		012223.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012223.40
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012224.00
	BRZ,\$+1.32		12226.74	C2		012225.00
	SIC,SEN		1310.00	80		012225.40
	B,SERS	-BITS IN IX 7.	1304.10	00		012226.00
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012226.40
	BRZ,\$+1.32		12231.34	C2		012227.40
	SIC,SEN		1310.00	80		012230.00
	B,SERS	-BITS IN IX 8.	1304.10	00		012230.40
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012231.00
	BRZ,\$+1.32		12233.74	C2		012232.00
	SIC,SEN		1310.00	80		012232.40
	B,SERS	-BITS IN IX 10.	1304.10	00		012233.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012233.40
	BRZ,\$+1.32		12236.34	C2		012234.40
	SIC,SEN		1310.00	80		012235.00
	B,SERS	-BITS IN IX 11.	1304.10	00		012235.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012236.00
	BRZ,\$+1.32		12240.74	C2		012237.00
	SIC,SEN		1310.00	80		012237.40
	B,SERS	-BITS IN IX 12.	1304.10	00		012240.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012240.40
	BRZ,\$+1.32		12243.34	C2		012241.40
	SIC,SEN		1310.00	80		012242.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012242.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012243.00
	BRZ,\$+1.32		12245.74	C2		012244.00
	SIC,SEN		1310.00	80		012244.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012245.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012245.40
	BRZ,\$+1.32		12250.34	C2		012246.40
	SIC,SEN		1310.00	80		012247.00

B,SERS	-BITS IN IX 15.	1304.10 00	012247.40
B,\$+1.0		12251.10 00	012250.00
BD,I0810		12177.04 00	012250.40
SIC,SEN0+.32		1311.40 80	012251.00
B,SSW	-TO SSIP	1301.10 00	012251.40
BD,\$+.32		12252.44 00	012252.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012252.40
V+,\$X13,BIT9		17337.32 80	012253.00
SX,\$X13,IC208		12675.33 10	012253.40

10811	SIC,108S17	-LOAD INDEX 10 AND CHK NO OTHERS	12757.00	80		012254.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		012254.40
	LX,\$X10,1000		17307.24	10		012255.00
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012255.40
	SIC,SEN		1310.00	80		012256.40
	BRZ,SERS	-NO BITS IN IX 10.	1304.34	C2		012257.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012257.40
	BRZ,\$+1.32		12262.34	C2		012260.40
	SIC,SEN		1310.00	80		012261.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012261.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012262.00
	BRZ,\$+1.32		12264.74	C2		012263.00
	SIC,SEN		1310.00	80		012263.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012264.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012264.40
	BRZ,\$+1.32		12267.34	C2		012265.40
	SIC,SEN		1310.00	80		012266.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012266.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012267.00
	BRZ,\$+1.32		12271.74	C2		012270.00
	SIC,SEN		1310.00	80		012270.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012271.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012271.40
	BRZ,\$+1.32		12274.34	C2		012272.40
	SIC,SEN		1310.00	80		012273.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012273.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012274.00
	BRZ,\$+1.32		12276.74	C2		012275.00
	SIC,SEN		1310.00	80		012275.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012276.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012276.40
	BRZ,\$+1.32		12301.34	C2		012277.40
	SIC,SEN		1310.00	80		012300.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012300.40
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012301.00
	BRZ,\$+1.32		12303.74	C2		012302.00
	SIC,SEN		1310.00	80		012302.40
	B,SERS	-BITS IN IX 7.	1304.10	00		012303.00
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012303.40
	BRZ,\$+1.32		12306.34	C2		012304.40
	SIC,SEN		1310.00	80		012305.00
	B,SERS	-BITS IN IX 8.	1304.10	00		012305.40
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012306.00
	BRZ,\$+1.32		12310.74	C2		012307.00
	SIC,SEN		1310.00	80		012307.40
	B,SERS	-BITS IN IX 9.	1304.10	00		012310.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012310.40
	BRZ,\$+1.32		12313.34	C2		012311.40
	SIC,SEN		1310.00	80		012312.00
	B,SERS	-BITS IN IX 11.	1304.10	00		012312.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012313.00
	BRZ,\$+1.32		12315.74	C2		012314.00
	SIC,SEN		1310.00	80		012314.40
	B,SERS	-BITS IN IX 12.	1304.10	00		012315.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012315.40
	BRZ,\$+1.32		12320.34	C2		012316.40
	SIC,SEN		1310.00	80		012317.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012317.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012320.00
	BRZ,\$+1.32		12322.74	C2		012321.00
	SIC,SEN		1310.00	80		012321.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012322.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012322.40
	BRZ,\$+1.32		12325.34	C2		012323.40
	SIC,SEN		1310.00	80		012324.00

B,SERS	-BITS IN IX 15.	1304.10 00	012324.40
B,\$+1.0		12326.10 00	012325.00
BD,I0811		12254.04 00	012325.40
SIC,SENO+.32		1311.40 80	012326.00
B,SSW	-TO SSIP	1301.10 00	012326.40
BD,\$+.32		12327.44 00	012327.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012327.40
V+,\$X13,BIT10		17340.32 80	012330.00
SX,\$X13,IC208		12675.33 10	012330.40

10812	SIC,108S17	-LOAD INDEX 11 AND CHK NO OTHERS	12757.00	80		012331.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		012331.40
	LX,\$X11,1000		17307.26	10		012332.00
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012332.40
	SIC,SEN		1310.00	80		012333.40
	BRZ,SERS	-NO BITS IN IX 11.	1304.34	C2		012334.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012334.40
	BRZ,\$+1.32		12337.34	C2		012335.40
	SIC,SEN		1310.00	80		012336.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012336.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012337.00
	BRZ,\$+1.32		12341.74	C2		012340.00
	SIC,SEN		1310.00	80		012340.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012341.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012341.40
	BRZ,\$+1.32		12344.34	C2		012342.40
	SIC,SEN		1310.00	80		012343.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012343.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012344.00
	BRZ,\$+1.32		12346.74	C2		012345.00
	SIC,SEN		1310.00	80		012345.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012346.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012346.40
	BRZ,\$+1.32		12351.34	C2		012347.40
	SIC,SEN		1310.00	80		012350.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012350.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012351.00
	BRZ,\$+1.32		12353.74	C2		012352.00
	SIC,SEN		1310.00	80		012352.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012353.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012353.40
	BRZ,\$+1.32		12356.34	C2		012354.40
	SIC,SEN		1310.00	80		012355.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012355.40
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012356.00
	BRZ,\$+1.32		12360.74	C2		012357.00
	SIC,SEN		1310.00	80		012357.40
	B,SERS	-BITS IN IX 7.	1304.10	00		012360.00
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012360.40
	BRZ,\$+1.32		12363.34	C2		012361.40
	SIC,SEN		1310.00	80		012362.00
	B,SERS	-BITS IN IX 8.	1304.10	00		012362.40
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012363.00
	BRZ,\$+1.32		12365.74	C2		012364.00
	SIC,SEN		1310.00	80		012364.40
	B,SERS	-BITS IN IX 9.	1304.10	00		012365.00
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012365.40
	BRZ,\$+1.32		12370.34	C2		012366.40
	SIC,SEN		1310.00	80		012367.00
	B,SERS	-BITS IN IX 10.	1304.10	00		012367.40
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012370.00
	BRZ,\$+1.32		12372.74	C2		012371.00
	SIC,SEN		1310.00	80		012371.40
	B,SERS	-BITS IN IX 12.	1304.10	00		012372.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012372.40
	BRZ,\$+1.32		12375.34	C2		012373.40
	SIC,SEN		1310.00	80		012374.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012374.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012375.00
	BRZ,\$+1.32		12377.74	C2		012376.00
	SIC,SEN		1310.00	80		012376.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012377.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012377.40
	BRZ,\$+1.32		12402.34	C2		012400.40
	SIC,SEN		1310.00	80		012401.00

B,SERS	-BITS IN IX 15.	1304.10 00	012401.40
B,\$+1.0		12403.10 00	012402.00
BD,I0812		12331.04 00	012402.40
SIC,SENO+.32		1311.40 80	012403.00
B,SSW	-TO SSIP	1301.10 00	012403.40
BD,\$+.32		12404.44 00	012404.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012404.40
V+,\$X13,BIT11		17341.32 80	012405.00
SX,\$X13,IC208		12675.33 10	012405.40

10813	SIC,108S17	-LOAD INDEX 12 AND CHK NO OTHERS	12757.00	80		012406.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		012406.40
	LX,\$X12,1000		17307.30	10		012407.00
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012407.40
	SIC,SEN		1310.00	80		012410.40
	BRZ,SERS	-NO BITS IN IX 12.	1304.34	C2		012411.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012411.40
	BRZ,\$+1.32		12414.34	C2		012412.40
	SIC,SEN		1310.00	80		012413.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012413.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012414.00
	BRZ,\$+1.32		12416.74	C2		012415.00
	SIC,SEN		1310.00	80		012415.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012416.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012416.40
	BRZ,\$+1.32		12421.34	C2		012417.40
	SIC,SEN		1310.00	80		012420.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012420.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012421.00
	BRZ,\$+1.32		12423.74	C2		012422.00
	SIC,SEN		1310.00	80		012422.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012423.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012423.40
	BRZ,\$+1.32		12426.34	C2		012424.40
	SIC,SEN		1310.00	80		012425.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012425.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012426.00
	BRZ,\$+1.32		12430.74	C2		012427.00
	SIC,SEN		1310.00	80		012427.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012430.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012430.40
	BRZ,\$+1.32		12433.34	C2		012431.40
	SIC,SEN		1310.00	80		012432.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012432.40
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012433.00
	BRZ,\$+1.32		12435.74	C2		012434.00
	SIC,SEN		1310.00	80		012434.40
	B,SERS	-BITS IN IX 7.	1304.10	00		012435.00
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012435.40
	BRZ,\$+1.32		12440.34	C2		012436.40
	SIC,SEN		1310.00	80		012437.00
	B,SERS	-BITS IN IX 8.	1304.10	00		012437.40
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012440.00
	BRZ,\$+1.32		12442.74	C2		012441.00
	SIC,SEN		1310.00	80		012441.40
	B,SERS	-BITS IN IX 9.	1304.10	00		012442.00
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012442.40
	BRZ,\$+1.32		12445.34	C2		012443.40
	SIC,SEN		1310.00	80		012444.00
	B,SERS	-BITS IN IX 10.	1304.10	00		012444.40
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012445.00
	BRZ,\$+1.32		12447.74	C2		012446.00
	SIC,SEN		1310.00	80		012446.40
	B,SERS	-BITS IN IX 11.	1304.10	00		012447.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012447.40
	BRZ,\$+1.32		12452.34	C2		012450.40
	SIC,SEN		1310.00	80		012451.00
	B,SERS	-BITS IN IX 13.	1304.10	00		012451.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012452.00
	BRZ,\$+1.32		12454.74	C2		012453.00
	SIC,SEN		1310.00	80		012453.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012454.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012454.40
	BRZ,\$+1.32		12457.34	C2		012455.40
	SIC,SEN		1310.00	80		012456.00

B,SERS	-BITS IN IX 15.	1304.10 00	012456.40
B,\$+1.0		12460.10 00	012457.00
BD,I0813		12406.04 00	012457.40
SIC,SENO+.32		1311.40 80	012460.00
B,SSW	-TO SSIP	1301.10 00	012460.40
BD,\$+.32		12461.44 00	012461.00
LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012461.40
V+,\$X13,BIT12		17342.32 B0	012462.00
SX,\$X13,IC208		12675.33 10	012462.40

10814	SIC,108S17	-LOAD INDEX 13 AND CHK NO OTHERS	12757.00	80		012463.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00		012463.40
	LX,\$X13,1000		17307.32	10		012464.00
	L%BU,64,8□,29.0		35.00	80	000000.20 50	012464.40
	SIC,SEN		1310.00	80		012465.40
	BRZ,SERS	-NO BITS IN IX 13.	1304.34	C2		012466.00
	L%BU,64,8□,16.0		20.00	80	000000.20 50	012466.40
	BRZ,\$+1.32		12471.34	C2		012467.40
	SIC,SEN		1310.00	80		012470.00
	B,SERS	-BITS IN IX 0.	1304.10	00		012470.40
	L%BU,64,8□,17.0		21.00	80	000000.20 50	012471.00
	BRZ,\$+1.32		12473.74	C2		012472.00
	SIC,SEN		1310.00	80		012472.40
	B,SERS	-BITS IN IX 1.	1304.10	00		012473.00
	L%BU,64,8□,18.0		22.00	80	000000.20 50	012473.40
	BRZ,\$+1.32		12476.34	C2		012474.40
	SIC,SEN		1310.00	80		012475.00
	B,SERS	-BITS IN IX 2.	1304.10	00		012475.40
	L%BU,64,8□,19.0		23.00	80	000000.20 50	012476.00
	BRZ,\$+1.32		12500.74	C2		012477.00
	SIC,SEN		1310.00	80		012477.40
	B,SERS	-BITS IN IX 3.	1304.10	00		012500.00
	L%BU,64,8□,20.0		24.00	80	000000.20 50	012500.40
	BRZ,\$+1.32		12503.34	C2		012501.40
	SIC,SEN		1310.00	80		012502.00
	B,SERS	-BITS IN IX 4.	1304.10	00		012502.40
	L%BU,64,8□,21.0		25.00	80	000000.20 50	012503.00
	BRZ,\$+1.32		12505.74	C2		012504.00
	SIC,SEN		1310.00	80		012504.40
	B,SERS	-BITS IN IX 5.	1304.10	00		012505.00
	L%BU,64,8□,22.0		26.00	80	000000.20 50	012505.40
	BRZ,\$+1.32		12510.34	C2		012506.40
	SIC,SEN		1310.00	80		012507.00
	B,SERS	-BITS IN IX 6.	1304.10	00		012507.40
	L%BU,64,8□,23.0		27.00	80	000000.20 50	012510.00
	BRZ,\$+1.32		12512.74	C2		012511.00
	SIC,SEN		1310.00	80		012511.40
	B,SERS	-BITS IN IX 7.	1304.10	00		012512.00
	L%BU,64,8□,24.0		30.00	80	000000.20 50	012512.40
	BRZ,\$+1.32		12515.34	C2		012513.40
	SIC,SEN		1310.00	80		012514.00
	B,SERS	-BITS IN IX 8.	1304.10	00		012514.40
	L%BU,64,8□,25.0		31.00	80	000000.20 50	012515.00
	BRZ,\$+1.32		12517.74	C2		012516.00
	SIC,SEN		1310.00	80		012516.40
	B,SERS	-BITS IN IX 9.	1304.10	00		012517.00
	L%BU,64,8□,26.0		32.00	80	000000.20 50	012517.40
	BRZ,\$+1.32		12522.34	C2		012520.40
	SIC,SEN		1310.00	80		012521.00
	B,SERS	-BITS IN IX 10.	1304.10	00		012521.40
	L%BU,64,8□,27.0		33.00	80	000000.20 50	012522.00
	BRZ,\$+1.32		12524.74	C2		012523.00
	SIC,SEN		1310.00	80		012523.40
	B,SERS	-BITS IN IX 11.	1304.10	00		012524.00
	L%BU,64,8□,28.0		34.00	80	000000.20 50	012524.40
	BRZ,\$+1.32		12527.34	C2		012525.40
	SIC,SEN		1310.00	80		012526.00
	B,SERS	-BITS IN IX 12.	1304.10	00		012526.40
	L%BU,64,8□,30.0		36.00	80	000000.20 50	012527.00
	BRZ,\$+1.32		12531.74	C2		012530.00
	SIC,SEN		1310.00	80		012530.40
	B,SERS	-BITS IN IX 14.	1304.10	00		012531.00
	L%BU,64,8□,31.0		37.00	80	000000.20 50	012531.40
	BRZ,\$+1.32		12534.34	C2		012532.40
	SIC,SEN		1310.00	80		012533.00

B,\$+1.0	B,SEKS	-BITS IN TX 15.	12535.10 00	012535.40
BD,I0814			12463.04 00	012534.40
SIC,SENO+.32			1311.40 80	012535.00
B,SSW		-TO SSIP	1301.10 00	012535.40
BD,\$+.32			12536.44 00	012536.00
LX,\$X13,IC208		-UPDATE CONTINUITY CHECK.	12675.32 10	012536.40
V+,\$X13,BIT13			17343.32 80	012537.00
SX,\$X13,IC208			12675.33 10	012537.40

10815	SIC,108S17	-LOAD INDEX 14 AND CHK NO OTHERS	12757.00	80	012540.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00	012540.40
	LX,\$X14,1000		17307.34	10	012541.00
	L%BU,64,8□,30.0		36.00	80 000000.20 50	012541.40
	SIC,SEN		1310.00	80	012542.40
	BRZ,SERS	-NO BITS IN IX 14.	1304.34	C2	012543.00
	L%BU,64,8□,16.0		20.00	80 000000.20 50	012543.40
	BRZ,\$+1.32		12546.34	C2	012544.40
	SIC,SEN		1310.00	80	012545.00
	B,SERS	-BITS IN IX 0.	1304.10	00	012545.40
	L%BU,64,8□,17.0		21.00	80 000000.20 50	012546.00
	BRZ,\$+1.32		12550.74	C2	012547.00
	SIC,SEN		1310.00	80	012547.40
	B,SERS	-BITS IN IX 1.	1304.10	00	012550.00
	L%BU,64,8□,18.0		22.00	80 000000.20 50	012550.40
	BRZ,\$+1.32		12553.34	C2	012551.40
	SIC,SEN		1310.00	80	012552.00
	B,SERS	-BITS IN IX 2.	1304.10	00	012552.40
	L%BU,64,8□,19.0		23.00	80 000000.20 50	012553.00
	BRZ,\$+1.32		12555.74	C2	012554.00
	SIC,SEN		1310.00	80	012554.40
	B,SERS	-BITS IN IX 3.	1304.10	00	012555.00
	L%BU,64,8□,20.0		24.00	80 000000.20 50	012555.40
	BRZ,\$+1.32		12560.34	C2	012556.40
	SIC,SEN		1310.00	80	012557.00
	B,SERS	-BITS IN IX 4.	1304.10	00	012557.40
	L%BU,64,8□,21.0		25.00	80 000000.20 50	012560.00
	BRZ,\$+1.32		12562.74	C2	012561.00
	SIC,SEN		1310.00	80	012561.40
	B,SERS	-BITS IN IX 5.	1304.10	00	012562.00
	L%BU,64,8□,22.0		26.00	80 000000.20 50	012562.40
	BRZ,\$+1.32		12565.34	C2	012563.40
	SIC,SEN		1310.00	80	012564.00
	B,SERS	-BITS IN IX 6.	1304.10	00	012564.40
	L%BU,64,8□,23.0		27.00	80 000000.20 50	012565.00
	BRZ,\$+1.32		12567.74	C2	012566.00
	SIC,SEN		1310.00	80	012566.40
	B,SERS	-BITS IN IX 7.	1304.10	00	012567.00
	L%BU,64,8□,24.0		30.00	80 000000.20 50	012567.40
	BRZ,\$+1.32		12572.34	C2	012570.40
	SIC,SEN		1310.00	80	012571.00
	B,SERS	-BITS IN IX 8.	1304.10	00	012571.40
	L%BU,64,8□,25.0		31.00	80 000000.20 50	012572.00
	BRZ,\$+1.32		12574.74	C2	012573.00
	SIC,SEN		1310.00	80	012573.40
	B,SERS	-BITS IN IX 9.	1304.10	00	012574.00
	L%BU,64,8□,26.0		32.00	80 000000.20 50	012574.40
	BRZ,\$+1.32		12577.34	C2	012575.40
	SIC,SEN		1310.00	80	012576.00
	B,SERS	-BITS IN IX 10.	1304.10	00	012576.40
	L%BU,64,8□,27.0		33.00	80 000000.20 50	012577.00
	BRZ,\$+1.32		12601.74	C2	012600.00
	SIC,SEN		1310.00	80	012600.40
	B,SERS	-BITS IN IX 11.	1304.10	00	012601.00
	L%BU,64,8□,28.0		34.00	80 000000.20 50	012601.40
	BRZ,\$+1.32		12604.34	C2	012602.40
	SIC,SEN		1310.00	80	012603.00
	B,SERS	-BITS IN IX 12.	1304.10	00	012603.40
	L%BU,64,8□,29.0		35.00	80 000000.20 50	012604.00
	BRZ,\$+1.32		12606.74	C2	012605.00
	SIC,SEN		1310.00	80	012605.40
	B,SERS	-BITS IN IX 13.	1304.10	00	012606.00
	L%BU,64,8□,31.0		37.00	80 000000.20 50	012606.40
	BRZ,\$+1.32		12611.34	C2	012607.40
	SIC,SEN		1310.00	80	012610.00

B,\$+1.0	B,SERS	-BITS IN TX 15.	1304.10 00	012610.40
BD,I0815			12612.10 00	012611.00
SIC,SENO+.32			12540.04 00	012611.40
B,SSW		-TO SSIP	1311.40 80	012612.00
BD,\$+.32			1301.10 00	012612.40
			12613.44 00	012613.00
LX,\$X13,IC208		-UPDATE CONTINUITY CHECK.	12675.32 10	012613.40
V+,\$X13,BIT14			17344.32 80	012614.00
SX,\$X13,IC208			12675.33 10	012614.40

10816	SIC,108S17	-LOAD INDEX 15 AND CHK NO OTHERS	12757.00	80	012615.00
	B,108S1	-GET DATA.CLR ALL IX REGS FIRST.	12677.10	00	012615.40
	LX,\$X15,1000		17307.36	10	012616.00
	L%BU,64,8□,31.0		37.00	80 000000.20 50	012616.40
	SIC,SEN		1310.00	80	012617.40
	BRZ,SERS	-NO BITS IN IX 15.	1304.34	C2	012620.00
	L%BU,64,8□,16.0		20.00	80 000000.20 50	012620.40
	BRZ,\$+1.32		12623.34	C2	012621.40
	SIC,SEN		1310.00	80	012622.00
	B,SERS	-BITS IN IX 0.	1304.10	00	012622.40
	L%BU,64,8□,17.0		21.00	80 000000.20 50	012623.00
	BRZ,\$+1.32		12625.74	C2	012624.00
	SIC,SEN		1310.00	80	012624.40
	B,SERS	-BITS IN IX 1.	1304.10	00	012625.00
	L%BU,64,8□,18.0		22.00	80 000000.20 50	012625.40
	BRZ,\$+1.32		12630.34	C2	012626.40
	SIC,SEN		1310.00	80	012627.00
	B,SERS	-BITS IN IX 2.	1304.10	00	012627.40
	L%BU,64,8□,19.0		23.00	80 000000.20 50	012630.00
	BRZ,\$+1.32		12632.74	C2	012631.00
	SIC,SEN		1310.00	80	012631.40
	B,SERS	-BITS IN IX 3.	1304.10	00	012632.00
	L%BU,64,8□,20.0		24.00	80 000000.20 50	012632.40
	BRZ,\$+1.32		12635.34	C2	012633.40
	SIC,SEN		1310.00	80	012634.00
	B,SERS	-BITS IN IX 4.	1304.10	00	012634.40
	L%BU,64,8□,21.0		25.00	80 000000.20 50	012635.00
	BRZ,\$+1.32		12637.74	C2	012636.00
	SIC,SEN		1310.00	80	012636.40
	B,SERS	-BITS IN IX 5.	1304.10	00	012637.00
	L%BU,64,8□,22.0		26.00	80 000000.20 50	012637.40
	BRZ,\$+1.32		12642.34	C2	012640.40
	SIC,SEN		1310.00	80	012641.00
	B,SERS	-BITS IN IX 6.	1304.10	00	012641.40
	L%BU,64,8□,23.0		27.00	80 000000.20 50	012642.00
	BRZ,\$+1.32		12644.74	C2	012643.00
	SIC,SEN		1310.00	80	012643.40
	B,SERS	-BITS IN IX 7.	1304.10	00	012644.00
	L%BU,64,8□,24.0		30.00	80 000000.20 50	012644.40
	BRZ,\$+1.32		12647.34	C2	012645.40
	SIC,SEN		1310.00	80	012646.00
	B,SERS	-BITS IN IX 8.	1304.10	00	012646.40
	L%BU,64,8□,25.0		31.00	80 000000.20 50	012647.00
	BRZ,\$+1.32		12651.74	C2	012650.00
	SIC,SEN		1310.00	80	012650.40
	B,SERS	-BITS IN IX 9.	1304.10	00	012651.00
	L%BU,64,8□,26.0		32.00	80 000000.20 50	012651.40
	BRZ,\$+1.32		12654.34	C2	012652.40
	SIC,SEN		1310.00	80	012653.00
	B,SERS	-BITS IN IX 10.	1304.10	00	012653.40
	L%BU,64,8□,27.0		33.00	80 000000.20 50	012654.00
	BRZ,\$+1.32		12656.74	C2	012655.00
	SIC,SEN		1310.00	80	012655.40
	B,SERS	-BITS IN IX 11.	1304.10	00	012656.00
	L%BU,64,8□,28.0		34.00	80 000000.20 50	012656.40
	BRZ,\$+1.32		12661.34	C2	012657.40
	SIC,SEN		1310.00	80	012660.00
	B,SERS	-BITS IN IX 12.	1304.10	00	012660.40
	L%BU,64,8□,29.0		35.00	80 000000.20 50	012661.00
	BRZ,\$+1.32		12663.74	C2	012662.00
	SIC,SEN		1310.00	80	012662.40
	B,SERS	-BITS IN IX 13.	1304.10	00	012663.00
	L%BU,64,8□,30.0		36.00	80 000000.20 50	012663.40
	BRZ,\$+1.32		12666.34	C2	012664.40
	SIC,SEN		1310.00	80	012665.00

	B,SERS	-BITS IN TX 14.	1304.10 00	012665.40
	B,\$+1.0		12667.10 00	012666.00
	BD,10816		12615.04 00	012666.40
	SIC,SEN0+.32		1311.40 80	012667.00
	B,SSW	-TO SSIP	1301.10 00	012667.40
	BD,\$+.32		12670.44 00	012670.00
	LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012670.40
	V+,\$X13,BIT15		17345.32 B0	012671.00
	SX,\$X13,IC208		12675.33 10	012671.40
	LX,\$X13,IC208	-UPDATE CONTINUITY CHECK.	12675.32 10	012672.00
	KV,\$X13,ICK208		12676.32 90	012672.40
	SIC,SEN		1310.00 80	012673.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	012673.40
	B,110		12757.50 00	012674.00
	CNOP		0.30 00	012674.40
IC208	XW,0,0,0	-CONTINUITY REG 1208.	0.00 00 000000.00 00	012675.00
ICK208	XW,%8□777774.00,0,0		777774.00 00 000000.00 00	012676.00

10851	Z,16		-SUBROUTINE TO CLEAR ALL	20.22	00		012677.00
	L%BU,64,8□,16.0		-INDEX REGS AND CHECK SAME	20.00	80	000000.20 50	012677.40
	BRZ,10852			12702.34	C2		012700.40
	SIC,SEN			1310.00	80		012701.00
	B,SERS		-CANT CLR IX 0.	1304.10	00		012701.40
10852	Z,17			21.22	00		012702.00
	L%BU,64,8□,17.0			21.00	80	000000.20 50	012702.40
	BRZ,10853			12705.34	C2		012703.40
	SIC,SEN			1310.00	80		012704.00
	B,SERS		-CANT CLR IX 1.	1304.10	00		012704.40
10853	Z,18			22.22	00		012705.00
	L%BU,64,8□,18.0			22.00	80	000000.20 50	012705.40
	BRZ,10854			12710.34	C2		012706.40
	SIC,SEN			1310.00	80		012707.00
	B,SERS		-CANT CLR IX 2.	1304.10	00		012707.40
10854	Z,19			23.22	00		012710.00
	L%BU,64,8□,19.0			23.00	80	000000.20 50	012710.40
	BRZ,10855			12713.34	C2		012711.40
	SIC,SEN			1310.00	80		012712.00
	B,SERS		-CANT CLR IX 3.	1304.10	00		012712.40
10855	Z,20			24.22	00		012713.00
	L%BU,64,8□,20.0			24.00	80	000000.20 50	012713.40
	BRZ,10856			12716.34	C2		012714.40
	SIC,SEN			1310.00	80		012715.00
	B,SERS		-CANT CLR IX 4.	1304.10	00		012715.40
10856	Z,21			25.22	00		012716.00
	L%BU,64,8□,21.0			25.00	80	000000.20 50	012716.40
	BRZ,10857			12721.34	C2		012717.40
	SIC,SEN			1310.00	80		012720.00
	B,SERS		-CANT CLR IX 5.	1304.10	00		012720.40
10857	Z,22			26.22	00		012721.00
	L%BU,64,8□,22.0			26.00	80	000000.20 50	012721.40
	BRZ,10858			12724.34	C2		012722.40
	SIC,SEN			1310.00	80		012723.00
	B,SERS		-CANT CLR IX 6.	1304.10	00		012723.40
10858	Z,23			27.22	00		012724.00
	L%BU,64,8□,23.0			27.00	80	000000.20 50	012724.40
	BRZ,10859			12727.34	C2		012725.40
	SIC,SEN			1310.00	80		012726.00
	B,SERS		-CANT CLR IX 7.	1304.10	00		012726.40

10859	Z,24			30.22	00			012727.00
	L%BU,64,8□,24.0			30.00	80	000000.20	50	012727.40
	BRZ,108510			12732.34	C2			012730.40
	SIC,SEN			1310.00	80			012731.00
	B,SERS	-CANT CLR IX 8.		1304.10	00			012731.40
108510	Z,25			31.22	00			012732.00
	L%BU,64,8□,25.0			31.00	80	000000.20	50	012732.40
	BRZ,108511			12735.34	C2			012733.40
	SIC,SEN			1310.00	80			012734.00
	B,SERS	-CANT CLR IX 9.		1304.10	00			012734.40
108511	Z,26			32.22	00			012735.00
	L%BU,64,8□,26.0			32.00	80	000000.20	50	012735.40
	BRZ,108512			12740.34	C2			012736.40
	SIC,SEN			1310.00	80			012737.00
	B,SERS	-CANT CLR IX 10.		1304.10	00			012737.40
108512	Z,27			33.22	00			012740.00
	L%BU,64,8□,27.0			33.00	80	000000.20	50	012740.40
	BRZ,108513			12743.34	C2			012741.40
	SIC,SEN			1310.00	80			012742.00
	B,SERS	-CANT CLR IX 11.		1304.10	00			012742.40
108513	Z,28			34.22	00			012743.00
	L%BU,64,8□,28.0			34.00	80	000000.20	50	012743.40
	BRZ,108514			12746.34	C2			012744.40
	SIC,SEN			1310.00	80			012745.00
	B,SERS	-CANT CLR IX 12.		1304.10	00			012745.40
108514	Z,29			35.22	00			012746.00
	L%BU,64,8□,29.0			35.00	80	000000.20	50	012746.40
	BRZ,108515			12751.34	C2			012747.40
	SIC,SEN			1310.00	80			012750.00
	B,SERS	-CANT CLR IX 13.		1304.10	00			012750.40
108515	Z,30			36.22	00			012751.00
	L%BU,64,8□,30.0			36.00	80	000000.20	50	012751.40
	BRZ,108516			12754.34	C2			012752.40
	SIC,SEN			1310.00	80			012753.00
	B,SERS	-CANT CLR IX 14.		1304.10	00			012753.40
108516	Z,31			37.22	00			012754.00
	L%BU,64,8□,31.0			37.00	80	000000.20	50	012754.40
	BRZ,108517			12757.34	C2			012755.40
	SIC,SEN			1310.00	80			012756.00
	B,SERS	-CANT CLR IX 15.		1304.10	00			012756.40
108517	\$B,0			0.10	00			012757.00

110	LX,\$X1,I10ID		12763.02	10		012757.40
	SX,\$X1,DPET13		1437.03	10		012760.00
	SIC,RET		1306.40	80		012760.40
	B,IDF1		1443.10	00		012761.00
	Z,IC210		13427.22	00		012761.40
	BD,I101		12764.04	00		012762.00
	CNOP		0.30	00		012762.40
I10ID	%IQSZ=DD%BU,64,8,1210	Z				012763.00
1101	L%BU,BIT0	-CHK LX FROM INTERNAL MEMORY	17326.00	80	000000.20 50	012764.00
	LX,\$X0,\$R		11.00	10		012765.00
	KV,\$X0,BIT0		17326.00	90		012765.40
	BXE,\$+1.32		12767.72	C2		012766.00
	SIC,SEN		1310.00	80		012766.40
	B,SERS	-BIT 0 FRM RACC TO IX 0 FAILS.	1304.10	00		012767.00
	L%BU,BIT1		17327.00	80	000000.20 50	012767.40
	LX,\$X0,\$R		11.00	10		012770.40
	KV,\$X0,BIT1		17327.00	90		012771.00
	BXE,\$+1.32		12773.32	C2		012771.40
	SIC,SEN		1310.00	80		012772.00
	B,SERS	-BIT 1 FRM RACC TO IX 0 FAILS.	1304.10	00		012772.40
	L%BU,BIT2		17330.00	80	000000.20 50	012773.00
	LX,\$X0,\$R		11.00	10		012774.00
	KV,\$X0,BIT2		17330.00	90		012774.40
	BXE,\$+1.32		12776.72	C2		012775.00
	SIC,SEN		1310.00	80		012775.40
	B,SERS	-BIT 2 FRM RACC TO IX 0 FAILS.	1304.10	00		012776.00
	L%BU,BIT3		17331.00	80	000000.20 50	012776.40
	LX,\$X0,\$R		11.00	10		012777.40
	KV,\$X0,BIT3		17331.00	90		013000.00
	BXE,\$+1.32		13002.32	C2		013000.40
	SIC,SEN		1310.00	80		013001.00
	B,SERS	-BIT 3 FRM RACC TO IX 0 FAILS.	1304.10	00		013001.40
	L%BU,BIT4		17332.00	80	000000.20 50	013002.00
	LX,\$X0,\$R		11.00	10		013003.00
	KV,\$X0,BIT4		17332.00	90		013003.40
	BXE,\$+1.32		13005.72	C2		013004.00
	SIC,SEN		1310.00	80		013004.40
	B,SERS	-BIT 4 FRM RACC TO IX 0 FAILS.	1304.10	00		013005.00
	L%BU,BIT5		17333.00	80	000000.20 50	013005.40
	LX,\$X0,\$R		11.00	10		013006.40
	KV,\$X0,BIT5		17333.00	90		013007.00
	BXE,\$+1.32		13011.32	C2		013007.40
	SIC,SEN		1310.00	80		013010.00
	B,SERS	-BIT 5 FRM RACC TO IX 0 FAILS.	1304.10	00		013010.40
	L%BU,BIT6		17334.00	80	000000.20 50	013011.00
	LX,\$X0,\$R		11.00	10		013012.00
	KV,\$X0,BIT6		17334.00	90		013012.40
	BXE,\$+1.32		13014.72	C2		013013.00
	SIC,SEN		1310.00	80		013013.40
	B,SERS	-BIT 6 FRM RACC TO IX 0 FAILS.	1304.10	00		013014.00
	L%BU,BIT7		17335.00	80	000000.20 50	013014.40
	LX,\$X0,\$R		11.00	10		013015.40
	KV,\$X0,BIT7		17335.00	90		013016.00
	BXE,\$+1.32		13020.32	C2		013016.40
	SIC,SEN		1310.00	80		013017.00
	B,SERS	-BIT 7 FRM RACC TO IX 0 FAILS.	1304.10	00		013017.40
	L%BU,BIT8		17336.00	80	000000.20 50	013020.00
	LX,\$X0,\$R		11.00	10		013021.00
	KV,\$X0,BIT8		17336.00	90		013021.40
	BXE,\$+1.32		13023.72	C2		013022.00

SIC,SEN		1310.00	80	013022.40
B,SERS	-BIT 8 FRM RACC TO IX 0 FAILS.	1304.10	00	013023.00
L%BU□,BIT9		17337.00	80 000000.20 50	013023.40
LX,\$X0,\$R		11.00	10	013024.40
KV,\$X0,BIT9		17337.00	90	013025.00
BXE,\$+1.32		13027.32	C2	013025.40
SIC,SEN		1310.00	80	013026.00
B,SERS	-BIT 9 FRM RACC TO IX 0 FAILS.	1304.10	00	013026.40
L%BU□,BIT10		17340.00	80 000000.20 50	013027.00
LX,\$X0,\$R		11.00	10	013030.00
KV,\$X0,BIT10		17340.00	90	013030.40
BXE,\$+1.32		13032.72	C2	013031.00
SIC,SEN		1310.00	80	013031.40
B,SERS	-BIT 10 FRM RACC TO IX 0 FAILS.	1304.10	00	013032.00
L%BU□,BIT11		17341.00	80 000000.20 50	013032.40
LX,\$X0,\$R		11.00	10	013033.40
KV,\$X0,BIT11		17341.00	90	013034.00
BXE,\$+1.32		13036.32	C2	013034.40
SIC,SEN		1310.00	80	013035.00
B,SERS	-BIT 11 FRM RACC TO IX 0 FAILS.	1304.10	00	013035.40
L%BU□,BIT12		17342.00	80 000000.20 50	013036.00
LX,\$X0,\$R		11.00	10	013037.00
KV,\$X0,BIT12		17342.00	90	013037.40
BXE,\$+1.32		13041.72	C2	013040.00
SIC,SEN		1310.00	80	013040.40
B,SERS	-BIT 12 FRM RACC TO IX 0 FAILS.	1304.10	00	013041.00
L%BU□,BIT13		17343.00	80 000000.20 50	013041.40
LX,\$X0,\$R		11.00	10	013042.40
KV,\$X0,BIT13		17343.00	90	013043.00
BXE,\$+1.32		13045.32	C2	013043.40
SIC,SEN		1310.00	80	013044.00
B,SERS	-BIT 13 FRM RACC TO IX 0 FAILS.	1304.10	00	013044.40
L%BU□,BIT14		17344.00	80 000000.20 50	013045.00
LX,\$X0,\$R		11.00	10	013046.00
KV,\$X0,BIT14		17344.00	90	013046.40
BXE,\$+1.32		13050.72	C2	013047.00
SIC,SEN		1310.00	80	013047.40
B,SERS	-BIT 14 FRM RACC TO IX 0 FAILS.	1304.10	00	013050.00
L%BU□,BIT15		17345.00	80 000000.20 50	013050.40
LX,\$X0,\$R		11.00	10	013051.40
KV,\$X0,BIT15		17345.00	90	013052.00
BXE,\$+1.32		13054.32	C2	013052.40
SIC,SEN		1310.00	80	013053.00
B,SERS	-BIT 15 FRM RACC TO IX 0 FAILS.	1304.10	00	013053.40
L%BU□,BIT16		17346.00	80 000000.20 50	013054.00
LX,\$X0,\$R		11.00	10	013055.00
KV,\$X0,BIT16		17346.00	90	013055.40
BXE,\$+1.32		13057.72	C2	013056.00
SIC,SEN		1310.00	80	013056.40
B,SERS	-BIT 16 FRM RACC TO IX 0 FAILS.	1304.10	00	013057.00
L%BU□,BIT17		17347.00	80 000000.20 50	013057.40
LX,\$X0,\$R		11.00	10	013060.40
KV,\$X0,BIT17		17347.00	90	013061.00
BXE,\$+1.32		13063.32	C2	013061.40
SIC,SEN		1310.00	80	013062.00
B,SERS	-BIT 17 FRM RACC TO IX 0 FAILS.	1304.10	00	013062.40


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L%BU□,BIT18
LX,$X0,$R
KV,$X0,BIT18
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 18 FRM RACC TO IX 0 FAILS.
L%BU□,BIT19
LX,$X0,$R
KV,$X0,BIT19
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 19 FRM RACC TO IX 0 FAILS.
L%BU□,BIT20
LX,$X0,$R
KV,$X0,BIT20
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 20 FRM RACC TO IX 0 FAILS.
L%BU□,BIT21
LX,$X0,$R
KV,$X0,BIT21
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 21 FRM RACC TO IX 0 FAILS.
L%BU□,BIT22
LX,$X0,$R
KV,$X0,BIT22
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 22 FRM RACC TO IX 0 FAILS.
L%BU□,BIT23
LX,$X0,$R
KV,$X0,BIT23
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 23 FRM RACC TO IX 0 FAILS.
L%BU□,BIT24
LX,$X0,$R
KV,$X0,BIT24
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 24 FRM RACC TO IX 0 FAILS.

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17350.00 80 000000.20 50 013063.00
      11.00 10 013064.00
17350.00 90 013064.40
13066.72 C2 013065.00
      1310.00 80 013065.40
      1304.10 00 013066.00
17351.00 80 000000.20 50 013066.40
      11.00 10 013067.40
17351.00 90 013070.00
13072.32 C2 013070.40
      1310.00 80 013071.00
      1304.10 00 013071.40
17352.00 80 000000.20 50 013072.00
      11.00 10 013073.00
17352.00 90 013073.40
13075.72 C2 013074.00
      1310.00 80 013074.40
      1304.10 00 013075.00
17353.00 80 000000.20 50 013075.40
      11.00 10 013076.40
17353.00 90 013077.00
13101.32 C2 013077.40
      1310.00 80 013100.00
      1304.10 00 013100.40
17354.00 80 000000.20 50 013101.00
      11.00 10 013102.00
17354.00 90 013102.40
13104.72 C2 013103.00
      1310.00 80 013103.40
      1304.10 00 013104.00
17355.00 80 000000.20 50 013104.40
      11.00 10 013105.40
17355.00 90 013106.00
13110.32 C2 013106.40
      1310.00 80 013107.00
      1304.10 00 013107.40
17356.00 80 000000.20 50 013110.00
      11.00 10 013111.00
17356.00 90 013111.40
13113.72 C2 013112.00
      1310.00 80 013112.40
      1304.10 00 013113.00

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L%BU□,BIT28
LX,$X0,$R
KC,$X0,BIT 0
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 28 FRM RACC TO IX 0 FAILS.
L%BU□,BIT29
LX,$X0,$R
KC,$X0,BIT 1
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 29 FRM RACC TO IX 0 FAILS.
L%BU□,BIT30
LX,$X0,$R
KC,$X0,BIT 2
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 30 FRM RACC TO IX 0 FAILS.
L%BU□,BIT31
LX,$X0,$R
KC,$X0,BIT 3
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 31 FRM RACC TO IX 0 FAILS.
L%BU□,BIT32
LX,$X0,$R
KC,$X0,BIT 4
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 32 FRM RACC TO IX 0 FAILS.
L%BU□,BIT33
LX,$X0,$R
KC,$X0,BIT 5
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 33 FRM RACC TO IX 0 FAILS.
L%BU□,BIT34
LX,$X0,$R
KC,$X0,BIT 6
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 34 FRM RACC TO IX 0 FAILS.
L%BU□,BIT35
LX,$X0,$R
KC,$X0,BIT 7
BXE,$+1.32
SIC,$SEN
      B,$SERS      -BIT 35 FRM RACC TO IX 0 FAILS.
L%BU□,BIT36
LX,$X0,$R
KC,$X0,BIT 8
BXE,$+1.32

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17362.00 80 000000.20 50 013113.40
      11.00 10 013114.40
17326.01 90 013115.00
13117.32 C2 013115.40
      1310.00 80 013116.00
      1304.10 00 013116.40
17363.00 80 000000.20 50 013117.00
      11.00 10 013120.00
17327.01 90 013120.40
13122.72 C2 013121.00
      1310.00 80 013121.40
      1304.10 00 013122.00
17364.00 80 000000.20 50 013122.40
      11.00 10 013123.40
17330.01 90 013124.00
13126.32 C2 013124.40
      1310.00 80 013125.00
      1304.10 00 013125.40
17365.00 80 000000.20 50 013126.00
      11.00 10 013127.00
17331.01 90 013127.40
13131.72 C2 013130.00
      1310.00 80 013130.40
      1304.10 00 013131.00
17366.00 80 000000.20 50 013131.40
      11.00 10 013132.40
17332.01 90 013133.00
13135.32 C2 013133.40
      1310.00 80 013134.00
      1304.10 00 013134.40
17367.00 80 000000.20 50 013135.00
      11.00 10 013136.00
17333.01 90 013136.40
13140.72 C2 013137.00
      1310.00 80 013137.40
      1304.10 00 013140.00
17370.00 80 000000.20 50 013140.40
      11.00 10 013141.40
17334.01 90 013142.00
13144.32 C2 013142.40
      1310.00 80 013143.00
      1304.10 00 013143.40
17371.00 80 000000.20 50 013144.00
      11.00 10 013145.00
17335.01 90 013145.40
13147.72 C2 013146.00
      1310.00 80 013146.40
      1304.10 00 013147.00
17372.00 80 000000.20 50 013147.40
      11.00 10 013150.40
17336.01 90 013151.00
13153.32 C2 013151.40

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SIC,SEN			1310.00	80		013152.00
B,SERS	-BIT 36 FRM RACC TO IX 0 FAILS.		1304.10	00		013152.40
L%BU□,BIT37			17373.00	80	000000.20 50	013153.00
LX,\$X0,\$R			11.00	10		013154.00
KC,\$X0,BIT 9			17337.01	90		013154.40
BXE,\$+1.32			13156.72	C2		013155.00
SIC,SEN			1310.00	80		013155.40
B,SERS	-BIT 37 FRM RACC TO IX 0 FAILS.		1304.10	00		013156.00
L%BU□,BIT38			17374.00	80	000000.20 50	013156.40
LX,\$X0,\$R			11.00	10		013157.40
KC,\$X0,BIT 10			17340.01	90		013160.00
BXE,\$+1.32			13162.32	C2		013160.40
SIC,SEN			1310.00	80		013161.00
B,SERS	-BIT 38 FRM RACC TO IX 0 FAILS.		1304.10	00		013161.40
L%BU□,BIT39			17375.00	80	000000.20 50	013162.00
LX,\$X0,\$R			11.00	10		013163.00
KC,\$X0,BIT 11			17341.01	90		013163.40
BXE,\$+1.32			13165.72	C2		013164.00
SIC,SEN			1310.00	80		013164.40
B,SERS	-BIT 39 FRM RACC TO IX 0 FAILS.		1304.10	00		013165.00
L%BU□,BIT40			17376.00	80	000000.20 50	013165.40
LX,\$X0,\$R			11.00	10		013166.40
KC,\$X0,BIT 12			17342.01	90		013167.00
BXE,\$+1.32			13171.32	C2		013167.40
SIC,SEN			1310.00	80		013170.00
B,SERS	-BIT 40 FRM RACC TO IX 0 FAILS.		1304.10	00		013170.40
L%BU□,BIT41			17377.00	80	000000.20 50	013171.00
LX,\$X0,\$R			11.00	10		013172.00
KC,\$X0,BIT 13			17343.01	90		013172.40
BXE,\$+1.32			13174.72	C2		013173.00
SIC,SEN			1310.00	80		013173.40
B,SERS	-BIT 41 FRM RACC TO IX 0 FAILS.		1304.10	00		013174.00
L%BU□,BIT42			17400.00	80	000000.20 50	013174.40
LX,\$X0,\$R			11.00	10		013175.40
KC,\$X0,BIT 14			17344.01	90		013176.00
BXE,\$+1.32			13200.32	C2		013176.40
SIC,SEN			1310.00	80		013177.00
B,SERS	-BIT 42 FRM RACC TO IX 0 FAILS.		1304.10	00		013177.40
L%BU□,BIT43			17401.00	80	000000.20 50	013200.00
LX,\$X0,\$R			11.00	10		013201.00
KC,\$X0,BIT 15			17345.01	90		013201.40
BXE,\$+1.32			13203.72	C2		013202.00
SIC,SEN			1310.00	80		013202.40
B,SERS	-BIT 43 FRM RACC TO IX 0 FAILS.		1304.10	00		013203.00
L%BU□,BIT44			17402.00	80	000000.20 50	013203.40
LX,\$X0,\$R			11.00	10		013204.40
KC,\$X0,BIT 16			17346.01	90		013205.00
BXE,\$+1.32			13207.32	C2		013205.40
SIC,SEN			1310.00	80		013206.00
B,SERS	-BIT 44 FRM RACC TO IX 0 FAILS.		1304.10	00		013206.40
L%BU□,BIT45			17403.00	80	000000.20 50	013207.00
LX,\$X0,\$R			11.00	10		013210.00
KC,\$X0,BIT 17			17347.01	90		013210.40
BXE,\$+1.32			13212.72	C2		013211.00
SIC,SEN			1310.00	80		013211.40
B,SERS	-BIT 45 FRM RACC TO IX 0 FAILS.		1304.10	00		013212.00
B,\$+1.0			13213.50	00		013212.40
BD,I101			12764.04	00		013213.00
SIC,SEN0+.32			1311.40	80		013213.40
B,SSW	-TO SSIP.		1301.10	00		013214.00
BD,\$+.32			13215.04	00		013214.40

	LX,\$X13,IC210 V+,\$X13,BIT0 SX,\$X13,IC210	-UPDATE CONTINUITY CHECK.	13427.32 10 17326.32 B0 13427.33 10	013215.00 013215.40 013216.00
1102	LX,\$X1,BIT0 LX,\$X0,\$X1 KV,\$X0,BIT0 BXE,\$+1.32 SIC,SEN B,SERS	-CHK LX FROM IX CORE STG.	17326.02 10 21.00 10 17326.00 90 13221.72 C2 1310.00 80 1304.10 00	013216.40 013217.00 013217.40 013220.00 013220.40 013221.00
	LX,\$X1,BIT1 LX,\$X0,\$X1 KV,\$X0,BIT1 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 0 FRM IX 1 TO IX 0 FAILS.	17327.02 10 21.00 10 17327.00 90 13224.72 C2 1310.00 80 1304.10 00	013221.40 013222.00 013222.40 013223.00 013223.40 013224.00
	LX,\$X1,BIT2 LX,\$X0,\$X1 KV,\$X0,BIT2 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 1 FRM IX 1 TO IX 0 FAILS.	17330.02 10 21.00 10 17330.00 90 13227.72 C2 1310.00 80 1304.10 00	013224.40 013225.00 013225.40 013226.00 013226.40 013227.00
	LX,\$X1,BIT3 LX,\$X0,\$X1 KV,\$X0,BIT3 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 2 FRM IX 1 TO IX 0 FAILS.	17331.02 10 21.00 10 17331.00 90 13232.72 C2 1310.00 80 1304.10 00	013227.40 013230.00 013230.40 013231.00 013231.40 013232.00
	LX,\$X1,BIT4 LX,\$X0,\$X1 KV,\$X0,BIT4 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 3 FRM IX 1 TO IX 0 FAILS.	17332.02 10 21.00 10 17332.00 90 13235.72 C2 1310.00 80 1304.10 00	013232.40 013233.00 013233.40 013234.00 013234.40 013235.00
	LX,\$X1,BIT5 LX,\$X0,\$X1 KV,\$X0,BIT5 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 4 FRM IX 1 TO IX 0 FAILS.	17333.02 10 21.00 10 17333.00 90 13240.72 C2 1310.00 80 1304.10 00	013235.40 013236.00 013236.40 013237.00 013237.40 013240.00
	LX,\$X1,BIT6 LX,\$X0,\$X1 KV,\$X0,BIT6 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 5 FRM IX 1 TO IX 0 FAILS.	17334.02 10 21.00 10 17334.00 90 13243.72 C2 1310.00 80 1304.10 00	013240.40 013241.00 013241.40 013242.00 013242.40 013243.00
	LX,\$X1,BIT7 LX,\$X0,\$X1 KV,\$X0,BIT7 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 6 FRM IX 1 TO IX 0 FAILS.	17335.02 10 21.00 10 17335.00 90 13246.72 C2 1310.00 80 1304.10 00	013243.40 013244.00 013244.40 013245.00 013245.40 013246.00
	LX,\$X1,BIT8 LX,\$X0,\$X1 KV,\$X0,BIT8 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 7 FRM IX 1 TO IX 0 FAILS.	17336.02 10 21.00 10 17336.00 90 13251.72 C2 1310.00 80 1304.10 00	013246.40 013247.00 013247.40 013250.00 013250.40 013251.00
	LX,\$X1,BIT9 LX,\$X0,\$X1 KV,\$X0,BIT9 BXE,\$+1.32 SIC,SEN B,SERS	-BIT 8 FRM IX 1 TO IX 0 FAILS.	17337.02 10 21.00 10 17337.00 90 13254.72 C2 1310.00 80 1304.10 00	013251.40 013252.00 013252.40 013253.00 013253.40 013254.00
		-BIT 9 FRM IX 1 TO IX 0 FAILS		

LX,\$X1,BIT10		17340.02	10	013254.40
LX,\$X0,\$X1		21.00	10	013255.00
KV,\$X0,BIT10		17340.00	90	013255.40
BXE,\$+1.32		13257.72	C2	013256.00
SIC,SEN		1310.00	80	013256.40
	B,SERS	1304.10	00	013257.00
		17341.02	10	013257.40
LX,\$X1,BIT11	-BIT 10 FRM IX 1 TO IX 0 FAILS.	21.00	10	013260.00
LX,\$X0,\$X1		17341.00	90	013260.40
KV,\$X0,BIT11		13262.72	C2	013261.00
BXE,\$+1.32		1310.00	80	013261.40
SIC,SEN		1304.10	00	013262.00
	B,SERS	17342.02	10	013262.40
		21.00	10	013263.00
LX,\$X1,BIT12	-BIT 11 FRM IX 1 TO IX 0 FAILS.	17342.00	90	013263.40
LX,\$X0,\$X1		13265.72	C2	013264.00
KV,\$X0,BIT12		1310.00	80	013264.40
BXE,\$+1.32		1304.10	00	013265.00
SIC,SEN		17343.02	10	013265.40
	B,SERS	21.00	10	013266.00
		17343.00	90	013266.40
LX,\$X1,BIT13	-BIT 12 FRM IX 1 TO IX 0 FAILS.	13270.72	C2	013267.00
LX,\$X0,\$X1		1310.00	80	013267.40
KV,\$X0,BIT13		1304.10	00	013270.00
BXE,\$+1.32		17344.02	10	013270.40
SIC,SEN		21.00	10	013271.00
	B,SERS	17344.00	90	013271.40
		13273.72	C2	013272.00
LX,\$X1,BIT14	-BIT 13 FRM IX 1 TO IX 0 FAILS.	1310.00	80	013272.40
LX,\$X0,\$X1		1304.10	00	013273.00
KV,\$X0,BIT14		17345.02	10	013273.40
BXE,\$+1.32		21.00	10	013274.00
SIC,SEN		17345.00	90	013274.40
	B,SERS	13276.72	C2	013275.00
		1310.00	80	013275.40
LX,\$X1,BIT15	-BIT 14 FRM IX 1 TO IX 0 FAILS.	1304.10	00	013276.00
LX,\$X0,\$X1		17346.02	10	013276.40
KV,\$X0,BIT15		21.00	10	013277.00
BXE,\$+1.32		17346.00	90	013277.40
SIC,SEN		13301.72	C2	013300.00
	B,SERS	1310.00	80	013300.40
		1304.10	00	013301.00
LX,\$X1,BIT16	-BIT 15 FRM IX 1 TO IX 0 FAILS.	21.00	10	013301.40
LX,\$X0,\$X1		17347.02	10	013302.00
KV,\$X0,BIT16		21.00	10	013302.40
BXE,\$+1.32		17347.00	90	013303.00
SIC,SEN		13305.32	C2	013303.40
	B,SERS	1310.00	80	013304.00
		1304.10	00	013304.40
LX,\$X1,BIT17	-BIT 16 FRM IX 1 TO IX 0 FAILS.	17350.02	10	013305.00
LX,\$X0,\$X1		21.00	10	013305.40
KV,\$X0,BIT17		17350.00	90	013306.00
BXE,\$+1.32		13310.32	C2	013306.40
SIC,SEN		1310.00	80	013307.00
	B,SERS	1304.10	00	013307.40
		17351.02	10	013310.00
LX,\$X1,BIT18	-BIT 17 FRM IX 1 TO IX 0 FAILS.	21.00	10	013310.40
LX,\$X0,\$X1		17351.00	90	013311.00
KV,\$X0,BIT18		13313.32	C2	013311.40
BXE,\$+1.32		1310.00	80	013312.00
SIC,SEN		1304.10	00	013312.40
	B,SERS			
LX,\$X1,BIT19	-BIT 18 FRM IX 1 TO IX 0 FAILS.			
LX,\$X0,\$X1				
KV,\$X0,BIT19				
BXE,\$+1.32				
SIC,SEN				
	B,SERS			
LX,\$X1,BIT19	-BIT 19 FRM IX 1 TO IX 0 FAILS			
LX,\$X0,\$X1				
KV,\$X0,BIT19				
BXE,\$+1.32				
SIC,SEN				
	B,SERS			

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LX,$X1,BIT20
LX,$X0,$X1
KV,$X0,BIT20
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 20 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT21
LX,$X0,$X1
KV,$X0,BIT21
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 21 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT22
LX,$X0,$X1
KV,$X0,BIT22
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 22 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT23
LX,$X0,$X1
KV,$X0,BIT23
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 23 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT24
LX,$X0,$X1
KV,$X0,BIT24
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 24 FRM IX 1 TO IX 0 FAILS
LX,$X1,BIT28
LX,$X0,$X1
KC,$X0,BIT0
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 28 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT29
LX,$X0,$X1
KC,$X0,BIT1
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 29 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT30
LX,$X0,$X1
KC,$X0,BIT2
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 30 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT31
LX,$X0,$X1
KC,$X0,BIT3
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 31 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT32
LX,$X0,$X1
KC,$X0,BIT4
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 32 FRM IX 1 TO IX 0 FAILS.

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17352.02 10      013313.00
      21.00 10      013313.40
17352.00 90      013314.00
13316.32 C2      013314.40
      1310.00 80      013315.00
      1304.10 00      013315.40
17353.02 10      013316.00
      21.00 10      013316.40
17353.00 90      013317.00
13321.32 C2      013317.40
      1310.00 80      013320.00
      1304.10 00      013320.40
17354.02 10      013321.00
      21.00 10      013321.40
17354.00 90      013322.00
13324.32 C2      013322.40
      1310.00 80      013323.00
      1304.10 00      013323.40
17355.02 10      013324.00
      21.00 10      013324.40
17355.00 90      013325.00
13327.32 C2      013325.40
      1310.00 80      013326.00
      1304.10 00      013326.40
17356.02 10      013327.00
      21.00 10      013327.40
17356.00 90      013330.00
13332.32 C2      013330.40
      1310.00 80      013331.00
      1304.10 00      013331.40
17362.02 10      013332.00
      21.00 10      013332.40
17326.01 90      013333.00
13335.32 C2      013333.40
      1310.00 80      013334.00
      1304.10 00      013334.40
17363.02 10      013335.00
      21.00 10      013335.40
17327.01 90      013336.00
13340.32 C2      013336.40
      1310.00 80      013337.00
      1304.10 00      013337.40
17364.02 10      013340.00
      21.00 10      013340.40
17330.01 90      013341.00
13343.32 C2      013341.40
      1310.00 80      013342.00
      1304.10 00      013342.40
17365.02 10      013343.00
      21.00 10      013343.40
17331.01 90      013344.00
13346.32 C2      013344.40
      1310.00 80      013345.00
      1304.10 00      013345.40
17366.02 10      013346.00
      21.00 10      013346.40
17332.01 90      013347.00
13351.32 C2      013347.40
      1310.00 80      013350.00
      1304.10 00      013350.40

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LX,$X1,BIT33
LX,$X0,$X1
KC,$X0,BIT5
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 33 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT34
LX,$X0,$X1
KC,$X0,BIT6
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 34 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT35
LX,$X0,$X1
KC,$X0,BIT7
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 35 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT36
LX,$X0,$X1
KC,$X0,BIT8
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 36 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT37
LX,$X0,$X1
KC,$X0,BIT9
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 37 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT38
LX,$X0,$X1
KC,$X0,BIT10
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 38 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT39
LX,$X0,$X1
KC,$X0,BIT11
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 39 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT40
LX,$X0,$X1
KC,$X0,BIT12
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 40 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT41
LX,$X0,$X1
KC,$X0,BIT13
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 41 FRM IX 1 TO IX 0 FAILS.
LX,$X1,BIT42
LX,$X0,$X1
KC,$X0,BIT14
BXE,$+1.32
SIC,SEN
      B,SERS      -BIT 42 FRM IX 1 TO IX 0 FAILS.

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17367.02 10
  21.00 10
17333.01 90
13354.32 C2
  1310.00 80
  1304.10 00
17370.02 10
  21.00 10
17334.01 90
13357.32 C2
  1310.00 80
  1304.10 00
17371.02 10
  21.00 10
17335.01 90
13362.32 C2
  1310.00 80
  1304.10 00
17372.02 10
  21.00 10
17336.01 90
13365.32 C2
  1310.00 80
  1304.10 00
17373.02 10
  21.00 10
17337.01 90
13370.32 C2
  1310.00 80
  1304.10 00
17374.02 10
  21.00 10
17340.01 90
13373.32 C2
  1310.00 80
  1304.10 00
17375.02 10
  21.00 10
17341.01 90
13376.32 C2
  1310.00 80
  1304.10 00
17376.02 10
  21.00 10
17342.01 90
13401.32 C2
  1310.00 80
  1304.10 00
17377.02 10
  21.00 10
17343.01 90
13404.32 C2
  1310.00 80
  1304.10 00
17400.02 10
  21.00 10
17344.01 90
13407.32 C2
  1310.00 80
  1304.10 00
013351.00
013351.40
013352.00
013352.40
013353.00
013353.40
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013402.00
013402.40
013403.00
013403.40
013404.00
013404.40
013405.00
013405.40
013406.00
013406.40

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LX,\$X1,BIT43		17401.02 10	013407.00
LX,\$X0,\$X1		21.00 10	013407.40
KC,\$X0,BIT15		17345.01 90	013410.00
BXE,\$+1.32		13412.32 C2	013410.40
SIC,\$SEN		1310.00 80	013411.00
	B,\$SERS	1304.10 00	013411.40
	-BIT 43 FRM IX 1 TO IX 0 FAILS.	17402.02 10	013412.00
LX,\$X1,BIT44		21.00 10	013412.40
LX,\$X0,\$X1		17346.01 90	013413.00
KC,\$X0,BIT16		13415.32 C2	013413.40
BXE,\$+1.32		1310.00 80	013414.00
SIC,\$SEN		1304.10 00	013414.40
	B,\$SERS	17403.02 10	013415.00
	-BIT 44 FRM IX 1 TO IX 0 FAILS.	21.00 10	013415.40
LX,\$X1,BIT45		17347.01 90	013416.00
LX,\$X0,\$X1		13420.32 C2	013416.40
KC,\$X0,BIT17		1310.00 80	013417.00
BXE,\$+1.32		1304.10 00	013417.40
SIC,\$SEN		13421.10 00	013420.00
	B,\$SERS	13216.44 00	013420.40
	-BIT 45 FRM IX 1 TO IX 0 FAILS.	1311.40 80	013421.00
B,\$+1.0		1301.10 00	013421.40
BD,\$102		13422.44 00	013422.00
SIC,\$SEN0+.32			
B,\$SSW			
BD,\$+.32			
	-TO SSIP		
LX,\$X13,IC210		13427.32 10	013422.40
V+,\$X13,BIT1		17327.32 B0	013423.00
SX,\$X13,IC210		13427.33 10	013423.40
	-UPDATE CONTINUITY CHECK.		
LX,\$X13,IC210		13427.32 10	013424.00
KV,\$X13,ICK210		13430.32 90	013424.40
SIC,\$SEN		1310.00 80	013425.00
BZXE,\$SERS		1304.32 C0	013425.40
B,\$112		13431.10 00	013426.00
CNOP		0.30 00	013426.40
CNOP			
IC210	XW,0,0,0	0.00 00 000000.00 00	013427.00
ICK210	XW,\$8#600000.00,0,0	600000.00 00 000000.00 00	013430.00
	-CONTINUITY REG 1210.		

-----1212---KV,KC IX.CORE STG AND INT. MEM. CHK.

112	LX,\$X1,112ID	-UPDATE IDENT.	13434.02	10		013431.00
	SX,\$X1,DPET13		1437.03	10		013431.40
	SIC,RET		1306.40	80		013432.00
	B,1DF1		1443.10	00		013432.40
	Z,1C212		14203.22	00		013433.00
	BD,1121		13435.04	00		013433.40
	CNOP					
112ID	%1QSZ=DD%BU,64,8=,1212	Z.				013434.00
1121	LX,\$X0,BIT0	-TEST KV, BIT 0, INT. MEM. AND	17326.00	10		013435.00
	L%BU=,BIT 0	-INDEX CORE STORAGE.	17326.00	80	000000.20 50	013435.40
	LX,\$X1,BIT0		17326.02	10		013436.40
	KV,\$X0,\$R		11.00	90		013437.00
	BXE,\$+1.32		13441.32	C2		013437.40
	SIC,SEN		1310.00	80		013440.00
	B,SERS	-KV,BIT 0, INT. MEM. FAILED.	1304.10	00		013440.40
	KV,\$X0,\$X1		21.00	90		013441.00
	BXE,\$+1.32		13443.32	C2		013441.40
	SIC,SEN		1310.00	80		013442.00
	B,SERS	-KV,BIT 0, IX. CORE STG. FAILED.	1304.10	00		013442.40
	LX,\$X0,BIT1	-TEST KV, BIT 1, INT. MEM. AND	17327.00	10		013443.00
	L%BU=,BIT1	-INDEX CORE STORAGE.	17327.00	80	000000.20 50	013443.40
	LX,\$X1,BIT1		17327.02	10		013444.40
	KV,\$X0,\$R		11.00	90		013445.00
	BXE,\$+1.32		13447.32	C2		013445.40
	SIC,SEN		1310.00	80		013446.00
	B,SERS	-KV, BIT 1, INT. MEM. FAILED.	1304.10	00		013446.40
	KV,\$X0,\$X1		21.00	90		013447.00
	BXE,\$+1.32		13451.32	C2		013447.40
	SIC,SEN		1310.00	80		013450.00
	B,SERS	-KV, BIT 1, IX. CORE STG. FAILED.	1304.10	00		013450.40
	LX,\$X0,BIT2	-TEST KV, BIT 2, INT. MEM. AND	17330.00	10		013451.00
	L%BU=,BIT2	-INDEX CORE STORAGE.	17330.00	80	000000.20 50	013451.40
	LX,\$X1,BIT2		17330.02	10		013452.40
	KV,\$X0,\$R		11.00	90		013453.00
	BXE,\$+1.32		13455.32	C2		013453.40
	SIC,SEN		1310.00	80		013454.00
	B,SERS	-KV, BIT 2, INT. MEM. FAILED.	1304.10	00		013454.40
	KV,\$X0,\$X1		21.00	90		013455.00
	BXE,\$+1.32		13457.32	C2		013455.40
	SIC,SEN		1310.00	80		013456.00
	B,SERS	-KV,BIT 2, IX. CORE STG. FAILED.	1304.10	00		013456.40
	LX,\$X0,BIT3	-TEST KV, BIT 3, INT. MEM. AND	17331.00	10		013457.00
	L%BU=,BIT3	-INDEX CORE STORAGE.	17331.00	80	000000.20 50	013457.40
	LX,\$X1,BIT3		17331.02	10		013460.40
	KV,\$X0,\$R		11.00	90		013461.00
	BXE,\$+1.32		13463.32	C2		013461.40
	SIC,SEN		1310.00	80		013462.00
	B,SERS	-KV, BIT 3, INT. MEM. FAILED.	1304.10	00		013462.40
	KV,\$X0,\$X1		21.00	90		013463.00
	BXE,\$+1.32		13465.32	C2		013463.40
	SIC,SEN		1310.00	80		013464.00

B,SERS	-KV, BIT 3, IX. CORE STG. FAILED.	1304.10 00	013464.40
LX,\$X0,BIT4	-TEST KV, BIT 4, INT. MEM. AND	17332.00 10	013465.00
L%BU□,BIT4	-INDEX CORE STORAGE.	17332.00 80 000000.20 50	013465.40
LX,\$X1,BIT4		17332.02 10	013466.40
KV,\$X0,\$R		11.00 90	013467.00
BXE,\$+1.32		13471.32 C2	013467.40
SIC,SEN		1310.00 80	013470.00
B,SERS	-KV, BIT 4, INT. MEM. FAILED.	1304.10 00	013470.40
-			
KV,\$X0,\$X1		21.00 90	013471.00
BXE,\$+1.32		13473.32 C2	013471.40
SIC,SEN		1310.00 80	013472.00
B,SERS	-KV,BIT 4, IX. CORE STG. FAILED.	1304.10 00	013472.40
-			
LX,\$X0,BIT5	-TEST KV,BIT 5, INT. MEM. AND	17333.00 10	013473.00
L%BU□,BIT5	-INDEX CORE STORAGE.	17333.00 80 000000.20 50	013473.40
LX,\$X1,BIT5		17333.02 10	013474.40
KV,\$X0,\$R		11.00 90	013475.00
BXE,\$+1.32		13477.32 C2	013475.40
SIC,SEN		1310.00 80	013476.00
B,SERS	-KV, BIT 5, INT. MEM. FAILED.	1304.10 00	013476.40
-			
KV,\$X0,\$X1		21.00 90	013477.00
BXE,\$+1.32		13501.32 C2	013477.40
SIC,SEN		1310.00 80	013500.00
B,SERS	-KV,BIT 5, IX. CORE STG. FAILED.	1304.10 00	013500.40
-			
LX,\$X0,BIT6	-TEST KV, BIT 6, INT. MEM. AND	17334.00 10	013501.00
L%BU□,BIT6	-INDEX CORE STORAGE.	17334.00 80 000000.20 50	013501.40
LX,\$X1,BIT6		17334.02 10	013502.40
KV,\$X0,\$R		11.00 90	013503.00
BXE,\$+1.32		13505.32 C2	013503.40
SIC,SEN		1310.00 80	013504.00
B,SERS	-KV,BIT 6, INT. MEM. FAILED.	1304.10 00	013504.40
-			
KV,\$X0,\$X1		21.00 90	013505.00
BXE,\$+1.32		13507.32 C2	013505.40
SIC,SEN		1310.00 80	013506.00
B,SERS	-KV, BIT 6, IX. CORE STG. FAILED.	1304.10 00	013506.40
-			
LX,\$X0,BIT7	-TEST KV, BIT 7, INT. MEM. AND	17335.00 10	013507.00
L%BU□,BIT7	-INDEX CORE STORAGE.	17335.00 80 000000.20 50	013507.40
LX,\$X1,BIT7		17335.02 10	013510.40
KV,\$X0,\$R		11.00 90	013511.00
BXE,\$+1.32		13513.32 C2	013511.40
SIC,SEN		1310.00 80	013512.00
B,SERS	-KV, BIT 7, INT. MEM. FAILED.	1304.10 00	013512.40
-			
KV,\$X0,\$X1		21.00 90	013513.00
BXE,\$+1.32		13515.32 C2	013513.40
SIC,SEN		1310.00 80	013514.00
B,SERS	-KV, BIT 7, IX. CORE STG. FAILED.	1304.10 00	013514.40

LX,\$X0,BIT8	-TEST KV, BIT 8, INT. MEM. AND	17336.00 10	013515.00
L%BU□,BIT8	-INDEX CORE STORAGE.	17336.00 80 000000.20 50	013515.40
LX,\$X1,BIT8		17336.02 10	013516.40
KV,\$X0,\$R		11.00 90	013517.00
BXE,\$+1.32		13521.32 C2	013517.40
SIC,SEN		1310.00 80	013520.00
B,SERS	-KV, BIT 8, INT. MEM. FAILED.	1304.10 00	013520.40
-			
KV,\$X0,\$X1		21.00 90	013521.00
BXE,\$+1.32		13523.32 C2	013521.40
SIC,SEN		1310.00 80	013522.00
B,SERS	-KV, BIT 8, IX. CORE STG. FAILED.	1304.10 00	013522.40
-			
LX,\$X0,BIT9	-TEST KV, BIT 9, INT. MEM. AND	17337.00 10	013523.00
L%BU□,BIT9	-INDEX CORE STORAGE.	17337.00 80 000000.20 50	013523.40
LX,\$X1,BIT9		17337.02 10	013524.40
KV,\$X0,\$R		11.00 90	013525.00
BXE,\$+1.32		13527.32 C2	013525.40
SIC,SEN		1310.00 80	013526.00
B,SERS	-KV, BIT 9, INT. MEM. FAILED.	1304.10 00	013526.40
-			
KV,\$X0,\$X1		21.00 90	013527.00
BXE,\$+1.32		13531.32 C2	013527.40
SIC,SEN		1310.00 80	013530.00
B,SERS	-KV, BIT 9, IX. CORE STG. FAILED.	1304.10 00	013530.40
-			
LX,\$X0,BIT10	-TEST KV, BIT 10, INT. MEM. AND	17340.00 10	013531.00
L%BU□,BIT10	-INDEX CORE STORAGE.	17340.00 80 000000.20 50	013531.40
LX,\$X1,BIT10		17340.02 10	013532.40
KV,\$X0,\$R		11.00 90	013533.00
BXE,\$+1.32		13535.32 C2	013533.40
SIC,SEN		1310.00 80	013534.00
B,SERS	-KV, BIT 10, INT. MEM. FAILED.	1304.10 00	013534.40
-			
KV,\$X0,\$X1		21.00 90	013535.00
BXE,\$+1.32		13537.32 C2	013535.40
SIC,SEN		1310.00 80	013536.00
B,SERS	-KV, BIT 10, IX. CORE STG. FAILED.	1304.10 00	013536.40
-			
LX,\$X0,BIT11	-TEST KV, BIT 11, INT. MEM. AND	17341.00 10	013537.00
L%BU□,BIT11	-INDEX CORE STORAGE.	17341.00 80 000000.20 50	013537.40
LX,\$X1,BIT11		17341.02 10	013540.40
KV,\$X0,\$R		11.00 90	013541.00
BXE,\$+1.32		13543.32 C2	013541.40
SIC,SEN		1310.00 80	013542.00
B,SERS	-KV, BIT 11, INT. MEM. FAILED.	1304.10 00	013542.40
KV,\$X0,\$X1		21.00 90	013543.00
BXE,\$+1.32		13545.32 C2	013543.40
SIC,SEN		1310.00 80	013544.00
B,SERS	-KV, BIT 11, IX. CORE STG. FAILED.	1304.10 00	013544.40

LX,\$X0,BIT12	-TEST KV, BIT 12, INT. MEM. AND	17342.00 10		013545.00
L%BU□,BIT12	-INDEX CORE STORAGE.	17342.00 80	000000.20 50	013545.40
LX,\$X1,BIT12		17342.02 10		013546.40
KV,\$X0,\$R		11.00 90		013547.00
BXE,\$+1.32		13551.32 C2		013547.40
SIC,SEN		1310.00 80		013550.00
B,SERS	-KV, BIT 12, INT. MEM. FAILED.	1304.10 00		013550.40
-				
KV,\$X0,\$X1		21.00 90		013551.00
BXE,\$+1.32		13553.32 C2		013551.40
SIC,SEN		1310.00 80		013552.00
B,SERS	-KV, BIT 12, IX. CORE STG. FAILED.	1304.10 00		013552.40
-				
LX,\$X0,BIT13	-TEST KV, BIT 13, INT. MEM. AND	17343.00 10		013553.00
L%BU□,BIT13	-INDEX CORE STORAGE.	17343.00 80	000000.20 50	013553.40
LX,\$X1,BIT13		17343.02 10		013554.40
KV,\$X0,\$R		11.00 90		013555.00
BXE,\$+1.32		13557.32 C2		013555.40
SIC,SEN		1310.00 80		013556.00
B,SERS	-KV, BIT 13, INT. MEM. FAILED.	1304.10 00		013556.40
-				
KV,\$X0,\$X1		21.00 90		013557.00
BXE,\$+1.32		13561.32 C2		013557.40
SIC,SEN		1310.00 80		013560.00
B,SERS	-KV, BIT 13, IX. CORE STG. FAILED.	1304.10 00		013560.40
-				
LX,\$X0,BIT14	-TEST KV, BIT 14, INT. MEM. AND	17344.00 10		013561.00
L%BU□,BIT14	-INDEX CORE STORAGE.	17344.00 80	000000.20 50	013561.40
LX,\$X1,BIT14		17344.02 10		013562.40
KV,\$X0,\$R		11.00 90		013563.00
BXE,\$+1.32		13565.32 C2		013563.40
SIC,SEN		1310.00 80		013564.00
B,SERS	-KV, BIT 14, INT. MEM. FAILED.	1304.10 00		013564.40
-				
KV,\$X0,\$X1		21.00 90		013565.00
BXE,\$+1.32		13567.32 C2		013565.40
SIC,SEN		1310.00 80		013566.00
B,SERS	-KV, BIT 14, IX. CORE STG. FAILED.	1304.10 00		013566.40
-				
LX,\$X0,BIT15	-TEST KV, BIT 15, INT. MEM. AND	17345.00 10		013567.00
L%BU□,BIT15	-INDEX CORE STORAGE.	17345.00 80	000000.20 50	013567.40
LX,\$X1,BIT15		17345.02 10		013570.40
KV,\$X0,\$R		11.00 90		013571.00
BXE,\$+1.32		13573.32 C2		013571.40
SIC,SEN		1310.00 80		013572.00
B,SERS	-KV, BIT 15, INT. MEM. FAILED.	1304.10 00		013572.40
-				
KV,\$X0,\$X1		21.00 90		013573.00
BXE,\$+1.32		13575.32 C2		013573.40
SIC,SEN		1310.00 80		013574.00
B,SERS	-KV, BIT 15, IX. CORE STG. FAILED.	1304.10 00		013574.40

LX,\$X0,BIT16	-TEST KV, BIT 16, INT. MEM. AND	17346.00 10	013575.00
L%BU□,BIT16	-INDEX CORE STORAGE.	17346.00 80 000000.20 50	013575.40
LX,\$X1,BIT16		17346.02 10	013576.40
KV,\$X0,\$R		11.00 90	013577.00
BXE,\$+1.32		13601.32 C2	013577.40
SIC,SEN		1310.00 80	013600.00
B,SERS	-KV, BIT 16, INT. MEM. FAILED.	1304.10 00	013600.40
-			
KV,\$X0,\$X1		21.00 90	013601.00
BXE,\$+1.32		13603.32 C2	013601.40
SIC,SEN		1310.00 80	013602.00
B,SERS	-KV, BIT 16, IX. CORE STG. FAILED.	1304.10 00	013602.40
-			
LX,\$X0,BIT17	-TEST KV, BIT 17, INT. MEM. AND	17347.00 10	013603.00
L%BU□,BIT17	-INDEX CORE STORAGE.	17347.00 80 000000.20 50	013603.40
LX,\$X1,BIT17		17347.02 10	013604.40
KV,\$X0,\$R		11.00 90	013605.00
BXE,\$+1.32		13607.32 C2	013605.40
SIC,SEN		1310.00 80	013606.00
B,SERS	-KV, BIT 17, INT. MEM. FAILED.	1304.10 00	013606.40
-			
KV,\$X0,\$X1		21.00 90	013607.00
BXE,\$+1.32		13611.32 C2	013607.40
SIC,SEN		1310.00 80	013610.00
B,SERS	-KV, BIT 17, IX. CORE STG. FAILED.	1304.10 00	013610.40
-			
LX,\$X0,BIT18	-TEST KV, BIT 18, INT. MEM. AND	17350.00 10	013611.00
L%BU□,BIT18	-INDEX CORE STORAGE.	17350.00 80 000000.20 50	013611.40
LX,\$X1,BIT18		17350.02 10	013612.40
KV,\$X0,\$R		11.00 90	013613.00
BXE,\$+1.32		13615.32 C2	013613.40
SIC,SEN		1310.00 80	013614.00
B,SERS	-KV, BIT 18, INT. MEM. FAILED.	1304.10 00	013614.40
-			
KV,\$X0,\$X1		21.00 90	013615.00
BXE,\$+1.32		13617.32 C2	013615.40
SIC,SEN		1310.00 80	013616.00
B,SERS	-KV, BIT 18, IX. CORE STG. FAILED.	1304.10 00	013616.40
-			
LX,\$X0,BIT19	-TEST KV, BIT 19, INT. MEM. AND	17351.00 10	013617.00
L%BU□,BIT19	-INDEX CORE STORAGE.	17351.00 80 000000.20 50	013617.40
LX,\$X1,BIT19		17351.02 10	013620.40
KV,\$X0,\$R		11.00 90	013621.00
BXE,\$+1.32		13623.32 C2	013621.40
SIC,SEN		1310.00 80	013622.00
B,SERS	-KV, BIT 19, INT. MEM. FAILED.	1304.10 00	013622.40
-			
KV,\$X0,\$X1		21.00 90	013623.00
BXE,\$+1.32		13625.32 C2	013623.40
SIC,SEN		1310.00 80	013624.00
B,SERS	-KV, BIT 19, IX. CORE STG. FAILED.	1304.10 00	013624.40

LX,\$X0,BIT 20	-TEST KV,BIT 20,INT. MEM. AND	17352.00	10		013625.00
L%BU□,BIT 20	-INDEX CORE STORAGE.	17352.00	80	000000.20 50	013625.40
LX,\$X1,BIT 20		17352.02	10		013626.40
KV,\$X0,\$R		11.00	90		013627.00
BXE,\$+1.32		13631.32	C2		013627.40
SIC,SEN		1310.00	80		013630.00
B,SERS	-KV,BIT 20,INT. MEM. FAILED.	1304.10	00		013630.40
-					
KV,\$X0,\$X1		21.00	90		013631.00
BXE,\$+1.32		13633.32	C2		013631.40
SIC,SEN		1310.00	80		013632.00
B,SERS	-KV,BIT 20,IX.CORE STG. FAILED.	1304.10	00		013632.40
-					
LX,\$X0,BIT 21	-TEST KV,BIT 21,INT. MEM. AND	17353.00	10		013633.00
L%BU□,BIT21	-INDEX CORE STG.	17353.00	80	000000.20 50	013633.40
LX,\$X1,BIT 21		17353.02	10		013634.40
KV,\$X0,\$R		11.00	90		013635.00
BXE,\$+1.32		13637.32	C2		013635.40
SIC,SEN		1310.00	80		013636.00
B,SERS	-KV,BIT 21,INT. MEM.FAILED.	1304.10	00		013636.40
-					
KV,\$X0,\$X1		21.00	90		013637.00
BXE,\$+1.32		13641.32	C2		013637.40
SIC,SEN		1310.00	80		013640.00
B,SERS	-KV,BIT 21,IX. CORE STG. FAILED.	1304.10	00		013640.40
-					
LX,\$X0,BIT22	-TEST KV, BIT 22, INT. MEM. AND	17354.00	10		013641.00
L%BU□,BIT22	-INDEX CORE STORAGE.	17354.00	80	000000.20 50	013641.40
LX,\$X1,BIT22		17354.02	10		013642.40
KV,\$X0,\$R		11.00	90		013643.00
BXE,\$+1.32		13645.32	C2		013643.40
SIC,SEN		1310.00	80		013644.00
B,SERS	-KV, BIT 22, INT. MEM. FAILED.	1304.10	00		013644.40
-					
KV,\$X0,\$X1		21.00	90		013645.00
BXE,\$+1.32		13647.32	C2		013645.40
SIC,SEN		1310.00	80		013646.00
B,SERS	-KV, BIT 22,IX. CORE STG. FAILED	1304.10	00		013646.40
-					
LX,\$X0,BIT23	-TEST KV, BIT 23,INT. MEM. AND	17355.00	10		013647.00
L%BU□,BIT23	-INDEX CORE STORAGE.	17355.00	80	000000.20 50	013647.40
LX,\$X1,BIT23		17355.02	10		013650.40
KV,\$X0,\$R		11.00	90		013651.00
BXE,\$+1.32		13653.32	C2		013651.40
SIC,SEN		1310.00	80		013652.00
B,SERS	-KV, BIT 23,INT. MEM. FAILED.	1304.10	00		013652.40
-					
KV,\$X0,\$X1		21.00	90		013653.00
BXE,\$+1.32		13655.32	C2		013653.40
SIC,SEN		1310.00	80		013654.00
B,SERS	-KV, BIT 23, IX. CORE STG. FAILED.	1304.10	00		013654.40
-					
LX,\$X0,BIT24	-TEST KV, BIT 24, INT. MEM. AND	17356.00	10		013655.00
L%BU□,BIT24	-INDEX CORE STORAGE.	17356.00	80	000000.20 50	013655.40
LX,\$X1,BIT24		17356.02	10		013656.40
KV,\$X0,\$R		11.00	90		013657.00
BXE,\$+1.32		13661.32	C2		013657.40
-					
SIC,SEN		1310.00	80		013660.00
B,SERS	-KV, BIT 24, INT. MEM. FAILED.	1304.10	00		013660.40
-					
KV,\$X0,\$X1		21.00	90		013661.00
BXE,\$+1.32		13663.32	C2		013661.40
SIC,SEN		1310.00	80		013662.00

-	B,SERS	-KV, BIT 24, IX. CORE STG. FAILED.	1304.10 00	013662.40
B,\$+1.0			13664.10 00	013663.00
BD, 121			13435.04 00	013663.40
SIC,SEN0+.32			1311.40 80	013664.00
B,SSW		-TO SSIP	1301.10 00	013664.40
BD,\$+.32			13665.44 00	013665.00
LX,\$X13, C212		-UPDATE CONTINUITY CHECK.	14203.32 10	013665.40
V+,\$X13,B T0			17326.32 80	013666.00
SX,\$X13, C212			14203.33 10	013666.40

1122	LX,\$X0,BIT28 L%BU□,BIT0 LX,\$X1,BIT0 KC,\$X0,\$R BXE,\$+1.32 SIC,SEN B,SERS	-TEST KC BIT 28, INT MEM AND -INDEX CORE STORAGE. -KC, BIT 28, INT. MEM. FAILED.	17362.00 10 17326.00 80 000000.20 50 17326.02 10 11.01 90 13673.32 C2 1310.00 80 1304.10 00	013667.00 013667.40 013670.40 013671.00 013671.40 013672.00 013672.40
-	KC,\$X0,\$X1 BXE,\$+1.32 SIC,SEN B,SERS	-KC, BIT 28, IX. CORE STG. FAILED.	21.01 90 13675.32 C2 1310.00 80 1304.10 00	013673.00 013673.40 013674.00 013674.40
-	LX,\$X0,BIT29 L%BU□,BIT1 LX,\$X1,BIT1 KC,\$X0,\$R BXE,\$+1.32 SIC,SEN B,SERS	-TEST KC, BIT 29, INT. MEM. AND -INDEX CORE STORAGE. -KC, BIT 29, INT. MEM. FAILED.	17363.00 10 17327.00 80 000000.20 50 17327.02 10 11.01 90 13701.32 C2 1310.00 80 1304.10 00	013675.00 013675.40 013676.40 013677.00 013677.40 013700.00 013700.40
-	KC,\$X0,\$X1 BXE,\$+1.32 SIC,SEN B,SERS	-KC, BIT 29, IX. CORE STG. FAILED.	21.01 90 13703.32 C2 1310.00 80 1304.10 00	013701.00 013701.40 013702.00 013702.40
-	LX,\$X0,BIT 30 L%BU□,BIT2 LX,\$X1,BIT2 KC,\$X0,\$R BXE,\$+1.32 SIC,SEN B,SERS	-TEST KC,BIT 30,INT. MEM. AND -INDEX CORE STORAGE. -KC,BIT 30,INT. MEM. FAILED.	17364.00 10 17330.00 80 000000.20 50 17330.02 10 11.01 90 13707.32 C2 1310.00 80 1304.10 00	013703.00 013703.40 013704.40 013705.00 013705.40 013706.00 013706.40
-	KC,\$X0,\$X1 BXE,\$+1.32 SIC,SEN B,SERS	-KC,BIT 30,IX. CORE STG. FAILED.	21.01 90 13711.32 C2 1310.00 80 1304.10 00	013707.00 013707.40 013710.00 013710.40
-	LX,\$X0,BIT 31 L%BU□,BIT3 LX,\$X1,BIT3 KC,\$X0,\$R BXE,\$+1.32 SIC,SEN B,SERS	-TEST KC,BIT 31,INT. MEM. AND -INDEX CORE STORAGE. -KC,BIT 31,INT. MEM. FAILED.	17365.00 10 17331.00 80 000000.20 50 17331.02 10 11.01 90 13715.32 C2 1310.00 80 1304.10 00	013711.00 013711.40 013712.40 013713.00 013713.40 013714.00 013714.40
-	KC,\$X0,\$X1 BXE,\$+1.32 SIC,SEN B,SERS	-KC,BIT 31,IX. CORE STG. FAILED.	21.01 90 13717.32 C2 1310.00 80 1304.10 00	013715.00 013715.40 013716.00 013716.40

LX,\$X0,BIT32	-TEST KC, BIT 32, INT. MEM. AND	17366.00	10		013717.00
L%BU□,BIT4	-INDEX CORE STORAGE.	17332.00	80	000000.20 50	013717.40
LX,\$X1,BIT4		17332.02	10		013720.40
KC,\$X0,\$R		11.01	90		013721.00
BXE,\$+1.32		13723.32	C2		013721.40
SIC,SEN		1310.00	80		013722.00
B,SERS	-KC, BIT 32, INT. MEM. FAILED.	1304.10	00		013722.40
-					
KC,\$X0,\$X1		21.01	90		013723.00
BXE,\$+1.32		13725.32	C2		013723.40
SIC,SEN		1310.00	80		013724.00
B,SERS	-KC, BIT 32, IX. CORE STG. FAILED.	1304.10	00		013724.40
-					
LX,\$X0,BIT33	-TEST KC, BIT 33, INT. MEM. AND	17367.00	10		013725.00
L%BU□,BIT5	-INDEX CORE STORAGE.	17333.00	80	000000.20 50	013725.40
LX,\$X1,BIT5		17333.02	10		013726.40
KC,\$X0,\$R		11.01	90		013727.00
BXE,\$+1.32		13731.32	C2		013727.40
SIC,SEN		1310.00	80		013730.00
B,SERS	-KC, BIT 33, INT. MEM. FAILED.	1304.10	00		013730.40
-					
KC,\$X0,\$X1		21.01	90		013731.00
BXE,\$+1.32		13733.32	C2		013731.40
SIC,SEN		1310.00	80		013732.00
B,SERS	-KC, BIT 33, IX. CORE STG. FAILED.	1304.10	00		013732.40
-					
LX,\$X0,BIT34	-TEST KC, BIT 34, INT. MEM. AND	17370.00	10		013733.00
L%BU□,BIT6	-INDEX CORE STORAGE.	17334.00	80	000000.20 50	013733.40
LX,\$X1,BIT6		17334.02	10		013734.40
KC,\$X0,\$R		11.01	90		013735.00
BXE,\$+1.32		13737.32	C2		013735.40
SIC,SEN		1310.00	80		013736.00
B,SERS	-KC, BIT 34, INT. MEM. FAILED.	1304.10	00		013736.40
-					
KC,\$X0,\$X1		21.01	90		013737.00
BXE,\$+1.32		13741.32	C2		013737.40
SIC,SEN		1310.00	80		013740.00
B,SERS	-KC, BIT 34, IX. CORE STG. FAILED.	1304.10	00		013740.40
-					
LX,\$X0,BIT35	-TEST KC, BIT 35, INT. MEM. AND	17371.00	10		013741.00
L%BU□,BIT7	-INDEX CORE STORAGE.	17335.00	80	000000.20 50	013741.40
LX,\$X1,BIT7		17335.02	10		013742.40
KC,\$X0,\$R		11.01	90		013743.00
BXE,\$+1.32		13745.32	C2		013743.40
SIC,SEN		1310.00	80		013744.00
B,SERS	-KC, BIT 35, INT. MEM. FAILED.	1304.10	00		013744.40
-					
KC,\$X0,\$X1		21.01	90		013745.00
BXE,\$+1.32		13747.32	C2		013745.40
SIC,SEN		1310.00	80		013746.00
B,SERS	-KC, BIT 35, IX. CORE STG. FAILED.	1304.10	00		013746.40

LX,\$X0,BIT36	-TEST KC, BIT 36, INT. MEM. AND	17372.00	10		013747.00
L%BU□,BIT8	-INDEX CORE STORAGE.	17336.00	80	000000.20 50	013747.40
LX,\$X1,BIT8		17336.02	10		013750.40
KC,\$X0,\$R		11.01	90		013751.00
BXE,\$+1.32		13753.32	C2		013751.40
SIC,SEN		1310.00	80		013752.00
B,SERS	-KC, BIT 36, INT. MEM. FAILED.	1304.10	00		013752.40
-					
KC,\$X0,\$X1		21.01	90		013753.00
BXE,\$+1.32		13755.32	C2		013753.40
SIC,SEN		1310.00	80		013754.00
B,SERS	-KC, BIT 36, IX. CORE STG. FAILED.	1304.10	00		013754.40
-					
LX,\$X0,BIT37	-TEST KC, BIT 37, INT. MEM. AND	17373.00	10		013755.00
L%BU□,BIT9	-INDEX CORE STORAGE.	17337.00	80	000000.20 50	013755.40
LX,\$X1,BIT9		17337.02	10		013756.40
KC,\$X0,\$R		11.01	90		013757.00
BXE,\$+1.32		13761.32	C2		013757.40
SIC,SEN		1310.00	80		013760.00
B,SERS	-KC, BIT 37, INT. MEM. FAILED.	1304.10	00		013760.40
-					
KC,\$X0,\$X1		21.01	90		013761.00
BXE,\$+1.32		13763.32	C2		013761.40
SIC,SEN		1310.00	80		013762.00
B,SERS	-KC, BIT 37, IX. CORE STG. FAILED.	1304.10	00		013762.40
-					
LX,\$X0,BIT38	-TEST KC, BIT 38, INT. MEM. AND	17374.00	10		013763.00
L%BU□,BIT10	-INDEX CORE STORAGE.	17340.00	80	000000.20 50	013763.40
LX,\$X1,BIT10		17340.02	10		013764.40
KC,\$X0,\$R		11.01	90		013765.00
BXE,\$+1.32		13767.32	C2		013765.40
SIC,SEN		1310.00	80		013766.00
B,SERS	-KC, BIT 38, INT. MEM. FAILED.	1304.10	00		013766.40
-					
KC,\$X0,\$X1		21.01	90		013767.00
BXE,\$+1.32		13771.32	C2		013767.40
SIC,SEN		1310.00	80		013770.00
B,SERS	-KC, BIT 38, IX. CORE STG. FAILED.	1304.10	00		013770.40
-					
LX,\$X0,BIT39	-TEST KC, BIT 39, INT. MEM. AND	17375.00	10		013771.00
L%BU□,BIT11	-INDEX CORE STORAGE.	17341.00	80	000000.20 50	013771.40
LX,\$X1,BIT11		17341.02	10		013772.40
KC,\$X0,\$R		11.01	90		013773.00
BXE,\$+1.32		13775.32	C2		013773.40
SIC,SEN		1310.00	80		013774.00
B,SERS	-KC, BIT 39, INT. MEM. FAILED.	1304.10	00		013774.40
-					
KC,\$X0,\$X1		21.01	90		013775.00
BXE,\$+1.32		13777.32	C2		013775.40
SIC,SEN		1310.00	80		013776.00
B,SERS	-KC, BIT 39, IX. CORE STG. FAILED.	1304.10	00		013776.40
-					
LX,\$X0,BIT40	-TEST KC, BIT 40, INT. MEM. AND	17376.00	10		013777.00
L%BU□,BIT12	-INDEX CORE STORAGE.	17342.00	80	000000.20 50	013777.40
LX,\$X1,BIT12		17342.02	10		014000.40
KC,\$X0,\$R		11.01	90		014001.00
BXE,\$+1.32		14003.32	C2		014001.40
SIC,SEN		1310.00	80		014002.00
B,SERS	-KC, BIT 40, INT. MEM. FAILED.	1304.10	00		014002.40
-					
KC,\$X0,\$X1		21.01	90		014003.00
BXE,\$+1.32		14005.32	C2		014003.40
SIC,SEN		1310.00	80		014004.00

B,SERS	-KC, BIT 40, IX, CORE STG. FAILED.	1304.10 00	014004.40
LX,\$X0,BIT41	-TEST KC, BIT 41, INT. MEM. AND	17377.00 10	014005.00
L%BU□,BIT13	-INDEX CORE STORAGE.	17343.00 80 000000.20 50	014005.40
LX,\$X1,BIT13		17343.02 10	014006.40
KC,\$X0,\$R		11.01 90	014007.00
BXE,\$+1.32		14011.32 C2	014007.40
SIC,SEN		1310.00 80	014010.00
B,SERS	-KC, BIT 41, INT. MEM. FAILED.	1304.10 00	014010.40
-			
KC,\$X0,\$X1		21.01 90	014011.00
BXE,\$+1.32		14013.32 C2	014011.40
SIC,SEN		1310.00 80	014012.00
B,SERS	-KC, BIT 41, IX, CORE STG. FAILED	1304.10 00	014012.40
LX,\$X0,BIT42	-TEST KC, BIT 42, INT. MEM. AND	17400.00 10	014013.00
L%BU□,BIT14	-INDEX CORE STORAGE.	17344.00 80 000000.20 50	014013.40
LX,\$X1,BIT14		17344.02 10	014014.40
KC,\$X0,\$R		11.01 90	014015.00
BXE,\$+1.32		14017.32 C2	014015.40
-			
SIC,SEN		1310.00 80	014016.00
B,SERS	-KC, BIT 42, INT. MEM. FAILED.	1304.10 00	014016.40
KC,\$X0,\$X1		21.01 90	014017.00
BXE,\$+1.32		14021.32 C2	014017.40
SIC,SEN		1310.00 80	014020.00
B,SERS	-KC, BIT 42, IX, CORE STG. FAILED.	1304.10 00	014020.40
-			
LX,\$X0,BIT43	-TEST KC, BIT 43, INT. MEM. AND	17401.00 10	014021.00
L%BU□,BIT15	-INDEX CORE STORAGE.	17345.00 80 000000.20 50	014021.40
LX,\$X1,BIT15		17345.02 10	014022.40
KC,\$X0,\$R		11.01 90	014023.00
BXE,\$+1.32		14025.32 C2	014023.40
SIC,SEN		1310.00 80	014024.00
B,SERS	-KC, BIT 43, INT. MEM. FAILED.	1304.10 00	014024.40
-			
KC,\$X0,\$X1		21.01 90	014025.00
BXE,\$+1.32		14027.32 C2	014025.40
SIC,SEN		1310.00 80	014026.00
B,SERS	-KC, BIT 43, IX, CORE STG. FAILED.	1304.10 00	014026.40
-			
LX,\$X0,BIT44	-TEST KC, BIT 44, INT. MEM. AND	17402.00 10	014027.00
L%BU□,BIT16	-INDEX CORE STORAGE.	17346.00 80 000000.20 50	014027.40
LX,\$X1,BIT16		17346.02 10	014030.40
KC,\$X0,\$R		11.01 90	014031.00
BXE,\$+1.32		14033.32 C2	014031.40
SIC,SEN		1310.00 80	014032.00
B,SERS	-KC, BIT 44, INT. MEM. FAILED.	1304.10 00	014032.40
-			
KC,\$X0,\$X1		21.01 90	014033.00
BXE,\$+1.32		14035.32 C2	014033.40
SIC,SEN		1310.00 80	014034.00
B,SERS	-KC, BIT 44, IX, CORE STG. FAILED.	1304.10 00	014034.40
-			
LX,\$X0,BIT45	-TEST KC, BIT 45, INT. MEM. AND	17403.00 10	014035.00
L%BU□,BIT17	-INDEX CORE STORAGE.	17347.00 80 000000.20 50	014035.40
LX,\$X1,BIT17		17347.02 10	014036.40
KC,\$X0,\$R		11.01 90	014037.00
BXE,\$+1.32		14041.32 C2	014037.40
SIC,SEN		1310.00 80	014040.00
B,SERS	-KC, BIT 45, INT. MEM. FAILED.	1304.10 00	014040.40
-			
KC,\$X0,\$X1		21.01 90	014041.00
BXE,\$+1.32		14043.32 C2	014041.40
SIC,SEN		1310.00 80	014042.00
B,SERS	-KC, BIT 45, IX, CORE STG. FAILED.	1304.10 00	014042.40

B,\$+1.0
BD,I122
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP

LX,\$X13,IC212
V+,\$X13,BIT1
SX,\$X13,IC212

-UPDATE CONTINUITY CHECK.

14044.10 00
13667.04 00
1311.40 80
1301.10 00
14045.44 00

014043.00
014043.40
014044.00
014044.40
014045.00

14203.32 10
17327.32 80
14203.33 10

014045.40
014046.00
014046.40

-THIS SECTION TESTS THE 20 POSSIBLE COM-
 -BINATIONS OF KV AS DEFINED BY THE
 -BELOW TRUTH TABLE. THE CONDITIONS ARE
 -DEFINED AS FOLLOWS.

- A, INDEX POSITIVE.
- B, MEMORY POSITIVE.
- C, SUM OF MEM AND IX GRTR
 -THAN 2 EXP 24, IE, EAC.
- D, INDEX EQUAL TO MEMORY.
- E, ABSOLUTE INDEX GRTR THAN
 -ABSOLUTE MEMORY.

-A ONE IN THE TABLE INDICATES THE COND-
 -ITION IS MET. COND D AND E CAN NEVER
 -BE MET AND HENCE THOSE 8 CASES HAVE
 -BEEN DELETED, ALONG WITH FOUR OTHER
 -IMPOSSIBILITIES.

-CONDITIONS					INDEX	MEMORY
- A	B	C	D	E		
- 1	1	1	1	0	+400000.00	+400000.00
- 1	1	1	0	0	+400000.00	+600000.00
- 1	1	0	1	0	+000000.00	+000000.00
- 1	1	0	0	1	+000002.00	+000001.00
- 1	0	1	1	0	+400000.00	-400000.00
- 1	0	1	0	1	+600000.00	-400000.00
- 1	0	1	0	0	+400000.00	-600000.00
- 1	0	0	1	0	+000000.00	-000000.00
- 1	0	0	0	1	+000002.00	-000001.00
- 1	0	0	0	0	+000001.00	-000002.00
- 0	1	1	1	0	-400000.00	+400000.00
- 0	1	1	0	1	-600000.00	+400000.00
- 0	1	1	0	0	-400000.00	+600000.00
- 0	1	0	1	0	-000000.00	+000000.00
- 0	1	0	0	1	-000002.00	+000001.00
- 0	1	0	0	0	-000001.00	+000002.00
- 0	0	1	1	0	-400000.00	-400000.00
- 0	0	1	0	0	-400000.00	-600000.00
- 0	0	0	1	0	-000000.00	-000000.00
- 0	0	0	0	0	-000001.00	-000002.00

1123	LX,\$X0,BIT0 LX,\$X1,I12K1 LX,\$X2,BIT16 LX,\$X3,BIT17 LX,\$X4,I12K2 LX,\$X5,I12K3 LX,\$X6,I12K4 LX,\$X7,I12K5 LX,\$X8,I00Z LX,\$X9,BIT24 Z,\$IND	-INITIALIZE FOR EXHAUSTIVE -KV TEST	17326.00 10 14205.02 10 17346.04 10 17347.06 10 14206.10 10 14207.12 10 14210.14 10 14211.16 10 17306.20 10 17356.22 10 13.22 00	014047.00 014047.40 014050.00 014050.40 014051.00 014051.40 014052.00 014052.40 014053.00 014053.40 014054.00
	KV,\$X0,\$X0 SIC,SEN BXL,SERS SIC,SEN BXH,SERS SIC,SEN BZXE,SERS Z,\$IND	-X AND MEM PLUS, X EQUAL MEM, EAC. -11110	20.00 90 1310.00 80 1304.32 42 1310.00 80 1304.33 42 1310.00 80 1304.32 C0 13.22 00	014054.40 014055.00 014055.40 014056.00 014056.40 014057.00 014057.40 014060.00
	KV,\$X0,\$X1 SIC,SEN BXH,SERS SIC,SEN BXE,SERS SIC,SEN BZXL,SERS Z,\$IND	-X AND MEM PLUS, X LESS MEM, EAC. -11100	21.00 90 1310.00 80 1304.33 42 1310.00 80 1304.32 C2 1310.00 80 1304.32 40 13.22 00	014060.40 014061.00 014061.40 014062.00 014062.40 014063.00 014063.40 014064.00
	KV,\$X8,\$X8 SIC,SEN BXH,SERS SIC,SEN BXL,SERS SIC,SEN BZXE,SERS Z,\$IND	-X AND MEM PLUS ZERO. -11010	30.20 90 1310.00 80 1304.33 42 1310.00 80 1304.32 42 1310.00 80 1304.32 C0 13.22 00	014064.40 014065.00 014065.40 014066.00 014066.40 014067.00 014067.40 014070.00
	KV,\$X2,\$X3 SIC,SEN BXL,SERS SIC,SEN BXE,SERS SIC,SEN BZXH,SERS Z,\$IND	-X AND MEM PLUS, MEM LESS X. -11001	23.04 90 1310.00 80 1304.32 42 1310.00 80 1304.32 C2 1310.00 80 1304.33 40 13.22 00	014070.40 014071.00 014071.40 014072.00 014072.40 014073.00 014073.40 014074.00
	KV,\$X0,\$X4 SIC,SEN BXE,SERS SIC,SEN BXL,SERS SIC,SEN BZXH,SERS Z,\$IND	-MEM AND X EQUAL, OP SIGN, EAC. -10110	24.00 90 1310.00 80 1304.32 C2 1310.00 80 1304.32 42 1310.00 80 1304.33 40 13.22 00	014074.40 014075.00 014075.40 014076.00 014076.40 014077.00 014077.40 014100.00

KV,\$X1,\$X4	-X GRTR AND PLUS THAN MEM, MINUS, EAC.	24.02 90	014100.40
SIC,SEN	-10101	1310.00 80	014101.00
BXL,SERS		1304.32 42	014101.40
SIC,SEN		1310.00 80	014102.00
BXE,SERS		1304.32 C2	014102.40
SIC,SEN		1310.00 80	014103.00
BZXH,SERS		1304.33 40	014103.40
Z,\$IND		13.22 00	014104.00
-			
KV,\$X0,\$X5	-X LESS AND PLUS THAN MEM, MINUS, EAC.	25.00 90	014104.40
SIC,SEN	-10100	1310.00 80	014105.00
BXL,SERS		1304.32 42	014105.40
SIC,SEN		1310.00 80	014106.00
BXE,SERS		1304.32 C2	014106.40
SIC,SEN		1310.00 80	014107.00
BZXH,SERS		1304.33 40	014107.40
Z,\$IND		13.22 00	014110.00
-			
KV,\$X8,\$X9	-X PLUS ZERO, MEM MINUS ZERO	31.20 90	014110.40
SIC,SEN	-10010	1310.00 80	014111.00
BXL,SERS		1304.32 42	014111.40
SIC,SEN		1310.00 80	014112.00
BXH,SERS		1304.33 42	014112.40
SIC,SEN		1310.00 80	014113.00
BZXE,SERS		1304.32 C0	014113.40
Z,\$IND		13.22 00	014114.00
-			
KV,\$X2,\$X6	-X PLUS GRTR THAN MEM MINUS	26.04 90	014114.40
SIC,SEN	-10001	1310.00 80	014115.00
BXL,SERS		1304.32 42	014115.40
SIC,SEN		1310.00 80	014116.00
BXE,SERS		1304.32 C2	014116.40
SIC,SEN		1310.00 80	014117.00
BZXH,SERS		1304.33 40	014117.40
Z,\$IND		13.22 00	014120.00
-			
KV,\$X3,\$X7	-X PLUS LESS THAN MEM MINUS	27.06 90	014120.40
SIC,SEN	-10000	1310.00 80	014121.00
BXL,SERS		1304.32 42	014121.40
SIC,SEN		1310.00 80	014122.00
BXE,SERS		1304.32 C2	014122.40
SIC,SEN		1310.00 80	014123.00
BZXH,SERS		1304.33 40	014123.40
Z,\$IND		13.22 00	014124.00
-			
KV,\$X4,\$X0	-X MINUS EQUAL MEM PLUS, EAC.	20.10 90	014124.40
SIC,SEN	-01110	1310.00 80	014125.00
BXH,SERS		1304.33 42	014125.40
SIC,SEN		1310.00 80	014126.00
BZXL,SERS		1304.32 40	014126.40
SIC,SEN		1310.00 80	014127.00
BXE,SERS		1304.32 C2	014127.40
Z,\$IND		13.22 00	014130.00

KV,\$X5,\$X0	-X MINUS GRTR THAN MEM PLUS, EAC.	20.12 90	014130.40
SIC,SEN	-01101	1310.00 80	014131.00
BXH,SERS		1304.33 42	014131.40
SIC,SEN		1310.00 80	014132.00
BXE,SERS		1304.32 C2	014132.40
SIC,SEN		1310.00 80	014133.00
BZXL,SERS		1304.32 40	014133.40
Z,\$IND		13.22 00	014134.00
-			
KV,\$X4,\$X1	-X MINUS LESS THAN MEM PLUS, EAC.	21.10 90	014134.40
SIC,SEN	-01100	1310.00 80	014135.00
BXH,SERS		1304.33 42	014135.40
SIC,SEN		1310.00 80	014136.00
BXE,SERS		1304.32 C2	014136.40
SIC,SEN		1310.00 80	014137.00
BZXL,SERS		1304.32 40	014137.40
Z,\$IND		13.22 00	014140.00
-			
KV,\$X9,\$X8	-X MINUS ZERO, MEM PLUS ZERO	30.22 90	014140.40
SIC,SEN	-01010	1310.00 80	014141.00
BXH,SERS		1304.33 42	014141.40
SIC,SEN		1310.00 80	014142.00
BXL,SERS		1304.32 42	014142.40
SIC,SEN		1310.00 80	014143.00
BZXE,SERS		1304.32 C0	014143.40
Z,\$IND		13.22 00	014144.00
-			
KV,\$X7,\$X3	-X MINUS GRTR THAN MEM PLUS.	23.16 90	014144.40
SIC,SEN	-01001	1310.00 80	014145.00
BXH,SERS		1304.33 42	014145.40
SIC,SEN		1310.00 80	014146.00
BXE,SERS		1304.32 C2	014146.40
SIC,SEN		1310.00 80	014147.00
BZXL,SERS		1304.32 40	014147.40
Z,\$IND		13.22 00	014150.00
-			
KV,\$X6,\$X2	-X MINUS LESS THAN MEM PLUS.	22.14 90	014150.40
SIC,SEN	-01000	1310.00 80	014151.00
BXH,SERS		1304.33 42	014151.40
SIC,SEN		1310.00 80	014152.00
BXE,SERS		1304.32 C2	014152.40
SIC,SEN		1310.00 80	014153.00
BZXL,SERS		1304.32 40	014153.40
Z,\$IND		13.22 00	014154.00
-			
KV,\$X4,\$X4	-X MINUS EQUAL MEM MINUS, EAC.	24.10 90	014154.40
SIC,SEN	-00110	1310.00 80	014155.00
BXL,SERS		1304.32 42	014155.40
SIC,SEN		1310.00 80	014156.00
BXH,SERS		1304.33 42	014156.40
SIC,SEN		1310.00 80	014157.00
BZXE,SERS		1304.32 C0	014157.40
Z,\$IND		13.22 00	014160.00

KV,\$X4,\$X5	-X MINUS GRTR THAN MEM MINUS, EAC.	25.10 90	014160.40
SIC,SEN	-00100	1310.00 80	014161.00
BXE,SERS		1304.32 C2	014161.40
SIC,SEN		1310.00 80	014162.00
BXL,SERS		1304.32 42	014162.40
SIC,SEN		1310.00 80	014163.00
BZXH,SERS		1304.33 40	014163.40
Z,\$IND		13.22 00	014164.00
-			
KV,\$X9,\$X9	-X MINUS ZERO EQUAL MEM MINUS ZERO.	31.22 90	014164.40
SIC,SEN	-00010	1310.00 80	014165.00
BXH,SERS		1304.33 42	014165.40
SIC,SEN		1310.00 80	014166.00
BXL,SERS		1304.32 42	014166.40
SIC,SEN		1310.00 80	014167.00
BZXE,SERS		1304.32 C0	014167.40
Z,\$IND		13.22 00	014170.00
-			
KV,\$X6,\$X7	-X MINUS GRTR THAN MEM MINUS.	27.14 90	014170.40
SIC,SEN	-00000	1310.00 80	014171.00
BXL,SERS		1304.32 42	014171.40
SIC,SEN		1310.00 80	014172.00
BXE,SERS		1304.32 C2	014172.40
SIC,SEN		1310.00 80	014173.00
BZXH,SERS		1304.33 40	014173.40
Z,\$IND		13.22 00	014174.00
-			
B,\$+1.0		14175.50 00	014174.40
BD,I123		14047.04 00	014175.00
SIC,SENO+.32		1311.40 80	014175.40
B,SSW	-TO SSIP.	1301.10 00	014176.00
BD,\$+.32		14177.04 00	014176.40
-			
LX,\$X13,IC212	-UPDATE CONTINUITY CHECK.	14203.32 10	014177.00
V+,\$X13,BIT2		17330.32 B0	014177.40
SX,\$X13,IC212		14203.33 10	014200.00
-			
LX,\$X13,IC212	-UPDATE CONTINUITY CHECK.	14203.32 10	014200.40
KV,\$X13,ICK212		14204.32 90	014201.00
SIC,SEN		1310.00 80	014201.40
BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	014202.00
BD,I14		14212.04 00	014202.40
CNOP			
-			
IC212	XW,0,0,0	0.00 00 000000.00 00	014203.00
ICK212	XW,%8#700000.00,0,0	700000.00 00 000000.00 00	014204.00
I12K1	XW,%8#600000.00,0,0	600000.00 00 000000.00 00	014205.00
I12K2	XW,%8#-400000.00,0,0	400000.00 80 000000.00 00	014206.00
I12K3	XW,%8#-600000.00,0,0	600000.00 80 000000.00 00	014207.00
I12K4	XW,-1.00,0,0	1.00 80 000000.00 00	014210.00
I12K5	XW,-2.00,0,0	2.00 80 000000.00 00	014211.00

-----1214-----LX BITS 46-63 AND SR TEST.

I14	LX,\$X1,I14ID	-UPDATE IDENT.	14215.02 10	014212.00
	SX,\$X1,DPET13		1437.03 10	014212.40
	SIC,RET		1306.40 80	014213.00
	B,IDF1		1443.10 00	014213.40
	Z,IC214		15165.22 00	014214.00
	BD,I141		14216.04 00	014214.40
	CNOP			
I14ID	%IQSZ=DD%BU,64,8=,I214	Z.		014215.00
I141	Z,\$X0	-TEST LX AND SR, BIT 46.	20.22 00	014216.00
	LX,\$X0,BIT46		17404.00 10	014216.40
	LX,\$X1,BIT0	-REFILL BIT MINUS 46.	17326.02 10	014217.00
	Z,I14DMP		15167.22 00	014217.40
	SR,\$X0,I14DMP		15167.01 70	014220.00
	KV,\$X1,I14DMP		15167.02 90	014220.40
	BXE,\$+1.32		14222.72 C2	014221.00
	SIC,SEN	-LX BIT 46 FRM EXT. MEM. OR	1310.00 80	014221.40
	B,SERS	-SR BIT 46 TO EXT. MEM. FAILS.	1304.10 00	014222.00
	Z,\$X0		20.22 00	014222.40
	Z,I14DMP		15167.22 00	014223.00
	L%BU=,BIT46		17404.00 80	014223.40
	LX,\$X0,\$R		11.00 10	014224.40
	SR,\$X0,I14DMP		15167.01 70	014225.00
	KV,\$X1,I14DMP		15167.02 90	014225.40
	BXE,\$+1.32		14227.72 C2	014226.00
	SIC,SEN	-LX BIT 46 FRM INT. MEM. OR	1310.00 80	014226.40
	B,SERS	-SR BIT 46 TO EXT. MEM. FAILS.	1304.10 00	014227.00
	Z,\$X0		20.22 00	014227.40
	Z,I14DMP		15167.22 00	014230.00
	LX,\$X0,BIT46		17404.00 10	014230.40
	LX,\$X2,\$X0		20.04 10	014231.00
	SR,\$X2,I14DMP		15167.05 70	014231.40
	KV,\$X1,I14DMP		15167.02 90	014232.00
	BXE,\$+1.32		14234.32 C2	014232.40
	SIC,SEN	-LX BIT 46 FROM IX CORE STG OR	1310.00 80	014233.00
	B,SERS	-SR BIT 46 TO EXT. MEM. FAILS.	1304.10 00	014233.40
	Z,\$R		11.22 00	014234.00
	LX,\$X0,BIT 46		17404.00 10	014234.40
	SR,\$X0,\$R		11.01 70	014235.00
	KV,\$X1,\$R		11.02 90	014235.40
	BXE,\$+1.32		14237.72 C2	014236.00
	SIC,SEN	-LX BIT 46 FRM EXT. MEM. OR	1310.00 80	014236.40
	B,SERS	-SR BIT 46 TO INT. MEM. FAILS.	1304.10 00	014237.00
	Z,\$X3		23.22 00	014237.40
	LX,\$X0,BIT 46		17404.00 10	014240.00
	SR,\$X0,\$X3		23.01 70	014240.40
	KV,\$X1,\$X3		23.02 90	014241.00
	BXE,\$+1.32		14243.32 C2	014241.40
	SIC,SEN	-LX BIT 46 FRM EXT. MEM. OR	1310.00 80	014242.00
	B,SERS	-SR BIT 46 TO INDEX CORE STG FAILS.	1304.10 00	014242.40
	B,\$+1.0		14244.10 00	014243.00
	BD,I141		14216.04 00	014243.40
	SIC,SEN0+.32		1311.40 80	014244.00
	B,\$SW	-TO SSIP.	1301.10 00	014244.40
	BD,\$+.32		14245.44 00	014245.00

LX,\$X13,IC214
V+,\$X13,BIT0
SX,\$X13,IC214

-UPDATE CONTINUITY CHECK.

15165.32 10
17326.32 B0
15165.33 10

014245.40
014246.00
014246.40

1142	Z,\$X0	-TEST LX AND SR,BIT 47.	20.22 00	014247.00
	LX,\$X0,BIT 47		17405.00 10	014247.40
	LX,\$X1,BIT 1	-REFILL BIT MINUS 46.	17327.02 10	014250.00
	Z,I14DMP		15167.22 00	014250.40
	SR,\$X0,I14DMP		15167.01 70	014251.00
	KV,\$X1,I14DMP		15167.02 90	014251.40
	BXE,\$+1.32		14253.72 C2	014252.00
	SIC,SEN	-LX BIT 47 FRM EXT. MEM. OR	1310.00 80	014252.40
	B,SERS	-SR BIT 47 TO EXT. MEM. FAILS.	1304.10 00	014253.00
	-			
	Z,\$X0		20.22 00	014253.40
	Z,I14DMP		15167.22 00	014254.00
	L%BU\$,BIT 47		17405.00 80	014254.40
	LX,\$X0,SR		11.00 10	014255.40
	SR,\$X0,I14DMP		15167.01 70	014256.00
	KV,\$X1,I14DMP		15167.02 90	014256.40
	BXE,\$+1.32		14260.72 C2	014257.00
	SIC,SEN	-LX BIT 47 FRM INT. MEM. OR	1310.00 80	014257.40
	B,SERS	-SR BIT 47 TO EXT. MEM. FAILS.	1304.10 00	014260.00
	-			
	Z,\$X0		20.22 00	014260.40
	Z,I14DMP		15167.22 00	014261.00
	LX,\$X0,BIT 47		17405.00 10	014261.40
	LX,\$X2,\$X0		20.04 10	014262.00
	SR,\$X2,I14DMP		15167.05 70	014262.40
	KV,\$X1,I14DMP		15167.02 90	014263.00
	BXE,\$+1.32		14265.32 C2	014263.40
	SIC,SEN	-LX BIT 47 FROM IX CORE STG OR	1310.00 80	014264.00
	B,SERS	-SR BIT 47 TO EXT. MEM. FAILS.	1304.10 00	014264.40
	-			
	Z,\$SR		11.22 00	014265.00
	LX,\$X0,BIT47		17405.00 10	014265.40
	SR,\$X0,SR		11.01 70	014266.00
	KV,\$X1,SR		11.02 90	014266.40
	BXE,\$+1.32		14270.72 C2	014267.00
	SIC,SEN	-LX BIT 47 FRM EXT. MEM. OR	1310.00 80	014267.40
	B,SERS	-SR BIT 47 TO INT. MEM. FAILS.	1304.10 00	014270.00
	-			
	Z,\$X3		23.22 00	014270.40
	LX,\$X0,BIT47		17405.00 10	014271.00
	SR,\$X0,\$X3		23.01 70	014271.40
	KV,\$X1,\$X3		23.02 90	014272.00
	BXE,\$+1.32		14274.32 C2	014272.40
	SIC,SEN	-LX BIT 47 FRM EXT. MEM. OR	1310.00 80	014273.00
	B,SERS	-SR BIT 47 TO INDEX CORE STG FAILS.	1304.10 00	014273.40
	-			
	B,\$+1.0		14275.10 00	014274.00
	BD,I142		14247.04 00	014274.40
	SIC,SEN0+.32		1311.40 80	014275.00
	B,SSW	-TO SSIP.	1301.10 00	014275.40
	BD,\$+.32		14276.44 00	014276.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014276.40
	V+,\$X13,BIT1		17327.32 B0	014277.00
	SX,\$X13,IC214		15165.33 10	014277.40

1143	Z,\$X0	-TEST LX AND SR, BIT 48.	20.22 00		014300.00
	LX,\$X0,BIT48		17406.00 10		014300.40
	LX,\$X1,BIT2	-REFILL BIT MINUS 46.	17330.02 10		014301.00
	Z,I14DMP		15167.22 00		014301.40
	SR,\$X0,I14DMP		15167.01 70		014302.00
	KV,\$X1,I14DMP		15167.02 90		014302.40
	BXE,\$+1.32		14304.72 C2		014303.00
	SIC,SEN	-LX BIT 48 FRM EXT. MEM. OR	1310.00 80		014303.40
	B,SERS	-SR BIT 48 TO EXT. MEM. FAILS.	1304.10 00		014304.00
	-				
	Z,\$X0		20.22 00		014304.40
	Z,I14DMP		15167.22 00		014305.00
	L%BU,BIT48		17406.00 80	000000.20 50	014305.40
	LX,\$X0,\$R		11.00 10		014306.40
	SR,\$X0,I14DMP		15167.01 70		014307.00
	KV,\$X1,I14DMP		15167.02 90		014307.40
	BXE,\$+1.32		14311.72 C2		014310.00
	SIC,SEN	-LX BIT 48 FRM INT. MEM. OR	1310.00 80		014310.40
	B,SERS	-SR BIT 48 TO EXT. MEM. FAILS.	1304.10 00		014311.00
	-				
	Z,\$X0		20.22 00		014311.40
	Z,I14DMP		15167.22 00		014312.00
	LX,\$X0,BIT48		17406.00 10		014312.40
	LX,\$X2,\$X0		20.04 10		014313.00
	SR,\$X2,I14DMP		15167.05 70		014313.40
	KV,\$X1,I14DMP		15167.02 90		014314.00
	BXE,\$+1.32		14316.32 C2		014314.40
	SIC,SEN	-LX BIT 48 FROM IX CORE STG OR	1310.00 80		014315.00
	B,SERS	-SR BIT 48 TO EXT. MEM. FAILS.	1304.10 00		014315.40
	-				
	Z,\$R		11.22 00		014316.00
	LX,\$X0,BIT48		17406.00 10		014316.40
	SR,\$X0,\$R		11.01 70		014317.00
	KV,\$X1,\$R		11.02 90		014317.40
	BXE,\$+1.32		14321.72 C2		014320.00
	SIC,SEN	-LX BIT 48 FRM EXT. MEM. OR	1310.00 80		014320.40
	B,SERS	-SR BIT 48 TO INT. MEM. FAILS.	1304.10 00		014321.00
	-				
	Z,\$X3		23.22 00		014321.40
	LX,\$X0,BIT48		17406.00 10		014322.00
	SR,\$X0,\$X3		23.01 70		014322.40
	KV,\$X1,\$X3		23.02 90		014323.00
	BXE,\$+1.32		14325.32 C2		014323.40
	SIC,SEN	-LX BIT 48 FRM EXT. MEM. OR	1310.00 80		014324.00
	B,SERS	-SR BIT 48 TO INDEX CORE STG FAILS.	1304.10 00		014324.40
	-				
	B,\$+1.0		14326.10 00		014325.00
	BD,I143		14300.04 00		014325.40
	SIC,SEN0+.32		1311.40 80		014326.00
	B,SSW	-TO SSIP.	1301.10 00		014326.40
	BD,\$+.32		14327.44 00		014327.00
	-				
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10		014327.40
	V+,\$X13,BIT2		17330.32 B0		014330.00
	SX,\$X13,IC214		15165.33 10		014330.40

1144	Z,\$X0	-TEST LX AND SR, BIT 49.	20.22 00	014331.00
	LX,\$X0,BIT49		17407.00 10	014331.40
	LX,\$X1,BIT3	-REFILL BIT MINUS 46.	17331.02 10	014332.00
	Z,I14DMP		15167.22 00	014332.40
	SR,\$X0,I14DMP		15167.01 70	014333.00
	KV,\$X1,I14DMP		15167.02 90	014333.40
	BXE,\$+1.32		14335.72 C2	014334.00
	SIC,SEN	-LX BIT 49 FRM EXT. MEM. OR	1310.00 80	014334.40
	B,SERS	-SR BIT 49 TO EXT. MEM. FAILS.	1304.10 00	014335.00
	-			
	Z,\$X0		20.22 00	014335.40
	Z,I14DMP		15167.22 00	014336.00
	L%BU,BIT49		17407.00 80	014336.40
	LX,\$X0,\$R		11.00 10	014337.40
	SR,\$X0,I14DMP		15167.01 70	014340.00
	KV,\$X1,I14DMP		15167.02 90	014340.40
	BXE,\$+1.32		14342.72 C2	014341.00
	SIC,SEN	-LX BIT 49 FRM INT. MEM. OR	1310.00 80	014341.40
	B,SERS	-SR BIT 49 TO EXT. MEM. FAILS.	1304.10 00	014342.00
	-			
	Z,\$X0		20.22 00	014342.40
	Z,I14DMP		15167.22 00	014343.00
	LX,\$X0,BIT49		17407.00 10	014343.40
	LX,\$X2,\$X0		20.04 10	014344.00
	SR,\$X2,I14DMP		15167.05 70	014344.40
	KV,\$X1,I14DMP		15167.02 90	014345.00
	BXE,\$+1.32		14347.32 C2	014345.40
	SIC,SEN	-LX BIT 49 FROM IX CORE STG OR	1310.00 80	014346.00
	B,SERS	-SR BIT 49 TO EXT. MEM. FAILS.	1304.10 00	014346.40
	-			
	Z,\$R		11.22 00	014347.00
	LX,\$X0,BIT49		17407.00 10	014347.40
	SR,\$X0,\$R		11.01 70	014350.00
	KV,\$X1,\$R		11.02 90	014350.40
	BXE,\$+1.32		14352.72 C2	014351.00
	SIC,SEN	-LX BIT 49 FRM EXT. MEM. OR	1310.00 80	014351.40
	B,SERS	-SR BIT 49 TO INT. MEM. FAILS.	1304.10 00	014352.00
	-			
	Z,\$X3		23.22 00	014352.40
	LX,\$X0,BIT49		17407.00 10	014353.00
	SR,\$X0,\$X3		23.01 70	014353.40
	KV,\$X1,\$X3		23.02 90	014354.00
	BXE,\$+1.32		14356.32 C2	014354.40
	SIC,SEN	-LX BIT 49 FRM EXT. MEM. OR	1310.00 80	014355.00
	B,SERS	-SR BIT 49 TO INDEX CORE STG FAILS.	1304.10 00	014355.40
	-			
	B,\$+1.0		14357.10 00	014356.00
	BD,I144		14331.04 00	014356.40
	SIC,SEN0+.32		1311.40 80	014357.00
	B,SSW	-TO SSIP.	1301.10 00	014357.40
	BD,\$+.32		14360.44 00	014360.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014360.40
	V+,\$X13,BIT3		17331.32 B0	014361.00
	SX,\$X13,IC214		15165.33 10	014361.40

1145	Z,\$X0	-TEST LX AND SR, BIT 50.	20.22 00	014362.00
	LX,\$X0,BIT50		17410.00 10	014362.40
	LX,\$X1,BIT4	-REFILL BIT MINUS 46.	17332.02 10	014363.00
	Z,I14DMP		15167.22 00	014363.40
	SR,\$X0,I14DMP		15167.01 70	014364.00
	KV,\$X1,I14DMP		15167.02 90	014364.40
	BXE,\$+1.32		14366.72 C2	014365.00
	SIC,SEN	-LX BIT 50 FRM EXT. MEM. OR	1310.00 80	014365.40
	B,SERS	-SR BIT 50 TO EXT. MEM. FAILS.	1304.10 00	014366.00
	-			
	Z,\$X0		20.22 00	014366.40
	Z,I14DMP		15167.22 00	014367.00
	L%BU, BIT50		17410.00 80	014367.40
	LX,\$X0,\$R		11.00 10	014370.40
	SR,\$X0,I14DMP		15167.01 70	014371.00
	KV,\$X1,I14DMP		15167.02 90	014371.40
	BXE,\$+1.32		14373.72 C2	014372.00
	SIC,SEN	-LX BIT 50 FRM INT. MEM. OR	1310.00 80	014372.40
	B,SERS	-SR BIT 50 TO EXT. MEM. FAILS.	1304.10 00	014373.00
	-			
	Z,\$X0		20.22 00	014373.40
	Z,I14DMP		15167.22 00	014374.00
	LX,\$X0,BIT50		17410.00 10	014374.40
	LX,\$X2,\$X0		20.04 10	014375.00
	SR,\$X2,I14DMP		15167.05 70	014375.40
	KV,\$X1,I14DMP		15167.02 90	014376.00
	BXE,\$+1.32		14400.32 C2	014376.40
	SIC,SEN	-LX BIT 50 FROM IX CORE STG OR	1310.00 80	014377.00
	B,SERS	-SR BIT 50 TO EXT. MEM. FAILS.	1304.10 00	014377.40
	-			
	Z,\$R		11.22 00	014400.00
	LX,\$X0,BIT50		17410.00 10	014400.40
	SR,\$X0,\$R		11.01 70	014401.00
	KV,\$X1,\$R		11.02 90	014401.40
	BXE,\$+1.32		14403.72 C2	014402.00
	SIC,SEN	-LX BIT 50 FRM EXT. MEM. OR	1310.00 80	014402.40
	B,SERS	-SR BIT 50 TO INT. MEM. FAILS.	1304.10 00	014403.00
	-			
	Z,\$X3		23.22 00	014403.40
	LX,\$X0,BIT50		17410.00 10	014404.00
	SR,\$X0,\$X3		23.01 70	014404.40
	KV,\$X1,\$X3		23.02 90	014405.00
	BXE,\$+1.32		14407.32 C2	014405.40
	SIC,SEN	-LX BIT 50 FRM EXT. MEM. OR	1310.00 80	014406.00
	B,SERS	-SR BIT 50 TO INDEX CORE STG FAILS.	1304.10 00	014406.40
	-			
	B,\$+1.0		14410.10 00	014407.00
	BD,I145		14362.04 00	014407.40
	SIC,SEN0+.32		1311.40 80	014410.00
	B,SSW	-TO SSIP.	1301.10 00	014410.40
	BD,\$+.32		14411.44 00	014411.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014411.40
	V+,\$X13,BIT4		17332.32 80	014412.00
	SX,\$X13,IC214		15165.33 10	014412.40

1146	Z,\$X0	-TEST LX AND SR, BIT 51.	20.22 00	014413.00
	LX,\$X0,BIT51		17411.00 10	014413.40
	LX,\$X1,BIT5	-REFILL BIT MINUS 46.	17333.02 10	014414.00
	Z,I14DMP		15167.22 00	014414.40
	SR,\$X0,I14DMP		15167.01 70	014415.00
	KV,\$X1,I14DMP		15167.02 90	014415.40
	BXE,\$+1.32		14417.72 C2	014416.00
	SIC,SEN	-LX BIT 51 FRM EXT. MEM. OR	1310.00 80	014416.40
	B,SERS	-SR BIT 51 TO EXT. MEM. FAILS.	1304.10 00	014417.00
	-			
	Z,\$X0		20.22 00	014417.40
	Z,I14DMP		15167.22 00	014420.00
	L%BU,BIT51		17411.00 80	014420.40
	LX,\$X0,SR		11.00 10	014421.40
	SR,\$X0,I14DMP		15167.01 70	014422.00
	KV,\$X1,I14DMP		15167.02 90	014422.40
	BXE,\$+1.32		14424.72 C2	014423.00
	SIC,SEN	-LX BIT 51 FRM INT. MEM. OR	1310.00 80	014423.40
	B,SERS	-SR BIT 51 TO EXT. MEM. FAILS.	1304.10 00	014424.00
	-			
	Z,\$X0		20.22 00	014424.40
	Z,I14DMP		15167.22 00	014425.00
	LX,\$X0,BIT51		17411.00 10	014425.40
	LX,\$X2,\$X0		20.04 10	014426.00
	SR,\$X2,I14DMP		15167.05 70	014426.40
	KV,\$X1,I14DMP		15167.02 90	014427.00
	BXE,\$+1.32		14431.32 C2	014427.40
	SIC,SEN	-LX BIT 51 FROM IX CORE STG OR	1310.00 80	014430.00
	B,SERS	-SR BIT 51 TO EXT. MEM. FAILS.	1304.10 00	014430.40
	-			
	Z,\$SR		11.22 00	014431.00
	LX,\$X0,BIT51		17411.00 10	014431.40
	SR,\$X0,SR		11.01 70	014432.00
	KV,\$X1,SR		11.02 90	014432.40
	BXE,\$+1.32		14434.72 C2	014433.00
	SIC,SEN	-LX BIT 51 FRM EXT. MEM. OR	1310.00 80	014433.40
	B,SERS	-SR BIT 51 TO INT. MEM. FAILS.	1304.10 00	014434.00
	-			
	Z,\$X3		23.22 00	014434.40
	LX,\$X0,BIT51		17411.00 10	014435.00
	SR,\$X0,\$X3		23.01 70	014435.40
	KV,\$X1,\$X3		23.02 90	014436.00
	BXE,\$+1.32		14440.32 C2	014436.40
	SIC,SEN	-LX BIT 51 FRM EXT. MEM. OR	1310.00 80	014437.00
	B,SERS	-SR BIT 51 TO INDEX CORE STG FAILS.	1304.10 00	014437.40
	-			
	B,\$+1.0		14441.10 00	014440.00
	BD,I146		14413.04 00	014440.40
	SIC,SEN0+.32		1311.40 80	014441.00
	B,SSW	-TO SSIP.	1301.10 00	014441.40
	BD,\$+.32		14442.44 00	014442.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014442.40
	V+,\$X13,BIT5		17333.32 80	014443.00
	SX,\$X13,IC214		15165.33 10	014443.40

1147

Z,\$X0
LX,\$X0,BIT52
LX,\$X1,BIT6
Z,I14DMP
SR,\$X0,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-TEST LX AND SR, BIT 52.

-REFILL BIT MINUS 46.

-LX BIT 52 FRM EXT. MEM. OR
-SR BIT 52 TO EXT. MEM. FAILS.

20.22 00
17412.00 10
17334.02 10
15167.22 00
15167.01 70
15167.02 90
14450.72 C2
1310.00 80
1304.10 00

014444.00
014444.40
014445.00
014445.40
014446.00
014446.40
014447.00
014447.40
014450.00

Z,\$X0
Z,I14DMP
L%BU, BIT52
LX,\$X0,\$SR
SR,\$X0,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 52 FRM INT. MEM. OR
-SR BIT 52 TO EXT. MEM. FAILS.

20.22 00
15167.22 00
17412.00 80 000000.20 50
11.00 10
15167.01 70
15167.02 90
14455.72 C2
1310.00 80
1304.10 00

014450.40
014451.00
014451.40
014452.40
014453.00
014453.40
014454.00
014454.40
014455.00

Z,\$X0
Z,I14DMP
LX,\$X0,BIT52
LX,\$X2,\$X0
SR,\$X2,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 52 FROM IX CORE STG OR
-SR BIT 52 TO EXT. MEM. FAILS.

20.22 00
15167.22 00
17412.00 10
20.04 10
15167.05 70
15167.02 90
14462.32 C2
1310.00 80
1304.10 00

014455.40
014456.00
014456.40
014457.00
014457.40
014460.00
014460.40
014461.00
014461.40

Z,\$SR
LX,\$X0,BIT52
SR,\$X0,\$SR
KV,\$X1,\$SR
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 52 FRM EXT. MEM. OR
-SR BIT 52 TO INT. MEM. FAILS.

11.22 00
17412.00 10
11.01 70
11.02 90
14465.72 C2
1310.00 80
1304.10 00

014462.00
014462.40
014463.00
014463.40
014464.00
014464.40
014465.00

Z,\$X3
LX,\$X0,BIT52
SR,\$X0,\$X3
KV,\$X1,\$X3
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 52 FRM EXT. MEM. OR
-SR BIT 52 TO INDEX CORE STG FAILS.

23.22 00
17412.00 10
23.01 70
23.02 90
14471.32 C2
1310.00 80
1304.10 00

014465.40
014466.00
014466.40
014467.00
014467.40
014470.00
014470.40

B,\$+1.0
BD,I147
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP.

14472.10 00
14444.04 00
1311.40 80
1301.10 00
14473.44 00

014471.00
014471.40
014472.00
014472.40
014473.00

LX,\$X13,IC214
V+,\$X13,BIT6
SX,\$X13,IC214

-UPDATE CONTINUITY CHECK.

15165.32 10
17334.32 80
15165.33 10

014473.40
014474.00
014474.40

1148

Z,\$X0
LX,\$X0,BIT 53
LX,\$X1,BIT 7
Z,I14DMP
SR,\$X0,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-TEST LX AND SR,BIT 53.
-REFILL BIT MINUS 46.

-LX BIT 53 FRM EXT. MEM. OR
-SR BIT 53 TO EXT. MEM. FAILS.

20.22 00
17413.00 10
17335.02 10
15167.22 00
15167.01 70
15167.02 90
14501.72 C2
1310.00 80
1304.10 00

014475.00
014475.40
014476.00
014476.40
014477.00
014477.40
014500.00
014500.40
014501.00

Z,\$X0
Z,I14DMP
L%BU□,BIT 53
LX,\$X0,\$R
SR,\$X0,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 53 FRM INT. MEM. OR
-SR BIT 53 TO EXT. MEM. FAILS.

20.22 00
15167.22 00
17413.00 80 000000.20 50
11.00 10
15167.01 70
15167.02 90
14506.72 C2
1310.00 80
1304.10 00

014501.40
014502.00
014502.40
014503.40
014504.00
014504.40
014505.00
014505.40
014506.00

Z,\$X0
Z,I14DMP
LX,\$X0,BIT 53
LX,\$X2,\$X0
SR,\$X2,I14DMP
KV,\$X1,I14DMP
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 53 FROM IX CORE STG OR
-SR BIT 53 TO EXT. MEM. FAILS.

20.22 00
15167.22 00
17413.00 10
20.04 10
15167.05 70
15167.02 90
14513.32 C2
1310.00 80
1304.10 00

014506.40
014507.00
014507.40
014510.00
014510.40
014511.00
014511.40
014512.00
014512.40

Z,\$R
LX,\$X0,BIT 53
SR,\$X0,\$R
KV,\$X1,\$R
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 53 FRM EXT. MEM. OR
-SR BIT 53 TO INT. MEM. FAILS.

11.22 00
17413.00 10
11.01 70
11.02 90
14516.72 C2
1310.00 80
1304.10 00

014513.00
014513.40
014514.00
014514.40
014515.00
014515.40
014516.00

Z,\$X3
LX,\$X0,BIT 53
SR,\$X0,\$X3
KV,\$X1,\$X3
BXE,\$+1.32
SIC,SEN
B,SERS

-LX BIT 53 FRM EXT. MEM. OR
-SR BIT 53 TO INDEX CORE STG FAILS.

23.22 00
17413.00 10
23.01 70
23.02 90
14522.32 C2
1310.00 80
1304.10 00

014516.40
014517.00
014517.40
014520.00
014520.40
014521.00
014521.40

B,\$+1.0
BD,I148
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP.

14523.10 00
14475.04 00
1311.40 80
1301.10 00
14524.44 00

014522.00
014522.40
014523.00
014523.40
014524.00

LX,\$X13,IC214
V+,\$X13,BIT7
SX,\$X13,IC214

-UPDATE CONTINUITY CHECK.

15165.32 10
17335.32 B0
15165.33 10

014524.40
014525.00
014525.40

1149	Z,\$X0	-TEST LX AND SR,BIT 54.	20.22 00	014526.00
	LX,\$X0,BIT 54		17414.00 10	014526.40
	LX,\$X1,BIT 8	-REFILL BIT MINUS 46.	17336.02 10	014527.00
	Z,I14DMP		15167.22 00	014527.40
	SR,\$X0,I14DMP		15167.01 70	014530.00
	KV,\$X1,I14DMP		15167.02 90	014530.40
	BXE,\$+1.32		14532.72 C2	014531.00
	SIC,SEN	-LX BIT 54 FRM EXT. MEM. OR	1310.00 80	014531.40
	B,SERS	-SR BIT 54 TO EXT. MEM. FAILS.	1304.10 00	014532.00
	-			
	Z,\$X0		20.22 00	014532.40
	Z,I14DMP		15167.22 00	014533.00
	LX,BU,BIT 54		17414.00 80	014533.40
	LX,\$X0,\$R		11.00 10	014534.40
	SR,\$X0,I14DMP		15167.01 70	014535.00
	KV,\$X1,I14DMP		15167.02 90	014535.40
	BXE,\$+1.32		14537.72 C2	014536.00
	SIC,SEN	-LX BIT 54 FRM INT. MEM. OR	1310.00 80	014536.40
	B,SERS	-SR BIT 54 TO EXT. MEM. FAILS.	1304.10 00	014537.00
	-			
	Z,\$X0		20.22 00	014537.40
	Z,I14DMP		15167.22 00	014540.00
	LX,\$X0,BIT 54		17414.00 10	014540.40
	LX,\$X2,\$X0		20.04 10	014541.00
	SR,\$X2,I14DMP		15167.05 70	014541.40
	KV,\$X1,I14DMP		15167.02 90	014542.00
	BXE,\$+1.32		14544.32 C2	014542.40
	SIC,SEN	-LX BIT 54 FROM IX CORE STG OR	1310.00 80	014543.00
	B,SERS	-SR BIT 54 TO EXT. MEM. FAILS.	1304.10 00	014543.40
	-			
	Z,\$R		11.22 00	014544.00
	LX,\$X0,BIT 54		17414.00 10	014544.40
	SR,\$X0,\$R		11.01 70	014545.00
	KV,\$X1,\$R		11.02 90	014545.40
	BXE,\$+1.32		14547.72 C2	014546.00
	SIC,SEN	-LX BIT 54 FRM EXT. MEM. OR	1310.00 80	014546.40
	B,SERS	-SR BIT 54 TO INT. MEM. FAILS.	1304.10 00	014547.00
	-			
	Z,\$X3		23.22 00	014547.40
	LX,\$X0,BIT 54		17414.00 10	014550.00
	SR,\$X0,\$X3		23.01 70	014550.40
	KV,\$X1,\$X3		23.02 90	014551.00
	BXE,\$+1.32		14553.32 C2	014551.40
	SIC,SEN	-LX BIT 54 FRM EXT. MEM. OR	1310.00 80	014552.00
	B,SERS	-SR BIT 54 TO INDEX CORE STG FAILS.	1304.10 00	014552.40
	-			
	B,\$+1.0		14554.10 00	014553.00
	BD,I149		14526.04 00	014553.40
	SIC,SEN0+.32		1311.40 80	014554.00
	B,SSW	-TO SSIP.	1301.10 00	014554.40
	BD,\$+.32		14555.44 00	014555.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014555.40
	V+,\$X13,BIT8		17336.32 80	014556.00
	SX,\$X13,IC214		15165.33 10	014556.40

11410	Z,\$X0	-TEST LX AND SR,BIT 55.	20.22 00	014557.00
	LX,\$X0,BIT 55		17415.00 10	014557.40
	LX,\$X1,BIT 9	-REFILL BIT MINUS 46.	17337.02 10	014560.00
	Z,I14DMP		15167.22 00	014560.40
	SR,\$X0,I14DMP		15167.01 70	014561.00
	KV,\$X1,I14DMP		15167.02 90	014561.40
	BXE,\$+1.32		14563.72 C2	014562.00
	SIC,SEN	-LX BIT 55 FRM EXT. MEM. OR	1310.00 80	014562.40
	B,SERS	-SR BIT 55 TO EXT. MEM. FAILS.	1304.10 00	014563.00
	-			
	Z,\$X0		20.22 00	014563.40
	Z,I14DMP		15167.22 00	014564.00
	L%BU,BIT 55		17415.00 80	014564.40
	LX,\$X0,\$R		11.00 10	014565.40
	SR,\$X0,I14DMP		15167.01 70	014566.00
	KV,\$X1,I14DMP		15167.02 90	014566.40
	BXE,\$+1.32		14570.72 C2	014567.00
	SIC,SEN	-LX BIT 55 FRM INT. MEM. OR	1310.00 80	014567.40
	B,SERS	-SR BIT 55 TO EXT. MEM. FAILS.	1304.10 00	014570.00
	-			
	Z,\$X0		20.22 00	014570.40
	Z,I14DMP		15167.22 00	014571.00
	LX,\$X0,BIT 55		17415.00 10	014571.40
	LX,\$X2,\$X0		20.04 10	014572.00
	SR,\$X2,I14DMP		15167.05 70	014572.40
	KV,\$X1,I14DMP		15167.02 90	014573.00
	BXE,\$+1.32		14575.32 C2	014573.40
	SIC,SEN	-LX BIT 55 FROM IX CORE STG OR	1310.00 80	014574.00
	B,SERS	-SR BIT 55 TO EXT. MEM. FAILS.	1304.10 00	014574.40
	-			
	Z,\$R		11.22 00	014575.00
	LX,\$X0,BIT 55		17415.00 10	014575.40
	SR,\$X0,\$R		11.01 70	014576.00
	KV,\$X1,\$R		11.02 90	014576.40
	BXE,\$+1.32		14600.72 C2	014577.00
	SIC,SEN	-LX BIT 55 FRM EXT. MEM. OR	1310.00 80	014577.40
	B,SERS	-SR BIT 55 TO INT. MEM. FAILS.	1304.10 00	014600.00
	-			
	Z,\$X3		23.22 00	014600.40
	LX,\$X0,BIT 55		17415.00 10	014601.00
	SR,\$X0,\$X3		23.01 70	014601.40
	KV,\$X1,\$X3		23.02 90	014602.00
	BXE,\$+1.32		14604.32 C2	014602.40
	SIC,SEN	-LX BIT 55 FRM EXT. MEM. OR	1310.00 80	014603.00
	B,SERS	-SR BIT 55 TO INDEX CORE STG FAILS.	1304.10 00	014603.40
	-			
	B,\$+1.0		14605.10 00	014604.00
	BD,I1410		14557.04 00	014604.40
	SIC,SEN0+.32		1311.40 80	014605.00
	B,SSW	-TO SSIP.	1301.10 00	014605.40
	BD,\$+.32		14606.44 00	014606.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014606.40
	V+,\$X13,BIT9		17337.32 B0	014607.00
	SX,\$X13,IC214		15165.33 10	014607.40

11411	Z, SX0	-TEST LX AND SR, BIT 56.	20.22 00	014610.00
	LX, SX0, BIT 56		17416.00 10	014610.40
	LX, SX1, BIT 10	-REFILL BIT MINUS 46.	17340.02 10	014611.00
	Z, I14DMP		15167.22 00	014611.40
	SR, SX0, I14DMP		15167.01 70	014612.00
	KV, SX1, I14DMP		15167.02 90	014612.40
	BXE, \$+1.32		14614.72 C2	014613.00
	SIC, SEN	-LX BIT 56 FRM EXT. MEM. OR	1310.00 80	014613.40
	B, SERS	-SR BIT 56 TO EXT. MEM. FAILS.	1304.10 00	014614.00
	-			
	Z, SX0		20.22 00	014614.40
	Z, I14DMP		15167.22 00	014615.00
	L%BU, BIT 56		17416.00 80	014615.40
	LX, SX0, SR		11.00 10	014616.40
	SR, SX0, I14DMP		15167.01 70	014617.00
	KV, SX1, I14DMP		15167.02 90	014617.40
	BXE, \$+1.32		14621.72 C2	014620.00
	SIC, SEN	-LX BIT 56 FRM INT. MEM. OR	1310.00 80	014620.40
	B, SERS	-SR BIT 56 TO EXT. MEM. FAILS.	1304.10 00	014621.00
	-			
	Z, SX0		20.22 00	014621.40
	Z, I14DMP		15167.22 00	014622.00
	LX, SX0, BIT 56		17416.00 10	014622.40
	LX, SX2, SX0		20.04 10	014623.00
	SR, SX2, I14DMP		15167.05 70	014623.40
	KV, SX1, I14DMP		15167.02 90	014624.00
	BXE, \$+1.32		14626.32 C2	014624.40
	SIC, SEN	-LX BIT 56 FROM IX CORE STG OR	1310.00 80	014625.00
	B, SERS	-SR BIT 56 TO EXT. MEM. FAILS.	1304.10 00	014625.40
	-			
	Z, SR		11.22 00	014626.00
	LX, SX0, BIT 56		17416.00 10	014626.40
	SR, SX0, SR		11.01 70	014627.00
	KV, SX1, SR		11.02 90	014627.40
	BXE, \$+1.32		14631.72 C2	014630.00
	SIC, SEN	-LX BIT 56 FRM EXT. MEM. OR	1310.00 80	014630.40
	B, SERS	-SR BIT 56 TO INT. MEM. FAILS.	1304.10 00	014631.00
	-			
	Z, SX3		23.22 00	014631.40
	LX, SX0, BIT 56		17416.00 10	014632.00
	SR, SX0, SX3		23.01 70	014632.40
	KV, SX1, SX3		23.02 90	014633.00
	BXE, \$+1.32		14635.32 C2	014633.40
	SIC, SEN	-LX BIT 56 FRM EXT. MEM. OR	1310.00 80	014634.00
	B, SERS	-SR BIT 56 TO INDEX CORE STG FAILS.	1304.10 00	014634.40
	-			
	B, \$+1.0		14636.10 00	014635.00
	BD, I1411		14610.04 00	014635.40
	SIC, SEN0+.32		1311.40 80	014636.00
	B, SSW	-TO SSIP.	1301.10 00	014636.40
	BD, \$+.32		14637.44 00	014637.00
	-			
	LX, SX13, IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014637.40
	V+, SX13, BIT10		17340.32 80	014640.00
	SX, SX13, IC214		15165.33 10	014640.40

11412	Z,\$X0	-TEST LX AND SR, BIT 57.	20.22 00	014641.00
	LX,\$X0,BIT57		17417.00 10	014641.40
	LX,\$X1,BIT11	-REFILL BIT MINUS 46.	17341.02 10	014642.00
	Z,I14DMP		15167.22 00	014642.40
	SR,\$X0,I14DMP		15167.01 70	014643.00
	KV,\$X1,I14DMP		15167.02 90	014643.40
	BXE,\$+1.32		14645.72 C2	014644.00
	SIC,SEN	-LX BIT 57 FRM EXT. MEM. OR	1310.00 80	014644.40
	B,SERS	-SR BIT 57 TO EXT. MEM. FAILS.	1304.10 00	014645.00
	-			
	Z,\$X0		20.22 00	014645.40
	Z,I14DMP		15167.22 00	014646.00
	L%BU,BIT57		17417.00 80	014646.40
	LX,\$X0,SR		11.00 10	014647.40
	SR,\$X0,I14DMP		15167.01 70	014650.00
	KV,\$X1,I14DMP		15167.02 90	014650.40
	BXE,\$+1.32		14652.72 C2	014651.00
	SIC,SEN	-LX BIT 57 FRM INT. MEM. OR	1310.00 80	014651.40
	B,SERS	-SR BIT 57 TO EXT. MEM. FAILS.	1304.10 00	014652.00
	-			
	Z,\$X0		20.22 00	014652.40
	Z,I14DMP		15167.22 00	014653.00
	LX,\$X0,BIT57		17417.00 10	014653.40
	LX,\$X2,\$X0		20.04 10	014654.00
	SR,\$X2,I14DMP		15167.05 70	014654.40
	KV,\$X1,I14DMP		15167.02 90	014655.00
	BXE,\$+1.32		14657.32 C2	014655.40
	SIC,SEN	-LX BIT 57 FROM IX CORE STG OR	1310.00 80	014656.00
	B,SERS	-SR BIT 57 TO EXT. MEM. FAILS.	1304.10 00	014656.40
	-			
	Z,\$SR		11.22 00	014657.00
	LX,\$X0,BIT57		17417.00 10	014657.40
	SR,\$X0,\$SR		11.01 70	014660.00
	KV,\$X1,\$SR		11.02 90	014660.40
	BXE,\$+1.32		14662.72 C2	014661.00
	SIC,SEN	-LX BIT 57 FRM EXT. MEM. OR	1310.00 80	014661.40
	B,SERS	-SR BIT 57 TO INT. MEM. FAILS.	1304.10 00	014662.00
	-			
	Z,\$X3		23.22 00	014662.40
	LX,\$X0,BIT57		17417.00 10	014663.00
	SR,\$X0,\$X3		23.01 70	014663.40
	KV,\$X1,\$X3		23.02 90	014664.00
	BXE,\$+1.32		14666.32 C2	014664.40
	SIC,SEN	-LX BIT 57 FRM EXT. MEM. OR	1310.00 80	014665.00
	B,SERS	-SR BIT 57 TO INDEX CORE STG FAILS.	1304.10 00	014665.40
	-			
	B,\$+1.0		14667.10 00	014666.00
	BD,I1412		14641.04 00	014666.40
	SIC,SEN0+.32		1311.40 80	014667.00
	B,SSW	-TO SSIP.	1301.10 00	014667.40
	BD,\$+.32		14670.44 00	014670.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014670.40
	V+,\$X13,BIT11		17341.32 B0	014671.00
	SX,\$X13,IC214		15165.33 10	014671.40

11413	Z,\$X0	-TEST LX AND SR, BIT 58.	20.22 00	014672.00
	LX,\$X0,BIT58		17420.00 10	014672.40
	LX,\$X1,BIT12	-REFILL BIT MINUS 46.	17342.02 10	014673.00
	Z,I14DMP		15167.22 00	014673.40
	SR,\$X0,I14DMP		15167.01 70	014674.00
	KV,\$X1,I14DMP		15167.02 90	014674.40
	BXE,\$+1.32		14676.72 C2	014675.00
	SIC,SEN	-LX BIT 58 FRM EXT. MEM. OR	1310.00 80	014675.40
	B,SERS	-SR BIT 58 TO EXT. MEM. FAILS.	1304.10 00	014676.00
	-			
	Z,\$X0		20.22 00	014676.40
	Z,I14DMP		15167.22 00	014677.00
	L%BU,BIT58		17420.00 80	014677.40
	LX,\$X0,\$R		11.00 10	014700.40
	SR,\$X0,I14DMP		15167.01 70	014701.00
	KV,\$X1,I14DMP		15167.02 90	014701.40
	BXE,\$+1.32		14703.72 C2	014702.00
	SIC,SEN	-LX BIT 58 FRM INT. MEM. OR	1310.00 80	014702.40
	B,SERS	-SR BIT 58 TO EXT. MEM. FAILS.	1304.10 00	014703.00
	-			
	Z,\$X0		20.22 00	014703.40
	Z,I14DMP		15167.22 00	014704.00
	LX,\$X0,BIT58		17420.00 10	014704.40
	LX,\$X2,\$X0		20.04 10	014705.00
	SR,\$X2,I14DMP		15167.05 70	014705.40
	KV,\$X1,I14DMP		15167.02 90	014706.00
	BXE,\$+1.32		14710.32 C2	014706.40
	SIC,SEN	-LX BIT 58 FROM IX CORE STG OR	1310.00 80	014707.00
	B,SERS	-SR BIT 58 TO EXT. MEM. FAILS.	1304.10 00	014707.40
	-			
	Z,\$R		11.22 00	014710.00
	LX,\$X0,BIT 58		17420.00 10	014710.40
	SR,\$X0,\$R		11.01 70	014711.00
	KV,\$X1,\$R		11.02 90	014711.40
	BXE,\$+1.32		14713.72 C2	014712.00
	SIC,SEN	-LX BIT 58 FRM EXT. MEM. OR	1310.00 80	014712.40
	B,SERS	-SR BIT 58 TO INT. MEM. FAILS.	1304.10 00	014713.00
	-			
	Z,\$X3		23.22 00	014713.40
	LX,\$X0,BIT 58		17420.00 10	014714.00
	SR,\$X0,\$X3		23.01 70	014714.40
	KV,\$X1,\$X3		23.02 90	014715.00
	BXE,\$+1.32		14717.32 C2	014715.40
	SIC,SEN	-LX BIT 58 FRM EXT. MEM. OR	1310.00 80	014716.00
	B,SERS	-SR BIT 58 TO INDEX CORE STG FAILS.	1304.10 00	014716.40
	B,\$+1.0		14720.10 00	014717.00
	BD,I1413		14672.04 00	014717.40
	SIC,SEN0+.32		1311.40 80	014720.00
	B,SSW	-TO SSIP.	1301.10 00	014720.40
	BD,\$+.32		14721.44 00	014721.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014721.40
	V+,\$X13,BIT12		17342.32 B0	014722.00
	SX,\$X13,IC214		15165.33 10	014722.40

11414	Z,\$X0	-TEST LX AND SR, BIT 59.	20.22 00	014723.00
	LX,\$X0,BIT 59		17421.00 10	014723.40
	LX,\$X1,BIT 13	-REFILL BIT MINUS 46.	17343.02 10	014724.00
	Z,I14DMP		15167.22 00	014724.40
	SR,\$X0,I14DMP		15167.01 70	014725.00
	KV,\$X1,I14DMP		15167.02 90	014725.40
	BXE,\$+1.32		14727.72 C2	014726.00
	SIC,SEN	-LX BIT 59 FRM EXT. MEM. OR	1310.00 80	014726.40
	B,SERS	-SR BIT 59 TO EXT. MEM. FAILS.	1304.10 00	014727.00
	-			
	Z,\$X0		20.22 00	014727.40
	Z,I14DMP		15167.22 00	014730.00
	L%BU□,BIT 59		17421.00 80	014730.40
	LX,\$X0,\$R		11.00 10	014731.40
	SR,\$X0,I14DMP		15167.01 70	014732.00
	KV,\$X1,I14DMP		15167.02 90	014732.40
	BXE,\$+1.32		14734.72 C2	014733.00
	SIC,SEN	-LX BIT 59 FRM INT. MEM. OR	1310.00 80	014733.40
	B,SERS	-SR BIT 59 TO EXT. MEM. FAILS.	1304.10 00	014734.00
	-			
	Z,\$X0		20.22 00	014734.40
	Z,I14DMP		15167.22 00	014735.00
	LX,\$X0,BIT 59		17421.00 10	014735.40
	LX,\$X2,\$X0		20.04 10	014736.00
	SR,\$X2,I14DMP		15167.05 70	014736.40
	KV,\$X1,I14DMP		15167.02 90	014737.00
	BXE,\$+1.32		14741.32 C2	014737.40
	SIC,SEN	-LX BIT 59 FROM IX CORE STG OR	1310.00 80	014740.00
	B,SERS	-SR BIT 59 TO EXT. MEM. FAILS.	1304.10 00	014740.40
	-			
	Z,\$R		11.22 00	014741.00
	LX,\$X0,BIT59		17421.00 10	014741.40
	SR,\$X0,\$R		11.01 70	014742.00
	KV,\$X1,\$R		11.02 90	014742.40
	BXE,\$+1.32		14744.72 C2	014743.00
	SIC,SEN	-LX BIT 59 FRM EXT. MEM. OR	1310.00 80	014743.40
	B,SERS	-SR BIT 59 TO INT. MEM. FAILS.	1304.10 00	014744.00
	-			
	Z,\$X3		23.22 00	014744.40
	LX,\$X0,BIT59		17421.00 10	014745.00
	SR,\$X0,\$X3		23.01 70	014745.40
	KV,\$X1,\$X3		23.02 90	014746.00
	BXE,\$+1.32		14750.32 C2	014746.40
	SIC,SEN	-LX BIT 59 FRM EXT. MEM. OR	1310.00 80	014747.00
	B,SERS	-SR BIT 59 TO INDEX CORE STG FAILS.	1304.10 00	014747.40
	-			
	B,\$+1.0		14751.10 00	014750.00
	BD,I1414		14723.04 00	014750.40
	SIC,SEN0+.32		1311.40 80	014751.00
	B,SSW	-TO SSIP.	1301.10 00	014751.40
	BD,\$+.32		14752.44 00	014752.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	014752.40
	V+,\$X13,BIT13		17343.32 B0	014753.00
	SX,\$X13,IC214		15165.33 10	014753.40

11415	Z,\$X0	-TEST LX AND SR, BIT 60.	20.22 00	014754.00
	LX,\$X0,BIT60		17422.00 10	014754.40
	LX,\$X1,BIT14	-REFILL BIT MINUS 46.	17344.02 10	014755.00
	Z,I14DMP		15167.22 00	014755.40
	SR,\$X0,I14DMP		15167.01 70	014756.00
	KV,\$X1,I14DMP		15167.02 90	014756.40
	BXE,\$+1.32		14760.72 C2	014757.00
	SIC,SEN	-LX BIT 60 FRM EXT. MEM. OR	1310.00 80	014757.40
	B,SERS	-SR BIT 60 TO EXT. MEM. FAILS.	1304.10 00	014760.00
	-			
	Z,\$X0		20.22 00	014760.40
	Z,I14DMP		15167.22 00	014761.00
	L%BU, BIT60		17422.00 80	014761.40
	LX,\$X0,\$R		11.00 10	014762.40
	SR,\$X0,I14DMP		15167.01 70	014763.00
	KV,\$X1,I14DMP		15167.02 90	014763.40
	BXE,\$+1.32		14765.72 C2	014764.00
	SIC,SEN	-LX BIT 60 FRM INT. MEM. OR	1310.00 80	014764.40
	B,SERS	-SR BIT 60 TO EXT. MEM. FAILS.	1304.10 00	014765.00
	-			
	Z,\$X0		20.22 00	014765.40
	Z,I14DMP		15167.22 00	014766.00
	LX,\$X0,BIT60		17422.00 10	014766.40
	LX,\$X2,\$X0		20.04 10	014767.00
	SR,\$X2,I14DMP		15167.05 70	014767.40
	KV,\$X1,I14DMP		15167.02 90	014770.00
	BXE,\$+1.32		14772.32 C2	014770.40
	SIC,SEN	-LX BIT 60 FROM IX CORE STG OR	1310.00 80	014771.00
	B,SERS	-SR BIT 60 TO EXT. MEM. FAILS.	1304.10 00	014771.40
	-			
	Z,\$R		11.22 00	014772.00
	LX,\$X0,BIT60		17422.00 10	014772.40
	SR,\$X0,\$R		11.01 70	014773.00
	KV,\$X1,\$R		11.02 90	014773.40
	BXE,\$+1.32		14775.72 C2	014774.00
	SIC,SEN	-LX BIT 60 FRM EXT. MEM. OR	1310.00 80	014774.40
	B,SERS	-SR BIT 60 TO INT. MEM. FAILS.	1304.10 00	014775.00
	-			
	Z,\$X3		23.22 00	014775.40
	LX,\$X0,BIT60		17422.00 10	014776.00
	SR,\$X0,\$X3		23.01 70	014776.40
	KV,\$X1,\$X3		23.02 90	014777.00
	BXE,\$+1.32		15001.32 C2	014777.40
	SIC,SEN	-LX BIT 60 FRM EXT. MEM. OR	1310.00 80	015000.00
	B,SERS	-SR BIT 60 TO INDEX CORE STG FAILS.	1304.10 00	015000.40
	-			
	B,\$+1.0		15002.10 00	015001.00
	BD,I1415		14754.04 00	015001.40
	SIC,SEN0+.32		1311.40 80	015002.00
	B,SSW	-TO SSIP.	1301.10 00	015002.40
	BD,\$+.32		15003.44 00	015003.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	015003.40
	V+,\$X13,BIT14		17344.32 B0	015004.00
	SX,\$X13,IC214		15165.33 10	015004.40

11416	Z,\$X0	-TEST LX AND SR, BIT 61.	20.22 00	015005.00
	LX,\$X0,BIT61		17423.00 10	015005.40
	LX,\$X1,BIT15	-REFILL BIT MINUS 46.	17345.02 10	015006.00
	Z,I14DMP		15167.22 00	015006.40
	SR,\$X0,I14DMP		15167.01 70	015007.00
	KV,\$X1,I14DMP		15167.02 90	015007.40
	BXE,\$+1.32		15011.72 C2	015010.00
	SIC,SEN	-LX BIT 61 FRM EXT. MEM. OR	1310.00 80	015010.40
	B,SERS	-SR BIT 61 TO EXT. MEM. FAILS.	1304.10 00	015011.00
	-			
	Z,\$X0		20.22 00	015011.40
	Z,I14DMP		15167.22 00	015012.00
	L%BU□,BIT61		17423.00 80	015012.40
	LX,\$X0,SR		11.00 10	015013.40
	SR,\$X0,I14DMP		15167.01 70	015014.00
	KV,\$X1,I14DMP		15167.02 90	015014.40
	BXE,\$+1.32		15016.72 C2	015015.00
	SIC,SEN	-LX BIT 61 FRM INT. MEM. OR	1310.00 80	015015.40
	B,SERS	-SR BIT 61 TO EXT. MEM. FAILS.	1304.10 00	015016.00
	-			
	Z,\$X0		20.22 00	015016.40
	Z,I14DMP		15167.22 00	015017.00
	LX,\$X0,BIT61		17423.00 10	015017.40
	LX,\$X2,\$X0		20.04 10	015020.00
	SR,\$X2,I14DMP		15167.05 70	015020.40
	KV,\$X1,I14DMP		15167.02 90	015021.00
	BXE,\$+1.32		15023.32 C2	015021.40
	SIC,SEN	-LX BIT 61 FROM IX CORE STG OR	1310.00 80	015022.00
	B,SERS	-SR BIT 61 TO EXT. MEM. FAILS.	1304.10 00	015022.40
	-			
	Z,\$SR		11.22 00	015023.00
	LX,\$X0,BIT61		17423.00 10	015023.40
	SR,\$X0,SR		11.01 70	015024.00
	KV,\$X1,SR		11.02 90	015024.40
	BXE,\$+1.32		15026.72 C2	015025.00
	SIC,SEN	-LX BIT 61 FRM EXT. MEM. OR	1310.00 80	015025.40
	B,SERS	-SR BIT 61 TO INT. MEM. FAILS.	1304.10 00	015026.00
	-			
	Z,\$X3		23.22 00	015026.40
	LX,\$X0,BIT61		17423.00 10	015027.00
	SR,\$X0,\$X3		23.01 70	015027.40
	KV,\$X1,\$X3		23.02 90	015030.00
	BXE,\$+1.32		15032.32 C2	015030.40
	SIC,SEN	-LX BIT 61 FRM EXT. MEM. OR	1310.00 80	015031.00
	B,SERS	-SR BIT 61 TO INDEX CORE STG FAILS	1304.10 00	015031.40
	-			
	B,\$+1.0		15033.10 00	015032.00
	BD,I1416		15005.04 00	015032.40
	SIC,SEN0+.32		1311.40 80	015033.00
	B,SSW	-TO SSIP.	1301.10 00	015033.40
	BD,\$+.32		15034.44 00	015034.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	015034.40
	V+,\$X13,BIT15		17345.32 80	015035.00
	SX,\$X13,IC214		15165.33 10	015035.40

11417	Z,\$X0	-TEST LX AND SR,BIT 62.	20.22 00	015036.00
	LX,\$X0,BIT 62		17424.00 10	015036.40
	LX,\$X1,BIT 16	-REFILL BIT MINUS 46.	17346.02 10	015037.00
	Z,I14DMP		15167.22 00	015037.40
	SR,\$X0,I14DMP		15167.01 70	015040.00
	KV,\$X1,I14DMP		15167.02 90	015040.40
	BXE,\$+1.32		15042.72 C2	015041.00
	SIC,SEN	-LX BIT 62 FRM EXT. MEM. OR	1310.00 80	015041.40
	B,SERS	-SR BIT 62 TO EXT. MEM. FAILS.	1304.10 00	015042.00
	-			
	Z,\$X0		20.22 00	015042.40
	Z,I14DMP		15167.22 00	015043.00
	L%BU□,BIT 62		17424.00 80	015043.40
	LX,\$X0,\$R		11.00 10	015044.40
	SR,\$X0,I14DMP		15167.01 70	015045.00
	KV,\$X1,I14DMP		15167.02 90	015045.40
	BXE,\$+1.32		15047.72 C2	015046.00
	SIC,SEN	-LX BIT 62 FRM INT. MEM. OR	1310.00 80	015046.40
	B,SERS	-SR BIT 62 TO EXT. MEM. FAILS.	1304.10 00	015047.00
	-			
	Z,\$X0		20.22 00	015047.40
	Z,I14DMP		15167.22 00	015050.00
	LX,\$X0,BIT 62		17424.00 10	015050.40
	LX,\$X2,\$X0		20.04 10	015051.00
	SR,\$X2,I14DMP		15167.05 70	015051.40
	KV,\$X1,I14DMP		15167.02 90	015052.00
	BXE,\$+1.32		15054.32 C2	015052.40
	SIC,SEN	-LX BIT 62 FROM IX CORE STG OR	1310.00 80	015053.00
	B,SERS	-SR BIT 62 TO EXT. MEM. FAILS.	1304.10 00	015053.40
	-			
	Z,\$R		11.22 00	015054.00
	LX,\$X0,BIT62		17424.00 10	015054.40
	SR,\$X0,\$R		11.01 70	015055.00
	KV,\$X1,\$R		11.02 90	015055.40
	BXE,\$+1.32		15057.72 C2	015056.00
	SIC,SEN	-LX BIT 62 FRM EXT. MEM. OR	1310.00 80	015056.40
	B,SERS	-SR BIT 62 TO INT. MEM. FAILS.	1304.10 00	015057.00
	-			
	Z,\$X3		23.22 00	015057.40
	LX,\$X0,BIT62		17424.00 10	015060.00
	SR,\$X0,\$X3		23.01 70	015060.40
	KV,\$X1,\$X3		23.02 90	015061.00
	BXE,\$+1.32		15063.32 C2	015061.40
	SIC,SEN	-LX BIT 62 FRM EXT. MEM. OR	1310.00 80	015062.00
	B,SERS	-SR BIT 62 TO INDEX CORE STG FAILS.	1304.10 00	015062.40
	-			
	B,\$+1.0		15064.10 00	015063.00
	BD,I1417		15036.04 00	015063.40
	SIC,SEN0+.32		1311.40 80	015064.00
	B,SSW	-TO SSIP.	1301.10 00	015064.40
	BD,\$+.32		15065.44 00	015065.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	015065.40
	V+,\$X13,BIT16		17346.32 B0	015066.00
	SX,\$X13,IC214		15165.33 10	015066.40

11418	Z,\$X0	-TEST LX AND SR, BIT 63.	20.22 00	015067.00
	LX,\$X0,BIT63		17425.00 10	015067.40
	LX,\$X1,BIT17	-REFILL BIT MINUS 46.	17347.02 10	015070.00
	Z,I14DMP		15167.22 00	015070.40
	SR,\$X0,I14DMP		15167.01 70	015071.00
	KV,\$X1,I14DMP		15167.02 90	015071.40
	BXE,\$+1.32		15073.72 C2	015072.00
	SIC,SEN	-LX BIT 63 FRM EXT. MEM. OR	1310.00 80	015072.40
	B,SERS	-SR BIT 63 TO EXT. MEM. FAILS.	1304.10 00	015073.00
	-			
	Z,\$X0		20.22 00	015073.40
	Z,I14DMP		15167.22 00	015074.00
	L%BU, BIT63		17425.00 80	015074.40
	LX,\$X0,\$R		11.00 10	015075.40
	SR,\$X0,I14DMP		15167.01 70	015076.00
	KV,\$X1,I14DMP		15167.02 90	015076.40
	BXE,\$+1.32		15100.72 C2	015077.00
	SIC,SEN	-LX BIT 63 FRM INT. MEM. OR	1310.00 80	015077.40
	B,SERS	-SR BIT 63 TO EXT. MEM. FAILS.	1304.10 00	015100.00
	-			
	Z,\$X0		20.22 00	015100.40
	Z,I14DMP		15167.22 00	015101.00
	LX,\$X0,BIT63		17425.00 10	015101.40
	LX,\$X2,\$X0		20.04 10	015102.00
	SR,\$X2,I14DMP		15167.05 70	015102.40
	KV,\$X1,I14DMP		15167.02 90	015103.00
	BXE,\$+1.32		15105.32 C2	015103.40
	SIC,SEN	-LX BIT 63 FROM IX CORE STG OR	1310.00 80	015104.00
	B,SERS	-SR BIT 63 TO EXT. MEM. FAILS.	1304.10 00	015104.40
	-			
	Z,\$R		11.22 00	015105.00
	LX,\$X0,BIT63		17425.00 10	015105.40
	SR,\$X0,\$R		11.01 70	015106.00
	KV,\$X1,\$R		11.02 90	015106.40
	BXE,\$+1.32		15110.72 C2	015107.00
	SIC,SEN	-LX BIT 63 FRM EXT. MEM. OR	1310.00 80	015107.40
	B,SERS	-SR BIT 63 TO INT. MEM. FAILS.	1304.10 00	015110.00
	-			
	Z,\$X3		23.22 00	015110.40
	LX,\$X0,BIT63		17425.00 10	015111.00
	SR,\$X0,\$X3		23.01 70	015111.40
	KV,\$X1,\$X3		23.02 90	015112.00
	BXE,\$+1.32		15114.32 C2	015112.40
	SIC,SEN	-LX BIT 63 FRM EXT. MEM. OR	1310.00 80	015113.00
	B,SERS	-SR BIT 63 TO INDEX CORE STG FAILS.	1304.10 00	015113.40
	B,\$+1.0		15115.10 00	015114.00
	BD,I1418		15067.04 00	015114.40
	SIC,SEN0+.32		1311.40 80	015115.00
	B,SSW	-TO SSIP.	1301.10 00	015115.40
	BD,\$+.32		15116.44 00	015116.00
	-			
	LX,\$X13,IC214	-UPDATE CONTINUITY CHECK.	15165.32 10	015116.40
	V+,\$X13,BIT17		17347.32 80	015117.00
	SX,\$X13,IC214		15165.33 10	015117.40

11419	LX,\$X0,100Z SR,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=0.40 BXH,\$+1.0 B,11420 SR,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=0.40 BXH,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-CHECK THAT SR WILL STORE 18 -BITS INTO 1.0. -OK -SR OF ALL ZEROES -TO 1.0 FAILS.	17306.00 10 1.01 70 1.02 10 0.43 04 15123.33 42 15126.50 00 1.01 70 1.02 10 0.43 04 15125.73 42 15126.50 00 1310.00 80 1304.10 00	015120.00 015120.40 015121.00 015121.40 015122.00 015122.40 015123.00 015123.40 015124.00 015124.40 015125.00 015125.40 015126.00
11420	LX,\$X0,1000 SR,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=777777.0 BXE,11421 SR,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=777777.0 BXE,\$+1.32 SIC,SEN B,SERS	-OK -SR OF ALL ONES -TO 1.0 FAILS.	17307.00 10 1.01 70 1.02 10 777777.03 04 15134.32 C2 1.01 70 1.02 10 777777.03 04 15134.32 C2 1310.00 80 1304.10 00	015126.40 015127.00 015127.40 015130.00 015130.40 015131.00 015131.40 015132.00 015132.40 015133.00 015133.40
11421	LX,\$X0,100Z LX,\$X1,1000 Z,\$X2	-CHECK NO STORE TO 1.32.	17306.00 10 17307.02 10 22.22 00	015134.00 015134.40 015135.00
114100	SR,\$X0,1.32 LX,\$X3,1.0 SR,\$X1,1.32 LX,\$X4,1.0 SR,\$X3,\$X3 SR,\$X4,\$X4 KV,\$X3,\$X4 BXE,11422 LX,\$X2,\$X2 BXCZ,\$+1.0 B,\$+1.32 LX,\$X2,BIT 30 B,114100 SIC,SEN B,SERS	-IF REFILL FLDS OF X3 AND X4 -ARE EQUAL, THEN NO STORE TOOK -PLACE. IF NOT EQUAL, THEN DO -TEST ONCE MORE. -S TO 1.32 PERMITS -DATA TO ENTER.	1.41 70 1.06 10 1.43 70 1.10 10 23.07 70 24.11 70 24.06 90 15145.32 C2 22.04 10 15143.30 42 15144.10 00 17364.04 10 15135.50 00 1310.00 80 1304.10 00	015135.40 015135.40 015136.00 015136.40 015137.00 015137.40 015140.00 015140.40 015141.00 015141.40 015142.00 015142.40 015143.00 015143.40 015144.00 015144.40

ALL ZEROES
ALL ONES

11422	LX,\$X0,BIT63 LX,\$X1,1000 SR,\$X0,\$X1 KV,\$X1,BIT17 SIC,SEN BXL,SERS B,11423 - SIC,SEN BXH,SERS - 11423	-CHECK THAT SR WILL CLEAR 18-24. -SR TO IX CORE STG FAILS -TO CLEAR BIT 24. -SR TO IX CORE STG FAILS -TO CLEAR BITS 18023. L%BU,1000 SR,\$X0,\$R LX,\$X1,\$R KV,\$X1,BIT17 SIC,SEN BXL,SERS B,11424 - SIC,SEN BXH,SERS - 11424	-CHECK THAT SR WILL CLEAR 18-24. -SR TO IX CORE STG FAILS -TO CLEAR BIT 24. -SR TO IX CORE STG FAILS -TO CLEAR BITS 18023. -SR TO INT MEM FAILS TO -CLR BIT 24. -SR TO INT MEM FAILS TO -CLR BITS 18-23. -TO SSIP. -UPDATE CONTINUITY CHECK. -UPDATE CONTINUITY CHECK. -CONTINUITY ERROR. CNOP - 1C214 1CK214 114DMP	17425.00 10 17307.02 10 21.01 70 17347.02 90 1310.00 80 1304.32 42 15151.50 00 1310.00 80 1304.33 42 17307.00 80 000000.20 50 11.01 70 11.02 10 17347.02 90 1310.00 80 1304.32 42 15156.50 00 1310.00 80 1304.33 42 15157.50 00 15120.04 00 1311.40 80 1301.10 00 15161.04 00 15165.32 10 17350.32 80 15165.33 10 15165.32 10 15166.32 90 1310.00 80 1304.32 C0 15170.10 00 0.00 00 000000.00 00 777777.40 00 000000.00 00 0.30 00 0.30 00	015145.00 015145.40 015146.00 015146.40 015147.00 015147.40 015150.00 015150.40 015151.00 015151.40 015152.40 015153.00 015153.40 015154.00 015154.40 015155.00 015155.40 015156.00 015156.40 015157.00 015157.40 015160.00 015160.40 015161.00 015161.40 015162.00 015162.40 015163.00 015163.40 015164.00 015164.40 015165.00 015166.00 015167.00 015167.40
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----I216---CHECK SX TO 3 MEMS, BITS 0-23, 28-63.

I16	LX,\$X1,I16ID	-UPDATE IDENT.	15173.02 10	015170.00
	SX,\$X1,DPET13		1437.03 10	015170.40
	SIC,RET		1306.40 80	015171.00
	B,IDF1		1443.10 00	015171.40
	Z,IC216		16557.22 00	015172.00
	BD,I161		15174.04 00	015172.40
	CNOP			
I16ID	%IQSZ=DD%BU,64,8=,I216	Z		015173.00
I161	LX,\$X0,BIT0	-CHECK SX, BIT 0, INTO 3 MEMORIES.	17326.00 10	015174.00
	Z,I16DMP		16561.22 00	015174.40
	SX,\$X0,I16DMP		16561.01 10	015175.00
	KV,\$X0,I16DMP		16561.00 90	015175.40
	BXE,\$+1.32		15177.72 C2	015176.00
	SIC,SEN		1310.00 80	015176.40
	B,SERS	-SX, BIT 0, TO EXT. MEM. FAILS.	1304.10 00	015177.00
	Z,\$X1		21.22 00	015177.40
	SX,\$X0,\$X1		21.01 10	015200.00
	KV,\$X0,\$X1		21.00 90	015200.40
	BXE,\$+1.32		15202.72 C2	015201.00
	SIC,SEN		1310.00 80	015201.40
	B,SERS	-SX, BIT 0, TO IX CORE STG FAILS.	1304.10 00	015202.00
	Z,\$SR		11.22 00	015202.40
	SX,\$X0,\$SR		11.01 10	015203.00
	KV,\$X0,\$SR		11.00 90	015203.40
	BXE,\$+1.32		15205.72 C2	015204.00
	SIC,SEN		1310.00 80	015204.40
	B,SERS	-SX, BIT 0, TO INT. MEM. FAILS.	1304.10 00	015205.00
	LX,\$X0,BIT1	-CHECK SX, BIT 1, INTO 3 MEMORIES.	17327.00 10	015205.40
	Z,I16DMP		16561.22 00	015206.00
	SX,\$X0,I16DMP		16561.01 10	015206.40
	KV,\$X0,I16DMP		16561.00 90	015207.00
	BXE,\$+1.32		15211.32 C2	015207.40
	SIC,SEN		1310.00 80	015210.00
	B,SERS	-SX, BIT 1, TO EXT. MEM. FAILS.	1304.10 00	015210.40
	Z,\$X1		21.22 00	015211.00
	SX,\$X0,\$X1		21.01 10	015211.40
	KV,\$X0,\$X1		21.00 90	015212.00
	BXE,\$+1.32		15214.32 C2	015212.40
	SIC,SEN		1310.00 80	015213.00
	B,SERS	-SX, BIT 1, TO INT. MEM. FAILS.	1304.10 00	015213.40
	Z,\$SR		11.22 00	015214.00
	SX,\$X0,\$SR		11.01 10	015214.40
	KV,\$X0,\$SR		11.00 90	015215.00
	BXE,\$+1.32		15217.32 C2	015215.40
	SIC,SEN		1310.00 80	015216.00
	B,SERS	-SX, BIT 1, TO IX CORE STG FAILS.	1304.10 00	015216.40

	LX,\$X0,BIT2	-CHECK SX, BIT 2, INTO 3 MEMORIES.	17330.00 10	015217.00
	Z,I16DMP		16561.22 00	015217.40
	SX,\$X0,I16DMP		16561.01 10	015220.00
	KV,\$X0,I16DMP		16561.00 90	015220.40
	BXE,\$+1.32		15222.72 C2	015221.00
	SIC,SEN		1310.00 80	015221.40
	B,SERS	-SX, BIT 2, TO EXT. MEM. FAILS.	1304.10 00	015222.00
	-			
	Z,\$X1		21.22 00	015222.40
	SX,\$X0,\$X1		21.01 10	015223.00
	KV,\$X0,\$X1		21.00 90	015223.40
	BXE,\$+1.32		15225.72 C2	015224.00
	SIC,SEN		1310.00 80	015224.40
	B,SERS	-SX, BIT 2, TO IX CORE STG FAILS.	1304.10 00	015225.00
	-			
	Z,\$R		11.22 00	015225.40
	SX,\$X0,\$R		11.01 10	015226.00
	KV,\$X0,\$R		11.00 90	015226.40
	BXE,\$+1.32		15230.72 C2	015227.00
	SIC,SEN		1310.00 80	015227.40
	B,SERS	-SX, BIT 2, TO INT. MEM. FAILS.	1304.10 00	015230.00
	-			
	B,\$+1.0		15231.50 00	015230.40
	BD,I161		15174.04 00	015231.00
	SIC,SEN0+.32		1311.40 80	015231.40
	B,SSW	-TO SSIP.	1301.10 00	015232.00
	BD,\$+.32		15233.04 00	015232.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015233.00
	V+,\$X13,BIT0		17326.32 B0	015233.40
	SX,\$X13,IC216		16557.33 10	015234.00
	-			
1162	LX,\$X0,BIT3	-CHECK SX, BIT 3, INTO 3 MEMORIES.	17331.00 10	015234.40
	Z,I16DMP		16561.22 00	015235.00
	SX,\$X0,I16DMP		16561.01 10	015235.40
	KV,\$X0,I16DMP		16561.00 90	015236.00
	BXE,\$+1.32		15240.32 C2	015236.40
	SIC,SEN		1310.00 80	015237.00
	B,SERS	-SX, BIT 3, TO EXT. MEM. FAILS.	1304.10 00	015237.40
	-			
	Z,\$X1		21.22 00	015240.00
	SX,\$X0,\$X1		21.01 10	015240.40
	KV,\$X0,\$X1		21.00 90	015241.00
	BXE,\$+1.32		15243.32 C2	015241.40
	SIC,SEN		1310.00 80	015242.00
	B,SERS	-SX, BIT 3, TO IX CORE STG FAILS.	1304.10 00	015242.40
	-			
	Z,\$R		11.22 00	015243.00
	SX,\$X0,\$R		11.01 10	015243.40
	KV,\$X0,\$R		11.00 90	015244.00
	BXE,\$+1.32		15246.32 C2	015244.40
	SIC,SEN		1310.00 80	015245.00
	B,SERS	--SX, BIT 3, TO INT. MEM. FAILS.	1304.10 00	015245.40

LX,\$X0,BIT4	-CHECK SX, BIT 4, INTO 3 MEMORIES.	17332.00 10	015246.00
Z,I16DMP		16561.22 00	015246.40
SX,\$X0,I16DMP		16561.01 10	015247.00
KV,\$X0,I16DMP		16561.00 90	015247.40
BXE,\$+1.32		15251.72 C2	015250.00
SIC,SEN		1310.00 80	015250.40
B,SERS	-SX, BIT 4, TO EXT. MEM. FAILS.	1304.10 00	015251.00
-			
Z,\$X1		21.22 00	015251.40
SX,\$X0,\$X1		21.01 10	015252.00
KV,\$X0,\$X1		21.00 90	015252.40
BXE,\$+1.32		15254.72 C2	015253.00
SIC,SEN		1310.00 80	015253.40
B,SERS	-SX, BIT 4, TO IX CORE STG FAILS.	1304.10 00	015254.00
-			
Z,\$R		11.22 00	015254.40
SX,\$X0,\$R		11.01 10	015255.00
KV,\$X0,\$R		11.00 90	015255.40
BXE,\$+1.32		15257.72 C2	015256.00
SIC,SEN		1310.00 80	015256.40
B,SERS	--SX, BIT 4, TO INT. MEM. FAILS.	1304.10 00	015257.00
-			
LX,\$X0,BIT5	-CHECK SX, BIT 5, INTO 3 MEMORIES.	17333.00 10	015257.40
Z,I16DMP		16561.22 00	015260.00
SX,\$X0,I16DMP		16561.01 10	015260.40
KV,\$X0,I16DMP		16561.00 90	015261.00
BXE,\$+1.32		15263.32 C2	015261.40
SIC,SEN		1310.00 80	015262.00
B,SERS	-SX, BIT 5, TO EXT. MEM. FAILS.	1304.10 00	015262.40
-			
Z,\$X1		21.22 00	015263.00
SX,\$X0,\$X1		21.01 10	015263.40
KV,\$X0,\$X1		21.00 90	015264.00
BXE,\$+1.32		15266.32 C2	015264.40
SIC,SEN		1310.00 80	015265.00
B,SERS	-SX, BIT 5, TO IX CORE STG FAILS.	1304.10 00	015265.40
-			
Z,\$R		11.22 00	015266.00
SX,\$X0,\$R		11.01 10	015266.40
KV,\$X0,\$R		11.00 90	015267.00
BXE,\$+1.32		15271.32 C2	015267.40
SIC,SEN		1310.00 80	015270.00
B,SERS	-SX, BIT 5, TO INT. MEM. FAILS.	1304.10 00	015270.40
-			
B,\$+1.0		15272.10 00	015271.00
BD,I162		15234.44 00	015271.40
SIC,SEN0+.32		1311.40 80	015272.00
B,SSW	-TO SSIP.	1301.10 00	015272.40
BD,\$+.32		15273.44 00	015273.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015273.40
V+,\$X13,BIT1		17327.32 80	015274.00
SX,\$X13,IC216		16557.33 10	015274.40

1163	LX,\$X0,BIT6	-CHECK SX, BIT 6, INTO 3 MEMORIES.	17334.00 10	015275.00
	Z,I16DMP		16561.22 00	015275.40
	SX,\$X0,I16DMP		16561.01 10	015276.00
	KV,\$X0,I16DMP		16561.00 90	015276.40
	BXE,\$+1.32		15300.72 C2	015277.00
	SIC,SEN		1310.00 80	015277.40
	B,SERS	-SX, BIT 6, TO EXT. MEM. FAILS.	1304.10 00	015300.00
	-			
	Z,\$X1		21.22 00	015300.40
	SX,\$X0,\$X1		21.01 10	015301.00
	KV,\$X0,\$X1		21.00 90	015301.40
	BXE,\$+1.32		15303.72 C2	015302.00
	SIC,SEN		1310.00 80	015302.40
	B,SERS	-SX, BIT 6, TO IX CORE STG FAILS.	1304.10 00	015303.00
	-			
	Z,\$R		11.22 00	015303.40
	SX,\$X0,\$R		11.01 10	015304.00
	KV,\$X0,\$R		11.00 90	015304.40
	BXE,\$+1.32		15306.72 C2	015305.00
	SIC,SEN		1310.00 80	015305.40
	B,SERS	-SX, BIT 6, TO INT. MEM. FAILS.	1304.10 00	015306.00
	-			
	LX,\$X0,BIT7	-CHECK SX, BIT 7, INTO 3 MEMORIES.	17335.00 10	015306.40
	Z,I16DMP		16561.22 00	015307.00
	SX,\$X0,I16DMP		16561.01 10	015307.40
	KV,\$X0,I16DMP		16561.00 90	015310.00
	BXE,\$+1.32		15312.32 C2	015310.40
	SIC,SEN		1310.00 80	015311.00
	B,SERS	-SX, BIT 7, TO EXT. MEM. FAILS.	1304.10 00	015311.40
	-			
	Z,\$X1		21.22 00	015312.00
	SX,\$X0,\$X1		21.01 10	015312.40
	KV,\$X0,\$X1		21.00 90	015313.00
	BXE,\$+1.32		15315.32 C2	015313.40
	SIC,SEN		1310.00 80	015314.00
	B,SERS	-SX, BIT 7, TO IX CORE STG FAILS.	1304.10 00	015314.40
	-			
	Z,\$R		11.22 00	015315.00
	SX,\$X0,\$R		11.01 10	015315.40
	KV,\$X0,\$R		11.00 90	015316.00
	BXE,\$+1.32		15320.32 C2	015316.40
	SIC,SEN		1310.00 80	015317.00
	B,SERS	-SX, BIT 7, TO INT. MEM. FAILS.	1304.10 00	015317.40

	LX,\$X0,BIT8	-CHECK SX, BIT 8, INTO 3 MEMORIES.	17336.00 10	015320.00
	Z,I16DMP		16561.22 00	015320.40
	SX,\$X0,I16DMP		16561.01 10	015321.00
	KV,\$X0,I16DMP		16561.00 90	015321.40
	BXE,\$+1.32		15323.72 C2	015322.00
	SIC,SEN		1310.00 80	015322.40
	B,SERS	-SX, BIT 8, TO EXT. MEM. FAILS.	1304.10 00	015323.00
	-			
	Z,\$X1		21.22 00	015323.40
	SX,\$X0,\$X1		21.01 10	015324.00
	KV,\$X0,\$X1		21.00 90	015324.40
	BXE,\$+1.32		15326.72 C2	015325.00
	SIC,SEN		1310.00 80	015325.40
	B,SERS	-SX, BIT 8, TO IX CORE STG FAILS.	1304.10 00	015326.00
	-			
	Z,\$R		11.22 00	015326.40
	SX,\$X0,\$R		11.01 10	015327.00
	KV,\$X0,\$R		11.00 90	015327.40
	BXE,\$+1.32		15331.72 C2	015330.00
	SIC,SEN		1310.00 80	015330.40
	B,SERS	-SX, BIT 8, TO INT. MEM. FAILS.	1304.10 00	015331.00
	-			
	B,\$+1.0		15332.50 00	015331.40
	BD,I163		15275.04 00	015332.00
	SIC,SEN0+.32		1311.40 80	015332.40
	B,SSW	-TO SSIP.	1301.10 00	015333.00
	BD,\$+.32		15334.04 00	015333.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015334.00
	V+,\$X13,BIT2		17330.32 80	015334.40
	SX,\$X13,IC216		16557.33 10	015335.00
	-			
1164	LX,\$X0,BIT9	-CHECK SX, BIT 9, INTO 3 MEMORIES.	17337.00 10	015335.40
	Z,I16DMP		16561.22 00	015336.00
	SX,\$X0,I16DMP		16561.01 10	015336.40
	KV,\$X0,I16DMP		16561.00 90	015337.00
	BXE,\$+1.32		15341.32 C2	015337.40
	SIC,SEN		1310.00 80	015340.00
	B,SERS	-SX, BIT 9, TO EXT. MEM. FAILS.	1304.10 00	015340.40
	-			
	Z,\$X1		21.22 00	015341.00
	SX,\$X0,\$X1		21.01 10	015341.40
	KV,\$X0,\$X1		21.00 90	015342.00
	BXE,\$+1.32		15344.32 C2	015342.40
	SIC,SEN		1310.00 80	015343.00
	B,SERS	-SX, BIT 9, TO IX CORE STG FAILS.	1304.10 00	015343.40
	-			
	Z,\$R		11.22 00	015344.00
	SX,\$X0,\$R		11.01 10	015344.40
	KV,\$X0,\$R		11.00 90	015345.00
	BXE,\$+1.32		15347.32 C2	015345.40
	SIC,SEN		1310.00 80	015346.00
	B,SERS	--SX, BIT 9, TO INT. MEM. FAILS.	1304.10 00	015346.40

LX,\$X0,BIT10	-CHECK SX, BIT 10, INTO 3 MEMORIES.	17340.00 10	015347.00
Z,I16DMP		16561.22 00	015347.40
SX,\$X0,I16DMP		16561.01 10	015350.00
KV,\$X0,I16DMP		16561.00 90	015350.40
BXE,\$+1.32		15352.72 C2	015351.00
SIC,SEN		1310.00 80	015351.40
B,SERS	-SX, BIT 10, TO EXT. MEM. FAILS.	1304.10 00	015352.00
-			
Z,\$X1		21.22 00	015352.40
SX,\$X0,\$X1		21.01 10	015353.00
KV,\$X0,\$X1		21.00 90	015353.40
BXE,\$+1.32		15355.72 C2	015354.00
SIC,SEN		1310.00 80	015354.40
B,SERS	-SX, BIT 10, TO IX CORE STG FAILS.	1304.10 00	015355.00
-			
Z,\$R		11.22 00	015355.40
SX,\$X0,\$R		11.01 10	015356.00
KV,\$X0,\$R		11.00 90	015356.40
BXE,\$+1.32		15360.72 C2	015357.00
SIC,SEN		1310.00 80	015357.40
B,SERS	-SX, BIT 10, TO INT. MEM. FAILS.	1304.10 00	015360.00
-			
LX,\$X0,BIT11	-CHECK SX, BIT 11, INTO 3 MEMORIES.	17341.00 10	015360.40
Z,I16DMP		16561.22 00	015361.00
SX,\$X0,I16DMP		16561.01 10	015361.40
KV,\$X0,I16DMP		16561.00 90	015362.00
BXE,\$+1.32		15364.32 C2	015362.40
SIC,SEN		1310.00 80	015363.00
B,SERS	-SX, BIT 11, TO EXT. MEM. FAILS.	1304.10 00	015363.40
-			
Z,\$X1		21.22 00	015364.00
SX,\$X0,\$X1		21.01 10	015364.40
KV,\$X0,\$X1		21.00 90	015365.00
BXE,\$+1.32		15367.32 C2	015365.40
SIC,SEN		1310.00 80	015366.00
B,SERS	-SX, BIT 11, TO IX CORE STG FAILS.	1304.10 00	015366.40
-			
Z,\$R		11.22 00	015367.00
SX,\$X0,\$R		11.01 10	015367.40
KV,\$X0,\$R		11.00 90	015370.00
BXE,\$+1.32		15372.32 C2	015370.40
SIC,SEN		1310.00 80	015371.00
B,SERS	-SX, BIT 11, TO INT. MEM. FAILS.	1304.10 00	015371.40
-			
B,\$+1.0		15373.10 00	015372.00
BD,I164		15335.44 00	015372.40
SIC,SEN0+.32		1311.40 80	015373.00
B,SSW	-TO SSIP	1301.10 00	015373.40
BD,\$+.32		15374.44 00	015374.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015374.40
V+,\$X13,BIT3		17331.32 B0	015375.00
SX,\$X13,IC216		16557.33 10	015375.40

1165	LX,\$X0,BIT12	-CHECK SX, BIT 12, INTO 3 MEMORIES.	17342.00 10	015376.00
	Z,I16DMP		16561.22 00	015376.40
	SX,\$X0,I16DMP		16561.01 10	015377.00
	KV,\$X0,I16DMP		16561.00 90	015377.40
	BXE,\$+1.32		15401.72 C2	015400.00
	SIC,SEN		1310.00 80	015400.40
	B,SERS	-SX, BIT 12, TO EXT. MEM. FAILS.	1304.10 00	015401.00
	-			
	Z,\$X1		21.22 00	015401.40
	SX,\$X0,\$X1		21.01 10	015402.00
	KV,\$X0,\$X1		21.00 90	015402.40
	BXE,\$+1.32		15404.72 C2	015403.00
	SIC,SEN		1310.00 80	015403.40
	B,SERS	-SX, BIT 12, TO IX CORE STG FAILS.	1304.10 00	015404.00
	-			
	Z,\$R		11.22 00	015404.40
	SX,\$X0,\$R		11.01 10	015405.00
	KV,\$X0,\$R		11.00 90	015405.40
	BXE,\$+1.32		15407.72 C2	015406.00
	SIC,SEN		1310.00 80	015406.40
	B,SERS	-SX, BIT 12, TO INT. MEM. FAILS.	1304.10 00	015407.00
	-			
	LX,\$X0,BIT13	-CHECK SX, BIT 13, INTO 3 MEMORIES.	17343.00 10	015407.40
	Z,I16DMP		16561.22 00	015410.00
	SX,\$X0,I16DMP		16561.01 10	015410.40
	KV,\$X0,I16DMP		16561.00 90	015411.00
	BXE,\$+1.32		15413.32 C2	015411.40
	SIC,SEN		1310.00 80	015412.00
	B,SERS	-SX, BIT 13, TO EXT. MEM. FAILS.	1304.10 00	015412.40
	-			
	Z,\$X1		21.22 00	015413.00
	SX,\$X0,\$X1		21.01 10	015413.40
	KV,\$X0,\$X1		21.00 90	015414.00
	BXE,\$+1.32		15416.32 C2	015414.40
	SIC,SEN		1310.00 80	015415.00
	B,SERS	-SX, BIT 13, TO IX CORE STG FAILS.	1304.10 00	015415.40
	-			
	Z,\$R		11.22 00	015416.00
	SX,\$X0,\$R		11.01 10	015416.40
	KV,\$X0,\$R		11.00 90	015417.00
	BXE,\$+1.32		15421.32 C2	015417.40
	SIC,SEN		1310.00 80	015420.00
	B,SERS	-SX, BIT 13, TO INT. MEM. FAILS.	1304.10 00	015420.40

	LX,\$X0,BIT14	-CHECK SX, BIT 14, INTO 3 MEMORIES.	17344.00 10	015421.00
	Z,I16DMP		16561.22 00	015421.40
	SX,\$X0,I16DMP		16561.01 10	015422.00
	KV,\$X0,I16DMP		16561.00 90	015422.40
	BXE,\$+1.32		15424.72 C2	015423.00
	SIC,SEN		1310.00 80	015423.40
	B,SERS	-SX, BIT 14, TO EXT. MEM. FAILS.	1304.10 00	015424.00
	-			
	Z,\$X1		21.22 00	015424.40
	SX,\$X0,\$X1		21.01 10	015425.00
	KV,\$X0,\$X1		21.00 90	015425.40
	BXE,\$+1.32		15427.72 C2	015426.00
	SIC,SEN		1310.00 80	015426.40
	B,SERS	-SX, BIT 14, TO IX CORE STG FAILS.	1304.10 00	015427.00
	-			
	Z,\$R		11.22 00	015427.40
	SX,\$X0,\$R		11.01 10	015430.00
	KV,\$X0,\$R		11.00 90	015430.40
	BXE,\$+1.32		15432.72 C2	015431.00
	SIC,SEN		1310.00 80	015431.40
	B,SERS	-SX, BIT 14, TO INT. MEM. FAILS.	1304.10 00	015432.00
	-			
	B,\$+1.0		15433.50 00	015432.40
	BD,I165		15376.04 00	015433.00
	SIC,SEN0+.32		1311.40 80	015433.40
	B,SSW	-TO SSIP.	1301.10 00	015434.00
	BD,\$+.32		15435.04 00	015434.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015435.00
	V+,\$X13,BIT4		17332.32 B0	015435.40
	SX,\$X13,IC216		16557.33 10	015436.00
	-			
1166	LX,\$X0,BIT15	-CHECK SX, BIT 15, INTO 3 MEMORIES.	17345.00 10	015436.40
	Z,I16DMP		16561.22 00	015437.00
	SX,\$X0,I16DMP		16561.01 10	015437.40
	KV,\$X0,I16DMP		16561.00 90	015440.00
	BXE,\$+1.32		15442.32 C2	015440.40
	SIC,SEN		1310.00 80	015441.00
	B,SERS	-SX, BIT 15, TO EXT. MEM. FAILS.	1304.10 00	015441.40
	-			
	Z,\$X1		21.22 00	015442.00
	SX,\$X0,\$X1		21.01 10	015442.40
	KV,\$X0,\$X1		21.00 90	015443.00
	BXE,\$+1.32		15445.32 C2	015443.40
	SIC,SEN		1310.00 80	015444.00
	B,SERS	-SX, BIT 15, TO IX CORE STG FAILS.	1304.10 00	015444.40
	-			
	Z,\$R		11.22 00	015445.00
	SX,\$X0,\$R		11.01 10	015445.40
	KV,\$X0,\$R		11.00 90	015446.00
	BXE,\$+1.32		15450.32 C2	015446.40
	SIC,SEN		1310.00 80	015447.00
	B,SERS	-SX, BIT 15, TO INT. MEM. FAILS.	1304.10 00	015447.40

LX,\$X0,BIT16	-CHECK SX, BIT 16, INTO 3 MEMORIES.	17346.00 10	015450.00
Z,I16DMP		16561.22 00	015450.40
SX,\$X0,I16DMP		16561.01 10	015451.00
KV,\$X0,I16DMP		16561.00 90	015451.40
BXE,\$+1.32		15453.72 C2	015452.00
SIC,SEN		1310.00 80	015452.40
B,SERS	-SX, BIT 16, TO EXT. MEM. FAILS.	1304.10 00	015453.00
-			
Z,\$X1		21.22 00	015453.40
SX,\$X0,\$X1		21.01 10	015454.00
KV,\$X0,\$X1		21.00 90	015454.40
BXE,\$+1.32		15456.72 C2	015455.00
SIC,SEN		1310.00 80	015455.40
B,SERS	-SX, BIT 16, TO IX CORE STG FAILS.	1304.10 00	015456.00
-			
Z,\$R		11.22 00	015456.40
SX,\$X0,\$R		11.01 10	015457.00
KV,\$X0,\$R		11.00 90	015457.40
BXE,\$+1.32		15461.72 C2	015460.00
SIC,SEN		1310.00 80	015460.40
B,SERS	-SX, BIT 16, TO INT. MEM. FAILS.	1304.10 00	015461.00
-			
LX,\$X0,BIT17	-CHECK SX, BIT 17, INTO 3 MEMORIES.	17347.00 10	015461.40
Z,I16DMP		16561.22 00	015462.00
SX,\$X0,I16DMP		16561.01 10	015462.40
KV,\$X0,I16DMP		16561.00 90	015463.00
BXE,\$+1.32		15465.32 C2	015463.40
SIC,SEN		1310.00 80	015464.00
B,SERS	-SX, BIT 17, TO EXT. MEM. FAILS.	1304.10 00	015464.40
-			
Z,\$X1		21.22 00	015465.00
SX,\$X0,\$X1		21.01 10	015465.40
KV,\$X0,\$X1		21.00 90	015466.00
BXE,\$+1.32		15470.32 C2	015466.40
SIC,SEN		1310.00 80	015467.00
B,SERS	-SX, BIT 17, TO IX CORE STG FAILS.	1304.10 00	015467.40
-			
Z,\$R		11.22 00	015470.00
SX,\$X0,\$R		11.01 10	015470.40
KV,\$X0,\$R		11.00 90	015471.00
BXE,\$+1.32		15473.32 C2	015471.40
SIC,SEN		1310.00 80	015472.00
B,SERS	-SX, BIT 17, TO INT. MEM. FAILS.	1304.10 00	015472.40
-			
B,\$+1.0		15474.10 00	015473.00
BD,I166		15436.44 00	015473.40
SIC,SEN0+.32		1311.40 80	015474.00
B,SSW	-TO SSIP	1301.10 00	015474.40
BD,\$+.32		15475.44 00	015475.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015475.40
V+,\$X13,BIT5		17333.32 80	015476.00
SX,\$X13,IC216		16557.33 10	015476.40

1167	LX,\$X0,BIT18	-CHECK SX, BIT 18, INTO 3 MEMORIES.	17350.00 10	015477.00
	Z,I16DMP		16561.22 00	015477.40
	SX,\$X0,I16DMP		16561.01 10	015500.00
	KV,\$X0,I16DMP		16561.00 90	015500.40
	BXE,\$+1.32		15502.72 C2	015501.00
	SIC,SEN		1310.00 80	015501.40
	B,SERS	-SX, BIT 18, TO EXT. MEM. FAILS.	1304.10 00	015502.00
	-			
	Z,\$X1		21.22 00	015502.40
	SX,\$X0,\$X1		21.01 10	015503.00
	KV,\$X0,\$X1		21.00 90	015503.40
	BXE,\$+1.32		15505.72 C2	015504.00
	SIC,SEN		1310.00 80	015504.40
	B,SERS	-SX, BIT 18, TO IX CORE STG FAILS.	1304.10 00	015505.00
	-			
	Z,\$R		11.22 00	015505.40
	SX,\$X0,\$R		11.01 10	015506.00
	KV,\$X0,\$R		11.00 90	015506.40
	BXE,\$+1.32		15510.72 C2	015507.00
	SIC,SEN		1310.00 80	015507.40
	B,SERS	-SX, BIT 18, TO INT. MEM. FAILS.	1304.10 00	015510.00
	-			
	LX,\$X0,BIT19	-CHECK SX, BIT 19, INTO 3 MEMORIES.	17351.00 10	015510.40
	Z,I16DMP		16561.22 00	015511.00
	SX,\$X0,I16DMP		16561.01 10	015511.40
	KV,\$X0,I16DMP		16561.00 90	015512.00
	BXE,\$+1.32		15514.32 C2	015512.40
	SIC,SEN		1310.00 80	015513.00
	B,SERS	-SX, BIT 19, TO EXT. MEM. FAILS.	1304.10 00	015513.40
	-			
	Z,\$X1		21.22 00	015514.00
	SX,\$X0,\$X1		21.01 10	015514.40
	KV,\$X0,\$X1		21.00 90	015515.00
	BXE,\$+1.32		15517.32 C2	015515.40
	SIC,SEN		1310.00 80	015516.00
	B,SERS	-SX, BIT 19, TO IX CORE STG FAILS.	1304.10 00	015516.40
	-			
	Z,\$R		11.22 00	015517.00
	SX,\$X0,\$R		11.01 10	015517.40
	KV,\$X0,\$R		11.00 90	015520.00
	BXE,\$+1.32		15522.32 C2	015520.40
	SIC,SEN		1310.00 80	015521.00
	B,SERS	-SX, BIT 19, TO INT. MEM. FAILS.	1304.10 00	015521.40

	LX,\$X0,BIT20	-CHECK SX, BIT 20, INTO 3 MEMORIES.	17352.00 10	015522.00
	Z,I16DMP		16561.22 00	015522.40
	SX,\$X0,I16DMP		16561.01 10	015523.00
	KV,\$X0,I16DMP		16561.00 90	015523.40
	BXE,\$+1.32		15525.72 C2	015524.00
	SIC,SEN		1310.00 80	015524.40
	B,SERS	-SX, BIT 20, TO EXT. MEM. FAILS.	1304.10 00	015525.00
	-			
	Z,\$X1		21.22 00	015525.40
	SX,\$X0,\$X1		21.01 10	015526.00
	KV,\$X0,\$X1		21.00 90	015526.40
	BXE,\$+1.32		15530.72 C2	015527.00
	SIC,SEN	-SX, BIT 20, TO IX CORE STG FAILS.	1310.00 80	015527.40
	B,SERS		1304.10 00	015530.00
	-			
	Z,\$R		11.22 00	015530.40
	SX,\$X0,\$R		11.01 10	015531.00
	KV,\$X0,\$R		11.00 90	015531.40
	BXE,\$+1.32		15533.72 C2	015532.00
	SIC,SEN	-SX, BIT 20, TO INT. MEM. FAILS.	1310.00 80	015532.40
	B,SERS		1304.10 00	015533.00
	-			
	B,\$+1.0		15534.50 00	015533.40
	BD,I167		15477.04 00	015534.00
	SIC,SEN0+.32		1311.40 80	015534.40
	B,SSW	-TO SSIP.	1301.10 00	015535.00
	BD,\$+.32		15536.04 00	015535.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015536.00
	V+,\$X13,BIT6		17334.32 80	015536.40
	SX,\$X13,IC216		16557.33 10	015537.00
	-			
1168	LX,\$X0,BIT21	-CHECK SX, BIT 21, INTO 3 MEMORIES.	17353.00 10	015537.40
	Z,I16DMP		16561.22 00	015540.00
	SX,\$X0,I16DMP		16561.01 10	015540.40
	KV,\$X0,I16DMP		16561.00 90	015541.00
	BXE,\$+1.32		15543.32 C2	015541.40
	SIC,SEN		1310.00 80	015542.00
	B,SERS	-SX, BIT 21, TO EXT. MEM. FAILS.	1304.10 00	015542.40
	-			
	Z,\$X1		21.22 00	015543.00
	SX,\$X0,\$X1		21.01 10	015543.40
	KV,\$X0,\$X1		21.00 90	015544.00
	BXE,\$+1.32		15546.32 C2	015544.40
	SIC,SEN	-SX, BIT 21, TO IX CORE STG FAILS.	1310.00 80	015545.00
	B,SERS		1304.10 00	015545.40
	-			
	Z,\$R		11.22 00	015546.00
	SX,\$X0,\$R		11.01 10	015546.40
	KV,\$X0,\$R		11.00 90	015547.00
	BXE,\$+1.32		15551.32 C2	015547.40
	SIC,SEN	-SX, BIT 21, TO INT. MEM. FAILS.	1310.00 80	015550.00
	B,SERS		1304.10 00	015550.40

LX,\$X0,BIT22	-CHECK SX, BIT 22, INTO 3 MEMORIES.	17354.00 10	015551.00
Z,I16DMP		16561.22 00	015551.40
SX,\$X0,I16DMP		16561.01 10	015552.00
KV,\$X0,I16DMP		16561.00 90	015552.40
BXE,\$+1.32		15554.72 C2	015553.00
SIC,SEN		1310.00 80	015553.40
B,SERS	-SX, BIT 22, TO EXT. MEM. FAILS.	1304.10 00	015554.00
-			
Z,\$X1		21.22 00	015554.40
SX,\$X0,\$X1		21.01 10	015555.00
KV,\$X0,\$X1		21.00 90	015555.40
BXE,\$+1.32		15557.72 C2	015556.00
SIC,SEN		1310.00 80	015556.40
B,SERS	-SX, BIT 22, TO IX CORE STG FAILS.	1304.10 00	015557.00
-			
Z,\$R		11.22 00	015557.40
SX,\$X0,\$R		11.01 10	015560.00
KV,\$X0,\$R		11.00 90	015560.40
BXE,\$+1.32		15562.72 C2	015561.00
SIC,SEN		1310.00 80	015561.40
B,SERS	-SX, BIT 22, TO INT. MEM. FAILS.	1304.10 00	015562.00
-			
LX,\$X0,BIT23	-CHECK SX, BIT 23, INTO 3 MEMORIES.	17355.00 10	015562.40
Z,I16DMP		16561.22 00	015563.00
SX,\$X0,I16DMP		16561.01 10	015563.40
KV,\$X0,I16DMP		16561.00 90	015564.00
BXE,\$+1.32		15566.32 C2	015564.40
SIC,SEN		1310.00 80	015565.00
B,SERS	-SX, BIT 23, TO EXT. MEM. FAILS.	1304.10 00	015565.40
-			
Z,\$X1		21.22 00	015566.00
SX,\$X0,\$X1		21.01 10	015566.40
KV,\$X0,\$X1		21.00 90	015567.00
BXE,\$+1.32		15571.32 C2	015567.40
SIC,SEN		1310.00 80	015570.00
B,SERS	-SX, BIT 23, TO IX CORE STG FAILS.	1304.10 00	015570.40
-			
Z,\$R		11.22 00	015571.00
SX,\$X0,\$R		11.01 10	015571.40
KV,\$X0,\$R		11.00 90	015572.00
BXE,\$+1.32		15574.32 C2	015572.40
SIC,SEN		1310.00 80	015573.00
B,SERS	-SX, BIT 23, TO INT. MEM. FAILS.	1304.10 00	015573.40
-			
B,\$+1.0		15575.10 00	015574.00
BD,I168		15537.44 00	015574.40
SIC,SEN0+.32		1311.40 80	015575.00
B,SSW	-TO SSIP.	1301.10 00	015575.40
BD,\$+.32		15576.44 00	015576.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015576.40
V+,\$X13,BIT7		17335.32 80	015577.00
SX,\$X13,IC216		16557.33 10	015577.40

1169	LX,\$X0,BIT28A	-CHECK SX, BIT 28, INTO 3 MEMORIES.	16563.00 10	015600.00
	Z,I16DMP		16561.22 00	015600.40
	SX,\$X0,I16DMP		16561.01 10	015601.00
	KC,\$X0,I16DMP		16561.01 90	015601.40
	BXE,\$+1.32		15603.72 C2	015602.00
	SIC,SEN		1310.00 80	015602.40
	B,SERS	-SX, BIT 28, TO EXT. MEM. FAILS.	1304.10 00	015603.00
	-			
	Z,\$X1		21.22 00	015603.40
	SX,\$X0,\$X1		21.01 10	015604.00
	KC,\$X0,\$X1		21.01 90	015604.40
	BXE,\$+1.32		15606.72 C2	015605.00
	SIC,SEN		1310.00 80	015605.40
	B,SERS	-SX, BIT 28, TO IX CORE STG FAILS.	1304.10 00	015606.00
	-			
	Z,\$R		11.22 00	015606.40
	SX,\$X0,\$R		11.01 10	015607.00
	KC,\$X0,\$R		11.01 90	015607.40
	BXE,\$+1.32		15611.72 C2	015610.00
	SIC,SEN		1310.00 80	015610.40
	B,SERS	-SX, BIT 28, TO INT. MEM. FAILS.	1304.10 00	015611.00
	-			
	LX,\$X0,BIT29A	-CHECK SX, BIT 29, INTO 3 MEMORIES.	16564.00 10	015611.40
	Z,I16DMP		16561.22 00	015612.00
	SX,\$X0,I16DMP		16561.01 10	015612.40
	KC,\$X0,I16DMP		16561.01 90	015613.00
	BXE,\$+1.32		15615.32 C2	015613.40
	SIC,SEN		1310.00 80	015614.00
	B,SERS	-SX, BIT 29, TO EXT. MEM. FAILS.	1304.10 00	015614.40
	-			
	Z,\$X1		21.22 00	015615.00
	SX,\$X0,\$X1		21.01 10	015615.40
	KC,\$X0,\$X1		21.01 90	015616.00
	BXE,\$+1.32		15620.32 C2	015616.40
	SIC,SEN		1310.00 80	015617.00
	B,SERS	-SX, BIT 29, TO IX CORE STG FAILS.	1304.10 00	015617.40
	-			
	Z,\$R		11.22 00	015620.00
	SX,\$X0,\$R		11.01 10	015620.40
	KC,\$X0,\$R		11.01 90	015621.00
	BXE,\$+1.32		15623.32 C2	015621.40
	SIC,SEN		1310.00 80	015622.00
	B,SERS	-SX, BIT 29, TO INT. MEM. FAILS.	1304.10 00	015622.40

	LX,\$X0,BIT30A	-CHECK SX, BIT 30, INTO 3 MEMORIES.	16565.00 10	015623.00
	Z,I16DMP		16561.22 00	015623.40
	SX,\$X0,I16DMP		16561.01 10	015624.00
	KC,\$X0,I16DMP		16561.01 90	015624.40
	BXE,\$+1.32		15626.72 C2	015625.00
	SIC,SEN		1310.00 80	015625.40
	B,SERS	-SX, BIT 30, TO EXT. MEM. FAILS.	1304.10 00	015626.00
	Z,\$X1		21.22 00	015626.40
	SX,\$X0,\$X1		21.01 10	015627.00
	KC,\$X0,\$X1		21.01 90	015627.40
	BXE,\$+1.32		15631.72 C2	015630.00
	SIC,SEN		1310.00 80	015630.40
	B,SERS	-SX, BIT 30, TO IX CORE STG FAILS.	1304.10 00	015631.00
	-			
	Z,\$R		11.22 00	015631.40
	SX,\$X0,\$R		11.01 10	015632.00
	KC,\$X0,\$R		11.01 90	015632.40
	BXE,\$+1.32		15634.72 C2	015633.00
	SIC,SEN		1310.00 80	015633.40
	B,SERS	-SX, BIT 30, TO INT. MEM. FAILS.	1304.10 00	015634.00
	-			
	B,\$+1.0		15635.50 00	015634.40
	BD,I169		15600.04 00	015635.00
	SIC,SEN0+.32		1311.40 80	015635.40
	B,SSW	-TO SSIP.	1301.10 00	015636.00
	BD,\$+.32		15637.04 00	015636.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015637.00
	V+,\$X13,BIT8		17336.32 80	015637.40
	SX,\$X13,IC216		16557.33 10	015640.00
	-			
11610	LX,\$X0,BIT30A	-CHECK SX, BIT 31, INTO 3 MEMORIES.	16565.00 10	015640.40
	Z,I16DMP		16561.22 00	015641.00
	SX,\$X0,I16DMP		16561.01 10	015641.40
	KC,\$X0,I16DMP		16561.01 90	015642.00
	BXE,\$+1.32		15644.32 C2	015642.40
	SIC,SEN		1310.00 80	015643.00
	B,SERS	-SX, BIT 31, TO EXT. MEM. FAILS.	1304.10 00	015643.40
	-			
	Z,\$X1		21.22 00	015644.00
	SX,\$X0,\$X1		21.01 10	015644.40
	KC,\$X0,\$X1		21.01 90	015645.00
	BXE,\$+1.32		15647.32 C2	015645.40
	SIC,SEN		1310.00 80	015646.00
	B,SERS	-SX, BIT 31, TO IX CORE STG FAILS.	1304.10 00	015646.40
	-			
	Z,\$R		11.22 00	015647.00
	SX,\$X0,\$R		11.01 10	015647.40
	KC,\$X0,\$R		11.01 90	015650.00
	BXE,\$+1.32		15652.32 C2	015650.40
	SIC,SEN		1310.00 80	015651.00
	B,SERS	-SX, BIT 31, TO INT. MEM. FAILS.	1304.10 00	015651.40

LX,\$X0,BIT32A	-CHECK SX, BIT 32, INTO 3 MEMORIES.	16567.00 10	015652.00
Z,I16DMP		16561.22 00	015652.40
SX,\$X0,I16DMP		16561.01 10	015653.00
KC,\$X0,I16DMP		16561.01 90	015653.40
BXE,\$+1.32		15655.72 C2	015654.00
SIC,SEN		1310.00 80	015654.40
B,SERS	-SX, BIT 32, TO EXT. MEM. FAILS.	1304.10 00	015655.00
-			
Z,\$X1		21.22 00	015655.40
SX,\$X0,\$X1		21.01 10	015656.00
KC,\$X0,\$X1		21.01 90	015656.40
BXE,\$+1.32		15660.72 C2	015657.00
SIC,SEN		1310.00 80	015657.40
B,SERS	-SX, BIT 32, TO IX CORE STG FAILS.	1304.10 00	015660.00
-			
Z,\$R		11.22 00	015660.40
SX,\$X0,\$R		11.01 10	015661.00
KC,\$X0,\$R		11.01 90	015661.40
BXE,\$+1.32		15663.72 C2	015662.00
SIC,SEN		1310.00 80	015662.40
B,SERS	-SX, BIT 32, TO INT. MEM. FAILS.	1304.10 00	015663.00
-			
LX,\$X0,BIT33A	-CHECK SX, BIT 33, INTO 3 MEMORIES.	16570.00 10	015663.40
Z,I16DMP		16561.22 00	015664.00
SX,\$X0,I16DMP		16561.01 10	015664.40
KC,\$X0,I16DMP		16561.01 90	015665.00
BXE,\$+1.32		15667.32 C2	015665.40
SIC,SEN		1310.00 80	015666.00
B,SERS	-SX, BIT 33, TO EXT. MEM. FAILS.	1304.10 00	015666.40
-			
Z,\$X1		21.22 00	015667.00
SX,\$X0,\$X1		21.01 10	015667.40
KC,\$X0,\$X1		21.01 90	015670.00
BXE,\$+1.32		15672.32 C2	015670.40
SIC,SEN		1310.00 80	015671.00
B,SERS	-SX, BIT 33, TO IX CORE STG FAILS.	1304.10 00	015671.40
-			
Z,\$R		11.22 00	015672.00
SX,\$X0,\$R		11.01 10	015672.40
KC,\$X0,\$R		11.01 90	015673.00
BXE,\$+1.32		15675.32 C2	015673.40
SIC,SEN		1310.00 80	015674.00
B,SERS	-SX, BIT 33, TO INT. MEM. FAILS.	1304.10 00	015674.40
-			
B,\$+1.0		15676.10 00	015675.00
BD,I1610		15640.44 00	015675.40
SIC,SEN0+.32		1311.40 80	015676.00
B,SSW	-TO SSIP	1301.10 00	015676.40
BD,\$+.32		15677.44 00	015677.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015677.40
V+,\$X13,BIT9		17337.32 B0	015700.00
SX,\$X13,IC216		16557.33 10	015700.40

11611	LX,\$X0,BIT34A	-CHECK SX, BIT 34, INTO 3 MEMORIES.	16571.00 10	015701.00
	Z,I16DMP		16561.22 00	015701.40
	SX,\$X0,I16DMP		16561.01 10	015702.00
	KC,\$X0,I16DMP		16561.01 90	015702.40
	BXE,\$+1.32		15704.72 C2	015703.00
	SIC,SEN		1310.00 80	015703.40
	B,SERS	-SX, BIT 34, TO EXT. MEM. FAILS.	1304.10 00	015704.00
	-			
	Z,\$X1		21.22 00	015704.40
	SX,\$X0,\$X1		21.01 10	015705.00
	KC,\$X0,\$X1		21.01 90	015705.40
	BXE,\$+1.32		15707.72 C2	015706.00
	SIC,SEN		1310.00 80	015706.40
	B,SERS	-SX, BIT 34, TO IX CORE STG FAILS.	1304.10 00	015707.00
	-			
	Z,\$R		11.22 00	015707.40
	SX,\$X0,\$R		11.01 10	015710.00
	KC,\$X0,\$R		11.01 90	015710.40
	BXE,\$+1.32		15712.72 C2	015711.00
	SIC,SEN		1310.00 80	015711.40
	B,SERS	-SX, BIT 34, TO INT. MEM. FAILS.	1304.10 00	015712.00
	-			
	LX,\$X0,BIT35A	-CHECK SX, BIT 35, INTO 3 MEMORIES.	16572.00 10	015712.40
	Z,I16DMP		16561.22 00	015713.00
	SX,\$X0,I16DMP		16561.01 10	015713.40
	KC,\$X0,I16DMP		16561.01 90	015714.00
	BXE,\$+1.32		15716.32 C2	015714.40
	SIC,SEN		1310.00 80	015715.00
	B,SERS	-SX, BIT 35, TO EXT. MEM. FAILS.	1304.10 00	015715.40
	-			
	Z,\$X1		21.22 00	015716.00
	SX,\$X0,\$X1		21.01 10	015716.40
	KC,\$X0,\$X1		21.01 90	015717.00
	BXE,\$+1.32		15721.32 C2	015717.40
	SIC,SEN		1310.00 80	015720.00
	B,SERS	-SX, BIT 35, TO IX CORE STG FAILS.	1304.10 00	015720.40
	-			
	Z,\$R		11.22 00	015721.00
	SX,\$X0,\$R		11.01 10	015721.40
	KC,\$X0,\$R		11.01 90	015722.00
	BXE,\$+1.32		15724.32 C2	015722.40
	SIC,SEN		1310.00 80	015723.00
	B,SERS	-SX, BIT 35, TO INT. MEM. FAILS.	1304.10 00	015723.40

	LX,\$X0,BIT36A	-CHECK SX, BIT 36, INTO 3 MEMORIES.	16573.00 10	015724.00
	Z,I16DMP		16561.22 00	015724.40
	SX,\$X0,I16DMP		16561.01 10	015725.00
	KC,\$X0,I16DMP		16561.01 90	015725.40
	BXE,\$+1.32		15727.72 C2	015726.00
	SIC,SEN		1310.00 80	015726.40
	B,SERS	-SX, BIT 36, TO EXT. MEM. FAILS.	1304.10 00	015727.00
	-			
	Z,\$X1		21.22 00	015727.40
	SX,\$X0,\$X1		21.01 10	015730.00
	KC,\$X0,\$X1		21.01 90	015730.40
	BXE,\$+1.32		15732.72 C2	015731.00
	SIC,SEN		1310.00 80	015731.40
	B,SERS	-SX, BIT 36, TO IX CORE STG FAILS.	1304.10 00	015732.00
	-			
	Z,\$R		11.22 00	015732.40
	SX,\$X0,\$R		11.01 10	015733.00
	KC,\$X0,\$R		11.01 90	015733.40
	BXE,\$+1.32		15735.72 C2	015734.00
	SIC,SEN		1310.00 80	015734.40
	B,SERS	-SX, BIT 36, TO INT. MEM. FAILS.	1304.10 00	015735.00
	-			
	B,\$+1.0		15736.50 00	015735.40
	BD,I1611		15701.04 00	015736.00
	SIC,SEN0+.32		1311.40 80	015736.40
	B,\$SW	-TO SSIP.	1301.10 00	015737.00
	BD,\$+.32		15740.04 00	015737.40
	-			
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	015740.00
	V+,\$X13,BIT10		17340.32 B0	015740.40
	SX,\$X13,IC216		16557.33 10	015741.00
	-			
I1613	LX,\$X0,BIT37A	-CHECK SX, BIT 37, INTO 3 MEMORIES.	16574.00 10	015741.40
	Z,I16DMP		16561.22 00	015742.00
	SX,\$X0,I16DMP		16561.01 10	015742.40
	KC,\$X0,I16DMP		16561.01 90	015743.00
	BXE,\$+1.32		15745.32 C2	015743.40
	SIC,SEN		1310.00 80	015744.00
	B,SERS	-SX, BIT 37, TO EXT. MEM. FAILS.	1304.10 00	015744.40
	-			
	Z,\$X1		21.22 00	015745.00
	SX,\$X0,\$X1		21.01 10	015745.40
	KC,\$X0,\$X1		21.01 90	015746.00
	BXE,\$+1.32		15750.32 C2	015746.40
	SIC,SEN		1310.00 80	015747.00
	B,SERS	-SX, BIT 37, TO IX CORE STG FAILS.	1304.10 00	015747.40
	-			
	Z,\$R		11.22 00	015750.00
	SX,\$X0,\$R		11.01 10	015750.40
	KC,\$X0,\$R		11.01 90	015751.00
	BXE,\$+1.32		15753.32 C2	015751.40
	SIC,SEN		1310.00 80	015752.00
	B,SERS	-SX, BIT 37, TO INT. MEM. FAILS.	1304.10 00	015752.40

LX,\$X0,BIT38A	-CHECK SX, BIT 38, INTO 3 MEMORIES.	16575.00 10	015753.00
Z,I16DMP		16561.22 00	015753.40
SX,\$X0,I16DMP		16561.01 10	015754.00
KC,\$X0,I16DMP		16561.01 90	015754.40
BXE,\$+1.32		15756.72 C2	015755.00
SIC,SEN		1310.00 80	015755.40
B,SERS	-SX, BIT 38, TO EXT. MEM. FAILS.	1304.10 00	015756.00
-			
Z,\$X1		21.22 00	015756.40
SX,\$X0,\$X1		21.01 10	015757.00
KC,\$X0,\$X1		21.01 90	015757.40
BXE,\$+1.32		15761.72 C2	015760.00
SIC,SEN		1310.00 80	015760.40
B,SERS	-SX, BIT 38, TO IX CORE STG FAILS.	1304.10 00	015761.00
-			
Z,\$R		11.22 00	015761.40
SX,\$X0,\$R		11.01 10	015762.00
KC,\$X0,\$R		11.01 90	015762.40
BXE,\$+1.32		15764.72 C2	015763.00
SIC,SEN		1310.00 80	015763.40
B,SERS	-SX, BIT 38, TO INT. MEM. FAILS.	1304.10 00	015764.00
-			
LX,\$X0,BIT39A	-CHECK SX, BIT 39, INTO 3 MEMORIES.	16576.00 10	015764.40
Z,I16DMP		16561.22 00	015765.00
SX,\$X0,I16DMP		16561.01 10	015765.40
KC,\$X0,I16DMP		16561.01 90	015766.00
BXE,\$+1.32		15770.32 C2	015766.40
SIC,SEN		1310.00 80	015767.00
B,SERS	-SX, BIT 39, TO EXT. MEM. FAILS.	1304.10 00	015767.40
-			
Z,\$X1		21.22 00	015770.00
SX,\$X0,\$X1		21.01 10	015770.40
KC,\$X0,\$X1		21.01 90	015771.00
BXE,\$+1.32		15773.32 C2	015771.40
SIC,SEN		1310.00 80	015772.00
B,SERS	-SX, BIT 39, TO IX CORE STG FAILS.	1304.10 00	015772.40
-			
Z,\$R		11.22 00	015773.00
SX,\$X0,\$R		11.01 10	015773.40
KC,\$X0,\$R		11.01 90	015774.00
BXE,\$+1.32		15776.32 C2	015774.40
SIC,SEN		1310.00 80	015775.00
B,SERS	-SX, BIT 39, TO INT. MEM. FAILS.	1304.10 00	015775.40
-			
B,\$+1.0		15777.10 00	015776.00
BD,I1613		15741.44 00	015776.40
SIC,SEN0+.32		1311.40 80	015777.00
B,SSW		1301.10 00	015777.40
BD,\$+.32		16000.44 00	016000.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	016000.40
V+,\$X13,BIT11		17341.32 80	016001.00
SX,\$X13,IC216		16557.33 10	016001.40

11614	LX,\$X0,BIT40A	-CHECK SX, BIT 40, INTO 3 MEMORIES.	16577.00 10	016002.00
	Z,I16DMP		16561.22 00	016002.40
	SX,\$X0,I16DMP		16561.01 10	016003.00
	KC,\$X0,I16DMP		16561.01 90	016003.40
	BXE,\$+1.32		16005.72 C2	016004.00
	SIC,SEN		1310.00 80	016004.40
	B,SERS	-SX, BIT 40, TO EXT. MEM. FAILS.	1304.10 00	016005.00
	-			
	Z,\$X1		21.22 00	016005.40
	SX,\$X0,\$X1		21.01 10	016006.00
	KC,\$X0,\$X1		21.01 90	016006.40
	BXE,\$+1.32		16010.72 C2	016007.00
	SIC,SEN		1310.00 80	016007.40
	B,SERS	-SX, BIT 40, TO IX CORE STG FAILS.	1304.10 00	016010.00
	-			
	Z,\$R		11.22 00	016010.40
	SX,\$X0,\$R		11.01 10	016011.00
	KC,\$X0,\$R		11.01 90	016011.40
	BXE,\$+1.32		16013.72 C2	016012.00
	SIC,SEN		1310.00 80	016012.40
	B,SERS	-SX, BIT 40, TO INT. MEM. FAILS.	1304.10 00	016013.00
	-			
	LX,\$X0,BIT41A	-CHECK SX, BIT 41, INTO 3 MEMORIES.	16600.00 10	016013.40
	Z,I16DMP		16561.22 00	016014.00
	SX,\$X0,I16DMP		16561.01 10	016014.40
	KC,\$X0,I16DMP		16561.01 90	016015.00
	BXE,\$+1.32		16017.32 C2	016015.40
	SIC,SEN		1310.00 80	016016.00
	B,SERS	-SX, BIT 41, TO EXT. MEM. FAILS.	1304.10 00	016016.40
	-			
	Z,\$X1		21.22 00	016017.00
	SX,\$X0,\$X1		21.01 10	016017.40
	KC,\$X0,\$X1		21.01 90	016020.00
	BXE,\$+1.32		16022.32 C2	016020.40
	SIC,SEN		1310.00 80	016021.00
	B,SERS	-SX, BIT 41, TO IX CORE STG FAILS.	1304.10 00	016021.40
	-			
	Z,\$R		11.22 00	016022.00
	SX,\$X0,\$R		11.01 10	016022.40
	KC,\$X0,\$R		11.01 90	016023.00
	BXE,\$+1.32		16025.32 C2	016023.40
	SIC,SEN		1310.00 80	016024.00
	B,SERS	-SX, BIT 41, TO INT. MEM. FAILS.	1304.10 00	016024.40

LX,\$X0,BIT42A	-CHECK SX, BIT 42, INTO 3 MEMORIES.	16601.00 10	016025.00
Z,I16DMP		16561.22 00	016025.40
SX,\$X0,I16DMP		16561.01 10	016026.00
KC,\$X0,I16DMP		16561.01 90	016026.40
BXE,\$+1.32		16030.72 C2	016027.00
SIC,SEN		1310.00 80	016027.40
B,SERS	-SX, BIT 42, TO EXT. MEM. FAILS.	1304.10 00	016030.00
-			
Z,\$X1		21.22 00	016030.40
SX,\$X0,\$X1		21.01 10	016031.00
KC,\$X0,\$X1		21.01 90	016031.40
BXE,\$+1.32		16033.72 C2	016032.00
SIC,SEN		1310.00 80	016032.40
B,SERS	-SX, BIT 42, TO IX CORE STG FAILS.	1304.10 00	016033.00
-			
Z,\$R		11.22 00	016033.40
SX,\$X0,\$R		11.01 10	016034.00
KC,\$X0,\$R		11.01 90	016034.40
BXE,\$+1.32		16036.72 C2	016035.00
SIC,SEN		1310.00 80	016035.40
B,SERS	-SX, BIT 42, TO INT. MEM. FAILS.	1304.10 00	016036.00
-			
B,\$+1.0		16037.50 00	016036.40
BD,I1614		16002.04 00	016037.00
SIC,SEN0+.32		1311.40 80	016037.40
B,SSW	-TO SSIP.	1301.10 00	016040.00
BD,\$+.32		16041.04 00	016040.40
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	016041.00
V+,\$X13,BIT12		17342.32 B0	016041.40
SX,\$X13,IC216		16557.33 10	016042.00
-			
11615 LX,\$X0,BIT43A	-CHECK SX, BIT 43, INTO 3 MEMORIES.	16602.00 10	016042.40
Z,I16DMP		16561.22 00	016043.00
SX,\$X0,I16DMP		16561.01 10	016043.40
KC,\$X0,I16DMP		16561.01 90	016044.00
BXE,\$+1.32		16046.32 C2	016044.40
SIC,SEN		1310.00 80	016045.00
B,SERS	-SX, BIT 43, TO EXT. MEM. FAILS.	1304.10 00	016045.40
-			
Z,\$X1		21.22 00	016046.00
SX,\$X0,\$X1		21.01 10	016046.40
KC,\$X0,\$X1		21.01 90	016047.00
BXE,\$+1.32		16051.32 C2	016047.40
SIC,SEN		1310.00 80	016050.00
B,SERS	-SX, BIT 43, TO IX CORE STG FAILS.	1304.10 00	016050.40
-			
Z,\$R		11.22 00	016051.00
SX,\$X0,\$R		11.01 10	016051.40
KC,\$X0,\$R		11.01 90	016052.00
BXE,\$+1.32		16054.32 C2	016052.40
SIC,SEN		1310.00 80	016053.00
B,SERS	-SX, BIT 43, TO INT. MEM. FAILS.	1304.10 00	016053.40

LX,\$X0,BIT44A	-CHECK SX, BIT 44, INTO 3 MEMORIES.	16603.00 10	016054.00
Z,I16DMP		16561.22 00	016054.40
SX,\$X0,I16DMP		16561.01 10	016055.00
KC,\$X0,I16DMP		16561.01 90	016055.40
BXE,\$+1.32		16057.72 C2	016056.00
SIC,SEN		1310.00 80	016056.40
B,SERS	-SX, BIT 44, TO EXT. MEM. FAILS.	1304.10 00	016057.00
-			
Z,\$X1		21.22 00	016057.40
SX,\$X0,\$X1		21.01 10	016060.00
KC,\$X0,\$X1		21.01 90	016060.40
BXE,\$+1.32		16062.72 C2	016061.00
SIC,SEN		1310.00 80	016061.40
B,SERS	-SX, BIT 44, TO IX CORE STG FAILS.	1304.10 00	016062.00
-			
Z,\$R		11.22 00	016062.40
SX,\$X0,\$R		11.01 10	016063.00
KC,\$X0,\$R		11.01 90	016063.40
BXE,\$+1.32		16065.72 C2	016064.00
SIC,SEN		1310.00 80	016064.40
B,SERS	-SX, BIT 44, TO INT. MEM. FAILS.	1304.10 00	016065.00
-			
LX,\$X0,BIT45A	-CHECK SX, BIT 45, INTO 3 MEMORIES.	16604.00 10	016065.40
Z,I16DMP		16561.22 00	016066.00
SX,\$X0,I16DMP		16561.01 10	016066.40
KC,\$X0,I16DMP		16561.01 90	016067.00
BXE,\$+1.32		16071.32 C2	016067.40
SIC,SEN		1310.00 80	016070.00
B,SERS	-SX, BIT 45, TO EXT. MEM. FAILS.	1304.10 00	016070.40
-			
Z,\$X1		21.22 00	016071.00
SX,\$X0,\$X1		21.01 10	016071.40
KC,\$X0,\$X1		21.01 90	016072.00
BXE,\$+1.32		16074.32 C2	016072.40
SIC,SEN		1310.00 80	016073.00
B,SERS	-SX, BIT 45, TO IX CORE STG FAILS.	1304.10 00	016073.40
-			
Z,\$R		11.22 00	016074.00
SX,\$X0,\$R		11.01 10	016074.40
KC,\$X0,\$R		11.01 90	016075.00
BXE,\$+1.32		16077.32 C2	016075.40
SIC,SEN		1310.00 80	016076.00
B,SERS	-SX, BIT 45, TO INT. MEM. FAILS.	1304.10 00	016076.40
-			
B,\$+1.0		16100.10 00	016077.00
BD,I1615		16042.44 00	016077.40
SIC,SEN0+.32		1311.40 80	016100.00
B,SSW	-TO SSIP.	1301.10 00	016100.40
BD,\$+.32		16101.44 00	016101.00
-			
LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	016101.40
V+,\$X13,BIT13		17343.32 B0	016102.00
SX,\$X13,IC216		16557.33 10	016102.40

11616	LX,\$X0,BIT46	-CHECK SX, BIT 46, INTO 3 MEMORIES.	17404.00 10	016103.00
	Z,I16DMP		16561.22 00	016103.40
	SX,\$X0,I16DMP		16561.01 10	016104.00
	Z,I16DP		16562.22 00	016104.40
	LX,\$X1,I16DMP		16561.02 10	016105.00
	LX,\$X2,BIT0	-REFILL SHFTD LEFT 46.	17326.04 10	016105.40
	SR,\$X1,I16DP		16562.03 70	016106.00
	KV,\$X2,I16DP		16562.04 90	016106.40
	BXE,\$+1.32		16110.72 C2	016107.00
	SIC,SEN		1310.00 80	016107.40
	B,SERS	-SX, BIT 46, TO EXT. MEM. FAILS.	1304.10 00	016110.00
	-			
	Z,\$X3		23.22 00	016110.40
	SX,\$X0,\$X3		23.01 10	016111.00
	LX,\$X1,\$X3		23.02 10	016111.40
	Z,I16DP		16562.22 00	016112.00
	SR,\$X1,I16DP		16562.03 70	016112.40
	KV,\$X2,I16DP		16562.04 90	016113.00
	BXE,\$+1.32		16115.32 C2	016113.40
	SIC,SEN		1310.00 80	016114.00
	B,SERS	-SX, BIT 46, TO IX. CORE STG. FAILS.	1304.10 00	016114.40
	-			
	Z,\$R		11.22 00	016115.00
	SX,\$X0,\$R		11.01 10	016115.40
	LX,\$X1,\$R		11.02 10	016116.00
	Z,I16DP		16562.22 00	016116.40
	SR,\$X1,I16DP		16562.03 70	016117.00
	KV,\$X2,I16DP		16562.04 90	016117.40
	BXE,\$+1.32		16121.72 C2	016120.00
	SIC,SEN		1310.00 80	016120.40
	B,SERS	-SX,BIT 46, TO INT. MEM. FAILS.	1304.10 00	016121.00
	-			
	LX,\$X0,BIT47	-CHECK SX, BIT 47, INTO 3 MEMORIES.	17405.00 10	016121.40
	Z,I16DMP		16561.22 00	016122.00
	SX,\$X0,I16DMP		16561.01 10	016122.40
	Z,I16DP		16562.22 00	016123.00
	LX,\$X1,I16DMP		16561.02 10	016123.40
	LX,\$X2,BIT1	-REFILL SHFTD LEFT 46.	17327.04 10	016124.00
	SR,\$X1,I16DP		16562.03 70	016124.40
	KV,\$X2,I16DP		16562.04 90	016125.00
	BXE,\$+1.32		16127.32 C2	016125.40
	SIC,SEN		1310.00 80	016126.00
	B,SERS	-SX, BIT 47, TO EXT. MEM. FAILS.	1304.10 00	016126.40
	-			
	Z,\$X3		23.22 00	016127.00
	SX,\$X0,\$X3		23.01 10	016127.40
	LX,\$X1,\$X3		23.02 10	016130.00
	Z,I16DP		16562.22 00	016130.40
	SR,\$X1,I16DP		16562.03 70	016131.00
	KV,\$X2,I16DP		16562.04 90	016131.40
	BXE,\$+1.32		16133.72 C2	016132.00
	SIC,SEN		1310.00 80	016132.40
	B,SERS	-SX, BIT 47, TO IX. CORE STG. FAILS.	1304.10 00	016133.00
	-			
	Z,\$R		11.22 00	016133.40
	SX,\$X0,\$R		11.01 10	016134.00
	LX,\$X1,\$R		11.02 10	016134.40
	Z,I16DP		16562.22 00	016135.00
	SR,\$X1,I16DP		16562.03 70	016135.40
	KV,\$X2,I16DP		16562.04 90	016136.00
	BXE,\$+1.32		16140.32 C2	016136.40
	SIC,SEN		1310.00 80	016137.00
	B,SERS	-SX, BIT 47, TO INT. MEM. FAILS.	1304.10 00	016137.40

B,\$+1.0
BD,\$1616
SIC,\$EN0+.32
B,\$SSW
BD,\$+.32

-TO SSIP

LX,\$X13,\$C216
V+,\$X13,\$BIT14
SX,\$X13,\$C216

-UPDATE CONTINUITY CHECK.

16141.10 00
16103.04 00
1311.40 80
1301.10 00
16142.44 00

016140.00
016140.40
016141.00
016141.40
016142.00

16557.32 10
17344.32 80
16557.33 10

016142.40
016143.00
016143.40

11617	LX,\$X0,BIT48	-CHECK SX, BIT 48, INTO 3 MEMORIES.	17406.00 10	016144.00
	Z,I16DMP		16561.22 00	016144.40
	SX,\$X0,I16DMP		16561.01 10	016145.00
	Z,I16DP		16562.22 00	016145.40
	LX,\$X1,I16DMP		16561.02 10	016146.00
	LX,\$X2,BIT2	-REFILL SHFTD LEFT 46.	17330.04 10	016146.40
	SR,\$X1,I16DP		16562.03 70	016147.00
	KV,\$X2,I16DP		16562.04 90	016147.40
	BXE,\$+1.32		16151.72 C2	016150.00
	SIC,SEN		1310.00 80	016150.40
	B,SERS	-SX, BIT 48, TO EXT. MEM. FAILS.	1304.10 00	016151.00
	-			
	Z,\$X3		23.22 00	016151.40
	SX,\$X0,\$X3		23.01 10	016152.00
	LX,\$X1,\$X3		23.02 10	016152.40
	Z,I16DP		16562.22 00	016153.00
	SR,\$X1,I16DP		16562.03 70	016153.40
	KV,\$X2,I16DP		16562.04 90	016154.00
	BXE,\$+1.32		16156.32 C2	016154.40
	SIC,SEN		1310.00 80	016155.00
	B,SERS	-SX, BIT 48, TO IX. CORE STG. FAILS.	1304.10 00	016155.40
	-			
	Z,\$R		11.22 00	016156.00
	SX,\$X0,\$R		11.01 10	016156.40
	LX,\$X1,\$R		11.02 10	016157.00
	Z,I16DP		16562.22 00	016157.40
	SR,\$X1,I16DP		16562.03 70	016160.00
	KV,\$X2,I16DP		16562.04 90	016160.40
	BXE,\$+1.32		16162.72 C2	016161.00
	SIC,SEN		1310.00 80	016161.40
	B,SERS	-SX, BIT 48, TO INT. MEM. FAILS.	1304.10 00	016162.00
	-			
	LX,\$X0,BIT49	-CHECK SX, BIT 49, INTO 3 MEMORIES.	17407.00 10	016162.40
	Z,I16DMP		16561.22 00	016163.00
	SX,\$X0,I16DMP		16561.01 10	016163.40
	Z,I16DP		16562.22 00	016164.00
	LX,\$X1,I16DMP		16561.02 10	016164.40
	LX,\$X2,BIT3	-REFILL SHFTD LEFT 46.	17331.04 10	016165.00
	SR,\$X1,I16DP		16562.03 70	016165.40
	KV,\$X2,I16DP		16562.04 90	016166.00
	BXE,\$+1.32		16170.32 C2	016166.40
	SIC,SEN		1310.00 80	016167.00
	B,SERS	-SX, BIT 49, TO EXT. MEM. FAILS.	1304.10 00	016167.40
	-			
	Z,\$X3		23.22 00	016170.00
	SX,\$X0,\$X3		23.01 10	016170.40
	LX,\$X1,\$X3		23.02 10	016171.00
	Z,I16DP		16562.22 00	016171.40
	SR,\$X1,I16DP		16562.03 70	016172.00
	KV,\$X2,I16DP		16562.04 90	016172.40
	BXE,\$+1.32		16174.72 C2	016173.00
	SIC,SEN		1310.00 80	016173.40
	B,SERS	-SX, BIT 49, TO IX. CORE STG. FAILS.	1304.10 00	016174.00
	-			
	Z,\$R		11.22 00	016174.40
	SX,\$X0,\$R		11.01 10	016175.00
	LX,\$X1,\$R		11.02 10	016175.40
	Z,I16DP		16562.22 00	016176.00
	SR,\$X1,I16DP		16562.03 70	016176.40
	KV,\$X2,I16DP		16562.04 90	016177.00
	BXE,\$+1.32		16201.32 C2	016177.40
	SIC,SEN		1310.00 80	016200.00
	B,SERS	-SX, BIT 49, TO INT. MEM. FAILS.	1304.10 00	016200.40

B,\$+1.0
BD,l1617
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP.

LX,\$X13,IC216
V+,\$X13,BIT15
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16202.10 00
16144.04 00
1311.40 80
1301.10 00
16203.44 00

016201.00
016201.40
016202.00
016202.40
016203.00

16557.32 10
17345.32 80
16557.33 10

016203.40
016204.00
016204.40

11618	LX,\$X0,BIT50	-CHECK SX, BIT 50, INTO 3 MEMORIES.	17410.00 10	016205.00
	Z,I16DMP		16561.22 00	016205.40
	SX,\$X0,I16DMP		16561.01 10	016206.00
	Z,I16DP		16562.22 00	016206.40
	LX,\$X1,I16DMP		16561.02 10	016207.00
	LX,\$X2,BIT4	-REFILL SHFTD LEFT 46.	17332.04 10	016207.40
	SR,\$X1,I16DP		16562.03 70	016210.00
	KV,\$X2,I16DP		16562.04 90	016210.40
	BXE,\$+1.32		16212.72 C2	016211.00
	SIC,SEN		1310.00 80	016211.40
	B,SERS	-SX, BIT 50, TO EXT. MEM. FAILS.	1304.10 00	016212.00
	-			
	Z,\$X3		23.22 00	016212.40
	SX,\$X0,\$X3		23.01 10	016213.00
	LX,\$X1,\$X3		23.02 10	016213.40
	Z,I16DP		16562.22 00	016214.00
	SR,\$X1,I16DP		16562.03 70	016214.40
	KV,\$X2,I16DP		16562.04 90	016215.00
	BXE,\$+1.32		16217.32 C2	016215.40
	SIC,SEN		1310.00 80	016216.00
	B,SERS	-SX, BIT 50, TO IX. CORE STG. FAILS.	1304.10 00	016216.40
	-			
	Z,\$SR		11.22 00	016217.00
	SX,\$X0,\$SR		11.01 10	016217.40
	LX,\$X1,\$SR		11.02 10	016220.00
	Z,I16DP		16562.22 00	016220.40
	SR,\$X1,I16DP		16562.03 70	016221.00
	KV,\$X2,I16DP		16562.04 90	016221.40
	BXE,\$+1.32		16223.72 C2	016222.00
	SIC,SEN		1310.00 80	016222.40
	B,SERS	-SX,BIT 50, TO INT. MEM. FAILS.	1304.10 00	016223.00
	-			
	LX,\$X0,BIT51	-CHECK SX, BIT 51, INTO 3 MEMORIES.	17411.00 10	016223.40
	Z,I16DMP		16561.22 00	016224.00
	SX,\$X0,I16DMP		16561.01 10	016224.40
	Z,I16DP		16562.22 00	016225.00
	LX,\$X1,I16DMP		16561.02 10	016225.40
	LX,\$X2,BIT5	-REFILL SHFTD LEFT 46.	17333.04 10	016226.00
	SR,\$X1,I16DP		16562.03 70	016226.40
	KV,\$X2,I16DP		16562.04 90	016227.00
	BXE,\$+1.32		16231.32 C2	016227.40
	SIC,SEN		1310.00 80	016230.00
	B,SERS	-SX, BIT 51, TO EXT. MEM. FAILS.	1304.10 00	016230.40
	-			
	Z,\$X3		23.22 00	016231.00
	SX,\$X0,\$X3		23.01 10	016231.40
	LX,\$X1,\$X3		23.02 10	016232.00
	Z,I16DP		16562.22 00	016232.40
	SR,\$X1,I16DP		16562.03 70	016233.00
	KV,\$X2,I16DP		16562.04 90	016233.40
	BXE,\$+1.32		16235.72 C2	016234.00
	SIC,SEN		1310.00 80	016234.40
	B,SERS	-SX, BIT 51, TO IX. CORE STG. FAILS.	1304.10 00	016235.00
	-			
	Z,\$SR		11.22 00	016235.40
	SX,\$X0,\$SR		11.01 10	016236.00
	LX,\$X1,\$SR		11.02 10	016236.40
	Z,I16DP		16562.22 00	016237.00
	SR,\$X1,I16DP		16562.03 70	016237.40
	KV,\$X2,I16DP		16562.04 90	016240.00
	BXE,\$+1.32		16242.32 C2	016240.40
	SIC,SEN		1310.00 80	016241.00
	B,SERS	-SX, BIT 51, TO INT. MEM. FAILS.	1304.10 00	016241.40

-
B,\$+1.0
BD,I1618
SIC,SEN0+,32
B,SSW
BD,\$+.32

-TO SSIP.

LX,\$X13,IC216
V+,\$X13,BIT16
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16243.10 00
16205.04 00
1311.40 80
1301.10 00
16244.44 00

016242.00
016242.40
016243.00
016243.40
016244.00

-
16557.32 10
17346.32 80
16557.33 10

016244.40
016245.00
016245.40

11619	LX,\$X0,BIT52	-CHECK SX, BIT 52, INTO 3 MEMORIES.	17412.00	10	016246.00
	Z,I16DMP		16561.22	00	016246.40
	SX,\$X0,I16DMP		16561.01	10	016247.00
	Z,I16DP		16562.22	00	016247.40
	LX,\$X1,I16DMP		16561.02	10	016250.00
	LX,\$X2,BIT6	-REFILL SHFTD LEFT 46.	17334.04	10	016250.40
	SR,\$X1,I16DP		16562.03	70	016251.00
	KV,\$X2,I16DP		16562.04	90	016251.40
	BXE,\$+1.32		16253.72	C2	016252.00
	SIC,SEN		1310.00	80	016252.40
	B,SERS	-SX, BIT 52, TO EXT. MEM. FAILS.	1304.10	00	016253.00
	-				
	Z,\$X3		23.22	00	016253.40
	SX,\$X0,\$X3		23.01	10	016254.00
	LX,\$X1,\$X3		23.02	10	016254.40
	Z,I16DP		16562.22	00	016255.00
	SR,\$X1,I16DP		16562.03	70	016255.40
	KV,\$X2,I16DP		16562.04	90	016256.00
	BXE,\$+1.32		16260.32	C2	016256.40
	SIC,SEN		1310.00	80	016257.00
	B,SERS	-SX, BIT 52, TO IX. CORE STG. FAILS.	1304.10	00	016257.40
	-				
	Z,\$R		11.22	00	016260.00
	SX,\$X0,\$R		11.01	10	016260.40
	LX,\$X1,\$R		11.02	10	016261.00
	Z,I16DP		16562.22	00	016261.40
	SR,\$X1,I16DP		16562.03	70	016262.00
	KV,\$X2,I16DP		16562.04	90	016262.40
	BXE,\$+1.32		16264.72	C2	016263.00
	SIC,SEN		1310.00	80	016263.40
	B,SERS	-SX, BIT 52, TO INT. MEM. FAILS.	1304.10	00	016264.00
	-				
	LX,\$X0,BIT53	-CHECK SX, BIT 53, INTO 3 MEMORIES.	17413.00	10	016264.40
	Z,I16DMP		16561.22	00	016265.00
	SX,\$X0,I16DMP		16561.01	10	016265.40
	Z,I16DP		16562.22	00	016266.00
	LX,\$X1,I16DMP		16561.02	10	016266.40
	LX,\$X2,BIT7	-REFILL SHFTD LEFT 46.	17335.04	10	016267.00
	SR,\$X1,I16DP		16562.03	70	016267.40
	KV,\$X2,I16DP		16562.04	90	016270.00
	BXE,\$+1.32		16272.32	C2	016270.40
	SIC,SEN		1310.00	80	016271.00
	B,SERS	-SX, BIT 53, TO EXT. MEM. FAILS.	1304.10	00	016271.40
	-				
	Z,\$X3		23.22	00	016272.00
	SX,\$X0,\$X3		23.01	10	016272.40
	LX,\$X1,\$X3		23.02	10	016273.00
	Z,I16DP		16562.22	00	016273.40
	SR,\$X1,I16DP		16562.03	70	016274.00
	KV,\$X2,I16DP		16562.04	90	016274.40
	BXE,\$+1.32		16276.72	C2	016275.00
	SIC,SEN		1310.00	80	016275.40
	B,SERS	-SX, BIT 53, TO IX. CORE STG. FAILS.	1304.10	00	016276.00
	-				
	Z,\$R		11.22	00	016276.40
	SX,\$X0,\$R		11.01	10	016277.00
	LX,\$X1,\$R		11.02	10	016277.40
	Z,I16DP		16562.22	00	016300.00
	SR,\$X1,I16DP		16562.03	70	016300.40
	KV,\$X2,I16DP		16562.04	90	016301.00
	BXE,\$+1.32		16303.32	C2	016301.40
	SIC,SEN		1310.00	80	016302.00
	B,SERS	-SX, BIT 53, TO INT. MEM. FAILS.	1304.10	00	016302.40

B,\$+1.0
BD,I1619
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP.

LX,\$X13,IC216
V+,\$X13,BIT17
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16304.10 00
16246.04 00
1311.40 80
1301.10 00
16305.44 00

016303.00
016303.40
016304.00
016304.40
016305.00

16557.32 10
17347.32 80
16557.33 10

016305.40
016306.00
016306.40

11620	LX,\$X0,BIT54	-CHECK SX, BIT 54, INTO 3 MEMORIES.	17414.00 10	016307.00
	Z,I16DMP		16561.22 00	016307.40
	SX,\$X0,I16DMP		16561.01 10	016310.00
	Z,I16DP		16562.22 00	016310.40
	LX,\$X1,I16DMP		16561.02 10	016311.00
	LX,\$X2,BIT8	-REFIL SHFTD LEFT 46.	17336.04 10	016311.40
	SR,\$X1,I16DP		16562.03 70	016312.00
	KV,\$X2,I16DP		16562.04 90	016312.40
	BXE,\$+1.32		16314.72 C2	016313.00
	SIC,SEN		1310.00 80	016313.40
	B,SERS	-SX, BIT 54, TO EXT. MEM. FAILS.	1304.10 00	016314.00
	-			
	Z,\$X3		23.22 00	016314.40
	SX,\$X0,\$X3		23.01 10	016315.00
	LX,\$X1,\$X3		23.02 10	016315.40
	Z,I16DP		16562.22 00	016316.00
	SR,\$X1,I16DP		16562.03 70	016316.40
	KV,\$X2,I16DP		16562.04 90	016317.00
	BXE,\$+1.32		16321.32 C2	016317.40
	SIC,SEN		1310.00 80	016320.00
	B,SERS	-SX, BIT 54, TO IX. CORE STG. FAILS.	1304.10 00	016320.40
	-			
	Z,\$R		11.22 00	016321.00
	SX,\$X0,\$R		11.01 10	016321.40
	LX,\$X1,\$R		11.02 10	016322.00
	Z,I16DP		16562.22 00	016322.40
	SR,\$X1,I16DP		16562.03 70	016323.00
	KV,\$X2,I16DP		16562.04 90	016323.40
	BXE,\$+1.32		16325.72 C2	016324.00
	SIC,SEN		1310.00 80	016324.40
	B,SERS	-SX, BIT 54, TO INT. MEM. FAILS.	1304.10 00	016325.00
	-			
	LX,\$X0,BIT55	-CHECK SX, BIT 55, INTO 3 MEMORIES.	17415.00 10	016325.40
	Z,I16DMP		16561.22 00	016326.00
	SX,\$X0,I16DMP		16561.01 10	016326.40
	Z,I16DP		16562.22 00	016327.00
	LX,\$X1,I16DMP		16561.02 10	016327.40
	LX,\$X2,BIT9	-REFILL SHFTD LEFT 46.	17337.04 10	016330.00
	SR,\$X1,I16DP		16562.03 70	016330.40
	KV,\$X2,I16DP		16562.04 90	016331.00
	BXE,\$+1.32		16333.32 C2	016331.40
	SIC,SEN		1310.00 80	016332.00
	B,SERS	-SX, BIT 55, TO EXT. MEM. FAILS.	1304.10 00	016332.40
	-			
	Z,\$X3		23.22 00	016333.00
	SX,\$X0,\$X3		23.01 10	016333.40
	LX,\$X1,\$X3		23.02 10	016334.00
	Z,I16DP		16562.22 00	016334.40
	SR,\$X1,I16DP		16562.03 70	016335.00
	KV,\$X2,I16DP		16562.04 90	016335.40
	BXE,\$+1.32		16337.72 C2	016336.00
	SIC,SEN		1310.00 80	016336.40
	B,SERS	-SX, BIT 55, TO IX. CORE STG. FAILS.	1304.10 00	016337.00
	-			
	Z,\$R		11.22 00	016337.40
	SX,\$X0,\$R		11.01 10	016340.00
	LX,\$X1,\$R		11.02 10	016340.40
	Z,I16DP		16562.22 00	016341.00
	SR,\$X1,I16DP		16562.03 70	016341.40
	KV,\$X2,I16DP		16562.04 90	016342.00
	BXE,\$+1.32		16344.32 C2	016342.40
	SIC,SEN		1310.00 80	016343.00
	B,SERS	-SX, BIT 55 TO INT MEM FAILS.	1304.10 00	016343.40

B,\$+1.0
BD,I1620
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP.

LX,\$X13,IC216
V+,\$X13,BIT18
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16345.10 00
16307.04 00
1311.40 80
1301.10 00
16346.44 00

016344.00
016344.40
016345.00
016345.40
016346.00

16557.32 10
17350.32 80
16557.33 10

016346.40
016347.00
016347.40

11621	LX,\$X0,BIT56	-CHECK SX, BIT 56, INTO 3 MEMORIES.	17416.00	10	016350.00
	Z,I16DMP		16561.22	00	016350.40
	SX,\$X0,I16DMP		16561.01	10	016351.00
	Z,I16DP		16562.22	00	016351.40
	LX,\$X1,I16DMP		16561.02	10	016352.00
	LX,\$X2,BIT10	-REFILL SHFTD LEFT 46.	17340.04	10	016352.40
	SR,\$X1,I16DP		16562.03	70	016353.00
	KV,\$X2,I16DP		16562.04	90	016353.40
	BXE,\$+1.32		16355.72	C2	016354.00
	SIC,SEN		1310.00	80	016354.40
	B,SERS	-SX, BIT 56, TO EXT. MEM. FAILS.	1304.10	00	016355.00
	-				
	Z,\$X3		23.22	00	016355.40
	SX,\$X0,\$X3		23.01	10	016356.00
	LX,\$X1,\$X3		23.02	10	016356.40
	Z,I16DP		16562.22	00	016357.00
	SR,\$X1,I16DP		16562.03	70	016357.40
	KV,\$X2,I16DP		16562.04	90	016360.00
	BXE,\$+1.32		16362.32	C2	016360.40
	SIC,SEN		1310.00	80	016361.00
	B,SERS	-SX, BIT 56, TO IX. CORE STG. FAILS.	1304.10	00	016361.40
	-				
	Z,\$SR		11.22	00	016362.00
	SX,\$X0,\$SR		11.01	10	016362.40
	LX,\$X1,\$SR		11.02	10	016363.00
	Z,I16DP		16562.22	00	016363.40
	SR,\$X1,I16DP		16562.03	70	016364.00
	KV,\$X2,I16DP		16562.04	90	016364.40
	BXE,\$+1.32		16366.72	C2	016365.00
	SIC,SEN		1310.00	80	016365.40
	B,SERS	-SX, BIT 56, TO INT. MEM. FAILS.	1304.10	00	016366.00
	-				
	LX,\$X0,BIT57	-CHECK SX, BIT 57, INTO 3 MEMORIES.	17417.00	10	016366.40
	Z,I16DMP		16561.22	00	016367.00
	SX,\$X0,I16DMP		16561.01	10	016367.40
	Z,I16DP		16562.22	00	016370.00
	LX,\$X1,I16DMP		16561.02	10	016370.40
	LX,\$X2,BIT11	-REFILL SHFTD LEFT 46.	17341.04	10	016371.00
	SR,\$X1,I16DP		16562.03	70	016371.40
	KV,\$X2,I16DP		16562.04	90	016372.00
	BXE,\$+1.32		16374.32	C2	016372.40
	SIC,SEN		1310.00	80	016373.00
	B,SERS	-SX, BIT 57, TO EXT. MEM. FAILS.	1304.10	00	016373.40
	-				
	Z,\$X3		23.22	00	016374.00
	SX,\$X0,\$X3		23.01	10	016374.40
	LX,\$X1,\$X3		23.02	10	016375.00
	Z,I16DP		16562.22	00	016375.40
	SR,\$X1,I16DP		16562.03	70	016376.00
	KV,\$X2,I16DP		16562.04	90	016376.40
	BXE,\$+1.32		16400.72	C2	016377.00
	SIC,SEN		1310.00	80	016377.40
	B,SERS	-SX, BIT 57, TO IX. CORE STG. FAILS.	1304.10	00	016400.00
	-				
	Z,\$SR		11.22	00	016400.40
	SX,\$X0,\$SR		11.01	10	016401.00
	LX,\$X1,\$SR		11.02	10	016401.40
	Z,I16DP		16562.22	00	016402.00
	SR,\$X1,I16DP		16562.03	70	016402.40
	KV,\$X2,I16DP		16562.04	90	016403.00
	BXE,\$+1.32		16405.32	C2	016403.40
	SIC,SEN		1310.00	80	016404.00
	B,SERS	-SX, BIT 57, TO INT. MEM. FAILS.	1304.10	00	016404.40

B,\$+1.0
BD,I1621
SIC,SENO+.32
B,SSW
BD,\$+.32

-TO SSIP

LX,\$X13,IC216
V+,\$X13,BIT19
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16406.10 00
16350.04 00
1311.40 80
1301.10 00
16407.44 00

016405.00
016405.40
016406.00
016406.40
016407.00

16557.32 10
17351.32 80
16557.33 10

016407.40
016410.00
016410.40

11622	LX,\$X0,BIT58	-CHECK SX, BIT 58, INTO 3 MEMORIES.	17420.00	10	016411.00
	Z,I16DMP		16561.22	00	016411.40
	SX,\$X0,I16DMP		16561.01	10	016412.00
	Z,I16DP		16562.22	00	016412.40
	LX,\$X1,I16DMP		16561.02	10	016413.00
	LX,\$X2,BIT12	-REFILL SHFTD LEFT 46.	17342.04	10	016413.40
	SR,\$X1,I16DP		16562.03	70	016414.00
	KV,\$X2,I16DP		16562.04	90	016414.40
	BXE,\$+1.32		16416.72	C2	016415.00
	SIC,SEN		1310.00	80	016415.40
	B,SERS	-SX, BIT 58, TO EXT. MEM. FAILS.	1304.10	00	016416.00
	-				
	Z,\$X3		23.22	00	016416.40
	SX,\$X0,\$X3		23.01	10	016417.00
	LX,\$X1,\$X3		23.02	10	016417.40
	Z,I16DP		16562.22	00	016420.00
	SR,\$X1,I16DP		16562.03	70	016420.40
	KV,\$X2,I16DP		16562.04	90	016421.00
	BXE,\$+1.32		16423.32	C2	016421.40
	SIC,SEN		1310.00	80	016422.00
	B,SERS	-SX, BIT 58, TO IX. CORE STG. FAILS.	1304.10	00	016422.40
	-				
	Z,\$R		11.22	00	016423.00
	SX,\$X0,\$R		11.01	10	016423.40
	LX,\$X1,\$R		11.02	10	016424.00
	Z,I16DP		16562.22	00	016424.40
	SR,\$X1,I16DP		16562.03	70	016425.00
	KV,\$X2,I16DP		16562.04	90	016425.40
	BXE,\$+1.32		16427.72	C2	016426.00
	SIC,SEN		1310.00	80	016426.40
	B,SERS	-SX, BIT 58, TO INT. MEM. FAILS.	1304.10	00	016427.00
	-				
	LX,\$X0,BIT59	-CHECK SX, BIT 59, INTO 3 MEMORIES.	17421.00	10	016427.40
	Z,I16DMP		16561.22	00	016430.00
	SX,\$X0,I16DMP		16561.01	10	016430.40
	Z,I16DP		16562.22	00	016431.00
	LX,\$X1,I16DMP		16561.02	10	016431.40
	LX,\$X2,BIT13	-REFILL SHFTD LEFT 46.	17343.04	10	016432.00
	SR,\$X1,I16DP		16562.03	70	016432.40
	KV,\$X2,I16DP		16562.04	90	016433.00
	BXE,\$+1.32		16435.32	C2	016433.40
	SIC,SEN		1310.00	80	016434.00
	B,SERS	-SX, BIT 59, TO EXT. MEM. FAILS.	1304.10	00	016434.40
	-				
	Z,\$X3		23.22	00	016435.00
	SX,\$X0,\$X3		23.01	10	016435.40
	LX,\$X1,\$X3		23.02	10	016436.00
	Z,I16DP		16562.22	00	016436.40
	SR,\$X1,I16DP		16562.03	70	016437.00
	KV,\$X2,I16DP		16562.04	90	016437.40
	BXE,\$+1.32		16441.72	C2	016440.00
	SIC,SEN		1310.00	80	016440.40
	B,SERS	-SX, BIT 59, TO IX. CORE STG. FAILS.	1304.10	00	016441.00
	-				
	Z,\$R		11.22	00	016441.40
	SX,\$X0,\$R		11.01	10	016442.00
	LX,\$X1,\$R		11.02	10	016442.40
	Z,I16DP		16562.22	00	016443.00
	SR,\$X1,I16DP		16562.03	70	016443.40
	KV,\$X2,I16DP		16562.04	90	016444.00
	BXE,\$+1.32		16446.32	C2	016444.40
	SIC,SEN		1310.00	80	016445.00
	B,SERS	-SX, BIT 59 TO INT MEM FAILS.	1304.10	00	016445.40

-
B,S+1.0
BD,I1622
SIC,SEN0+.32
B,SSW
BD,S+.32

-TO SSIP.

LX,\$X13,IC216
V+,\$X13,BIT20
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16447.10 00
16411.04 00
1311.40 80
1301.10 00
16450.44 00

016446.00
016446.40
016447.00
016447.40
016450.00

-
16557.32 10
17352.32 80
16557.33 10

016450.40
016451.00
016451.40

11623	LX,\$X0,BIT60	-CHECK SX, BIT 60, INTO 3 MEMORIES.	17422.00 10	016452.00
	Z,I16DMP		16561.22 00	016452.40
	SX,\$X0,I16DMP		16561.01 10	016453.00
	Z,I16DP		16562.22 00	016453.40
	LX,\$X1,I16DMP		16561.02 10	016454.00
	LX,\$X2,BIT14	-REFILL SHFTD LEFT 46.	17344.04 10	016454.40
	SR,\$X1,I16DP		16562.03 70	016455.00
	KV,\$X2,I16DP		16562.04 90	016455.40
	BXE,\$+1.32		16457.72 C2	016456.00
	SIC,SEN		1310.00 80	016456.40
	B,SERS	-SX, BIT 60, TO EXT. MEM. FAILS.	1304.10 00	016457.00
	-			
	Z,\$X3		23.22 00	016457.40
	SX,\$X0,\$X3		23.01 10	016460.00
	LX,\$X1,\$X3		23.02 10	016460.40
	Z,I16DP		16562.22 00	016461.00
	SR,\$X1,I16DP		16562.03 70	016461.40
	KV,\$X2,I16DP		16562.04 90	016462.00
	BXE,\$+1.32		16464.32 C2	016462.40
	SIC,SEN		1310.00 80	016463.00
	B,SERS	-SX, BIT 60, TO IX. CORE STG. FAILS.	1304.10 00	016463.40
	-			
	Z,\$R		11.22 00	016464.00
	SX,\$X0,\$R		11.01 10	016464.40
	LX,\$X1,\$R		11.02 10	016465.00
	Z,I16DP		16562.22 00	016465.40
	SR,\$X1,I16DP		16562.03 70	016466.00
	KV,\$X2,I16DP		16562.04 90	016466.40
	BXE,\$+1.32		16470.72 C2	016467.00
	SIC,SEN		1310.00 80	016467.40
	B,SERS	-SX, BIT 60, TO INT. MEM. FAILS.	1304.10 00	016470.00
	-			
	LX,\$X0,BIT61	-CHECK SX, BIT 61, INTO 3 MEMORIES.	17423.00 10	016470.40
	Z,I16DMP		16561.22 00	016471.00
	SX,\$X0,I16DMP		16561.01 10	016471.40
	Z,I16DP		16562.22 00	016472.00
	LX,\$X1,I16DMP		16561.02 10	016472.40
	LX,\$X2,BIT15	-REFILL SHFTD LEFT 46.	17345.04 10	016473.00
	SR,\$X1,I16DP		16562.03 70	016473.40
	KV,\$X2,I16DP		16562.04 90	016474.00
	BXE,\$+1.32		16476.32 C2	016474.40
	SIC,SEN		1310.00 80	016475.00
	B,SERS	-SX, BIT 61, TO EXT. MEM. FAILS.	1304.10 00	016475.40
	-			
	Z,\$X3		23.22 00	016476.00
	SX,\$X0,\$X3		23.01 10	016476.40
	LX,\$X1,\$X3		23.02 10	016477.00
	Z,I16DP		16562.22 00	016477.40
	SR,\$X1,I16DP		16562.03 70	016500.00
	KV,\$X2,I16DP		16562.04 90	016500.40
	BXE,\$+1.32		16502.72 C2	016501.00
	SIC,SEN		1310.00 80	016501.40
	B,SERS	-SX, BIT 61, TO IX. CORE STG. FAILS.	1304.10 00	016502.00
	-			
	Z,\$R		11.22 00	016502.40
	SX,\$X0,\$R		11.01 10	016503.00
	LX,\$X1,\$R		11.02 10	016503.40
	Z,I16DP		16562.22 00	016504.00
	SR,\$X1,I16DP		16562.03 70	016504.40
	KV,\$X2,I16DP		16562.04 90	016505.00
	BXE,\$+1.32		16507.32 C2	016505.40
	SIC,SEN		1310.00 80	016506.00
	B,SERS	-SX, BIT 61, TO INT. MEM. FAILS.	1304.10 00	016506.40

B,\$+1.0
BD,l1623
SIC,SEN0+.32
B,SSW
BD,\$+.32

-TO SSIP

LX,\$X13,IC216
V+,\$X13,BIT21
SX,\$X13,IC216

-UPDATE CONTINUITY CHECK.

16510.10 00
16452.04 00
1311.40 80
1301.10 00
16511.44 00

016507.00
016507.40
016510.00
016510.40
016511.00

16557.32 10
17353.32 80
16557.33 10

016511.40
016512.00
016512.40

11624	LX,\$X0,BIT62	-CHECK SX, BIT 62, INTO 3 MEMORIES.	17424.00 10	016513.00
	Z,I16DMP		16561.22 00	016513.40
	SX,\$X0,I16DMP		16561.01 10	016514.00
	Z,I16DP		16562.22 00	016514.40
	LX,\$X1,I16DMP		16561.02 10	016515.00
	LX,\$X2,BIT16	-REFILL SHFTD LEFT 46.	17346.04 10	016515.40
	SR,\$X1,I16DP		16562.03 70	016516.00
	KV,\$X2,I16DP		16562.04 90	016516.40
	BXE,\$+1.32		16520.72 C2	016517.00
	SIC,SEN		1310.00 80	016517.40
	B,SERS	-SX, BIT 62, TO EXT. MEM. FAILS.	1304.10 00	016520.00
	-			
	Z,\$X3		23.22 00	016520.40
	SX,\$X0,\$X3		23.01 10	016521.00
	LX,\$X1,\$X3		23.02 10	016521.40
	Z,I16DP		16562.22 00	016522.00
	SR,\$X1,I16DP		16562.03 70	016522.40
	KV,\$X2,I16DP		16562.04 90	016523.00
	BXE,\$+1.32		16525.32 C2	016523.40
	SIC,SEN		1310.00 80	016524.00
	B,SERS	-SX, BIT 62, TO IX. CORE STG. FAILS.	1304.10 00	016524.40
	-			
	Z,\$R		11.22 00	016525.00
	SX,\$X0,\$R		11.01 10	016525.40
	LX,\$X1,\$R		11.02 10	016526.00
	Z,I16DP		16562.22 00	016526.40
	SR,\$X1,I16DP		16562.03 70	016527.00
	KV,\$X2,I16DP		16562.04 90	016527.40
	BXE,\$+1.32		16531.72 C2	016530.00
	SIC,SEN		1310.00 80	016530.40
	B,SERS	-SX, BIT 62, TO INT. MEM. FAILS.	1304.10 00	016531.00
	-			
	LX,\$X0,BIT63	-CHECK SX, BIT 63, INTO 3 MEMORIES.	17425.00 10	016531.40
	Z,I16DP		16562.22 00	016532.00
	SX,\$X0,I16DMP		16561.01 10	016532.40
	Z,I16DP		16562.22 00	016533.00
	LX,\$X1,I16DMP		16561.02 10	016533.40
	LX,\$X2,BIT17	-REFILL SHFTD LEFT 46.	17347.04 10	016534.00
	SR,\$X1,I16DP		16562.03 70	016534.40
	KV,\$X2,I16DP		16562.04 90	016535.00
	BXE,\$+1.32		16537.32 C2	016535.40
	SIC,SEN		1310.00 80	016536.00
	B,SERS	-SX, BIT 63, TO EXT. MEM. FAILS.	1304.10 00	016536.40
	-			
	Z,\$X3		23.22 00	016537.00
	SX,\$X0,\$X3		23.01 10	016537.40
	LX,\$X1,\$X3		23.02 10	016540.00
	Z,I16DP		16562.22 00	016540.40
	SR,\$X1,I16DP		16562.03 70	016541.00
	KV,\$X2,I16DP		16562.04 90	016541.40
	BXE,\$+1.32		16543.72 C2	016542.00
	SIC,SEN		1310.00 80	016542.40
	B,SERS	-SX, BIT 63, TO IX. CORE STG. FAILS.	1304.10 00	016543.00
	-			
	Z,\$R		11.22 00	016543.40
	SX,\$X0,\$R		11.01 10	016544.00
	LX,\$X1,\$R		11.02 10	016544.40
	Z,I16DP		16562.22 00	016545.00
	SR,\$X1,I16DP		16562.03 70	016545.40
	KV,\$X2,I16DP		16562.04 90	016546.00
	BXE,\$+1.32		16550.32 C2	016546.40
	SIC,SEN		1310.00 80	016547.00
	B,SERS	-SX, BIT 63, TO INT. MEM. FAILS.	1304.10 00	016547.40

	B,\$+1.0		16551.10 00	016550.00
	BD,I1624		16513.04 00	016550.40
	SIC,SEN0+.32		1311.40 80	016551.00
	B,SSW	-TO SSIP.	1301.10 00	016551.40
	BD,\$+.32		16552.44 00	016552.00
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	016552.40
	V+,\$X13,BIT22		17354.32 80	016553.00
	SX,\$X13,IC216		16557.33 10	016553.40
	LX,\$X13,IC216	-UPDATE CONTINUITY CHECK.	16557.32 10	016554.00
	KV,\$X13,ICK216		16560.32 90	016554.40
	SIC,SEN		1310.00 80	016555.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	016555.40
	B,I18		16605.10 00	016556.00
	CNOP		0.30 00	016556.40
IC216	XW,0,0,0	-CONTINUITY REG 1216.	0.00 00 000000.00 00	016557.00
ICK216	XW,%8□777777.76,0,0		777777.76 00 000000.00 00	016560.00

```

I16DMP NOP
      NOP
I16DP  NOP
      NOP
BIT28A XW,1,0,1,0
BIT29A XW,2,0,2,0
BIT30A XW,4,0,4,0
BIT31A XW,8,0,8,0
BIT32A XW,16,0,16,0
BIT33A XW,32,0,32,0
BIT34A XW,64,0,64,0
BIT35A XW,128,0,128,0
BIT36A XW,256,0,256,0
BIT37A XW,512,0,512,0
BIT38A XW,1024,0,1024,0
BIT39A XW,2048,0,2048,0
BIT40A XW,4096,0,4096,0
BIT41A XW,8192,0,8192,0
BIT42A XW,16384,0,16384,0
BIT43A XW,32768,0,32768,0
BIT44A XW,65536,0,65536,0
BIT45A XW,131072,0,131072,0

```

-DUMP LOCATIONS.

```

0.30 00 016561.00
0.30 00 016561.40
0.30 00 016562.00
0.30 00 016562.40
1.00 00 000020.00 00 016563.00
2.00 00 000040.00 00 016564.00
4.00 00 000100.00 00 016565.00
10.00 00 000200.00 00 016566.00
20.00 00 000400.00 00 016567.00
40.00 00 001000.00 00 016570.00
100.00 00 002000.00 00 016571.00
200.00 00 004000.00 00 016572.00
400.00 00 010000.00 00 016573.00
1000.00 00 020000.00 00 016574.00
2000.00 00 040000.00 00 016575.00
4000.00 00 100000.00 00 016576.00
10000.00 00 200000.00 00 016577.00
20000.00 00 400000.00 00 016600.00
40000.00 01 000000.00 00 016601.00
100000.00 02 000000.00 00 016602.00
200000.00 04 000000.00 00 016603.00
400000.00 08 000000.00 00 016604.00

```

----I218---LX AND SX,BITS 24-27, 3MEMS.

-THIS TEST CHECKS THE LOADING AND STORING OF
-INDEX BITS 24-27 FROM AND INTO THE THREE
-MEMORIES. INSTRUCTION MODIFICATION IS USED
-TO CHECK BITS 26 AND 27.

-INDEX REGS USED IN THIS AND THEIR MEANING ARE,

-IX 0, WORKING REG.
-IX 1, WORKING REG.
-IX 2 NOT ZERO - LX,SX, EXT MEM FAILED.
-IX 3 NOT ZERO - LX INT AND SX EXT MEM FAILED.
-IX 4 NOT ZERO- LX IX CORE STG AND SX EXT MEM
-FAILED.
-IX 5 NOT ZERO - SX TO INT MEM FAILED.
-IX 6 NOT ZERO - SX TO IX CORE STG FAILED.
-IX 7, WORKING REG.

I18 LX,SX1,I18ID -UPDATE IDENT.
SX,SX1,DPET13
SIC,RET
B,IDF1
Z,IC218
BD,I180
CNOP
I18ID %IQSZ=DD%BU,64,8=,I218 Z

16610.02 10
1437.03 10
1306.40 80
1443.10 00
17247.22 00
16611.04 00

016605.00
016605.40
016606.00
016606.40
016607.00
016607.40

016610.00

1180	LX,\$X0,I18K1	-CHECK BIT 24 FRM EXT MEM, LX.	17270.00	10		016611.00
	KV,\$X0,BIT23		17355.00	90		016611.40
	SIC,SEN		1310.00	80		016612.00
	BZXL,SERS	-LOST BIT 24, LX FRM EXT MEM.	1304.32	40		016612.40
	-					
	LX,\$X0,BIT23		17355.00	10		016613.00
	KV,\$X0,I18K1		17270.00	90		016613.40
	SIC,SEN		1310.00	80		016614.00
	BZXH,SERS	-SPUR BIT 24, LX FRM EXT MEM.	1304.33	40		016614.40
	-					
	LX,\$X0,I18K1	-CHECK BIT 24 FRM IX CORE STG, LX.	17270.00	10		016615.00
	LX,\$X1,\$X0		20.02	10		016615.40
KV,\$X1,BIT23		17355.02	90		016616.00	
SIC,SEN		1310.00	80		016616.40	
BZXL,SERS	-LOST BIT 24, LX FRM IX CORE STG.	1304.32	40		016617.00	
-						
LX,\$X2,BIT23		17355.04	10		016617.40	
LX,\$X1,\$X2		22.02	10		016620.00	
KV,\$X1,I18K1		17270.02	90		016620.40	
L%BU□,I18K1		17270.00	80	000000.20	50	016621.00
SIC,SEN		1310.00	80		016622.00	
BZXH,SERS	-SPUR BIT 24, LX FRM IX CORE STG.	1304.33	40		016622.40	
-						
LX,\$X0,\$R	-CHECK BIT 24, LX FRM INT MEM.	11.00	10		016623.00	
L%BU□,BIT23		17355.00	80	000000.20	50	016623.40
KV,\$X0,BIT23		17355.00	90		016624.40	
SIC,SEN		1310.00	80		016625.00	
BZXL,SERS	-LOST BIT 24, LX FRM INT MEM.	1304.32	40		016625.40	
-						
LX,\$X1,\$R		11.02	10		016626.00	
KV,\$X1,I18K1		17270.02	90		016626.40	
SIC,SEN		1310.00	80		016627.00	
BZXH,SERS	-SPUR BIT 24, LX FRM INT MEM.	1304.33	40		016627.40	
-						

	Z,I18DMP	-CHECK BIT 24 TO EXT MEM, SX.	17251.22 00	016630.00
	LX,\$X0,I18K1		17270.00 10	016630.40
	SX,\$X0,I18DMP		17251.01 10	016631.00
	LX,\$X0,BIT23		17355.00 10	016631.40
	KV,\$X0,I18DMP		17251.00 90	016632.00
	BXH,I181		16634.73 42	016632.40
	SIC,SEN		1310.00 80	016633.00
	B,SERS	-LOST BIT 24 ON SX TO EXT MEM.	1304.10 00	016633.40
	B,I182	-BYPASS NEXT TEST.	16637.50 00	016634.00
	-			
I181	SX,\$X0,I18DMP		17251.01 10	016634.40
	LX,\$X0,BIT24		17356.00 10	016635.00
	KV,\$X0,I18DMP		17251.00 90	016635.40
	BXL,\$+1.32		16637.72 42	016636.00
	SIC,SEN		1310.00 80	016636.40
	B,SERS	-SPUR BIT 24 ON SX TO EXT MEM.	1304.10 00	016637.00
	-			
I182	Z,\$R	-CHECK BIT 24 TO INT MEM, SX.	11.22 00	016637.40
	LX,\$X0,I18K1		17270.00 10	016640.00
	SX,\$X0,\$R		11.01 10	016640.40
	LX,\$X0,BIT23		17355.00 10	016641.00
	KV,\$X0,\$R		11.00 90	016641.40
	BXH,I182A		16644.33 42	016642.00
	SIC,SEN		1310.00 80	016642.40
	B,SERS	-LOST BIT 24 ON SX TO INT MEM.	1304.10 00	016643.00
	B,I183	-BYPASS NEXT TEST.	16650.10 00	016643.40
	-			
I182A	L%BU, BIT24		17356.00 80	000000.20 50
	SX,\$X0,\$R		11.01 10	016645.00
	LX,\$X0,BIT24		17356.00 10	016645.40
	KV,\$X0,\$R		11.00 90	016646.00
	BXL,\$+1.32		16650.32 42	016646.40
	SIC,SEN		1310.00 80	016647.00
	B,SERS	-SPUR BIT 24 ON SX TO INT MEM.	1304.10 00	016647.40
	-			
I183	Z,\$X1	-CHECK BIT 24 TO IX CORE STG, SX.	21.22 00	016650.00
	LX,\$X0,I18K1		17270.00 10	016650.40
	SX,\$X0,\$X1		21.01 10	016651.00
	LX,\$X0,BIT23		17355.00 10	016651.40
	KV,\$X0,\$X1		21.00 90	016652.00
	BXH,I184		16654.73 42	016652.40
	SIC,SEN		1310.00 80	016653.00
	B,SERS	-LOST BIT 24 ON SX TO IX CORE STG.	1304.10 00	016653.40
	B,I185	-BYPASS NEXT TEST.	16660.10 00	016654.00

1184	LX,\$X1,BIT24 SX,\$X0,\$X1 LX,\$X0,BIT24 KV,\$X0,\$X1 BXL,\$+1.32 SIC,SEN B,SERS	-SPUR BIT 24 ON SX TO IX CORE STG.	17356.02 10 21.01 10 17356.00 10 21.00 90 16660.32 42 1310.00 80 1304.10 00	016654.40 016655.00 016655.40 016656.00 016656.40 016657.00 016657.40
1185	B,\$+1.0 BD,1180 SIC,SEN0+.32 B,SSW BD,\$+.32	-TO SSIP.	16661.10 00 16611.04 00 1311.40 80 1301.10 00 16662.44 00	016660.00 016660.40 016661.00 016661.40 016662.00
	LX,\$X13,IC218 V+,\$X13,BIT0 SX,\$X13,IC218	-UPDATE CONTINUITY CHECK.	17247.32 10 17326.32 80 17247.33 10	016662.40 016663.00 016663.40
1185A	LX,\$X0,BIT38 SX,\$X0,\$IND BXF,\$+1.0 B,11824 Z,\$IND BXF,11824 SX,\$X0,\$IND LX,\$X0,100Z BXF,11824	-CHECK BIT 25 FROM EXT MEM, LX, -AND TO EXT MEM, SX.	17374.00 10 13.01 10 16666.23 42 16746.10 00 13.22 00 16746.23 42 13.01 10 17306.00 10 16746.23 42 17357.00 10	016664.00 016664.40 016665.00 016665.40 016666.00 016666.40 016667.00 016667.40 016670.00 016670.40
1186	LX,\$X0,BIT25 BXF,1187 SIC,SEN B,SERS B,11812	-LOST BIT 25 ON LX FROM EXT. MEM. -BYPASS REST OF TEST.	16673.23 42 1310.00 80 1304.10 00 16706.50 00	016671.00 016671.40 016672.00 016672.40
1187	Z,\$IND Z,118DMP SX,\$X0,118DMP LX,\$X0,118DMP BXF,1189 SIC,SEN B,SERS B,11812	-LOST BIT 25 ON SX TO EXT MEM. -BYPASS REST OF TEST.	13.22 00 17251.22 00 17251.01 10 17251.00 10 16677.23 42 1310.00 80 1304.10 00 16706.50 00	016673.00 016673.40 016674.00 016674.40 016675.00 016675.40 016676.00 016676.40
1189	Z,\$IND LX,\$X0,100Z BXF,\$+1.0 B,11810 SIC,SEN B,SERS B,11812	-SPUR BIT 25 ON LX FRM EXT MEM. -BYPASS REST OF TEST.	13.22 00 17306.00 10 16701.23 42 16702.50 00 1310.00 80 1304.10 00 16706.50 00	016677.00 016677.40 016700.00 016700.40 016701.00 016701.40 016702.00
11810	Z,\$IND Z,118DMP SX,\$X0,118DMP LX,\$X0,118DMP BXF,\$+1.0 B,11812 SIC,SEN B,SERS	-SPUR BIT 25 ON SX TO EXT MEM.	13.22 00 17251.22 00 17251.01 10 17251.00 10 16705.63 42 16706.50 00 1310.00 80 1304.10 00	016702.40 016703.00 016703.40 016704.00 016704.40 016705.00 016705.40 016706.00

11812	Z,\$IND L%BU, BIT25 LX,\$X0,\$R BXF, 11813 SIC, SEN B, SERS B, 11818 -	-CHECK BIT 25 FROM INT MEM, LX, -AND TO INT MEM, SX. -LOST BIT 25 ON LX FROM INT MEM. -BYPASS REST OF TEST.	13.22 00 17357.00 80 000000.20 50 11.00 10 16712.63 42 1310.00 80 1304.10 00 16726.10 00	016706.40 016707.00 016710.00 016710.40 016711.00 016711.40 016712.00
11813	Z,\$IND Z,\$R SX,\$X0,\$R LX,\$X0,\$R BXF, 11815 SIC, SEN B, SERS B, 11818 -	-LOST BIT 25 ON SX TO INT MEM. -BYPASS REST OF TEST.	13.22 00 11.22 00 11.01 10 11.00 10 16716.63 42 1310.00 80 1304.10 00 16726.10 00	016712.40 016713.00 016713.40 016714.00 016714.40 016715.00 016715.40 016716.00
11815	Z,\$IND Z,\$R LX,\$X0,\$R BXF,\$+1.0 B, 11816 SIC, SEN B, SERS B, 11818 -	-SPUR BIT 25 ON LX FRM INT MEM. -BYPASS REST OF TEST.	13.22 00 11.22 00 11.00 10 16721.23 42 16722.50 00 1310.00 80 1304.10 00 16726.10 00	016716.40 016717.00 016717.40 016720.00 016720.40 016721.00 016721.40 016722.00
11816	Z,\$IND SX,\$X0,\$R LX,\$X0,\$R BXF,\$+1.0 B, 11818 SIC, SEN B, SERS -	-SPUR BIT 25 ON SX TO INT MEM.	13.22 00 11.01 10 11.00 10 16725.23 42 16726.10 00 1310.00 80 1304.10 00	016722.40 016723.00 016723.40 016724.00 016724.40 016725.00 016725.40
11818	LX,\$X1, BIT25 Z,\$IND LX,\$X0,\$X1 BXF, 11819 SIC, SEN B, SERS B, 11825	-CHECK BIT 25 FROM IX CORE STG, LX, -AND TO IX CORE STG, SX. -LOST BIT 25 ON LX FRM IX CORE STG. -BYPASS REST OF TEST.	17357.02 10 13.22 00 21.00 10 16731.63 42 1310.00 80 1304.10 00 16747.10 00	016726.00 016726.40 016727.00 016727.40 016730.00 016730.40 016731.00

11819	Z,\$X1		21.22 00	016731.40
	Z,\$IND		13.22 00	016732.00
	SX,\$X0,\$X1		21.01 10	016732.40
	LX,\$X0,\$X1		21.00 10	016733.00
	BXF,I1821		16735.63 42	016733.40
	SIC,SEN		1310.00 80	016734.00
	B,SERS	-LOST BIT 25 ON SX TO IX CORE STG.	1304.10 00	016734.40
	B,I1825	-BYPASS REST OF TEST.	16747.10 00	016735.00
	-			
11821	Z,\$IND		13.22 00	016735.40
	Z,\$X1		21.22 00	016736.00
	LX,\$X0,\$X1		21.00 10	016736.40
	BXF,\$+1.0		16740.23 42	016737.00
	B,I1822		16741.50 00	016737.40
	SIC,SEN		1310.00 80	016740.00
	B,SERS	-SPUR BIT 25 ON LX FROM IX CORE STG.	1304.10 00	016740.40
	B,I1825	-BYPASS REST OF TEST.	16747.10 00	016741.00
	-			
11822	Z,\$X1		21.22 00	016741.40
	Z,\$IND		13.22 00	016742.00
	SX,\$X0,\$X1		21.01 10	016742.40
	LX,\$X0,\$X1		21.00 10	016743.00
	BXF,\$+1.0		16744.63 42	016743.40
	B,I1825		16747.10 00	016744.00
	SIC,SEN		1310.00 80	016744.40
	B,SERS	-SPUR BIT 25 ON SX TO IX CORE STG.	1304.10 00	016745.00
	B,I1825		16747.10 00	016745.40
	-			
11824	SIC,SEN	-UNABLE TO SET OR CLR XF INDIC.	1310.00 80	016746.00
	B,SERS	-NO BIT 25 TESTS MADE.	1304.10 00	016746.40
	-			
11825	B,\$+1.0		16750.10 00	016747.00
	BD,I185A		16664.04 00	016747.40
	SIC,SEN0+.32		1311.40 80	016750.00
	B,SSW	-TO SSIP	1301.10 00	016750.40
	BD,\$+.32		16751.44 00	016751.00
	-			
	LX,\$X13,IC218	-UPDATE CONTINUITY CHECK.	17247.32 10	016751.40
	V+,\$X13,BIT1		17327.32 80	016752.00
	SX,\$X13,IC218		17247.33 10	016752.40

11826	Z,\$X0 LX,\$X1,118W1 SX,\$X1,11827 Z,\$X2 CNOP	-CHECK BITS 26 AND 27 FRM EXT MEM,LX, -AND TO EXT MEM, SX.	20.22 00 17252.02 10 16755.03 10 22.22 00	016753.00 016753.40 016754.00 016754.40
11827	NOP NOP B,11829 CNOP	-27 NOT LOST.	0.30 00 0.30 00 16761.10 00 0.30 00	016755.00 016755.40 016756.00 016756.40
11828	BD,\$+.32 LX,\$X2,1000 SIC,SEN B,SERS	-IX 2 NOT ZERO TO IND. ERROR. -LX BIT 27 FROM EXT MEM LOST OR -SX BIT 27 TO EXT MEM LOST.	16757.44 00 17307.04 10 1310.00 80 1304.10 00	016757.00 016757.40 016760.00 016760.40
11829	Z,\$X3 Z,\$X0 L%BU□,118W2 LX,\$X1,\$R SX,\$X1,11830 NOP CNOP		23.22 00 20.22 00 17253.00 80 000000.20 50 11.02 10 16765.03 10 0.30 00 0.30 00	016761.00 016761.40 016762.00 016763.00 016763.40 016764.00 016764.40
11830	NOP NOP B,11832 CNOP	-27 NOT LOST	0.30 00 0.30 00 16771.10 00 0.30 00	016765.00 016765.40 016766.00 016766.40
11831	BD,\$+.32 LX,\$X3,1000 SIC,SEN B,SERS	-IX 3 NOT ZERO TO INDICATE ERROR. -LX BIT 27 FROM INT MEM LOST OR -SX BIT 27 TO EXT MEM LOST.	16767.44 00 17307.06 10 1310.00 80 1304.10 00	016767.00 016767.40 016770.00 016770.40
11832	L%BU□,\$X2 BRZ,11833 L%BU□,\$X3 BRZ,11834 B,11848	-CANT GET BIT 27 INTO IX REG,MUST -CONCLUDE TEST.	22.00 80 000000.20 50 16774.74 C2 23.00 80 000000.20 50 16775.74 C2 17036.10 00	016771.00 016772.00 016772.40 016773.40 016774.00
11833	LX,\$X7,118W3 B,11835		17254.16 10 16777.10 00	016774.40 016775.00
11834	L%BU□,118W3 LX,\$X7,\$R		17254.00 80 000000.20 50 11.16 10	016775.40 016776.40
11835	Z,\$X0 Z,\$X4 LX,\$X1,\$X7 SX,\$X1,11836 NOP NOP CNOP		20.22 00 24.22 00 27.02 10 17002.03 10 0.30 00 0.30 00	016777.00 016777.40 017000.00 017000.40 017001.00 017001.40
11836	NOP NOP B,11838 CNOP		0.30 00 0.30 00 17006.10 00 0.30 00	017002.00 017002.40 017003.00 017003.40
11837	BD,\$+.32 LX,\$X4,1000 SIC,SEN B,SERS	-IX4 TO INDICATE ERROR. -LX BIT 27 FROM IX CORE STG LOST OR -SX BIT 27 TO EXT MEM LOST.	17004.44 00 17307.10 10 1310.00 80 1304.10 00	017004.00 017004.40 017005.00 017005.40
11838	L%BU□,\$X3 BRZ,11840 B,11844	-CANT LX BIT 27 FRM INT MEM,TERMINATE.	23.00 80 000000.20 50 17010.34 C2 17023.10 00	017006.00 017007.00 017007.40

11840	L%BU□,118W4 LX,\$X1,\$R Z,\$X5		17255.00 80 000000.20 50	017010.00
			11.02 10	017011.00
			25.22 00	017011.40
11841	Z,\$X0 Z,\$R SX,\$X1,\$R NOP NOP LX,\$X1,\$R SX,\$X1,11842		20.22 00	017012.00
			11.22 00	017012.40
			11.03 10	017013.00
			0.30 00	017013.40
			0.30 00	017014.00
			11.02 10	017014.40
			17017.03 10	017015.00
			0.30 00	017015.40
			0.30 00	017016.00
			0.30 00	017016.40
11842	NOP NOP CNOP NOP NOP B,11844	-27 NOT LOST	0.30 00	017017.00
			0.30 00	017017.40
			17023.10 00	017020.00
			0.30 00	017020.40
11843	BD,\$+.32 LX,\$X5,1000 SIC,\$SEN B,\$ERS	-IX 5 TO INDICATE ERROR. -SX BIT 27 TO INT MEM LOST.	17021.44 00	017021.00
			17307.12 10	017021.40
			1310.00 80	017022.00
			1304.10 00	017022.40
11844	L%BU□,\$X4 BRZ,11845 B,11848	-CANT LX FROM IX CORE STG,TERMINATE	24.00 80 000000.20 50	017023.00
			17025.34 C2	017024.00
			17036.10 00	017024.40
11845	LX,\$X1,118W5 Z,\$X6 Z,\$X0 Z,\$X7 SX,\$X1,\$X7 NOP NOP SX,\$X7,11846 NOP NOP CNOP		17256.02 10	017025.00
			26.22 00	017025.40
			20.22 00	017026.00
			27.22 00	017026.40
			27.03 10	017027.00
			0.30 00	017027.40
			0.30 00	017030.00
			17032.17 10	017030.40
			0.30 00	017031.00
			0.30 00	017031.40
11846	NOP NOP B,11848	-27 NOT LOST.	0.30 00	017032.00
			0.30 00	017032.40
			17036.10 00	017033.00
			0.30 00	017033.40
11847	BD,\$+.32 LX,\$X6,1000 SIC,\$SEN B,\$ERS	-IX 6 TO INDICATE ERROR. -SX BIT 27 TO IX CORE STG LOST	17034.44 00	017034.00
			17307.14 10	017034.40
			1310.00 80	017035.00
			1304.10 00	017035.40
11848	B,\$+1.0 BD,11826 SIC,\$SEN0+.32 B,\$SW BD,\$+.32	-TO SSIP.	17037.10 00	017036.00
			16753.04 00	017036.40
			1311.40 80	017037.00
			1301.10 00	017037.40
			17040.44 00	017040.00
	LX,\$X13,IC218 V+,\$X13,BIT2 SX,\$X13,IC218	-UPDATE CONTINUITY CHECK.	17247.32 10	017040.40
			17330.32 80	017041.00
			17247.33 10	017041.40

11848A	LX,\$X1,118W6 Z,\$X0 Z,\$R SX,\$X1,11849 Z,\$X2 NOP CNOP	-START CHECK FOR BIT 27 SPUR, AND -BIT 26 LOST.	17257.02 10 20.22 00 11.22 00 17045.03 10 22.22 00 0.30 00		017042.00 017042.40 017043.00 017043.40 017044.00 017044.40
11849	NOP NOP B,11851 -		0.30 00 0.30 00 17051.10 00		017045.00 017045.40 017046.00
11850	CNOP BD,\$+.32 SIC,SEN B,SERS B,11853 -	-IF GET HERE, 26 LOST, 27 OK. -LX BIT 26 FRM EXT MEM LOST OR -SX BIT 26 TO EXT MEM LOST.	0.30 00 17047.44 00 1310.00 80 1304.10 00 17055.50 00		017046.40 017047.00 017047.40 017050.00 017050.40
11851	LX,\$X0,\$X0 BXCZ,11852 SIC,SEN B,SERS B,11853 -	-LX, EXT MEM, BIT 26 LOST AND 27 SPUR OR -SX, EXT MEM, BIT 26 LOST AND 27 SPUR.	20.00 10 17053.70 42 1310.00 80 1304.10 00 17055.50 00		017051.00 017051.40 017052.00 017052.40 017053.00
11852	BXVZ,11854 SIC,SEN B,SERS B,11854 -	-ALL OK -LX BIT 27 FRM EXT MEM SPUR OR -SX BIT 27 TO EXT MEM SPUR.	17056.31 42 1310.00 80 1304.10 00 17056.10 00		017053.40 017054.00 017054.40 017055.00
11853	LX,\$X2,1000	-LX 2 TO INDICATE 26 LOST.	17307.04 10		017055.40
11854	LXBU,118W7 LX,\$X1,\$R Z,\$X0 Z,\$R SX,\$X1,11855 Z,\$X3 NOP CNOP		17260.00 80 11.02 10 20.22 00 11.22 00 17062.03 10 23.22 00 0.30 00	000000.20 50	017056.00 017057.00 017057.40 017060.00 017060.40 017061.00 017061.40
11855	NOP + NOP B,11857 -		017064.0020 0.30 00 0.30 00 17066.10 00		017062.00 017062.40 017063.00
11856	CNOP BD,\$+.32 SIC,SEN B,SERS B,11859 -	-IF GET HERE, 26 LOST, 27 OK. -LX BIT 26 FRM INT MEM LOST OR -SX BIT 26 TO EXT MEM LOST.	0.30 00 17064.44 00 1310.00 80 1304.10 00 17072.50 00		017063.40 017064.00 017064.40 017065.00 017065.40
11857	LX,\$X0,\$X0 BXCZ,11858 SIC,SEN B,SERS -	-LX, INT MEM, BIT 26 LOST AND 27 SPUR OR -SX, EXT MEM, BIT 26 LOST AND 27 SPUR.	20.00 10 17070.70 42 1310.00 80 1304.10 00		017066.00 017066.40 017067.00 017067.40
11858	B,11859 BXVZ,11860 -	-ALL OK	17072.50 00 17075.31 42		017070.00 017070.40
	SIC,SEN B,SERS B,11860 -	-LX BIT 27 FRM INT MEM SPUR OR -SX BIT 27 TO EXT MEM SPUR.	1310.00 80 1304.10 00 17075.10 00		017071.00 017071.40 017072.00
11859	LX,\$X3,1000 LXBU,\$X2 BRZ,11861 B,11873	-LX 3 TO INDICATE 26 LOST. -CANT DO IX CORE STG OR INT MEM TEST.	17307.06 10 22.00 80 17077.34 C2 17135.10 00	000000.20 50	017072.40 017073.00 017074.00 017074.40

11860	L%BU□,118W8 LX,\$X1,\$R B,11862		17261.00 80 000000.20 50	017075.00
			11.02 10	017076.00
11861	LX,\$X1,118W8		17077.50 00	017076.40
11862	Z,\$X0 Z,\$R SX,\$X1,\$X7 NOP NOP SX,\$X7,11863 NOP NOP CNOP		17261.02 10 20.22 00 11.22 00 27.03 10 0.30 00 0.30 00 17104.17 10 0.30 00 0.30 00 0.30 00	017077.00 017077.40 017100.00 017100.40 017101.00 017101.40 017102.00 017102.40 017103.00 017103.40
11863	NOP NOP B,11865 -		0.30 00 0.30 00 0.30 00 17110.10 00	017104.00 017104.40 017105.00
			0.30 00	017105.40
11864	BD,\$+,32 SIC,SEN B,SERS B,11867 -	-IF GET HERE, 26 LOST, 27 OK. -SX BIT 26 TO IX CORE STG LOST.	17106.44 00 1310.00 80 1304.10 00 17114.10 00	017106.00 017106.40 017107.00 017107.40
11865	LX,\$X0,\$X0 BXCZ,11866 SIC,SEN B,SERS B,11867 -	-SX TO IX CORE STG BIT 26 -LOST AND BIT 27 SPUR.	20.00 10 17112.70 42 1310.00 80 1304.10 00 17114.10 00	017110.00 017110.40 017111.00 017111.40 017112.00
11866	BXVZ,11867 SIC,SEN B,SERS -	-ALL OK -SX TO IX CORE STG, BIT 27 SPUR.	17114.31 42 1310.00 80 1304.10 00	017112.40 017113.00 017113.40
11867	L%BU□,\$X3 BRZ,11868 B,11873 -	-TERMINATE	23.00 80 000000.20 50 17116.34 C2 17135.10 00	017114.00 017115.00 017115.40
11868	Z,\$X0 LX,\$X1,118W9 Z,\$R SX,\$X1,\$R NOP NOP Z,\$X1 NOP LX,\$X1,\$R NOP SX,\$X1,11869 NOP NOP CNOP		20.22 00 17262.02 10 11.22 00 11.03 10 0.30 00 0.30 00 21.22 00 0.30 00 11.02 10 0.30 00 17125.03 10 0.30 00 0.30 00 0.30 00	017116.00 017116.40 017117.00 017117.40 017120.00 017120.40 017121.00 017121.40 017122.00 017122.40 017123.00 017123.40 017124.00 017124.40
11869	NOP NOP B,11871 -		0.30 00 0.30 00 17131.10 00	017125.00 017125.40 017126.00
			0.30 00	017126.40
11870	BD,\$+,32 SIC,SEN B,SERS B,11873 -	-IF GET HERE, 26 LOST, 27 OK. -SX TO INT MEM, BIT 26 LOST.	17127.44 00 1310.00 80 1304.10 00 17135.10 00	017127.00 017127.40 017130.00 017130.40
11871	LX,\$X0,\$X0 BXCZ,11872 SIC,SEN	-SX TO INT MEM, BIT 26	20.00 10 17133.70 42 1310.00 80	017131.00 017131.40 017132.00

B,SERS
B,I1873
-
I1872 BXVZ,I1873
SIC,SEN
B,SERS
-LOST AND BIT 27 SPUR.
-ALL OK
-SX TO INT MEM, BIT 27 SPUR.

1304.10 00
17135.10 00
17135.31 42
1310.00 80
1304.10 00

017132.40
017133.00
017133.40
017134.00
017134.40

11873	Z,\$X0	-START TEST OF 26 SPUR	20.22 00	017135.00
	LX,\$X1,118W10		17263.02 10	017135.40
	NOP		0.30 00	017136.00
	SX,\$X1,11874		17140.03 10	017136.40
	NOP		0.30 00	017137.00
	CNOP		0.30 00	017137.40
11874	NOP		0.30 00	017140.00
	NOP		0.30 00	017140.40
	B,11876		17144.10 00	017141.00
	-			
	CNOP		0.30 00	017141.40
11875	BD,\$+.32	-IF GET HERE, 27 LOST, 26 OK.	17142.44 00	017142.00
	SIC,SEN	-LX BIT 27 FRM EXT MEM OR	1310.00 80	017142.40
	B,SERS	-SX BIT 27 TO EXT MEM LOST.	1304.10 00	017143.00
	B,11879		17150.50 00	017143.40
	-			
11876	LX,\$X0,\$X0		20.00 10	017144.00
	BXVZ,11877	-26 SPUR, 27 LOST.	17146.31 42	017144.40
	BXCZ,11878	-26 SPUR.	17147.70 42	017145.00
	B,11879	-ALL OK.	17150.50 00	017145.40
	-			
11877	SIC,SEN	-LX EXT MEM, BIT 26 SPUR, 27 LOST OR	1310.00 80	017146.00
	B,SERS	-SX EXT MEM, BIT 26 SPUR, 27 LOST.	1304.10 00	017146.40
	B,11879		17150.50 00	017147.00
	-			
11878	SIC,SEN	-LX EXT MEM BIT 26 SPUR OR	1310.00 80	017147.40
	B,SERS	-SX EXT MEM BIT 26 SPUR.	1304.10 00	017150.00
	-			
11879	L%BU,118W11		17264.00 80	017150.40
	LX,\$X1,\$R		11.02 10	017151.40
	Z,\$X0		20.22 00	017152.00
	SX,\$X1,11880		17154.03 10	017152.40
	NOP		0.30 00	017153.00
	NOP		0.30 00	017153.40
	CNOP			
11880	NOP		0.30 00	017154.00
	NOP		0.30 00	017154.40
	B,11882		17160.10 00	017155.00
	-			
	CNOP		0.30 00	017155.40
11881	BD,\$+.32	-IF GET HERE, 27 LOST, 26 OK.	17156.44 00	017156.00
	SIC,SEN	-LX BIT 27 FRM INT MEM OR	1310.00 80	017156.40
	B,SERS	-SX BIT 27 TO EXT MEM LOST.	1304.10 00	017157.00
	B,11885		17164.50 00	017157.40
	-			
11882	LX,\$X0,\$X0		20.00 10	017160.00
	BXVZ,11883	-26 SPUR, 27 LOST.	17162.31 42	017160.40
	BXCZ,11884	-26 SPUR.	17163.70 42	017161.00
	B,11885	-ALL OK.	17164.50 00	017161.40
	-			
11883	SIC,SEN	-LX INT MEM BIT 26 SPUR, 27 LOST OR	1310.00 80	017162.00
	B,SERS	-SX EXT MEM BIT 26 SPUR, 27 LOST.	1304.10 00	017162.40
	B,11885		17164.50 00	017163.00
	-			
11884	SIC,SEN	-LX INT MEM BIT 26 SPUR OR	1310.00 80	017163.40
	B,SERS	-SX EXT MEM BIT 26 SPUR.	1304.10 00	017164.00
	-			
11885	Z,\$X0		20.22 00	017164.40
	Z,\$X1		21.22 00	017165.00
	Z,\$X7		27.22 00	017165.40
	LX,\$X7,118W12		17265.16 10	017166.00
	NOP		0.30 00	017166.40
	LX,\$X1,\$X7		27.02 10	017167.00

NOP
SX,5X1,11886
NOP
NOP
CNOP
11886 NOP
NOP
B,11888

0.30 00
17172.03 10
0.30 00
0.30 00
0.30 00
0.30 00
0.30 00
17176.10 00

017167.40
017170.00
017170.40
017171.00
017171.40
017172.00
017172.40
017173.00

11887	CNOP BD,\$+.32 SIC,SEN B,SERS B,11891	-IF GET HERE, 27 LOST, 26 OK. -LX BIT 27 FRM IX CORE STG OR -SX BIT 27 TO EXT MEM LOST.	0.30 00 17174.44 00 1310.00 80 1304.10 00 17202.50 00	017173.40 017174.00 017174.40 017175.00 017175.40
11888	LX,\$X0,\$X0 BXVZ,11889 BXCZ,11890 B,11891	-26 SPUR, 27 LOST. -26 SPUR. -ALL OK.	20.00 10 17200.31 42 17201.70 42 17202.50 00	017176.00 017176.40 017177.00 017177.40
11889	SIC,SEN B,SERS B,11891	-LX IX CORE STG, BIT 26 SPUR AND 27 LOST 0 -SX EXT MEM, BIT 26 SPUR AND 27 LOST.	1310.00 80 1304.10 00 17202.50 00	017200.00 017200.40 017201.00
11890	SIC,SEN B,SERS	-LX IX CORE STG BIT 26 SPUR OR -SX EXT MEM, BIT 26 SPUR.	1310.00 80 1304.10 00	017201.40 017202.00
11891	Z,\$X0 LX,\$X1,118W13 Z,\$R NOP SX,\$X1,\$R Z,\$X1 NOP LX,\$X1,\$R NOP SX,\$X1,11892 NOP NOP CNOP NOP NOP B,11894		20.22 00 17266.02 10 11.22 00 0.30 00 11.03 10 21.22 00 0.30 00 11.02 10 0.30 00 17211.03 10 0.30 00 0.30 00 0.30 00 0.30 00 0.30 00 17215.10 00	017202.40 017203.00 017203.40 017204.00 017204.40 017205.00 017205.40 017206.00 017206.40 017207.00 017207.40 017210.00 017210.40 017211.00 017211.40 017212.00
11892	CNOP NOP NOP B,11894		0.30 00 0.30 00 0.30 00 17215.10 00	017212.40 017213.00 017213.40 017214.00 017214.40
11893	CNOP BD,\$+.32 SIC,SEN B,SERS B,11897	-IF GET HERE, 27 LOST, 26 OK. -SX TO INT MEM, BIT 27 LOST.	0.30 00 17213.44 00 1310.00 80 1304.10 00 17221.50 00	017212.40 017213.00 017213.40 017214.00 017214.40
11894	LX,\$X0,\$X0 BXVZ,11895 BXCZ,11896 B,11897	-26 SPUR, 27 LOST. -26 SPUR. -ALL OK.	20.00 10 17217.31 42 17220.70 42 17221.50 00	017215.00 017215.40 017216.00 017216.40
11895	SIC,SEN B,SERS B,11897	-SX TO INT MEM, 26 SPUR, 27 LOST.	1310.00 80 1304.10 00 17221.50 00	017217.00 017217.40 017220.00
11896	SIC,SEN B,SERS	-SX TO INT MEM, BIT 26 LOST.	1310.00 80 1304.10 00	017220.40 017221.00
11897	Z,\$X0 Z,\$X1 Z,\$X7 LX,\$X7,118W14 NOP NOP SX,\$X7,\$X1 NOP NOP SX,\$X1,11898		20.22 00 21.22 00 27.22 00 17267.16 10 0.30 00 0.30 00 21.17 10 0.30 00 0.30 00 17230.03 10	017221.40 017222.00 017222.40 017223.00 017223.40 017224.00 017224.40 017225.00 017225.40 017226.00

11898

NOP
NOP
CNOP
NOP
NOP
B,118100

0.30 00
0.30 00
0.30 00
0.30 00
0.30 00
17234.10 00

017226.40
017227.00
017227.40
017230.00
017230.40
017231.00

11899	CNOP BD,\$+.32 SIC,SEN B,SERS B,118103	-IF GET HERE, 27 LOST, 26 OK. -SX TO IX CORE STG, 27 LOST, 26 OK.	0.30 00 17232.44 00 1310.00 80 1304.10 00 17240.50 00	017231.40 017232.00 017232.40 017233.00 017233.40
118100	LX,\$X0,\$X0 BXVZ,118101 BXCZ,118102 B,118103	-26 SPUR, 27 LOST. -26 SPUR. -ALL OK.	20.00 10 17236.31 42 17237.70 42 17240.50 00	017234.00 017234.40 017235.00 017235.40
118101	SIC,SEN B,SERS B,118103	-SX TO IX CORE STG, 26 SPUR, 27 LOST.	1310.00 80 1304.10 00 17240.50 00	017236.00 017236.40 017237.00
118102	SIC,SEN B,SERS	-SX TO IX CORE SIG, BIT 26 SPUR.	1310.00 80 1304.10 00	017237.40 017240.00
118103	B,\$+1.0 BD,11848A SIC,SEN0+.32 B,SSW BD,\$+.32	-TO SSIP.	17241.50 00 17042.04 00 1311.40 80 1301.10 00 17243.04 00	017240.40 017241.00 017241.40 017242.00 017242.40
	LX,\$X13,IC218 V+,\$X13,BIT3 SX,\$X13,IC218	-UPDATE CONTINUITY CHECK.	17247.32 10 17331.32 80 17247.33 10	017243.00 017243.40 017244.00
	LX,\$X13,IC218 KV,\$X13,ICK218 SIC,SEN BZXE,SERS B,120	-UPDATE CONTINUITY CHECK. -CONTINUITY ERROR.	17247.32 10 17250.32 90 1310.00 80 1304.32 C0 17271.10 00	017244.40 017245.00 017245.40 017246.00 017246.40
IC218 ICK218	CNOP XW,0,0,0 XW,%8□740000.00,0,0	-CONTINUITY REG 1218.	0.00 00 000000.00 00 740000.00 00 000000.00 00	017247.00 017250.00

Label	Code	Address	Description	Value	Unit	Scale	Offset	Final Value
	CNOP		-CONSTANTS FOR 1218.					
I18DMP	XW,0,0,0		-DUMP LOCN.	0.00	00	000000.00	00	017251.00
I18W1	LX,\$X0,I1828		-TEST WD.	16757.00	10			017252.00
	NOP			0.30	00			017252.40
I18W2	LX,\$X0,I1831		-TEST WD.	16767.00	10			017253.00
	NOP			0.30	00			017253.40
I18W3	LX,\$X0,I1837		-TEST WD.	17004.00	10			017254.00
	NOP			0.30	00			017254.40
I18W4	LX,\$X0,I1843		-TEST WD.	17021.00	10			017255.00
	NOP			0.30	00			017255.40
I18W5	LX,\$X0,I1847		-TEST WD.	17034.00	10			017256.00
	NOP			0.30	00			017256.40
I18W6	+%N□,I1850		-TEST WD.	17047.00	20			017257.00
	NOP			0.30	00			017257.40
I18W7	+%N□,I1856		-TEST WD.	17064.00	20			017260.00
	NOP			0.30	00			017260.40
I18W8	+%N□,I1864		-TEST WD.	17106.00	20			017261.00
	NOP			0.30	00			017261.40
I18W9	+%N□,I1870		-TEST WD.	17127.00	20			017262.00
	NOP			0.30	00			017262.40
I18W10	LX,\$X0,I1875		-TEST WD.	17142.00	10			017263.00
	NOP			0.30	00			017263.40
I18W11	LX,\$X0,I1881		-TEST WD.	17156.00	10			017264.00
	NOP			0.30	00			017264.40
I18W12	LX,\$X0,I1887		-TEST WD.	17174.00	10			017265.00
	NOP			0.30	00			017265.40
I18W13	LX,\$X0,I1893		-TEST WD.	17213.00	10			017266.00
	NOP			0.30	00			017266.40
I18W14	LX,\$X0,I1899		-TEST WD.	17232.00	10			017267.00
	NOP			0.30	00			017267.40
I18K1	XW,-0.01,0,0			0.01	80	000000.00	00	017270.00

BIT17	XW,%8□1.00,0,0	1.00 00 000000.00 00	017347.00
BIT18	XW,%8□.40,0,0	0.40 00 000000.00 00	017350.00
BIT19	XW,%8□.20,0,0	0.20 00 000000.00 00	017351.00
BIT20	XW,%8□.10,0,0	0.10 00 000000.00 00	017352.00
BIT21	XW,%8□0.04,0,0	0.04 00 000000.00 00	017353.00
BIT22	XW,%8□0.02,0,0	0.02 00 000000.00 00	017354.00
BIT23	XW,%8□0.01,0,0	0.01 00 000000.00 00	017355.00
BIT24	%8□DD%BU□,0 000 000 010 000 000 000 000	0000000010000000000000	017356.00
BIT25	XW,0,0,0,4	0.00 40 000000.00 00	017357.00
BIT26	XW,0,0,0,2	0.00 20 000000.00 00	017360.00
BIT27	XW,0,0,0,1	0.00 10 000000.00 00	017361.00
BIT28	XW,0,131072,0	0.00 08 000000.00 00	017362.00
BIT29	XW,0,65536,0	0.00 04 000000.00 00	017363.00
BIT30	XW,0,32768,0	0.00 02 000000.00 00	017364.00
BIT31	XW,0,16384,0	0.00 01 000000.00 00	017365.00
BIT32	XW,0,8192,0	0.00 00 400000.00 00	017366.00
BIT33	XW,0,4096,0	0.00 00 200000.00 00	017367.00
BIT34	XW,0,2048,0	0.00 00 100000.00 00	017370.00
BIT35	XW,0,1024,0	0.00 00 040000.00 00	017371.00

BIT36	XW,0,512,0	0.00	00	020000.00	00	017372.00
BIT37	XW,0,256,0	0.00	00	010000.00	00	017373.00
BIT38	XW,0,128,0	0.00	00	004000.00	00	017374.00
BIT39	XW,0,64,0	0.00	00	002000.00	00	017375.00
BIT40	XW,0,32,0	0.00	00	001000.00	00	017376.00
BIT41	XW,0,16,0	0.00	00	000400.00	00	017377.00
BIT42	XW,0,8,0	0.00	00	000200.00	00	017400.00
BIT43	XW,0,4,0	0.00	00	000100.00	00	017401.00
BIT44	XW,0,2,0	0.00	00	000040.00	00	017402.00
BIT45	XW,0,1,0	0.00	00	000020.00	00	017403.00
BIT46	XW,0,0,131072	0.00	00	000010.00	00	017404.00
BIT47	XW,0,0,65536	0.00	00	000004.00	00	017405.00
BIT48	XW,0,0,32768	0.00	00	000002.00	00	017406.00
BIT49	XW,0,0,16384	0.00	00	000001.00	00	017407.00
BIT50	XW,0,0,8192	0.00	00	000000.40	00	017410.00
BIT51	XW,0,0,4096	0.00	00	000000.20	00	017411.00
BIT52	XW,0,0,2048	0.00	00	000000.10	00	017412.00
BIT53	XW,0,0,1024	0.00	00	000000.04	00	017413.00
BIT54	XW,0,0,512	0.00	00	000000.02	00	017414.00
BIT55	XW,0,0,256	0.00	00	000000.01	00	017415.00
BIT56	XW,0,0,128	0.00	00	000000.00	80	017416.00
BIT57	XW,0,0,64	0.00	00	000000.00	40	017417.00
BIT58	XW,0,0,32	0.00	00	000000.00	20	017420.00
BIT59	XW,0,0,16	0.00	00	000000.00	10	017421.00
BIT60	XW,0,0,8	0.00	00	000000.00	08	017422.00
BIT61	XW,0,0,4	0.00	00	000000.00	04	017423.00
BIT62	XW,0,0,2	0.00	00	000000.00	02	017424.00
BIT63	XW,0,0,1	0.00	00	000000.00	01	017425.00

SSW	SYN,%8□1301.0	1301.00+	+00000000
ERS	SYN,%8□1302.0	1302.00+	+00000000
SERS	SYN,%8□1304.0	1304.00+	+00000000
RET	SYN,%8□1306.40	1306.40+	+00000000
RET1	SYN,%8□1307.0	1307.00+	+00000000
RET2	SYN,%8□1307.40	1307.40+	+00000000
SEN	SYN,%8□1310.0	1310.00+	+00000000
SENO	SYN,%8□1311.0	1311.00+	+00000000
DPET13	SYN,%8□1437.0	1437.00+	+00000000
INT	SYN,%8□1353.0	1353.00+	+00000000
IDF1	SYN,%8□1443.0	1443.00+	+00000000
IDF2	SYN,%8□1444.40	1444.40+	+00000000
	END,%8□6500.0	6500.00	

017426.00