

7030 DPS

SMFI-3A

File No. JB AX3A

SMFI - 3
I-BOX 3 PROGRAM

JB AX 3

April 5, 1961

1. Programs becoming obsolete: None
2. Used to provide a test of the Central Processor I-Box.

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1. PURPOSE

The I-Box 3 Program provides the maintenance engineer with a test for the proper loading and storing of the index registers.

2. PROGRAM INTRODUCTION

- 2.1. This program has been designed for use after the I-Box 1 and I-Box 2 Programs have been run successfully.
- 2.2. The program operates under the control of the Sense Switch Interrogation Program (SSIP).

3. OPERATING PROCEDURES

- 3.1. The Sense Switch Interrogation Program must be in the machine.
- 3.2. Loading Procedures (PUNFUL Cards)
 - 3.2.1. At the Maintenance Console:
 - 1) Depress Master Reset
 - 2) Depress Start Clock
 - 3) Depress IPL
 - 4) Disable Interrupt and Time Clock
 - 5) Enable Maintenance Mode
 - 3.2.2. Place binary deck in card reader.
 - 3.2.3. Depress Start on card reader, the program will start itself.
- 3.3. Error Indications

The program operates under Sense Switch Interrogation Program (SSIP) control, all error indication options of the SSIP Program apply to this program. Refer to the SSIP Program write-up.

3.4. Success Indications

All success indication options of the SSIP Program apply to this program. Refer to the SSIP Program write-up.

3.5. Operation Options

Refer to the SSIP Program write-up for all operation options.

4. PROGRAM PHILOSOPHY

This program is designed to test I-Box instructions and associated hardware. The entire program is under SSIP control. Below are listed all of the routines that are a part of this program and a brief description of what each tests:

- XCS Tests Index Core Storage for reading and writing of ones, zeros, and one-zero patterns.
- 1222 Checks SV and SVA to all three memories and checks SVA to all classes of instructions (Internal, External, and Index Memories)
- 1224 Checks SC to all three memories.
- 1226 Checks LV from all three memories, LVI, and LVNI.
- 1228 Checks LC from all three memories and LCI.
- 1230 Checks LR from all three memories and LRI.

7030 DPS

PROGRAM WRITEUP ADDENDUM

Program I-Box 3A
File No. JB AX3A

MAINTENANCE TAPE CONTROL CARD

Location/s of Exit Branch/es

1.	<u>3</u>	<u>3</u>	<u>3</u>	<u>0</u>	<u>3</u>	<u>0</u>
2.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>
3.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>
6.	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>	<u> </u>

Pre-Loading Manual Intervention Required ? Yes No x

Pre-Loading Procedure (If Any)

PRNID, SUENRAM SMFI-3A

JA AX 3A

Handwritten notes in the top right corner, including the word "DRAFT" and some illegible scribbles.

Vertical handwritten marks on the left margin, including a bracket and several tick marks.

-FEBRUARY 6, 1961

SLC,%8=17777.0

017777.00

PUNFUL
PRNS
SEM,6,C,G

-START SMFI-3A MAKE DUMMY PASS TO
-SSIP FOR HOUSEKEEPING PURPOSES.

	XW,%8=20000.0,BIT63+1.00-START,0,2		20000.00	20	270620.00	00	017777.00
START	B,\$+1.0		20001.10	00			020000.00
	BD,\$+1.32		20002.04	00			020000.40
	SIC,SENO+.32		1311.40	80			020001.00
	B,SSW		1301.10	00			020001.40
XCS0	BD,\$+1.0		20003.04	00			020002.00
	NOP,0.0		0.30	00			020002.40
	LX,\$X1,PRED1	-ALL ZERO WORD	20167.02	10			020003.00
	SX,\$X1,0.	-SET ALL	0.03	10			020003.40
	SV,\$X1,1.0		1.03	30			020004.00
	SX,\$X1,2.0	-SPECIAL	2.03	10			020004.40
	NOP,		0.30	00			020005.00
	SX,\$X1,5.0		5.03	10			020005.40
	SX,\$X1,6.0		6.03	10			020006.00
	SX,\$X1,7.0		7.03	10			020006.40
	SX,\$X1,8.0		10.03	10			020007.00
	SX,\$X1,9.0		11.03	10			020007.40
	SX,\$X1,10.0		12.03	10			020010.00
	SX,\$X1,11.0		13.03	10			020010.40
	SX,\$X1,12.0		14.03	10			020011.00
	SX,\$X1,13.0		15.03	10			020011.40
	SX,\$X1,14.0		16.03	10			020012.00
	SX,\$X1,15.0		17.03	10			020012.40
	SX,\$X1,16.0		20.03	10			020013.00
	SX,\$X1,17.0		21.03	10			020013.40
	SX,\$X1,18.0		22.03	10			020014.00
	SX,\$X1,19.0		23.03	10			020014.40
	SX,\$X1,20.0		24.03	10			020015.00
	SX,\$X1,21.0		25.03	10			020015.40
	SX,\$X1,22.0		26.03	10			020016.00
	SX,\$X1,23.0		27.03	10			020016.40
	SX,\$X1,24.0		30.03	10			020017.00
	SX,\$X1,25.0		31.03	10			020017.40
	SX,\$X1,26.0		32.03	10			020020.00
	SX,\$X1,27.0		33.03	10			020020.40
	SX,\$X1,28.0		34.03	10			020021.00
	SX,\$X1,29.0		35.03	10			020021.40
	SX,\$X1,30.0		36.03	10			020022.00
	SX,\$X1,31.0		37.03	10			020022.40
	L%BU,64,8=,PRED1+.32	-CLEAR ECC IN C + D REGS	20167.40	80	000000.20	50	020023.00
	LX,\$X1,PRED3	-ALL ONES INTO MASK	20170.02	10			020024.00
	SX,\$X1,12.0	-REGISTER	14.03	10			020024.40
	LV,\$X1,PRED4	-SET INTERRUPT	20171.02	30			020025.00
	SV,\$X1,2.0	-BASE ADDRESS	2.03	30			020025.40
	BD,XCS1	-TO ACTUAL PROGRAM	20171.44	00			020026.00
INRT	SIC,MK		20106.40	80			020026.40
	BD,MK1	-THE INTERRUPT TABLE	20136.44	00			020027.00
	SIC,IK		20107.00	80			020027.40
	BD,IK1		20137.04	00			020030.00
	SIC,IJ		20107.40	80			020030.40
	BD,IJ1		20137.44	00			020031.00
	SIC,EK		20110.00	80			020031.40

BD,EK1	20140.04	00	020032.00
SIC,TS	20110.40	80	020032.40
BD,TS1	20140.44	00	020033.00
SIC,CPUS	20111.00	80	020033.40
BD,CPUS1	20141.04	00	020034.00
SIC,EKJ	20111.40	80	020034.40
BD,EKJ1	20141.44	00	020035.00
SIC,UNRJ	20112.00	80	020035.40
BD,UNRJ1	20142.04	00	020036.00
SIC,CBJ	20112.40	80	020036.40
BD,CBJ1	20142.44	00	020037.00
SIC,EPGK	20113.00	80	020037.40
BD,EPGK1	20143.04	00	020040.00
SIC,UK	20113.40	80	020040.40
BD,UK1	20143.44	00	020041.00
SIC,EE	20114.00	80	020041.40
BD,EE1	20144.04	00	020042.00
SIC,EOP	20114.40	80	020042.40
BD,EOP1	20144.44	00	020043.00
SIC,CS	20115.00	80	020043.40
BD,CS1	20145.04	00	020044.00
SIC,RSV	20115.40	80	020044.40
BD,RSV1	20145.44	00	020045.00
SIC,OP	20116.00	80	020045.40
BD,OP1	20146.04	00	020046.00
SIC,AD	20116.40	80	020046.40
BD,AD1	20146.44	00	020047.00
SIC,USA	20117.00	80	020047.40
BD,USA1	20147.04	00	020050.00
SIC,EXE	20117.40	80	020050.40
BD,EXE1	20147.44	00	020051.00
SIC,DS	20120.00	80	020051.40
BD,DS1	20150.04	00	020052.00
SIC,DF	20120.40	80	020052.40
BD,DF1	20150.44	00	020053.00
SIC,IF	20121.00	80	020053.40
BD,IF1	20151.04	00	020054.00
SIC,LC	20121.40	80	020054.40
BD,LC1	20151.44	00	020055.00
SIC,PF	20122.00	80	020055.40
BD,PF1	20152.04	00	020056.00
SIC,ZD	20122.40	80	020056.40
BD,ZD1	20152.44	00	020057.00
SIC,IR	20123.00	80	020057.40
BD,IR1	20153.04	00	020060.00
SIC,LS	20123.40	80	020060.40
BD,LS1	20153.44	00	020061.00
SIC,PSH	20124.00	80	020061.40
BD,PSH1	20154.04	00	020062.00
SIC,XFPF	20124.40	80	020062.40
BD,XFPF1	20154.44	00	020063.00
SIC,XPO	20125.00	80	020063.40
BD,XPO1	20155.04	00	020064.00
SIC,XPH	20125.40	80	020064.40
BD,XPH1	20155.44	00	020065.00
SIC,XPL	20126.00	80	020065.40
BD,XPL1	20156.04	00	020066.00
SIC,XPU	20126.40	80	020066.40
BD,XPU	20126.44	00	020067.00
SIC,XPFN	20127.00	80	020067.40
BD,XPFN1	20157.04	00	020070.00
SIC,RU	20127.40	80	020070.40
BD,RU1	20157.44	00	020071.00
SIC,TF	20130.00	80	020071.40
BD,TF1	20160.04	00	020072.00
SIC,UF	20130.40	80	020072.40

	SIC,VF	BD,UF1		20160.44	00	020073.00
		BD,VF1		20131.00	80	020073.40
	SIC,XF	BD,VF1		20161.04	00	020074.00
		BD,XF1		20131.40	80	020074.40
	SIC,BTR	BD,BTR1		20161.44	00	020075.00
		BD,BTR1		20132.00	80	020075.40
	SIC,DTR	BD,DTR1		20162.04	00	020076.00
		BD,DTR1		20132.40	80	020076.40
	SIC,PG0	BD,PG01		20162.44	00	020077.00
		BD,PG01		20133.00	80	020077.40
	SIC,PG1	BD,PG11		20163.04	00	020100.00
		BD,PG11		20133.40	80	020100.40
	SIC,PG2	BD,PG21		20163.44	00	020101.00
		BD,PG21		20134.00	80	020101.40
	SIC,PG3	BD,PG31		20164.04	00	020102.00
		BD,PG31		20134.40	80	020102.40
	SIC,PG4	BD,PG41		20164.44	00	020103.00
		BD,PG41		20135.00	80	020103.40
	SIC,PG5	BD,PG51		20165.04	00	020104.00
		BD,PG51		20135.40	80	020104.40
	SIC,PG6	BD,PG61		20165.44	00	020105.00
		BD,PG61		20136.00	80	020105.40
				20166.04	00	020106.00
MK	BD,0		-PLACE RESERVED	0.04	00	020106.40
IK	BD,0		-FOR INSTRUCTION	0.04	00	020107.00
IJ	BD,0		-COUNTER TO BE	0.04	00	020107.40
EK	BD,0		-STORED UPON	0.04	00	020110.00
TS	BD,0		-INTERRUPTS	0.04	00	020110.40
CPUS	BD,0			0.04	00	020111.00
EKJ	BD,0			0.04	00	020111.40
UNRJ	BD,0			0.04	00	020112.00
CBJ	BD,0			0.04	00	020112.40
EPGK	BD,0			0.04	00	020113.00
UK	BD,0			0.04	00	020113.40
EE	BD,0			0.04	00	020114.00
EOP	BD,0			0.04	00	020114.40
CS	BD,0			0.04	00	020115.00
RSV	BD,0			0.04	00	020115.40
OP	BD,0			0.04	00	020116.00
AD	BD,0			0.04	00	020116.40
USA	BD,0			0.04	00	020117.00
EXE	BD,0			0.04	00	020117.40
DS	BD,0			0.04	00	020120.00
DF	BD,0			0.04	00	020120.40
IF	BD,0			0.04	00	020121.00
LC	BD,0			0.04	00	020121.40
PF	BD,0			0.04	00	020122.00
ZD	BD,0			0.04	00	020122.40
IR	BD,0			0.04	00	020123.00
LS	BD,0			0.04	00	020123.40
PSH	BD,0			0.04	00	020124.00
XFPF	BD,0			0.04	00	020124.40
XPO	BD,0			0.04	00	020125.00
XPH	BD,0			0.04	00	020125.40
XPL	BD,0			0.04	00	020126.00
XPU	BD,0			0.04	00	020126.40
XPFN	BD,0			0.04	00	020127.00
RU	BD,0			0.04	00	020127.40
TF	BD,0			0.04	00	020130.00
UF	BD,0			0.04	00	020130.40
VF	BD,0			0.04	00	020131.00
XF	BD,0			0.04	00	020131.40
BTR	BD,0			0.04	00	020132.00
DTR	BD,0			0.04	00	020132.40
PGO	BD,0			0.04	00	020133.00
PG1	BD,0			0.04	00	020133.40

PG2	BD,0		0.04 00	020134.00
PG3	BD,0		0.04 00	020134.40
PG4	BD,0		0.04 00	020135.00
PG5	BD,0		0.04 00	020135.40
PG6	BD,0		0.04 00	020136.00
MK1	BD,\$	-AREA OF UNIQUE	20136.44 00	020136.40
IK1	BD,\$	-INTERRUPT	20137.04 00	020137.00
IJ1	BD,\$	-PROGRAM	20137.44 00	020137.40
EK1	BD,\$	-HANG UPS	20140.04 00	020140.00
TS1	BD,\$		20140.44 00	020140.40
CPUS1	BD,\$		20141.04 00	020141.00
EKJ1	BD,\$		20141.44 00	020141.40
UNRJ1	BD,\$		20142.04 00	020142.00
CBJ1	BD,\$		20142.44 00	020142.40
EPGK1	BD,\$		20143.04 00	020143.00
UK1	BD,\$		20143.44 00	020143.40
EE1	BD,\$		20144.04 00	020144.00
EOP1	BD,\$		20144.44 00	020144.40
CS1	BD,\$		20145.04 00	020145.00
RSV1	BD,\$		20145.44 00	020145.40
OP1	BD,\$		20146.04 00	020146.00
AD1	BD,\$		20146.44 00	020146.40
USA1	BD,\$		20147.04 00	020147.00
EXE1	BD,\$		20147.44 00	020147.40
DS1	BD,\$		20150.04 00	020150.00
DF1	BD,\$		20150.44 00	020150.40
IF1	BD,\$		20151.04 00	020151.00
LC1	BD,\$		20151.44 00	020151.40
PF1	BD,\$		20152.04 00	020152.00
ZD1	BD,\$		20152.44 00	020152.40
IR1	BD,\$		20153.04 00	020153.00
LS1	BD,\$		20153.44 00	020153.40
PSH1	BD,\$		20154.04 00	020154.00
XPFP1	BD,\$		20154.44 00	020154.40
XPO1	BD,\$		20155.04 00	020155.00
XPH1	BD,\$		20155.44 00	020155.40
XPL1	BD,\$		20156.04 00	020156.00
XPU1	BD,\$		20156.44 00	020156.40
XPFN1	BD,\$		20157.04 00	020157.00
RU1	BD,\$		20157.44 00	020157.40
TF1	BD,\$		20160.04 00	020160.00
UF1	BD,\$		20160.44 00	020160.40
VF1	BD,\$		20161.04 00	020161.00
XF1	BD,\$		20161.44 00	020161.40
BTR1	BD,\$		20162.04 00	020162.00
DTR1	BD,0		0.04 00	020162.40
PG01	BD,\$		20163.04 00	020163.00
PG11	BD,\$		20163.44 00	020163.40
PG21	BD,\$		20164.04 00	020164.00
PG31	BD,\$		20164.44 00	020164.40
PG41	BD,\$		20165.04 00	020165.00
PG51	BD,\$		20165.44 00	020165.40
PG61	BD,\$		20166.04 00	020166.00
	CNOP,0		0.30 00	020166.40
PRED1	%8DD%BU,64,8D, 0 000 000 000 000 000 000 000 000	-ALL ZERO WORD	0000000000000000000000	020167.00
PRED3	%8DD%BU,64,8D, 1 777 777 777 777 777 777 777 777	-ALL ONES WORD	1777777777777777777777	020170.00
PRED4	VF,INRT		20026.40+	020171.00

-INDEX STORAGE TESTS

XCS1	BD,\$+0.32		20172.04 00	020171.40
	NOP,0		0.30 00	020172.00
	LX,\$X0,XCSZ1		21753.00 10	020172.40
		LX,\$X1,XCSZ1-INITIALIZE	21753.02 10	020173.00
	LX,\$X2,XCSZ1		21753.04 10	020173.40
		LX,\$X3,XCSZ1-BY SETTING	21753.06 10	020174.00
	LX,\$X4,XCSZ1		21753.10 10	020174.40
		LX,\$X5,XCSZ1-ALL ONES	21753.12 10	020175.00

	LX,\$X6,XCSZ1	LX,\$X7,XCSZ1-INTO EVERY	21753.14 10	020175.40
	LX,\$X8,XCSZ1	LX,\$X9,XCSZ1 -BIT POSITION	21753.16 10	020176.00
	LX,\$X10,XCSZ1	LX,\$X11,XCSZ1-IN THE	21753.20 10	020176.40
	LX,\$X12,XCSZ1	LX,\$X13,XCSZ1-INDEX	21753.22 10	020177.00
	LX,\$X14,XCSZ1	LX,\$X15,XCSZ1-REGISTERS	21753.24 10	020177.40
XCS1A	NOP,0-START ONE BIT TEST ON X0		21753.26 10	020200.00
	KV,\$X0,XCSZ3-WITH 25 ONES, BITS 0 TO 24		21753.30 10	020200.40
	SIC,SEN		21753.32 10	020201.00
	BXL,SERS-ERR, PATTERN MUST BE EQUAL		21753.34 10	020201.40
	SIC,SEN		21753.36 10	020202.00
	BXH,SERS-TO CONTINUE, ERR IF A BIT		0.30 00	020202.40
	SIC,SEN		21755.00 90	020203.00
	BZXEZ,SERS-PICKED UP OR LOST 0 TO 24		1310.00 80	020203.40
	KC,\$X0,XCSZ3-WITH 18 ONES, BITS 28 TO 45		1304.32 42	020204.00
	SIC,SEN		1310.00 80	020204.40
	BXL,SERS-ERR, PATTERN MUST BE EQUAL		1304.33 42	020205.00
	SIC,SEN		1310.00 80	020205.40
	BXH,SERS-TO CONTINUE, ERR IF A BIT		1304.32 C4	020206.00
	SIC,SEN		21755.01 90	020206.40
	BZXEZ,SERS-PICKED UP OR LOST 28 TO 45		1310.00 80	020207.00
	SR,\$X0,XCSZ5-REFILL INTO WORK AREA		1304.32 42	020207.40
	SIC,SEN		1310.00 80	020210.00
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25		1304.33 42	020210.40
	LC,\$X0,XCSZ5-REFILL IN COUNT FIELD		1310.00 80	020211.00
	KC,\$X0,XCSZ3-WITH 18 ONES, ACTUALLY BITS 46 TO 63		1304.32 C4	020211.40
	SIC,SEN		21756.01 70	020212.00
	BXL,SERS-ERR, MUST BE EQUAL		1310.00 80	020212.40
	SIC,SEN		1304.23 40	020213.00
	BXH,SERS-TO CONTINUE, ERR IF A BIT		21756.00 50	020213.40
	SIC,SEN		21755.01 90	020214.00
	BZXEZ,SERS-PICKED UP OR LOST		1310.00 80	020214.40
	LX,\$X0,XCSZ7-INSTR WD WITH BITS 26+27 ONES		1304.32 42	020215.00
XCS1A1	V+,\$X0,XCSZ8-PUT ADDR IN VAL FIELD		1310.00 80	020215.40
	SX,\$X0,XCS1A2-PUT INSTR WD IN PROG		1304.33 42	020216.00
	NOP,0		1310.00 80	020216.40
	NOP,0-SPACE IN PROGRAM		1304.32 C4	020217.00
	NOP,0		21757.00 10	020217.40
	CNOP,0		21760.00 80	020220.00
XCS1A2	SIC,SEN-SHOULD BECOME LV, 1 THEN LV, XCSZ8		20223.01 10	020220.40
	B,SERS-SHD BECOME NOP, -1		0.30 00	020221.00
	KV,\$X0,XCSZ8-PROVE V OF X0 WAS LOADED CORRECTLY		0.30 00	020221.40
	SIC,SEN		0.30 00	020222.00
	BZXE,SERS-ERR IF NOT EQUAL		0.30 00	020222.40
	LX,\$X0,XCSZ10-RESTORE PROGRAM TO		1310.00 80	020223.00
	SX,\$X0,XCS1A2-INITIAL CONDITION		1304.10 00	020223.40
	LX,\$X0,XCSZ1-PUT ALL ONES BACK IN X0		21760.00 90	020224.00
	NOP,0		1310.00 80	020224.40
	NOP,0		1304.32 C0	020225.00
	B,\$+1.0		21761.00 10	020225.40
	B,XCS1A-TO LOOP IN X0 ALL ONES TEST		20223.01 10	020226.00
	SIC,SEN0+.32		21753.00 10	020226.40
	B,SSW-TEST FOR LOOPING SWITCH		0.30 00	020227.00
XCS1B	NOP,0-START ONE BIT TEST ON X1		0.30 00	020227.40
	KV,\$X1,XCSZ3-WITH 25 ONES, BITS 0 TO 24		20231.10 00	020230.00
	SIC,SEN		20202.50 00	020230.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1311.40 80	020231.00
	KC,\$X1,XCSZ3-WITH 18 ONES, BITS 28 TO 45		1301.10 00	020231.40
	SIC,SEN		0.30 00	020232.00
	BZXE,SERS-ERR IF NOT EQUAL		21755.02 90	020232.40
	SR,\$X1,XCSZ5-REFILL INTO WORK AREA		1310.00 80	020233.00
	SIC,SEN		1304.32 C4	020233.40
	BZXE,SERS-ERR IF NOT EQUAL		21755.03 90	020234.00
	SIC,SEN		1310.00 80	020234.40
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020235.00
	SIC,SEN		21756.03 70	020235.40
	SIC,SEN		1310.00 80	020236.00

	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020236.40
	LC,\$X1,XCSZ5-REFILL IN COUNT FIELD	21756.02 50	020237.00
	KC,\$X1,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.03 90	020237.40
	SIC,SEN	1310.00 80	020240.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020240.40
XCS1B1	LX,\$X1,XCSZ7	21757.02 10	020241.00
	V+,\$X1,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.02 B0	020241.40
	SX,\$X1,XCS1B2	20244.03 10	020242.00
	NOP,0-INSTR WD IN PROG.	0.30 00	020242.40
	NOP,0	0.30 00	020243.00
	CNOP,0	0.30 00	020243.40
XCS1B2	SIC,SEN	1310.00 80	020244.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP	1304.10 00	020244.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020245.00
	SIC,SEN	1310.00 80	020245.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020246.00
	LX,\$X1,XCSZ10	21761.02 10	020246.40
	SX,\$X1,XCS1B2-RESTORE PROGRAM	20244.03 10	020247.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020247.40
	LX,\$X1,XCSZ1	21753.02 10	020250.00
	NOP,0-RESTORE IX REG	0.30 00	020250.40
	NOP,0	0.30 00	020251.00
	B,\$+1.0	20252.50 00	020251.40
	B,XCS1B-TO LOOP IN X1 ALL ONES TEST	20232.10 00	020252.00
	SIC,SEN0+.32	1311.40 80	020252.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020253.00
XCS1C	NOP,0-START ONE BIT TEST ON X2	0.30 00	020253.40
	KV,\$X2,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.04 90	020254.00
	SIC,SEN	1310.00 80	020254.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020255.00
	KC,\$X2,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.05 90	020255.40
	SIC,SEN	1310.00 80	020256.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020256.40
	SR,\$X2,XCSZ5-REFILL INTO WORK AREA	21756.05 70	020257.00
	SIC,SEN	1310.00 80	020257.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020260.00
	LC,\$X2,XCSZ5-REFILL IN COUNT FIELD	21756.04 50	020260.40
	KC,\$X2,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.05 90	020261.00
	SIC,SEN	1310.00 80	020261.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020262.00
XCS1C1	LX,\$X2,XCSZ7	21757.04 10	020262.40
	V+,\$X2,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.04 B0	020263.00
	SX,\$X2,XCS1C2	20265.05 10	020263.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020264.00
	NOP,0	0.30 00	020264.40
	CNOP,0		
XCS1C2	SIC,SEN	1310.00 80	020265.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP	1304.10 00	020265.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020266.00
	SIC,SEN	1310.00 80	020266.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020267.00
	LX,\$X2,XCSZ10	21761.04 10	020267.40
	SX,\$X2,XCS1C2-RESTORE PROGRAM	20265.05 10	020270.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020270.40
	LX,\$X2,XCSZ1	21753.04 10	020271.00
	NOP,0-RESTORE IX REG.	0.30 00	020271.40
	NOP,0	0.30 00	020272.00
	B,\$+1.0	20273.50 00	020272.40
	B,XCS1C-TO LOOP IN X2 ALL ONES TEST	20253.50 00	020273.00
	SIC,SEN0+.32	1311.40 80	020273.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020274.00
XCS1D	NOP,0-START ONE BIT TEST ON X3	0.30 00	020274.40
	KV,\$X3,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.06 90	020275.00
	SIC,SEN	1310.00 80	020275.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020276.00
	KC,\$X3,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.07 90	020276.40

	SIC,SEN		1310.00 80	020277.00
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020277.40
	SR,\$X3,XCSZ5-REFILL INTO WORK AREA		21756.07 70	020300.00
	SIC,SEN		1310.00 80	020300.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25		1304.23 40	020301.00
	LC,\$X3,XCSZ5-REFILL IN COUNT FIELD		21756.06 50	020301.40
	KC,\$X3,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63		21755.07 90	020302.00
	SIC,SEN		1310.00 80	020302.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1304.32 C4	020303.00
XCS1D1	LX,\$X3,XCSZ7		21757.06 10	020303.40
	V+,\$X3,XCSZ8-USE BITS 26, 27 AS OP CODE		21760.06 B0	020304.00
	SX,\$X3,XCS1D2		20306.07 10	020304.40
	NOP,0-INSTR WD IN PROG.		0.30 00	020305.00
	NOP,0		0.30 00	020305.40
	CNOP,0			
XCS1D2	SIC,SEN		1310.00 80	020306.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP		1304.10 00	020306.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE		21760.00 90	020307.00
	SIC,SEN		1310.00 80	020307.40
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020310.00
	LX,\$X3,XCSZ10		21761.06 10	020310.40
	SX,\$X3,XCS1D2-RESTORE PROGRAM		20306.07 10	020311.00
	LX,\$X0,XCSZ1 -RESTORE X0		21753.00 10	020311.40
	LX,\$X3,XCSZ1		21753.06 10	020312.00
	NOP,0-RESTORE IX REG.		0.30 00	020312.40
	NOP,0		0.30 00	020313.00
	B,\$+1.0		20314.50 00	020313.40
	B,XCS1D-TO LOOP IN X3 ALL ONES TEST		20274.50 00	020314.00
	SIC,SEN0+.32		1311.40 80	020314.40
	B,SSW-TEST FOR LOOPING SWITCH		1301.10 00	020315.00
XCS1E	NOP,0-START ONE BIT TEST ON X4		0.30 00	020315.40
	KV,\$X4,XCSZ3-WITH 25 ONES, BITS 0 TO 24		21755.10 90	020316.00
	SIC,SEN		1310.00 80	020316.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1304.32 C4	020317.00
	KC,\$X4,XCSZ3-WITH 18 ONES, BITS 28 TO 45		21755.11 90	020317.40
	SIC,SEN		1310.00 80	020320.00
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020320.40
	SR,\$X4,XCSZ5-REFILL INTO WORK AREA		21756.11 70	020321.00
	SIC,SEN		1310.00 80	020321.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25		1304.23 40	020322.00
	LC,\$X4,XCSZ5-REFILL IN COUNT FIELD		21756.10 50	020322.40
	KC,\$X4,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63		21755.11 90	020323.00
	SIC,SEN		1310.00 80	020323.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1304.32 C4	020324.00
XCS1E1	LX,\$X4,XCSZ7		21757.10 10	020324.40
	V+,\$X4,XCSZ8-USE BITS 26, 27 AS OP CODE		21760.10 B0	020325.00
	SX,\$X4,XCS1E2		20327.11 10	020325.40
	NOP,0-INSTR WD IN PROG.		0.30 00	020326.00
	NOP,0		0.30 00	020326.40
	CNOP,0			
XCS1E2	SIC,SEN		1310.00 80	020327.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP		1304.10 00	020327.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE		21760.00 90	020330.00
	SIC,SEN		1310.00 80	020330.40
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020331.00
	LX,\$X4,XCSZ10		21761.10 10	020331.40
	SX,\$X4,XCS1E2-RESTORE PROGRAM		20327.11 10	020332.00
	LX,\$X0,XCSZ1 -RESTORE X0		21753.00 10	020332.40
	LX,\$X4,XCSZ1		21753.10 10	020333.00
	NOP,0-RESTORE IX REG.		0.30 00	020333.40
	NOP,0		0.30 00	020334.00
	B,\$+1.0		20335.50 00	020334.40
	B,XCS1E-TO LOOP IN X4 ALL ONES TEST		20315.50 00	020335.00
	SIC,SEN0+.32		1311.40 80	020335.40
	B,SSW-TEST FOR LOOPING SWITCH		1301.10 00	020336.00
XCS1F	NOP,0-START ONE BIT TEST ON X5		0.30 00	020336.40

	KV,\$X5,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.12 90	020337.00
	SIC,SEN	1310.00 80	020337.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020340.00
	KC,\$X5,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.13 90	020340.40
	SIC,SEN	1310.00 80	020341.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020341.40
	SR,\$X5,XCSZ5-REFILL INTO WORK AREA	21756.13 70	020342.00
	SIC,SEN	1310.00 80	020342.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020343.00
	LC,\$X5,XCSZ5-REFILL IN COUNT FIELD	21756.12 50	020343.40
	KC,\$X5,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.13 90	020344.00
	SIC,SEN	1310.00 80	020344.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020345.00
XCS1F1	LX,\$X5,XCSZ7	21757.12 10	020345.40
	V+,\$X5,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.12 B0	020346.00
	SX,\$X5,XCS1F2	20350.13 10	020346.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020347.00
	NOP,0	0.30 00	020347.40
	CNOP,0		
XCS1F2	SIC,SEN	1310.00 80	020350.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP	1304.10 00	020350.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020351.00
	SIC,SEN	1310.00 80	020351.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020352.00
	LX,\$X5,XCSZ10	21761.12 10	020352.40
	SX,\$X5,XCS1F2-RESTORE PROGRAM	20350.13 10	020353.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020353.40
	LX,\$X5,XCSZ1	21753.12 10	020354.00
	NOP,0-RESTORE IX REG.	0.30 00	020354.40
	NOP,0	0.30 00	020355.00
	B,\$+1.0	20356.50 00	020355.40
	B,XCS1F-TO LOOP IN X5 ALL ONES TEST	20336.50 00	020356.00
	SIC,SEN0+.32	1311.40 80	020356.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020357.00
XCS1G	NOP,0-START ONE BIT TEST ON X6	0.30 00	020357.40
	KV,\$X6,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.14 90	020360.00
	SIC,SEN	1310.00 80	020360.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020361.00
	KC,\$X6,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.15 90	020361.40
	SIC,SEN	1310.00 80	020362.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020362.40
	SR,\$X6,XCSZ5-REFILL INTO WORK AREA	21756.15 70	020363.00
	SIC,SEN	1310.00 80	020363.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020364.00
	LC,\$X6,XCSZ5-REFILL IN COUNT FIELD	21756.14 50	020364.40
	KC,\$X6,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.15 90	020365.00
	SIC,SEN	1310.00 80	020365.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020366.00
XCS1G1	LX,\$X6,XCSZ7	21757.14 10	020366.40
	V+,\$X6,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.14 B0	020367.00
	SX,\$X6,XCS1G2	20371.15 10	020367.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020370.00
	NOP,0	0.30 00	020370.40
	CNOP,0		
XCS1G2	SIC,SEN	1310.00 80	020371.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020371.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020372.00
	SIC,SEN	1310.00 80	020372.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020373.00
	LX,\$X6,XCSZ10	21761.14 10	020373.40
	SX,\$X6,XCS1G2-RESTORE PROGRAM	20371.15 10	020374.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020374.40
	LX,\$X6,XCSZ1	21753.14 10	020375.00
	NOP,0-RESTORE IX REG.	0.30 00	020375.40
	NOP,0	0.30 00	020376.00
	B,\$+1.0	20377.50 00	020376.40

	B,XCS1G-TO LOOP IN X6 ALL ONES TEST	20357.50 00	020377.00
	SIC,SEN0+.32	1311.40 80	020377.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020400.00
XCS1H	NOP,0-START ONE BIT TEST ON X7	0.30 00	020400.40
	KV,\$X7,XCSZ3-WITH 25 ONES,BITS 0 TO 24	21755.16 90	020401.00
	SIC,SEN	1310.00 80	020401.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020402.00
	KC,\$X7,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.17 90	020402.40
	SIC,SEN	1310.00 80	020403.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020403.40
	SR,\$X7,XCSZ5-REFILL INTO WORK AREA	21756.17 70	020404.00
	SIC,SEN	1310.00 80	020404.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020405.00
	LC,\$X7,XCSZ5-REFILL IN COUNT FIELD	21756.16 50	020405.40
	KC,\$X7,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.17 90	020406.00
	SIC,SEN	1310.00 80	020406.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020407.00
XCS1H1	LX,\$X7,XCSZ7	21757.16 10	020407.40
	V+,\$X7,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.16 B0	020410.00
	SX,\$X7,XCS1H2	20412.17 10	020410.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020411.00
	NOP,0	0.30 00	020411.40
	CNOP,0		
XCS1H2	SIC,SEN	1310.00 80	020412.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020412.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020413.00
	SIC,SEN	1310.00 80	020413.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020414.00
	LX,\$X7,XCSZ10	21761.16 10	020414.40
	SX,\$X7,XCS1H2-RESTORE PROGRAM	20412.17 10	020415.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020415.40
	LX,\$X7,XCSZ1	21753.16 10	020416.00
	NOP,0-RESTORE IX REG.	0.30 00	020416.40
	NOP,0	0.30 00	020417.00
	B,\$+1.0	20420.50 00	020417.40
	B,XCS1H-TO LOOP IN X7 ALL ONES TEST	20400.50 00	020420.00
	SIC,SEN0+.32	1311.40 80	020420.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020421.00
XCS1J	NOP,0-START ONE BIT TEST ON X8	0.30 00	020421.40
	KV,\$X8,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.20 90	020422.00
	SIC,SEN	1310.00 80	020422.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020423.00
	KC,\$X8,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.21 90	020423.40
	SIC,SEN	1310.00 80	020424.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020424.40
	SR,\$X8,XCSZ5-REFILL INTO WORK AREA	21756.21 70	020425.00
	SIC,SEN	1310.00 80	020425.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020426.00
	LC,\$X8,XCSZ5-REFILL IN COUNT FIELD	21756.20 50	020426.40
	KC,\$X8,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.21 90	020427.00
	SIC,SEN	1310.00 80	020427.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020430.00
XCS1J1	LX,\$X8,XCSZ7	21757.20 10	020430.40
	V+,\$X8,XCSZ8-USE BITS 26,27 AS OP CODE	21760.20 B0	020431.00
	SX,\$X8,XCS1J2	20433.21 10	020431.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020432.00
	NOP,0	0.30 00	020432.40
	CNOP,0		
XCS1J2	SIC,SEN	1310.00 80	020433.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020433.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020434.00
	SIC,SEN	1310.00 80	020434.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020435.00
	LX,\$X8,XCSZ10	21761.20 10	020435.40
	SX,\$X8,XCS1J2-RESTORE PROGRAM	20433.21 10	020436.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020436.40

	LX,\$X8,XCSZ1	21753.20 10	020437.00
	NOP,0-RESTORE IX REG.	0.30 00	020437.40
	NOP,0	0.30 00	020440.00
	B,\$+1.0	20441.50 00	020440.40
	B,XCS1J-TO LOOP IN X8 ALL ONES TEST	20421.50 00	020441.00
	SIC,SEN0+.32	1311.40 80	020441.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020442.00
XCS1K	NOP,0-START ONE BIT TEST ON X9	0.30 00	020442.40
	KV,\$X9,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.22 90	020443.00
	SIC,SEN	1310.00 80	020443.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020444.00
	KC,\$X9,XCSZ3-WITH 18 ONES,BITS 28 TO 45	21755.23 90	020444.40
	SIC,SEN	1310.00 80	020445.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020445.40
	SR,\$X9,XCSZ5-REFILL INTO WORK AREA	21756.23 70	020446.00
	SIC,SEN	1310.00 80	020446.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020447.00
	LC,\$X9,XCSZ5-REFILL IN COUNT FIELD	21756.22 50	020447.40
	KC,\$X9,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.23 90	020450.00
	SIC,SEN	1310.00 80	020450.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020451.00
XCS1K1	LX,\$X9,XCSZ7	21757.22 10	020451.40
	V+,\$X9,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.22 B0	020452.00
	SX,\$X9,XCS1K2	20454.23 10	020452.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020453.00
	NOP,0	0.30 00	020453.40
	CNOP,0		
XCS1K2	SIC,SEN	1310.00 80	020454.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP	1304.10 00	020454.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020455.00
	SIC,SEN	1310.00 80	020455.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020456.00
	LX,\$X9,XCSZ10	21761.22 10	020456.40
	SX,\$X9,XCS1K2-RESTORE PROGRAM	20454.23 10	020457.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020457.40
	LX,\$X9,XCSZ1	21753.22 10	020460.00
	NOP,0-RESTORE IX REG.	0.30 00	020460.40
	NOP,0	0.30 00	020461.00
	B,\$+1.0	20462.50 00	020461.40
	B,XCS1K-TO LOOP IN X9 ALL ONES TEST	20442.50 00	020462.00
	SIC,SEN0+.32	1311.40 80	020462.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020463.00
XCS1L	NOP,0-START ONE BIT TEST ON X10	0.30 00	020463.40
	KV,\$X10,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.24 90	020464.00
	SIC,SEN	1310.00 80	020464.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020465.00
	KC,\$X10,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.25 90	020465.40
	SIC,SEN	1310.00 80	020466.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020466.40
	SR,\$X10,XCSZ5-REFILL INTO WORK AREA	21756.25 70	020467.00
	SIC,SEN	1310.00 80	020467.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020470.00
	LC,\$X10,XCSZ5-REFILL IN COUNT FIELD	21756.24 50	020470.40
	KC,\$X10,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.25 90	020471.00
	SIC,SEN	1310.00 80	020471.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020472.00
XCS1L1	LX,\$X10,XCSZ7	21757.24 10	020472.40
	V+,\$X10,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.24 B0	020473.00
	SX,\$X10,XCS1L2	20475.25 10	020473.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020474.00
	NOP,0	0.30 00	020474.40
	CNOP,0		
XCS1L2	SIC,SEN	1310.00 80	020475.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP	1304.10 00	020475.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020476.00
	SIC,SEN	1310.00 80	020476.40

	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020477.00
	LX,\$X10,XCSZ10	21761.24 10	020477.40
	SX,\$X10,XCS1L2-RESTORE PROGRAM	20475.25 10	020500.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020500.40
	LX,\$X10,XCSZ1	21753.24 10	020501.00
	NOP,0-RESTORE IX REG.	0.30 00	020501.40
	NOP,0	0.30 00	020502.00
	B,\$+1.0	20503.50 00	020502.40
	B,XCS1L-TO LOOP IN X10 ALL ONES TEST	20463.50 00	020503.00
	SIC,SEN0+.32	1311.40 80	020503.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020504.00
XCS1M	LX,\$X11,XCSZ1 -START ONE BIT TEST ON X11	21753.26 10	020504.40
	KV,\$X11,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.26 90	020505.00
	SIC,SEN	1310.00 80	020505.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020506.00
	KC,\$X11,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.27 90	020506.40
	SIC,SEN	1310.00 80	020507.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020507.40
	SR,\$X11,XCSZ5-REFILL INTO WORK AREA	21756.27 70	020510.00
	SIC,SEN	1310.00 80	020510.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020511.00
	LC,\$X11,XCSZ5-REFILL IN COUNT FIELD	21756.26 50	020511.40
	KC,\$X11,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.27 90	020512.00
	SIC,SEN	1310.00 80	020512.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020513.00
XCS1M1	LX,\$X11,XCSZ7	21757.26 10	020513.40
	V+,\$X11,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.26 B0	020514.00
	SX,\$X11,XCS1M2	20516.27 10	020514.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020515.00
	NOP,0	0.30 00	020515.40
	CNOP,0		
XCS1M2	SIC,SEN	1310.00 80	020516.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020516.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020517.00
	SIC,SEN	1310.00 80	020517.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020520.00
	LX,\$X11,XCSZ10	21761.26 10	020520.40
	SX,\$X11,XCS1M2-RESTORE PROGRAM	20516.27 10	020521.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020521.40
	LX,\$X11,XCSZ1	21753.26 10	020522.00
	NOP,0-RESTORE IX REG.	0.30 00	020522.40
	NOP,0	0.30 00	020523.00
	B,\$+1.0	20524.50 00	020523.40
	B,XCS1M-TO LOOP IN X11 ALL ONES TEST	20504.50 00	020524.00
	SIC,SEN0+.32	1311.40 80	020524.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020525.00
XCS1N	LX,\$X12,XCSZ1 -START ONE BIT TEST ON X12	21753.30 10	020525.40
	KV,\$X12,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.30 90	020526.00
	SIC,SEN	1310.00 80	020526.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020527.00
	KC,\$X12,XCSZ3-WITH 18 ONES,BITS 28 TO 45	21755.31 90	020527.40
	SIC,SEN	1310.00 80	020530.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020530.40
	SR,\$X12,XCSZ5-REFILL INTO WORK AREA	21756.31 70	020531.00
	SIC,SEN	1310.00 80	020531.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020532.00
	LC,\$X12,XCSZ5-REFILL IN COUNT FIELD	21756.30 50	020532.40
	KC,\$X12,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.31 90	020533.00
	SIC,SEN	1310.00 80	020533.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020534.00
XCS1N1	LX,\$X12,XCSZ7	21757.30 10	020534.40
	V+,\$X12,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.30 B0	020535.00
	SX,\$X12,XCS1N2	20537.31 10	020535.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020536.00
	NOP,0	0.30 00	020536.40
	CNOP,0		

XCS1N2	SIC,SEN	1310.00 80	020537.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020537.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020540.00
	SIC,SEN	1310.00 80	020540.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020541.00
	LX,\$X12,XCSZ10	21761.30 10	020541.40
	SX,\$X12,XCS1N2-RESTORE PROGRAM	20537.31 10	020542.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020542.40
	LX,\$X12,XCSZ1	21753.30 10	020543.00
	NOP,0-RESTORE IX REG.	0.30 00	020543.40
	NOP,0	0.30 00	020544.00
	B,\$+1.0	20545.50 00	020544.40
	B,XCS1N-TO LOOP IN X12 ALL ONES TEST	20525.50 00	020545.00
	SIC,SEN0+.32	1311.40 80	020545.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020546.00
XCS1P	LX,\$X13,XCSZ1 -START ONE BIT TEST ON X13	21753.32 10	020546.40
	KV,\$X13,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.32 90	020547.00
	SIC,SEN	1310.00 80	020547.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020550.00
	KC,\$X13,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.33 90	020550.40
	SIC,SEN	1310.00 80	020551.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020551.40
	SR,\$X13,XCSZ5-REFILL INTO WORK AREA	21756.33 70	020552.00
	SIC,SEN	1310.00 80	020552.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020553.00
	LC,\$X13,XCSZ5-REFILL IN COUNT FIELD	21756.32 50	020553.40
	KC,\$X13,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.33 90	020554.00
	SIC,SEN	1310.00 80	020554.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020555.00
XCS1P1	LX,\$X13,XCSZ7	21757.32 10	020555.40
	V+,\$X13,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.32 B0	020556.00
	SX,\$X13,XCS1P2	20560.33 10	020556.40
	NOP,0-INSTR WD IN PROG.	0.30 00	020557.00
	NOP,0	0.30 00	020557.40
	CNOP,0		
XCS1P2	SIC,SEN	1310.00 80	020560.00
	B,SERS-BECOMES LV,XCSZ8 AND NOP	1304.10 00	020560.40
	KV,\$X0,XCSZ8 -PROOF POSITIVE	21760.00 90	020561.00
	SIC,SEN	1310.00 80	020561.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020562.00
	LX,\$X13,XCSZ10	21761.32 10	020562.40
	SX,\$X13,XCS1P2-RESTORE PROGRAM	20560.33 10	020563.00
	LX,\$X0,XCSZ1 -RESTORE X0	21753.00 10	020563.40
	LX,\$X13,XCSZ1	21753.32 10	020564.00
	NOP,0-RESTORE IX REG.	0.30 00	020564.40
	NOP,0	0.30 00	020565.00
	B,\$+1.0	20566.50 00	020565.40
	B,XCS1P-TO LOOP IN X13 ALL ONES TEST	20546.50 00	020566.00
	SIC,SEN0+.32	1311.40 80	020566.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020567.00
XCS1Q	NOP,0-START ONE BIT TEST ON X14	0.30 00	020567.40
	KV,\$X14,XCSZ3-WITH 25 ONES, BITS 0 TO 24	21755.34 90	020570.00
	SIC,SEN	1310.00 80	020570.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020571.00
	KC,\$X14,XCSZ3-WITH 18 ONES, BITS 28 TO 45	21755.35 90	020571.40
	SIC,SEN	1310.00 80	020572.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020572.40
	SR,\$X14,XCSZ5-REFILL INTO WORK AREA	21756.35 70	020573.00
	SIC,SEN	1310.00 80	020573.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25	1304.23 40	020574.00
	LC,\$X14,XCSZ5-REFILL IN COUNT FIELD	21756.34 50	020574.40
	KC,\$X14,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63	21755.35 90	020575.00
	SIC,SEN	1310.00 80	020575.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020576.00
XCS1Q1	LX,\$X14,XCSZ7	21757.34 10	020576.40
	V+,\$X14,XCSZ8-USE BITS 26, 27 AS OP CODE	21760.34 B0	020577.00

	SX,\$X14,XCS1Q2	NOP,0-INSTR WD IN PROG.	20601.35 10	020577.40
	NOP,0		0.30 00	020600.00
	CNOP,0		0.30 00	020600.40
XCS1Q2	SIC,SEN		1310.00 80	020601.00
	B,SERS	-BECOME LV, XCSZ8 + NOP	1304.10 00	020601.40
	KV,\$X0,XCSZ8	-PROOF POSITIVE	21760.00 90	020602.00
	SIC,SEN		1310.00 80	020602.40
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020603.00
	LX,\$X14,XCSZ10		21761.34 10	020603.40
	SX,\$X14,XCS1Q2-RESTORE PROGRAM		20601.35 10	020604.00
	LX,\$X0,XCSZ1	-RESTORE X0	21753.00 10	020604.40
	LX,\$X14,XCSZ1		21753.34 10	020605.00
	NOP,0-RESTORE IX REG.		0.30 00	020605.40
	NOP,0		0.30 00	020606.00
	B,\$+1.0		20607.50 00	020606.40
	B,XCS1Q-TO LOOP IN X14 ALL ONES TEST		20567.50 00	020607.00
	SIC,SEN0+.32		1311.40 80	020607.40
	B,SSW-TEST FOR LOOPING SWITCH		1301.10 00	020610.00
XCS1R	NOP,0-START ONE BIT TEST FOR X15		0.30 00	020610.40
	KV,\$X15,XCSZ3-WITH 25 ONES, BITS 0 TO 24		21755.36 90	020611.00
	SIC,SEN		1310.00 80	020611.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1304.32 C4	020612.00
	KC,\$X15,XCSZ3-WITH 18 ONES, BITS 28 TO 45		21755.37 90	020612.40
	SIC,SEN		1310.00 80	020613.00
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020613.40
	SR,\$X15,XCSZ5-REFILL INTO WORK AREA		21756.37 70	020614.00
	SIC,SEN		1310.00 80	020614.40
	BZXF,SERS-ERR IF FLAG NOT 1, BIT 25		1304.23 40	020615.00
	LC,\$X15,XCSZ5-REFILL IN COUNT FIELD		21756.36 50	020615.40
	KC,\$X15,XCSZ3-WITH 18 ONES, ACTUAL BITS 46 TO 63		21755.37 90	020616.00
	SIC,SEN		1310.00 80	020616.40
	BZXEZ,SERS-ERR IF NOT EQUAL		1304.32 C4	020617.00
XCS1R1	LX,\$X15,XCSZ7		21757.36 10	020617.40
	V+,\$X15,XCSZ8-USE BITS 26, 27 AS OP CODE		21760.36 B0	020620.00
	SX,\$X15,XCS1R2		20622.37 10	020620.40
	NOP,0-INSTR WD IN PROG.		0.30 00	020621.00
	NOP,0		0.30 00	020621.40
	CNOP,0			
XCS1R2	SIC,SEN		1310.00 80	020622.00
	B,SERS-BECOMES LV, XCSZ8 AND NOP		1304.10 00	020622.40
	KV,\$X0,XCSZ8	-PROOF POSITIVE	21760.00 90	020623.00
	SIC,SEN		1310.00 80	020623.40
	BZXE,SERS-ERR IF NOT EQUAL		1304.32 C0	020624.00
	LX,\$X15,XCSZ10		21761.36 10	020624.40
	SX,\$X14,XCS1R2-RESTORE PROGRAM		20622.35 10	020625.00
	LX,\$X0,XCSZ1	-RESTORE X0	21753.00 10	020625.40
	LX,\$X15,XCSZ1		21753.36 10	020626.00
	NOP,0-RESTORE IX REG		0.30 00	020626.40
	NOP,0		0.30 00	020627.00
	B,\$+1.0		20630.50 00	020627.40
	B,XCS1R-TO LOOP IN X15 ALL ONES TEST		20610.50 00	020630.00
	SIC,SEN0+.32		1311.40 80	020630.40
	B,SSW-TEST FOR LOOPING SWITCH		1301.10 00	020631.00
	B,\$+1.0		20632.50 00	020631.40
	B,XCS1-TO LOOP IN ALL REGS ONES TEST		20171.50 00	020632.00
	SIC,SEN0+.32		1311.40 80	020632.40
	B,SSW		1301.10 00	020633.00
	NOP,0.		0.30 00	020633.40
	NOP,0.		0.30 00	020634.00
XCS2	LX,\$X0,XCSZ2		21754.00 10	020634.40
	LX,\$X1,XCSZ2-INITIALIZE		21754.02 10	020635.00
	LX,\$X2,XCSZ2		21754.04 10	020635.40
	LX,\$X3,XCSZ2-BY SETTING		21754.06 10	020636.00
	LX,\$X4,XCSZ2		21754.10 10	020636.40
	LX,\$X5,XCSZ2-ALL ZEROS		21754.12 10	020637.00

	LX,\$X6,XCSZ2	LX,\$X7,XCSZ2-INTO EVERY	21754.14	10	020637.40
	LX,\$X8,XCSZ2	LX,\$X9,XCSZ2-BIT POSITION	21754.16	10	020640.00
	LX,\$X10,XCSZ2	LX,\$X11,XCSZ2-IN THE	21754.20	10	020640.40
	LX,\$X12,XCSZ2	LX,\$X13,XCSZ2-INDEX	21754.22	10	020641.00
	LX,\$X14,XCSZ2	LX,\$X15,XCSZ2-REGISTERS	21754.24	10	020641.40
			21754.26	10	020642.00
			21754.30	10	020642.40
			21754.32	10	020643.00
			21754.34	10	020643.40
			21754.36	10	020644.00
XCS2A	KV,\$X0,XCSZ4-WITH ZEROS TO TEST X0		21755.40	90	020644.40
	SIC,SEN		1310.00	80	020645.00
		BZXEZ,SERS-ERR IF NOT EQUAL	1304.32	C4	020645.40
	KC,\$X0,XCSZ4-WITH ZEROS		21755.41	90	020646.00
	SIC,SEN		1310.00	80	020646.40
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020647.00
	SR,\$X0,XCSZ5-REFILL INTO WORK AREA		21756.01	70	020647.40
	SIC,SEN		1310.00	80	020650.00
		BXF,SERS-ERR IF XF IS ONE	1304.23	42	020650.40
	LC,\$X0,XCSZ5-REFILL IN COUNT FIELD		21756.00	50	020651.00
	KC,\$X0,XCSZ4-WITH ZEROS		21755.41	90	020651.40
	SIC,SEN		1310.00	80	020652.00
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020652.40
	LX,\$X0,XCSZ2-RESTORE REG. TO ALL ZEROS		21754.00	10	020653.00
	NOP,0		0.30	00	020653.40
		NOP,0	0.30	00	020654.00
	B,\$+1.0		20655.50	00	020654.40
	B,XCS2A-TO LOOP IN X0 ZERO TEST		20644.50	00	020655.00
	SIC,SEN0+.32		1311.40	80	020655.40
		B,SSW-TEST FOR LOOPING SWITCH	1301.10	00	020656.00
XCS2B	KV,\$X1,XCSZ4-TEST X1 FOR ZEROS, BITS 0 TO 24		21755.42	90	020656.40
	SIC,SEN		1310.00	80	020657.00
		BZXEZ,SERS-ERR IF NOT EQUAL	1304.32	C4	020657.40
	KC,\$X1,XCSZ4-TEST ZEROS IN 28 TO 45		21755.43	90	020660.00
	SIC,SEN		1310.00	80	020660.40
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020661.00
	SR,\$X1,XCSZ5-REFILL INTO WORK AREA		21756.03	70	020661.40
	SIC,SEN		1310.00	80	020662.00
		BXF,SERS-ERR IF XF IS ONE	1304.23	42	020662.40
	LC,\$X1,XCSZ5		21756.02	50	020663.00
		KC,\$X1,XCSZ4 -46-63 TEST FOR ZEROS	21755.43	90	020663.40
	SIC,SEN		1310.00	80	020664.00
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020664.40
	LX,\$X1,XCSZ2		21754.02	10	020665.00
		NOP,0-RESTORE REG. TO ALL ZEROS	0.30	00	020665.40
		NOP,0	0.30	00	020666.00
		B,\$+1.0	20667.50	00	020666.40
	B,XCS2B-TO LOOP IN X1 ZERO TEST		20656.50	00	020667.00
	SIC,SEN0+.32		1311.40	80	020667.40
		B,SSW-TEST FOR LOOPING SWITCH	1301.10	00	020670.00
XCS2C	KV,\$X2,XCSZ4-TEST X2 FOR ZERO BITS 0 TO 24		21755.44	90	020670.40
	SIC,SEN		1310.00	80	020671.00
		BZXEZ,SERS-ERR IF NOT EQUAL	1304.32	C4	020671.40
	KC,\$X2,XCSZ4-TEST ZEROS IN 28 TO 45		21755.45	90	020672.00
	SIC,SEN		1310.00	80	020672.40
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020673.00
	SR,\$X2,XCSZ5-REFILL INTO WORK AREA		21756.05	70	020673.40
	SIC,SEN		1310.00	80	020674.00
		BXF,SERS-ERR IF XF IS ONE	1304.23	42	020674.40
	LC,\$X2,XCSZ5		21756.04	50	020675.00
		KC,\$X2,XCSZ4 -46-63 TEST FOR ZEROS	21755.45	90	020675.40
	SIC,SEN		1310.00	80	020676.00
		BZXE,SERS-ERR IF NOT EQUAL	1304.32	C0	020676.40
	LX,\$X2,XCSZ2		21754.04	10	020677.00
		NOP,0-RESTORE REG TO ALL ZEROS	0.30	00	020677.40
		NOP,0	0.30	00	020700.00

	B,\$+1.0	20701.50 00	020700.40
	B,XCS2C-TO LOOP IN X2 ZERO TEST	20670.50 00	020701.00
	SIC,SEN0+.32	1311.40 80	020701.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020702.00
XCS2D	KV,\$X3,XCSZ4-TEST X3 FOR ZEROS, BITS 0 TO 24	21755.46 90	020702.40
	SIC,SEN	1310.00 80	020703.00
	BZXEZ,SERS -ERR IF NOT EQUAL	1304.32 C4	020703.40
	KC,\$X3,XCSZ4-TEST ZEROS IN 28 TO 45	21755.47 90	020704.00
	SIC,SEN	1310.00 80	020704.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020705.00
	SR,\$X3,XCSZ5-REFILL INTO WORK AREA	21756.07 70	020705.40
	SIC,SEN	1310.00 80	020706.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020706.40
	LC,\$X3,XCSZ5	21756.06 50	020707.00
	KC,\$X3,XCSZ4 -46-63 TEST FOR ZEROS	21755.47 90	020707.40
	SIC,SEN	1310.00 80	020710.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020710.40
	LX,\$X3,XCSZ2	21754.06 10	020711.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	020711.40
	NOP,0	0.30 00	020712.00
	B,\$+1.0	20713.50 00	020712.40
	B,XCS2D-TO LOOP IN X3 ZERO TEST	20702.50 00	020713.00
	SIC,SEN0+.32	1311.40 80	020713.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020714.00
XCS2E	KV,\$X4,XCSZ4-TEST X4 FOR ZERO BITS 0 TO 24	21755.50 90	020714.40
	SIC,SEN	1310.00 80	020715.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020715.40
	KC,\$X4,XCSZ4-TEST ZEROS IN 28 TO 45	21755.51 90	020716.00
	SIC,SEN	1310.00 80	020716.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020717.00
	SR,\$X4,XCSZ5-REFILL INTO WORK AREA	21756.11 70	020717.40
	SIC,SEN	1310.00 80	020720.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020720.40
	LC,\$X4,XCSZ5	21756.10 50	020721.00
	KC,\$X4,XCSZ4 -46-63 TEST FOR ZEROS	21755.51 90	020721.40
	SIC,SEN	1310.00 80	020722.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020722.40
	LX,\$X4,XCSZ2	21754.10 10	020723.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	020723.40
	NOP,0	0.30 00	020724.00
	B,\$+1.0	20725.50 00	020724.40
	B,XCS2E-TO LOOP IN X4 ZERO TEST	20714.50 00	020725.00
	SIC,SEN0+.32	1311.40 80	020725.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020726.00
XCS2F	KV,\$X5,XCSZ4-TEST X5 FOR ZEROS, BITS 0 TO 24	21755.52 90	020726.40
	SIC,SEN	1310.00 80	020727.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020727.40
	KC,\$X5,XCSZ4-TEST ZEROS IN 28 TO 45	21755.53 90	020730.00
	SIC,SEN	1310.00 80	020730.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020731.00
	SR,\$X5,XCSZ5-REFILL INTO WORK AREA	21756.13 70	020731.40
	SIC,SEN	1310.00 80	020732.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020732.40
	LC,\$X5,XCSZ5	21756.12 50	020733.00
	KC,\$X5,XCSZ4 -46-63 TEST FOR ZEROS	21755.53 90	020733.40
	SIC,SEN	1310.00 80	020734.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020734.40
	LX,\$X5,XCSZ2	21754.12 10	020735.00
	NOP,0-RESTORE REG. TO ALL ZEROS	0.30 00	020735.40
	NOP,0	0.30 00	020736.00
	B,\$+1.0	20737.50 00	020736.40
	B,XCS2F-TO LOOP IN X5 ZERO TEST	20726.50 00	020737.00
	SIC,SEN0+.32	1311.40 80	020737.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020740.00
XCS2G	KV,\$X6,XCSZ4-TEST X6 FOR ZERO BITS 0 TO 24	21755.54 90	020740.40
	SIC,SEN	1310.00 80	020741.00

	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020741.40
	KC,\$X6,XCSZ4-TEST ZEROS IN 28 TO 45	21755.55 90	020742.00
	SIC,SEN	1310.00 80	020742.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020743.00
	SR,\$X6,XCSZ5-REFILL INTO WORK AREA	21756.15 70	020743.40
	SIC,SEN	1310.00 80	020744.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020744.40
	LC,\$X6,XCSZ5	21756.14 50	020745.00
	KC,\$X6,XCSZ4 -46-63 TEST FOR ZEROS	21755.55 90	020745.40
	SIC,SEN	1310.00 80	020746.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020746.40
	LX,\$X6,XCSZ2	21754.14 10	020747.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	020747.40
	NOP,0	0.30 00	020750.00
	B,\$+1.0	20751.50 00	020750.40
	B,XCS2G-TO LOOP IN X6 ZERO TEST	20740.50 00	020751.00
	SIC,SEN0+.32	1311.40 80	020751.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020752.00
XCS2H	KV,\$X7,XCSZ4-TEST X7 FOR ZEROS,BITS 0 TO 24	21755.56 90	020752.40
	SIC,SEN	1310.00 80	020753.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020753.40
	KC,\$X7,XCSZ4-TEST ZEROS IN 28 TO 45	21755.57 90	020754.00
	SIC,SEN	1310.00 80	020754.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020755.00
	SR,\$X7,XCSZ5-REFILL INTO WORK AREA	21756.17 70	020755.40
	SIC,SEN	1310.00 80	020756.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020756.40
	LC,\$X7,XCSZ5	21756.16 50	020757.00
	KC,\$X7,XCSZ4 -46-63 TEST FOR ZEROS	21755.57 90	020757.40
	SIC,SEN	1310.00 80	020760.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020760.40
	LX,\$X7,XCSZ2	21754.16 10	020761.00
	NOP,0-RESTORE REG. TO ALL ZEROS	0.30 00	020761.40
	NOP,0	0.30 00	020762.00
	B,\$+1.0	20763.50 00	020762.40
	B,XCS2H-TO LOOP IN X7 ZERO TEST	20752.50 00	020763.00
	SIC,SEN0+.32	1311.40 80	020763.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020764.00
XCS2J	KV,\$X8,XCSZ4-TEST X8 FOR ZERO BITS 0 TO 24	21755.60 90	020764.40
	SIC,SEN	1310.00 80	020765.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020765.40
	KC,\$X8,XCSZ4-TEST ZEROS IN 28 TO 45	21755.61 90	020766.00
	SIC,SEN	1310.00 80	020766.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020767.00
	SR,\$X8,XCSZ5-REFILL INTO WORK AREA	21756.21 70	020767.40
	SIC,SEN	1310.00 80	020770.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	020770.40
	LC,\$X8,XCSZ5	21756.20 50	020771.00
	KC,\$X8,XCSZ4 -46-63 TEST FOR ZEROS	21755.61 90	020771.40
	SIC,SEN	1310.00 80	020772.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	020772.40
	LX,\$X8,XCSZ2	21754.20 10	020773.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	020773.40
	NOP,0	0.30 00	020774.00
	B,\$+1.0	20775.50 00	020774.40
	B,XCS2J-TO LOOP IN X8 ZERO TEST	20764.50 00	020775.00
	SIC,SEN0+.32	1311.40 80	020775.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	020776.00
XCS2K	KV,\$X9,XCSZ4-TEST X9 FOR ZEROS, BITS 0 TO 24	21755.62 90	020776.40
	SIC,SEN	1310.00 80	020777.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	020777.40
	KC,\$X9,XCSZ4-TEST ZEROS IN 28 TO 45	21755.63 90	021000.00
	SIC,SEN	1310.00 80	021000.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021001.00
	SR,\$X9,XCSZ5-REFILL INTO WORK AREA	21756.23 70	021001.40
	SIC,SEN	1310.00 80	021002.00

	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021002.40
	LC,\$X9,XCSZ5	21756.22 50	021003.00
	KC,\$X9,XCSZ4 -46-63 TEST FOR ZEROS	21755.63 90	021003.40
	SIC,SEN	1310.00 80	021004.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021004.40
	LX,\$X9,XCSZ2	21754.22 10	021005.00
	NOP,0-RESTORE REG.TO ALL ZEROS	0.30 00	021005.40
	NOP,0	0.30 00	021006.00
	B,\$+1.0	21007.50 00	021006.40
	B,XCS2K-TO LOOP IN X9 ZERO TEST	20776.50 00	021007.00
	SIC,SEN0+.32	1311.40 80	021007.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021010.00
XCS2L	KV,\$X10,XCSZ4-TEST X10 FOR ZERO BITS 0 TO 24	21755.64 90	021010.40
	SIC,SEN	1310.00 80	021011.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021011.40
	KC,\$X10,XCSZ4-TEST ZEROS IN 28 TO 45	21755.65 90	021012.00
	SIC,SEN	1310.00 80	021012.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021013.00
	SR,\$X10,XCSZ5-REFILL INTO WORK AREA	21756.25 70	021013.40
	SIC,SEN	1310.00 80	021014.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021014.40
	LC,\$X10,XCSZ5	21756.24 50	021015.00
	KC,\$X10,XCSZ4 -46-63 TEST FOR ZEROS	21755.65 90	021015.40
	SIC,SEN	1310.00 80	021016.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021016.40
	LX,\$X10,XCSZ2	21754.24 10	021017.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	021017.40
	NOP,0	0.30 00	021020.00
	B,\$+1.0	21021.50 00	021020.40
	B,XCS2L-TO LOOP IN X10 ZERO TEST	21010.50 00	021021.00
	SIC,SEN0+.32	1311.40 80	021021.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021022.00
XCS2M	LX,\$X11,XCSZ2 -REINITIALIZE X11	21754.26 10	021022.40
	KV,\$X11,XCSZ4 -TEST X11 FOR ZEROS, BITS 0-24	21755.66 90	021023.00
	SIC,SEN	1310.00 80	021023.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021024.00
	KC,\$X11,XCSZ4-TEST ZEROS IN 28 TO 45	21755.67 90	021024.40
	SIC,SEN	1310.00 80	021025.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021025.40
	SR,\$X11,XCSZ5-REFILL INTO WORK AREA	21756.27 70	021026.00
	SIC,SEN	1310.00 80	021026.40
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021027.00
	LC,\$X11,XCSZ5	21756.26 50	021027.40
	KC,\$X11,XCSZ4 -46-63 TEST FOR ZEROS	21755.67 90	021030.00
	SIC,SEN	1310.00 80	021030.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021031.00
	LX,\$X11,XCSZ2	21754.26 10	021031.40
	NOP,0-RESTORE REG.TO ALL ZEROS	0.30 00	021032.00
	NOP,0	0.30 00	021032.40
	B,\$+1.0	21034.10 00	021033.00
	B,XCS2M-TO LOOP IN X11 ZERO TEST	21022.50 00	021033.40
	SIC,SEN0+.32	1311.40 80	021034.00
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021034.40
XCS2N	LX,\$X12,XCSZ2 -REINITIALIZE X12	21754.30 10	021035.00
	KV,\$X12,XCSZ4 -TEST X12 FOR ZERO BITS 0-24	21755.70 90	021035.40
	SIC,SEN	1310.00 80	021036.00
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021036.40
	KC,\$X12,XCSZ4-TEST ZEROS IN 28 TO 45	21755.71 90	021037.00
	SIC,SEN	1310.00 80	021037.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021040.00
	SR,\$X12,XCSZ5-REFILL INTO WORK AREA	21756.31 70	021040.40
	SIC,SEN	1310.00 80	021041.00
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021041.40
	LC,\$X12,XCSZ5	21756.30 50	021042.00
	KC,\$X12,XCSZ4 -46-63 TEST FOR ZEROS	21755.71 90	021042.40
	SIC,SEN	1310.00 80	021043.00

	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021043.40
	LX,\$X12,XCSZ2	21754.30 10	021044.00
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	021044.40
	NOP,0	0.30 00	021045.00
	B,\$+1.0	21046.50 00	021045.40
	B,XCS2N-TO LOOP IN X12 ZERO TEST	21035.10 00	021046.00
	SIC,SEN0+.32	1311.40 80	021046.40
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021047.00
XCS2P	LX,\$X13,XCSZ2	21754.32 10	021047.40
	KV,\$X13,XCSZ4	21755.72 90	021050.00
	SIC,SEN	1310.00 80	021050.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021051.00
	KC,\$X13,XCSZ4-TEST ZEROS IN 28 TO 45	21755.73 90	021051.40
	SIC,SEN	1310.00 80	021052.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021052.40
	SR,\$X13,XCSZ5-REFILL INTO WORK AREA	21756.33 70	021053.00
	SIC,SEN	1310.00 80	021053.40
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021054.00
	LC,\$X13,XCSZ5	21756.32 50	021054.40
	KC,\$X13,XCSZ4 -46-63 TEST FOR ZEROS	21755.73 90	021055.00
	SIC,SEN	1310.00 80	021055.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021056.00
	LX,\$X13,XCSZ2	21754.32 10	021056.40
	NOP,0-RESTORE REG. TO ALL ZEROS	0.30 00	021057.00
	NOP,0	0.30 00	021057.40
	B,\$+1.0	21061.10 00	021060.00
	B,XCS2P-TO LOOP IN X13 ZERO TEST	21047.50 00	021060.40
	SIC,SEN0+.32	1311.40 80	021061.00
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021061.40
XCS2Q	KV,\$X14,XCSZ4-TEST X14 FOR ZERO BITS 0 TO 24	21755.74 90	021062.00
	SIC,SEN	1310.00 80	021062.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021063.00
	KC,\$X14,XCSZ4-TEST ZEROS IN 28 TO 45	21755.75 90	021063.40
	SIC,SEN	1310.00 80	021064.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021064.40
	SR,\$X14,XCSZ5-REFILL INTO WORK AREA	21756.35 70	021065.00
	SIC,SEN	1310.00 80	021065.40
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021066.00
	LC,\$X14,XCSZ5	21756.34 50	021066.40
	KC,\$X14,XCSZ4 -46-63 TEST FOR ZEROS	21755.75 90	021067.00
	SIC,SEN	1310.00 80	021067.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021070.00
	LX,\$X14,XCSZ2	21754.34 10	021070.40
	NOP,0-RESTORE REG TO ALL ZEROS	0.30 00	021071.00
	NOP,0	0.30 00	021071.40
	B,\$+1.0	21073.10 00	021072.00
	B,XCS2Q-TO LOOP IN X14 ZERO TEST	21062.10 00	021072.40
	SIC,SEN0+.32	1311.40 80	021073.00
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021073.40
XCS2R	KV,\$X15,XCSZ4-TEST X15 FOR ZEROS, BITS 0 TO 24	21755.76 90	021074.00
	SIC,SEN	1310.00 80	021074.40
	BZXEZ,SERS-ERR IF NOT EQUAL	1304.32 C4	021075.00
	KC,\$X15,XCSZ4-TEST ZEROS IN 28 TO 45	21755.77 90	021075.40
	SIC,SEN	1310.00 80	021076.00
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021076.40
	SR,\$X15,XCSZ5-REFILL INTO WORK AREA	21756.37 70	021077.00
	SIC,SEN	1310.00 80	021077.40
	BXF,SERS-ERR IF XF IS ONE	1304.23 42	021100.00
	LC,\$X15,XCSZ5	21756.36 50	021100.40
	KC,\$X15,XCSZ4 -46-63 TEST FOR ZEROS	21755.77 90	021101.00
	SIC,SEN	1310.00 80	021101.40
	BZXE,SERS-ERR IF NOT EQUAL	1304.32 C0	021102.00
	LX,\$X15,XCSZ2	21754.36 10	021102.40
	NOP,0-RESTORE REG. TO ALL ZEROS	0.30 00	021103.00
	NOP,0	0.30 00	021103.40
	B,\$+1.0	21105.10 00	021104.00

	B,XCS2R-TO LOOP IN X15 ZERO TEST	21074.10 00	021104.40
	SIC,SEN0+.32	1311.40 80	021105.00
	B,SSW-TEST FOR LOOPING SWITCH	1301.10 00	021105.40
	B,\$+1.0	21107.10 00	021106.00
	B,XCS2-TO LOOP IN ALL REGS ZERO TEST	20634.50 00	021106.40
	SIC,SEN0+.32	1311.40 80	021107.00
	B,SSW	1301.10 00	021107.40
	NOP,0	0.30 00	021110.00
	NOP,0	0.30 00	021110.40
XCS3	LX,\$X0,XCSZ11	21762.00 10	021111.00
	LX,\$X1,XCSZ11 -INITIALIZE BY SETTING	21762.02 10	021111.40
	LX,\$X2,XCSZ11	21762.04 10	021112.00
	LX,\$X3,XCSZ11-PATTERN A IN ALL IX	21762.06 10	021112.40
	LX,\$X4,XCSZ11	21762.10 10	021113.00
	LX,\$X5,XCSZ11-REGISTERS,ALL	21762.12 10	021113.40
	LX,\$X6,XCSZ11	21762.14 10	021114.00
	LX,\$X7,XCSZ11-PARITY BITS ON	21762.16 10	021114.40
	LX,\$X8,XCSZ11	21762.20 10	021115.00
	LX,\$X9,XCSZ11-INDEX FIELDS	21762.22 10	021115.40
	LX,\$X12,XCSZ11	21762.30 10	021116.00
	LX,\$X13,XCSZ11-ERRS PICKED UP BY CKTS.	21762.32 10	021116.40
	LX,\$X10,XCSZ11	21762.24 10	021117.00
	LX,\$X11,XCSZ11-ARE ZEROS. PARITY	21762.26 10	021117.40
	LX,\$X14,XCSZ11	21762.34 10	021120.00
	LX,\$X15,XCSZ11-PATTERN BITS TESTED IN PROG	21762.36 10	021120.40
XCS3A	KV,\$X0,XCSZ11 -TEST \$X0, FOR RIGHT PATTERN	21762.00 90	021121.00
	SIC,SEN	1310.00 80	021121.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021122.00
	KC,\$X0,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.01 90	021122.40
	SIC,SEN	1310.00 80	021123.00
	BZXE,SERS	1304.32 C0	021123.40
	SR,\$X0,XCSZ5-REFILL TO WORK AREA	21756.01 70	021124.00
	SIC,SEN	1310.00 80	021124.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021125.00
	LC,\$X0,XCSZ5-REFILL INTO COUNT FIELD	21756.00 50	021125.40
	KC,\$X0,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.41 90	021126.00
	SIC,SEN	1310.00 80	021126.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021127.00
	LX,\$X0,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.00 10	021127.40
	NOP,0	0.30 00	021130.00
	NOP,0	0.30 00	021130.40
XCS3B	KV,\$X1,XCSZ11 -TEST \$X1 FOR RIGHT PATTERN	21762.02 90	021131.00
	SIC,SEN	1310.00 80	021131.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021132.00
	KC,\$X1,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.03 90	021132.40
	SIC,SEN	1310.00 80	021133.00
	BZXE,SERS	1304.32 C0	021133.40
	SR,\$X1,XCSZ5-REFILL FLD TO WORK AREA	21756.03 70	021134.00
	SIC,SEN	1310.00 80	021134.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021135.00
	LC,\$X1,XCSZ5-REFILL INTO COUNT FIELD	21756.02 50	021135.40
	KC,\$X1,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.43 90	021136.00
	SIC,SEN	1310.00 80	021136.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021137.00
	LX,\$X1,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.02 10	021137.40
	NOP,0	0.30 00	021140.00
	NOP,0	0.30 00	021140.40
XCS3C	KV,\$X2,XCSZ11 -TEST \$X2 FOR RIGHT PATTERN	21762.04 90	021141.00
	SIC,SEN	1310.00 80	021141.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021142.00
	KC,\$X2,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.05 90	021142.40
	SIC,SEN	1310.00 80	021143.00
	BZXE,SERS	1304.32 C0	021143.40
	SR,\$X2,XCSZ5-REFILL TO WORK AREA	21756.05 70	021144.00
	SIC,SEN	1310.00 80	021144.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021145.00

	LC,\$X2,XCSZ5-REFILL INTO COUNT FIELD	21756.04 50	021145.40
	KC,\$X2,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.45 90	021146.00
	SIC,SEN	1310.00 80	021146.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021147.00
	LX,\$X2,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.04 10	021147.40
	NOP,0	0.30 00	021150.00
	NOP,0	0.30 00	021150.40
XCS3D	KV,\$X3,XCSZ11 -TEST \$X3 FOR RIGHT PATTERN	21762.06 90	021151.00
	SIC,SEN	1310.00 80	021151.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021152.00
	KC,\$X3,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.07 90	021152.40
	SIC,SEN	1310.00 80	021153.00
	BZXE,SERS	1304.32 C0	021153.40
	SR,\$X3,XCSZ5-REFILL FLD TO WORK AREA	21756.07 70	021154.00
	SIC,SEN	1310.00 80	021154.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021155.00
	LC,\$X3,XCSZ5-REFILL INTO COUNT FIELD	21756.06 50	021155.40
	KC,\$X3,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.47 90	021156.00
	SIC,SEN	1310.00 80	021156.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021157.00
	LX,\$X3,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.06 10	021157.40
	NOP,0	0.30 00	021160.00
	NOP,0	0.30 00	021160.40
XCS3E	KV,\$X4,XCSZ11 -TEST \$X4 FOR RIGHT PATTERN	21762.10 90	021161.00
	SIC,SEN	1310.00 80	021161.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021162.00
	KC,\$X4,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.11 90	021162.40
	SIC,SEN	1310.00 80	021163.00
	BZXE,SERS	1304.32 C0	021163.40
	SR,\$X4,XCSZ5-REFILL TO WORK AREA	21756.11 70	021164.00
	SIC,SEN	1310.00 80	021164.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021165.00
	LC,\$X4,XCSZ5-REFILL INTO COUNT FIELD	21756.10 50	021165.40
	KC,\$X4,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.51 90	021166.00
	SIC,SEN	1310.00 80	021166.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021167.00
	LX,\$X4,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.10 10	021167.40
	NOP,0	0.30 00	021170.00
	NOP,0	0.30 00	021170.40
XCS3F	KV,\$X5,XCSZ11 -TEST \$X5 FOR RIGHT PATTERN	21762.12 90	021171.00
	SIC,SEN	1310.00 80	021171.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021172.00
	KC,\$X5,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.13 90	021172.40
	SIC,SEN	1310.00 80	021173.00
	BZXE,SERS	1304.32 C0	021173.40
	SR,\$X5,XCSZ5-REFILL FLD TO WORK AREA	21756.13 70	021174.00
	SIC,SEN	1310.00 80	021174.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021175.00
	LC,\$X5,XCSZ5-REFILL INTO COUNT FIELD	21756.12 50	021175.40
	KC,\$X5,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.53 90	021176.00
	SIC,SEN	1310.00 80	021176.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021177.00
	LX,\$X5,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.12 10	021177.40
	NOP,0	0.30 00	021200.00
	NOP,0	0.30 00	021200.40
XCS3G	KV,\$X6,XCSZ11 -TEST \$X6 FOR RIGHT PATTERN	21762.14 90	021201.00
	SIC,SEN	1310.00 80	021201.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021202.00
	KC,\$X6,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.15 90	021202.40
	SIC,SEN	1310.00 80	021203.00
	BZXE,SERS	1304.32 C0	021203.40
	SR,\$X6,XCSZ5-REFILL TO WORK AREA	21756.15 70	021204.00
	SIC,SEN	1310.00 80	021204.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021205.00
	LC,\$X6,XCSZ5-REFILL INTO COUNT FIELD	21756.14 50	021205.40
	KC,\$X6,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.55 90	021206.00

	SIC,SEN		1310.00 80	021206.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021207.00
	LX,\$X6,XCSZ11-RESTORE IX REG TO INITIAL COND		21762.14 10	021207.40
	NOP,0		0.30 00	021210.00
	NOP,0		0.30 00	021210.40
XCS3H	KV,\$X7,XCSZ11	-TEST \$X7 FOR RIGHT PATTERN	21762.16 90	021211.00
	SIC,SEN		1310.00 80	021211.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.		1304.32 C4	021212.00
	KC,\$X7,XCSZ14-28-45 MUST BE IDENT. TO CONT		21765.17 90	021212.40
	SIC,SEN		1310.00 80	021213.00
	BZXE,SERS		1304.32 C0	021213.40
	SR,\$X7,XCSZ5-REFILL FLD TO WORK AREA		21756.17 70	021214.00
	SIC,SEN		1310.00 80	021214.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25		1304.23 40	021215.00
	LC,\$X7,XCSZ5-REFILL INTO COUNT FIELD		21756.16 50	021215.40
	KC,\$X7,XCSZ15-ACTUALLY 46-63 FROM REFILL		21765.57 90	021216.00
	SIC,SEN		1310.00 80	021216.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021217.00
	LX,\$X7,XCSZ11-RESTORE IX REG TO INITIAL COND.		21762.16 10	021217.40
	NOP,0		0.30 00	021220.00
	NOP,0		0.30 00	021220.40
XCS3J	KV,\$X8,XCSZ11-TEST X8 FOR RIGHT PATTERN		21762.20 90	021221.00
	SIC,SEN		1310.00 80	021221.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.		1304.32 C4	021222.00
	KC,\$X8,XCSZ14-28-45 MUST BE SAME TO CONT.		21765.21 90	021222.40
	SIC,SEN		1310.00 80	021223.00
	BZXE,SERS		1304.32 C0	021223.40
	SR,\$X8,XCSZ5-REFILL TO WORK AREA		21756.21 70	021224.00
	SIC,SEN		1310.00 80	021224.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25		1304.23 40	021225.00
	LC,\$X8,XCSZ5-REFILL INTO COUNT FIELD		21756.20 50	021225.40
	KC,\$X8,XCSZ15-ACTUALLY 46-63 FROM REFILL		21765.61 90	021226.00
	SIC,SEN		1310.00 80	021226.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021227.00
	LX,\$X8,XCSZ11-RESTORE IX REG TO INITIAL COND		21762.20 10	021227.40
	NOP,0		0.30 00	021230.00
	NOP,0		0.30 00	021230.40
XCS3K	KV,\$X9,XCSZ11-TEST X9 FOR RIGHT PATTERN		21762.22 90	021231.00
	SIC,SEN		1310.00 80	021231.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.		1304.32 C4	021232.00
	KC,\$X9,XCSZ14-28-45 MUST BE IDENT. TO CONT		21765.23 90	021232.40
	SIC,SEN		1310.00 80	021233.00
	BZXE,SERS		1304.32 C0	021233.40
	SR,\$X9,XCSZ5-REFILL FLD TO WORK AREA		21756.23 70	021234.00
	SIC,SEN		1310.00 80	021234.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25		1304.23 40	021235.00
	LC,\$X9,XCSZ5-REFILL INTO COUNT FIELD		21756.22 50	021235.40
	KC,\$X9,XCSZ15-ACTUALLY 46-63 FROM REFILL		21765.63 90	021236.00
	SIC,SEN		1310.00 80	021236.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021237.00
	LX,\$X9,XCSZ11-RESTORE IX REG TO INITIAL COND.		21762.22 10	021237.40
	NOP,0		0.30 00	021240.00
	NOP,0		0.30 00	021240.40
XCS3L	KV,\$X10,XCSZ11-TEST X10 FOR RIGHT PATTERN		21762.24 90	021241.00
	SIC,SEN		1310.00 80	021241.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.		1304.32 C4	021242.00
	KC,\$X10,XCSZ14-28-45 MUST BE SAME TO CONT.		21765.25 90	021242.40
	SIC,SEN		1310.00 80	021243.00
	BZXE,SERS		1304.32 C0	021243.40
	SR,\$X10,XCSZ5-REFILL TO WORK AREA		21756.25 70	021244.00
	SIC,SEN		1310.00 80	021244.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25		1304.23 40	021245.00
	LC,\$X10,XCSZ5-REFILL INTO COUNT FIELD		21756.24 50	021245.40
	KC,\$X10,XCSZ15-ACTUALLY 46-63 FROM REFILL		21765.65 90	021246.00
	SIC,SEN		1310.00 80	021246.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021247.00

	LX,\$X10,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.26 10	021247.40
	NOP,0	0.30 00	021250.00
	NOP,0	0.30 00	021250.40
XCS3M	KV,\$X11,XCSZ11-TEST X11 FOR RIGHT PATTERN	21762.26 90	021251.00
	SIC,SEN	1310.00 80	021251.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021252.00
	KC,\$X11,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.27 90	021252.40
	SIC,SEN	1310.00 80	021253.00
	BZXE,SERS	1304.32 C0	021253.40
	SR,\$X11,XCSZ5-REFILL FLD TO WORK AREA	21756.27 70	021254.00
	SIC,SEN	1310.00 80	021254.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021255.00
	LC,\$X11,XCSZ5-REFILL INTO COUNT FIELD	21756.26 50	021255.40
	KC,\$X11,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.67 90	021256.00
	SIC,SEN	1310.00 80	021256.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021257.00
	LX,\$X11,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.26 10	021257.40
	NOP,0	0.30 00	021260.00
	NOP,0	0.30 00	021260.40
XCS3N	KV,\$X12,XCSZ11-TEST X12 FOR RIGHT PATTERN	21762.30 90	021261.00
	SIC,SEN	1310.00 80	021261.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021262.00
	KC,\$X12,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.31 90	021262.40
	SIC,SEN	1310.00 80	021263.00
	BZXE,SERS	1304.32 C0	021263.40
	SR,\$X12,XCSZ5-REFILL TO WORK AREA	21756.31 70	021264.00
	SIC,SEN	1310.00 80	021264.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021265.00
	LC,\$X12,XCSZ5-REFILL INTO COUNT FIELD	21756.30 50	021265.40
	KC,\$X12,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.71 90	021266.00
	SIC,SEN	1310.00 80	021266.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021267.00
	LX,\$X12,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.30 10	021267.40
	NOP,0	0.30 00	021270.00
	NOP,0	0.30 00	021270.40
XCS3P	LX,\$X13,XCSZ11 -REINITIALIZE X13	21762.32 10	021271.00
	KV,\$X13,XCSZ11 -TEST X13 FOR RIGHT PATTERN	21762.32 90	021271.40
	SIC,SEN	1310.00 80	021272.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021272.40
	KC,\$X13,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.33 90	021273.00
	SIC,SEN	1310.00 80	021273.40
	BZXE,SERS	1304.32 C0	021274.00
	SR,\$X13,XCSZ5-REFILL FLD TO WORK AREA	21756.33 70	021274.40
	SIC,SEN	1310.00 80	021275.00
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021275.40
	LC,\$X13,XCSZ5-REFILL INTO COUNT FIELD	21756.32 50	021276.00
	KC,\$X13,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.73 90	021276.40
	SIC,SEN	1310.00 80	021277.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021277.40
	LX,\$X13,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.32 10	021300.00
	NOP,0	0.30 00	021300.40
	NOP,0	0.30 00	021301.00
XCS3Q	KV,\$X14,XCSZ11-TEST X14 FOR RIGHT PATTERN	21762.34 90	021301.40
	SIC,SEN	1310.00 80	021302.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021302.40
	KC,\$X14,XCSZ14-28-45 MUST BE SAME TO CONT.	21765.35 90	021303.00
	SIC,SEN	1310.00 80	021303.40
	BZXE,SERS	1304.32 C0	021304.00
	SR,\$X14,XCSZ5-REFILL TO WORK AREA	21756.35 70	021304.40
	SIC,SEN	1310.00 80	021305.00
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021305.40
	LC,\$X14,XCSZ5-REFILL INTO COUNT FIELD	21756.34 50	021306.00
	KC,\$X14,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.75 90	021306.40
	SIC,SEN	1310.00 80	021307.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021307.40
	LX,\$X14,XCSZ11-RESTORE IX REG TO INITIAL COND	21762.34 10	021310.00

	NOP,0	0.30 00	021310.40
XCS3R	KV,\$X15,XCSZ11-TEST X15 FOR RIGHT PATTERN	21762.36 90	021311.00
	SIC,SEN	1310.00 80	021311.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021312.00
	KC,\$X15,XCSZ14-28-45 MUST BE IDENT. TO CONT	21765.37 90	021312.40
	SIC,SEN	1310.00 80	021313.00
	BZXE,SERS	1304.32 C0	021313.40
	SR,\$X15,XCSZ5-REFILL FLD TO WORK AREA	21756.37 70	021314.00
	SIC,SEN	1310.00 80	021314.40
	BZXF,SERS-XF NOT 1 AS IT SHD BE %25	1304.23 40	021315.00
	LC,\$X15,XCSZ5-REFILL INTO COUNT FIELD	21756.36 50	021315.40
	KC,\$X15,XCSZ15-ACTUALLY 46-63 FROM REFILL	21765.77 90	021316.00
	SIC,SEN	1310.00 80	021316.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021317.00
	LX,\$X15,XCSZ11-RESTORE IX REG TO INITIAL COND.	21762.36 10	021317.40
	NOP,0	0.30 00	021320.00
	NOP,0	0.30 00	021320.40
	B,\$+1.0	21322.50 00	021321.00
	B,XCS3-LOOP IN ALL REG. A PATTERN TEST	21111.10 00	021321.40
	SIC,SEN0+.32	1311.40 80	021322.00
	B,SSW	1301.10 00	021322.40
XCS4	LX,\$X0,XCSZ12	21763.00 10	021323.00
	LX,\$X1,XCSZ12-INITIALIZE BY SETTING	21763.02 10	021323.40
	LX,\$X2,XCSZ12	21763.04 10	021324.00
	LX,\$X3,XCSZ12-PATTERN B IN ALL IX	21763.06 10	021324.40
	LX,\$X4,XCSZ12	21763.10 10	021325.00
	LX,\$X5,XCSZ12-REGISTERS, LEFT HALF	21763.12 10	021325.40
	LX,\$X6,XCSZ12	21763.14 10	021326.00
	LX,\$X7,XCSZ12-PARITYS ARE ONES	21763.16 10	021326.40
	LX,\$X8,XCSZ12	21763.20 10	021327.00
	LX,\$X9,XCSZ12-RIGHT HALF	21763.22 10	021327.40
	LX,\$X10,XCSZ12	21763.24 10	021330.00
	LX,\$X11,XCSZ12-PARITYS ARE ZEROS.	21763.26 10	021330.40
	LX,\$X12,XCSZ12	21763.30 10	021331.00
	LX,\$X13,XCSZ12-P ERRS FOUND BY CKTS	21763.32 10	021331.40
	LX,\$X14,XCSZ12	21763.34 10	021332.00
	LX,\$X15,XCSZ12-PATTERN ERRS BY PROG	21763.36 10	021332.40
XCS4A	KV,\$X0,XCSZ12-TEST X0 FOR RIGHT PATTERN	21763.00 90	021333.00
	SIC,SEN	1310.00 80	021333.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021334.00
	KC,\$X0,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.01 90	021334.40
	SIC,SEN	1310.00 80	021335.00
	BZXE,SERS	1304.32 C0	021335.40
	SR,\$X0,XCSZ5-REFILL TO WORK AREA	21756.01 70	021336.00
	SIC,SEN	1310.00 80	021336.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021337.00
	LC,\$X0,XCSZ5-REFILL INTO COUNT FIELD	21756.00 50	021337.40
	KC,\$X0,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.41 90	021340.00
	SIC,SEN	1310.00 80	021340.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021341.00
	LX,\$X0,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.00 10	021341.40
	NOP,0	0.30 00	021342.00
	NOP,0	0.30 00	021342.40
XCS4B	KV,\$X1,XCSZ12-TEST X1 FOR RIGHT PATTERN	21763.02 90	021343.00
	SIC,SEN	1310.00 80	021343.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021344.00
	KC,\$X1,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.03 90	021344.40
	SIC,SEN	1310.00 80	021345.00
	BZXE,SERS	1304.32 C0	021345.40
	SR,\$X1,XCSZ5-REFILL FLD TO WORK AREA	21756.03 70	021346.00
	SIC,SEN	1310.00 80	021346.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021347.00
	LC,\$X1,XCSZ5-REFILL INTO COUNT FIELD	21756.02 50	021347.40
	KC,\$X1,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.43 90	021350.00
	SIC,SEN	1310.00 80	021350.40
			021351.00

	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021351.40
	LX,\$X1,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.02 10	021352.00
	NOP,0	0.30 00	021352.40
	NOP,0	0.30 00	021353.00
XCS4C	KV,\$X2,XCSZ12-TEST X2 FOR RIGHT PATTERN	21763.04 90	021353.40
	SIC,SEN	1310.00 80	021354.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021354.40
	KC,\$X2,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.05 90	021355.00
	SIC,SEN	1310.00 80	021355.40
	BZXE,SERS	1304.32 C0	021356.00
	SR,\$X2,XCSZ5-REFILL TO WORK AREA	21756.05 70	021356.40
	SIC,SEN	1310.00 80	021357.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021357.40
	LC,\$X2,XCSZ5-REFILL INTO COUNT FIELD	21756.04 50	021360.00
	KC,\$X2,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.45 90	021360.40
	SIC,SEN	1310.00 80	021361.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021361.40
	LX,\$X2,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.04 10	021362.00
	NOP,0	0.30 00	021362.40
	NOP,0	0.30 00	021363.00
XCS4D	KV,\$X3,XCSZ12-TEST X3 FOR RIGHT PATTERN	21763.06 90	021363.40
	SIC,SEN	1310.00 80	021364.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021364.40
	KC,\$X3,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.07 90	021365.00
	SIC,SEN	1310.00 80	021365.40
	BZXE,SERS	1304.32 C0	021366.00
	SR,\$X3,XCSZ5-REFILL FLD TO WORK AREA	21756.07 70	021366.40
	SIC,SEN	1310.00 80	021367.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021367.40
	LC,\$X3,XCSZ5-REFILL INTO COUNT FIELD	21756.06 50	021370.00
	KC,\$X3,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.47 90	021370.40
	SIC,SEN	1310.00 80	021371.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021371.40
	LX,\$X3,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.06 10	021372.00
	NOP,0	0.30 00	021372.40
	NOP,0	0.30 00	021373.00
XCS4E	KV,\$X4,XCSZ12-TEST X4 FOR RIGHT PATTERN	21763.10 90	021373.40
	SIC,SEN	1310.00 80	021374.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021374.40
	KC,\$X4,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.11 90	021375.00
	SIC,SEN	1310.00 80	021375.40
	BZXE,SERS	1304.32 C0	021376.00
	SR,\$X4,XCSZ5-REFILL TO WORK AREA	21756.11 70	021376.40
	SIC,SEN	1310.00 80	021377.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021377.40
	LC,\$X4,XCSZ5-REFILL INTO COUNT FIELD	21756.10 50	021400.00
	KC,\$X4,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.51 90	021400.40
	SIC,SEN	1310.00 80	021401.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021401.40
	LX,\$X4,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.10 10	021402.00
	NOP,0	0.30 00	021402.40
	NOP,0	0.30 00	021403.00
XCS4F	KV,\$X5,XCSZ12-TEST X5 FOR RIGHT PATTERN	21763.12 90	021403.40
	SIC,SEN	1310.00 80	021404.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021404.40
	KC,\$X5,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.13 90	021405.00
	SIC,SEN	1310.00 80	021405.40
	BZXE,SERS	1304.32 C0	021406.00
	SR,\$X5,XCSZ5-REFILL FLD TO WORK AREA	21756.13 70	021406.40
	SIC,SEN	1310.00 80	021407.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021407.40
	LC,\$X5,XCSZ5-REFILL INTO COUNT FIELD	21756.12 50	021410.00
	KC,\$X5,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.53 90	021410.40
	SIC,SEN	1310.00 80	021411.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021411.40
	LX,\$X5,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.12 10	021412.00

	NOP,0	0.30 00	021412.40
XCS4G	KV,\$X6,XCSZ12-TEST X6 FOR RIGHT PATTERN	21763.14 90	021413.00
	SIC,SEN	1310.00 80	021413.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021414.00
	KC,\$X6,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.15 90	021414.40
	SIC,SEN	1310.00 80	021415.00
	BZXE,SERS	1304.32 C0	021415.40
	SR,\$X6,XCSZ5-REFILL TO WORK AREA	21756.15 70	021416.00
	SIC,SEN	1310.00 80	021416.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021417.00
	LC,\$X6,XCSZ5-REFILL INTO COUNT FIELD	21756.14 50	021417.40
	KC,\$X6,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.55 90	021420.00
	SIC,SEN	1310.00 80	021420.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021421.00
	LX,\$X6,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.14 10	021421.40
	NOP,0	0.30 00	021422.00
	NOP,0	0.30 00	021422.40
XCS4H	KV,\$X7,XCSZ12-TEST X7 FOR RIGHT PATTERN	21763.16 90	021423.00
	SIC,SEN	1310.00 80	021423.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021424.00
	KC,\$X7,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.17 90	021424.40
	SIC,SEN	1310.00 80	021425.00
	BZXE,SERS	1304.32 C0	021425.40
	SR,\$X7,XCSZ5-REFILL FLD TO WORK AREA	21756.17 70	021426.00
	SIC,SEN	1310.00 80	021426.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021427.00
	LC,\$X7,XCSZ5-REFILL INTO COUNT FIELD	21756.16 50	021427.40
	KC,\$X7,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.57 90	021430.00
	SIC,SEN	1310.00 80	021430.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021431.00
	LX,\$X7,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.16 10	021431.40
	NOP,0	0.30 00	021432.00
	NOP,0	0.30 00	021432.40
XCS4J	KV,\$X8,XCSZ12-TEST X8 FOR RIGHT PATTERN	21763.20 90	021433.00
	SIC,SEN	1310.00 80	021433.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021434.00
	KC,\$X8,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.21 90	021434.40
	SIC,SEN	1310.00 80	021435.00
	BZXE,SERS	1304.32 C0	021435.40
	SR,\$X8,XCSZ5-REFILL TO WORK AREA	21756.21 70	021436.00
	SIC,SEN	1310.00 80	021436.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021437.00
	LC,\$X8,XCSZ5-REFILL INTO COUNT FIELD	21756.20 50	021437.40
	KC,\$X8,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.61 90	021440.00
	SIC,SEN	1310.00 80	021440.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021441.00
	LX,\$X8,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.20 10	021441.40
	NOP,0	0.30 00	021442.00
	NOP,0	0.30 00	021442.40
XCS4K	KV,\$X9,XCSZ12-TEST X9 FOR RIGHT PATTERN	21763.22 90	021443.00
	SIC,SEN	1310.00 80	021443.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021444.00
	KC,\$X9,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.23 90	021444.40
	SIC,SEN	1310.00 80	021445.00
	BZXE,SERS	1304.32 C0	021445.40
	SR,\$X9,XCSZ5-REFILL FLD TO WORK AREA	21756.23 70	021446.00
	SIC,SEN	1310.00 80	021446.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021447.00
	LC,\$X9,XCSZ5-REFILL INTO COUNT FIELD	21756.22 50	021447.40
	KC,\$X9,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.63 90	021450.00
	SIC,SEN	1310.00 80	021450.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021451.00
	LX,\$X9,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.22 10	021451.40
	NOP,0	0.30 00	021452.00
	NOP,0	0.30 00	021452.40
	NOP,0	0.30 00	021453.00

XCS4L	KV,\$X10,XCSZ12-TEST X10 FOR RIGHT PATTERN	21763.24 90	021453.40
	SIC,SEN	1310.00 80	021454.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021454.40
	KC,\$X10,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.25 90	021455.00
	SIC,SEN	1310.00 80	021455.40
	BZXE,SERS	1304.32 C0	021456.00
	SR,\$X10,XCSZ5-REFILL TO WORK AREA	21756.25 70	021456.40
	SIC,SEN	1310.00 80	021457.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021457.40
	LC,\$X10,XCSZ5-REFILL INTO COUNT FIELD	21756.24 50	021460.00
	KC,\$X10,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.65 90	021460.40
	SIC,SEN	1310.00 80	021461.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021461.40
	LX,\$X10,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.24 10	021462.00
	NOP,0	0.30 00	021462.40
	NOP,0	0.30 00	021463.00
XCS4M	KV,\$X11,XCSZ12-TEST X11 FOR RIGHT PATTERN	21763.26 90	021463.40
	SIC,SEN	1310.00 80	021464.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021464.40
	KC,\$X11,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.27 90	021465.00
	SIC,SEN	1310.00 80	021465.40
	BZXE,SERS	1304.32 C0	021466.00
	SR,\$X11,XCSZ5-REFILL FLD TO WORK AREA	21756.27 70	021466.40
	SIC,SEN	1310.00 80	021467.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021467.40
	LC,\$X11,XCSZ5-REFILL INTO COUNT FIELD	21756.26 50	021470.00
	KC,\$X11,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.67 90	021470.40
	SIC,SEN	1310.00 80	021471.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021471.40
	LX,\$X11,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.26 10	021472.00
	NOP,0	0.30 00	021472.40
	NOP,0	0.30 00	021473.00
XCS4N	KV,\$X12,XCSZ12-TEST X12 FOR RIGHT PATTERN	21763.30 90	021473.40
	SIC,SEN	1310.00 80	021474.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021474.40
	KC,\$X12,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.31 90	021475.00
	SIC,SEN	1310.00 80	021475.40
	BZXE,SERS	1304.32 C0	021476.00
	SR,\$X12,XCSZ5-REFILL TO WORK AREA	21756.31 70	021476.40
	SIC,SEN	1310.00 80	021477.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021477.40
	LC,\$X12,XCSZ5-REFILL INTO COUNT FIELD	21756.30 50	021500.00
	KC,\$X12,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.71 90	021500.40
	SIC,SEN	1310.00 80	021501.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021501.40
	LX,\$X12,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.30 10	021502.00
	NOP,0	0.30 00	021502.40
	NOP,0	0.30 00	021503.00
XCS4P	LX,\$X13,XCSZ12 -REINITIALIZE X13	21763.32 10	021503.40
	KV,\$X13,XCSZ12 -TEST X13 FOR RIGHT PATTERN	21763.32 90	021504.00
	SIC,SEN	1310.00 80	021504.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021505.00
	KC,\$X13,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.33 90	021505.40
	SIC,SEN	1310.00 80	021506.00
	BZXE,SERS	1304.32 C0	021506.40
	SR,\$X13,XCSZ5-REFILL FLD TO WORK AREA	21756.33 70	021507.00
	SIC,SEN	1310.00 80	021507.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021510.00
	LC,\$X13,XCSZ5-REFILL INTO COUNT FIELD	21756.32 50	021510.40
	KC,\$X13,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.73 90	021511.00
	SIC,SEN	1310.00 80	021511.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021512.00
	LX,\$X13,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.32 10	021512.40
	NOP,0	0.30 00	021513.00
	NOP,0	0.30 00	021513.40
XCS4Q	KV,\$X14,XCSZ12-TEST X14 FOR RIGHT PATTERN	21763.34 90	021514.00

	SIC,SEN	1310.00 80	021514.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021515.00
	KC,\$X14,XCSZ16-28-45 MUST BE SAME TO CONT.	21766.35 90	021515.40
	SIC,SEN	1310.00 80	021516.00
	BZXE,SERS	1304.32 C0	021516.40
	SR,\$X14,XCSZ5-REFILL TO WORK AREA	21756.35 70	021517.00
	SIC,SEN	1310.00 80	021517.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021520.00
	LC,\$X14,XCSZ5-REFILL INTO COUNT FIELD	21756.34 50	021520.40
	KC,\$X14,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.75 90	021521.00
	SIC,SEN	1310.00 80	021521.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021522.00
	LX,\$X14,XCSZ12-RESTORE IX REG TO INITIAL COND	21763.34 10	021522.40
	NOP,0	0.30 00	021523.00
	NOP,0	0.30 00	021523.40
XCS4R	KV,\$X15,XCSZ12-TEST X15 FOR RIGHT PATTERN	21763.36 90	021524.00
	SIC,SEN	1310.00 80	021524.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021525.00
	KC,\$X15,XCSZ16-28-45 MUST BE IDENT. TO CONT	21766.37 90	021525.40
	SIC,SEN	1310.00 80	021526.00
	BZXE,SERS	1304.32 C0	021526.40
	SR,\$X15,XCSZ5-REFILL FLD TO WORK AREA	21756.37 70	021527.00
	SIC,SEN	1310.00 80	021527.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021530.00
	LC,\$X15,XCSZ5-REFILL INTO COUNT FIELD	21756.36 50	021530.40
	KC,\$X15,XCSZ17-ACTUALLY 46-63 FROM REFILL	21766.77 90	021531.00
	SIC,SEN	1310.00 80	021531.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021532.00
	LX,\$X15,XCSZ12-RESTORE IX REG TO INITIAL COND.	21763.36 10	021532.40
	NOP,0	0.30 00	021533.00
	NOP,0	0.30 00	021533.40
	B,\$+1.0	21535.10 00	021534.00
	B,XCS4-LOOP IN ALL REG. B PATTERN TEST	21323.50 00	021534.40
	SIC,SEN0+.32	1311.40 80	021535.00
	B,SSW	1301.10 00	021535.40
XCS5	LX,\$X0,XCSZ13	21764.00 10	021536.00
	LX,\$X1,XCSZ13-INITIALIZE BY SETTING	21764.02 10	021536.40
	LX,\$X2,XCSZ13	21764.04 10	021537.00
	LX,\$X3,XCSZ13-PATTERN C IN ALL IX	21764.06 10	021537.40
	LX,\$X4,XCSZ13	21764.10 10	021540.00
	LX,\$X5,XCSZ13-REGS. LEFT HALF	21764.12 10	021540.40
	LX,\$X6,XCSZ13	21764.14 10	021541.00
	LX,\$X7,XCSZ13-PARITYS ARE ZERO	21764.16 10	021541.40
	LX,\$X8,XCSZ13	21764.20 10	021542.00
	LX,\$X9,XCSZ13-RIGHT HALF PARITYS	21764.22 10	021542.40
	LX,\$X10,XCSZ13	21764.24 10	021543.00
	LX,\$X11,XCSZ13-ARE ONES	21764.26 10	021543.40
	LX,\$X12,XCSZ13	21764.30 10	021544.00
	LX,\$X13,XCSZ13-P ERRS FOUND BY CKTS	21764.32 10	021544.40
	LX,\$X14,XCSZ13	21764.34 10	021545.00
	LX,\$X15,XCSZ13-PATTERN ERRS BY PROG.	21764.36 10	021545.40
XCS5A	KV,\$X0,XCSZ13-TEST X0 FOR RIGHT PATTERN	21764.00 90	021546.00
	SIC,SEN	1310.00 80	021546.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021547.00
	KC,\$X0,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.01 90	021547.40
	SIC,SEN	1310.00 80	021550.00
	BZXE,SERS	1304.32 C0	021550.40
	SR,\$X0,XCSZ5-REFILL TO WORK AREA	21756.01 70	021551.00
	SIC,SEN	1310.00 80	021551.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021552.00
	LC,\$X0,XCSZ5-REFILL INTO COUNT FIELD	21756.00 50	021552.40
	KC,\$X0,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.41 90	021553.00
	SIC,SEN	1310.00 80	021553.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021554.00
	LX,\$X0,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.00 10	021554.40
	NOP,0	0.30 00	021555.00

	NOP,0	0.30 00	021555.40
XCS5B	KV,\$X1,XCSZ13-TEST X1 FOR RIGHT PATTERN	21764.02 90	021556.00
	SIC,SEN	1310.00 80	021556.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021557.00
	KC,\$X1,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.03 90	021557.40
	SIC,SEN	1310.00 80	021560.00
	BZXE,SERS	1304.32 C0	021560.40
	SR,\$X1,XCSZ5-REFILL FLD TO WORK AREA	21756.03 70	021561.00
	SIC,SEN	1310.00 80	021561.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021562.00
	LC,\$X1,XCSZ5-REFILL INTO COUNT FIELD	21756.02 50	021562.40
	KC,\$X1,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.43 90	021563.00
	SIC,SEN	1310.00 80	021563.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021564.00
	LX,\$X1,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.02 10	021564.40
	NOP,0	0.30 00	021565.00
	NOP,0	0.30 00	021565.40
XCS5C	KV,\$X2,XCSZ13-TEST X2 FOR RIGHT PATTERN	21764.04 90	021566.00
	SIC,SEN	1310.00 80	021566.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021567.00
	KC,\$X2,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.05 90	021567.40
	SIC,SEN	1310.00 80	021570.00
	BZXE,SERS	1304.32 C0	021570.40
	SR,\$X2,XCSZ5-REFILL TO WORK AREA	21756.05 70	021571.00
	SIC,SEN	1310.00 80	021571.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021572.00
	LC,\$X2,XCSZ5-REFILL INTO COUNT FIELD	21756.04 50	021572.40
	KC,\$X2,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.45 90	021573.00
	SIC,SEN	1310.00 80	021573.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021574.00
	LX,\$X2,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.04 10	021574.40
	NOP,0	0.30 00	021575.00
	NOP,0	0.30 00	021575.40
XCS5D	KV,\$X3,XCSZ13-TEST X3 FOR RIGHT PATTERN	21764.06 90	021576.00
	SIC,SEN	1310.00 80	021576.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021577.00
	KC,\$X3,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.07 90	021577.40
	SIC,SEN	1310.00 80	021600.00
	BZXE,SERS	1304.32 C0	021600.40
	SR,\$X3,XCSZ5-REFILL FLD TO WORK AREA	21756.07 70	021601.00
	SIC,SEN	1310.00 80	021601.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021602.00
	LC,\$X3,XCSZ5-REFILL INTO COUNT FIELD	21756.06 50	021602.40
	KC,\$X3,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.47 90	021603.00
	SIC,SEN	1310.00 80	021603.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021604.00
	LX,\$X3,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.06 10	021604.40
	NOP,0	0.30 00	021605.00
	NOP,0	0.30 00	021605.40
XCS5E	KV,\$X4,XCSZ13-TEST X4 FOR RIGHT PATTERN	21764.10 90	021606.00
	SIC,SEN	1310.00 80	021606.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021607.00
	KC,\$X4,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.11 90	021607.40
	SIC,SEN	1310.00 80	021610.00
	BZXE,SERS	1304.32 C0	021610.40
	SR,\$X4,XCSZ5-REFILL TO WORK AREA	21756.11 70	021611.00
	SIC,SEN	1310.00 80	021611.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021612.00
	LC,\$X4,XCSZ5-REFILL INTO COUNT FIELD	21756.10 50	021612.40
	KC,\$X4,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.51 90	021613.00
	SIC,SEN	1310.00 80	021613.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021614.00
	LX,\$X4,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.10 10	021614.40
	NOP,0	0.30 00	021615.00
	NOP,0	0.30 00	021615.40
XCS5F	KV,\$X5,XCSZ13-TEST X5 FOR RIGHT PATTERN	21764.12 90	021616.00

	SIC,SEN	1310.00 80	021616.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021617.00
	KC,\$X5,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.13 90	021617.40
	SIC,SEN	1310.00 80	021620.00
	BZXE,SERS	1304.32 C0	021620.40
	SR,\$X5,XCSZ5-REFILL FLD TO WORK AREA	21756.13 70	021621.00
	SIC,SEN	1310.00 80	021621.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021622.00
	LC,\$X5,XCSZ5-REFILL INTO COUNT FIELD	21756.12 50	021622.40
	KC,\$X5,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.53 90	021623.00
	SIC,SEN	1310.00 80	021623.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021624.00
	LX,\$X5,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.12 10	021624.40
	NOP,0	0.30 00	021625.00
	NOP,0	0.30 00	021625.40
XCS5G	KV,\$X6,XCSZ13-TEST X6 FOR RIGHT PATTERN	21764.14 90	021626.00
	SIC,SEN	1310.00 80	021626.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021627.00
	KC,\$X6,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.15 90	021627.40
	SIC,SEN	1310.00 80	021630.00
	BZXE,SERS	1304.32 C0	021630.40
	SR,\$X6,XCSZ5-REFILL TO WORK AREA	21756.15 70	021631.00
	SIC,SEN	1310.00 80	021631.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021632.00
	LC,\$X6,XCSZ5-REFILL INTO COUNT FIELD	21756.14 50	021632.40
	KC,\$X6,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.55 90	021633.00
	SIC,SEN	1310.00 80	021633.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021634.00
	LX,\$X6,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.14 10	021634.40
	NOP,0	0.30 00	021635.00
	NOP,0	0.30 00	021635.40
XCS5H	KV,\$X7,XCSZ13-TEST X7 FOR RIGHT PATTERN	21764.16 90	021636.00
	SIC,SEN	1310.00 80	021636.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021637.00
	KC,\$X7,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.17 90	021637.40
	SIC,SEN	1310.00 80	021640.00
	BZXE,SERS	1304.32 C0	021640.40
	SR,\$X7,XCSZ5-REFILL FLD TO WORK AREA	21756.17 70	021641.00
	SIC,SEN	1310.00 80	021641.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021642.00
	LC,\$X7,XCSZ5-REFILL INTO COUNT FIELD	21756.16 50	021642.40
	KC,\$X7,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.57 90	021643.00
	SIC,SEN	1310.00 80	021643.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021644.00
	LX,\$X7,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.16 10	021644.40
	NOP,0	0.30 00	021645.00
	NOP,0	0.30 00	021645.40
XCS5J	KV,\$X8,XCSZ13-TEST X8 FOR RIGHT PATTERN	21764.20 90	021646.00
	SIC,SEN	1310.00 80	021646.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021647.00
	KC,\$X8,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.21 90	021647.40
	SIC,SEN	1310.00 80	021650.00
	BZXE,SERS	1304.32 C0	021650.40
	SR,\$X8,XCSZ5-REFILL TO WORK AREA	21756.21 70	021651.00
	SIC,SEN	1310.00 80	021651.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021652.00
	LC,\$X8,XCSZ5-REFILL INTO COUNT FIELD	21756.20 50	021652.40
	KC,\$X8,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.61 90	021653.00
	SIC,SEN	1310.00 80	021653.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021654.00
	LX,\$X8,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.20 10	021654.40
	NOP,0	0.30 00	021655.00
	NOP,0	0.30 00	021655.40
XCS5K	KV,\$X9,XCSZ13-TEST X9 FOR RIGHT PATTERN	21764.22 90	021656.00
	SIC,SEN	1310.00 80	021656.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021657.00

	KC,\$X9,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.23 90	021657.40
	SIC,SEN	1310.00 80	021660.00
	BZXE,SERS	1304.32 C0	021660.40
	SR,\$X9,XCSZ5-REFILL FLD TO WORK AREA	21756.23 70	021661.00
	SIC,SEN	1310.00 80	021661.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021662.00
	LC,\$X9,XCSZ5-REFILL INTO COUNT FIELD	21756.22 50	021662.40
	KC,\$X9,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.63 90	021663.00
	SIC,SEN	1310.00 80	021663.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021664.00
	LX,\$X9,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.22 10	021664.40
	NOP,0	0.30 00	021665.00
	NOP,0	0.30 00	021665.40
XCS5L	KV,\$X10,XCSZ13-TEST X10 FOR RIGHT PATTERN	21764.24 90	021666.00
	SIC,SEN	1310.00 80	021666.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021667.00
	KC,\$X10,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.25 90	021667.40
	SIC,SEN	1310.00 80	021670.00
	BZXE,SERS	1304.32 C0	021670.40
	SR,\$X10,XCSZ5-REFILL TO WORK AREA	21756.25 70	021671.00
	SIC,SEN	1310.00 80	021671.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021672.00
	LC,\$X10,XCSZ5-REFILL INTO COUNT FIELD	21756.24 50	021672.40
	KC,\$X10,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.65 90	021673.00
	SIC,SEN	1310.00 80	021673.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021674.00
	LX,\$X10,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.24 10	021674.40
	NOP,0	0.30 00	021675.00
	NOP,0	0.30 00	021675.40
XCS5M	KV,\$X11,XCSZ13-TEST X11 FOR RIGHT PATTERN	21764.26 90	021676.00
	SIC,SEN	1310.00 80	021676.40
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021677.00
	KC,\$X11,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.27 90	021677.40
	SIC,SEN	1310.00 80	021700.00
	BZXE,SERS	1304.32 C0	021700.40
	SR,\$X11,XCSZ5-REFILL FLD TO WORK AREA	21756.27 70	021701.00
	SIC,SEN	1310.00 80	021701.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021702.00
	LC,\$X11,XCSZ5-REFILL INTO COUNT FIELD	21756.26 50	021702.40
	KC,\$X11,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.67 90	021703.00
	SIC,SEN	1310.00 80	021703.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021704.00
	LX,\$X11,XCSZ13-RESTORE IX REG TO INITIAL COND.	21764.26 10	021704.40
	NOP,0	0.30 00	021705.00
	NOP,0	0.30 00	021705.40
XCS5N	KV,\$X12,XCSZ13-TEST X12 FOR RIGHT PATTERN	21764.30 90	021706.00
	SIC,SEN	1310.00 80	021706.40
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.	1304.32 C4	021707.00
	KC,\$X12,XCSZ18-28-45 MUST BE SAME TO CONT.	21767.31 90	021707.40
	SIC,SEN	1310.00 80	021710.00
	BZXE,SERS	1304.32 C0	021710.40
	SR,\$X12,XCSZ5-REFILL TO WORK AREA	21756.31 70	021711.00
	SIC,SEN	1310.00 80	021711.40
	BXF,SERS-XF NOT 0 AS IT SHD BE %25	1304.23 42	021712.00
	LC,\$X12,XCSZ5-REFILL INTO COUNT FIELD	21756.30 50	021712.40
	KC,\$X12,XCSZ19-ACTUALLY 46-63 FROM REFILL	21767.71 90	021713.00
	SIC,SEN	1310.00 80	021713.40
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.	1304.32 C0	021714.00
	LX,\$X12,XCSZ13-RESTORE IX REG TO INITIAL COND	21764.30 10	021714.40
	NOP,0	0.30 00	021715.00
	NOP,0	0.30 00	021715.40
XCS5P	LX,\$X13,XCSZ13 -REINITIALIZE X13	21764.32 10	021716.00
	KV,\$X13,XCSZ13 -TEST X13 FOR RIGHT PATTERN	21764.32 90	021716.40
	SIC,SEN	1310.00 80	021717.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.	1304.32 C4	021717.40
	KC,\$X13,XCSZ18-28-45 MUST BE IDENT. TO CONT	21767.33 90	021720.00

	SIC,SEN		1310.00 80	021720.40
	BZXE,SERS		1304.32 C0	021721.00
	SR,\$X13,XCSZ5-REFILL FLD TO WORK AREA		21756.33 70	021721.40
	SIC,SEN		1310.00 80	021722.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25		1304.23 42	021722.40
	LC,\$X13,XCSZ5-REFILL INTO COUNT FIELD		21756.32 50	021723.00
	KC,\$X13,XCSZ19-ACTUALLY 46-63 FROM REFILL		21767.73 90	021723.40
	SIC,SEN		1310.00 80	021724.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021724.40
	LX,\$X13,XCSZ13-RESTORE IX REG TO INITIAL COND.		21764.32 10	021725.00
	NOP,0		0.30 00	021725.40
	NOP,0		0.30 00	021726.00
XCS5Q	KV,\$X14,XCSZ13-TEST X14 FOR RIGHT PATTERN		21764.34 90	021726.40
	SIC,SEN		1310.00 80	021727.00
	BZXEZ,SERS-0-24 MUST BE SAME TO CONT.		1304.32 C4	021727.40
	KC,\$X14,XCSZ18-28-45 MUST BE SAME TO CONT.		21767.35 90	021730.00
	SIC,SEN		1310.00 80	021730.40
	BZXE,SERS		1304.32 C0	021731.00
	SR,\$X14,XCSZ5-REFILL TO WORK AREA		21756.35 70	021731.40
	SIC,SEN		1310.00 80	021732.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25		1304.23 42	021732.40
	LC,\$X14,XCSZ5-REFILL INTO COUNT FIELD		21756.34 50	021733.00
	KC,\$X14,XCSZ19-ACTUALLY 46-63 FROM REFILL		21767.75 90	021733.40
	SIC,SEN		1310.00 80	021734.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021734.40
	LX,\$X14,XCSZ13-RESTORE IX REG TO INITIAL COND		21764.34 10	021735.00
	NOP,0		0.30 00	021735.40
	NOP,0		0.30 00	021736.00
XCS5R	KV,\$X15,XCSZ13-TEST X15 FOR RIGHT PATTERN		21764.36 90	021736.40
	SIC,SEN		1310.00 80	021737.00
	BZXEZ,SERS-0-24 MUST BE IDENTICAL TO CONT.		1304.32 C4	021737.40
	KC,\$X15,XCSZ18-28-45 MUST BE IDENT. TO CONT		21767.37 90	021740.00
	SIC,SEN		1310.00 80	021740.40
	BZXE,SERS		1304.32 C0	021741.00
	SR,\$X15,XCSZ5-REFILL FLD TO WORK AREA		21756.37 70	021741.40
	SIC,SEN		1310.00 80	021742.00
	BXF,SERS-XF NOT 0 AS IT SHD BE %25		1304.23 42	021742.40
	LC,\$X15,XCSZ5-REFILL INTO COUNT FIELD		21756.36 50	021743.00
	KC,\$X15,XCSZ19-ACTUALLY 45-63 FROM REFILL		21767.77 90	021743.40
	SIC,SEN		1310.00 80	021744.00
	BZXE,SERS-PATTERN MUST BE SAME TO CONT.		1304.32 C0	021744.40
	LX,\$X15,XCSZ13-RESTORE IX REG TO INITIAL COND.		21764.36 10	021745.00
	NOP,0		0.30 00	021745.40
	NOP,0		0.30 00	021746.00
	B,\$+1.0		21747.50 00	021746.40
	B,XCS5-LOOP IN ALL REG. C PATTERN TEST		21536.10 00	021747.00
	SIC,SEN0+.32		1311.40 80	021747.40
	B,SSW		1301.10 00	021750.00
	B,XCS6		22026.10 00	021750.40
	NOP,0		0.30 00	021751.00
XCSZ0	XW,0,0,0		0.00 00 000000.00 00	021752.00
XCSZ1	%8DD%BU,64,1,17777777777777777777-ALL ONES DATA WORD		17777777777777777777	021753.00
XCSZ2	%8DD%BU,64,1,00000000000000000000-ALL ZEROS DATA WORD		00000000000000000000	021754.00
XCSZ3	%8DD%BU,32,1,37777777600 -25 ONES IN A HALF WORD		37777777600	021755.00
XCSZ4	%8DD%BU,32,1,00000000000 -HALF WORD OF ZEROS		00000000000	021755.40
XCSZ5	%8DD%BU,32,8,00000000000 -HALF WD WORK AREA		00000000000	021756.00
XCSZ6	%8DD%BU,32,8,00000000000 -HALF WD WORK AREA		00000000000	021756.40
XCSZ7	%8DD%BU,32,8,00000040060 -INSTR HALF WD, LV, 1		00000040060	021757.00
	%8DD%BU,32,8,37777754000 -INSTR HALF WD,NOP,-1		37777754000	021757.40
XCSZ8	VF,XCSZ7		21757.00+	021760.00
XCSZ9	VF,XCSZ8		21760.00+	021760.40
XCSZ10	SIC,SEN		1310.00 80	021761.00
	B,SERS		1304.10 00	021761.40
XCSZ11	%8DD%BU,64,1,0741703607417036074170-PATTERN ALL P ARE 0 %A		0741703607417036074170	021762.00
XCSZ12	%8DD%BU,64,1,0314631463143416714673-LEFT P ARE 1, RT P ARE 0		0314631463143416714673	021763.00
XCSZ13	%8DD%BU,64,1,0146334633546314631463-LEFT P ARE 0, RT P ARE 1		0146334633546314631463	021764.00

XCSZ14	%8DD%BU,32,8,20741700000	-COUNT COMP FIELD	20741700000	021765.00
XCSZ15	%8DD%BU,32,8,03607400000	-%A REFILL COMP FIELD	03607400000	021765.40
XCSZ16	%8DD%BU,32,8,06160700000	-%B COUNT COMP FIELD	06160700000	021766.00
XCSZ17	%8DD%BU,32,8,34633540000	-%B REFILL COMP FIELD	34633540000	021766.40
XCSZ18	%8DD%BU,32,8,26314600000	-%C COUNT COMP FIELD	26314600000	021767.00
XCSZ19	%8DD%BU,32,8,31463140000	-%C REFILL COMP FIELD	31463140000	021767.40
XCSZ20	%8DD%BU,64,8,12525252525252525252	-PATTERN D 10101 XF IS	12525252525252525252	021770.00
XCSZ21	%8DD%BU,64,8,05252525252525252525	-PATTERN E 01010 XD IS	05252525252525252525	021771.00
XCSZ22	%8DD%BU,32,8,25252525200	-COMP HALF WD 101010	25252525200	021772.00
XCSZ23	%8DD%BU,32,8,12525252400	-COMP HALF WD 01010	12525252400	021772.40
XCSZ24	%8DD%BU,64,8,000000000000000040024	-ZERO WD WITH SYNCH BIT	000000000000000040024	021773.00
XCSZ25	%8DD%BU,64,8,100000000000000000000	-NON SYNCH ZERO WD	100000000000000000000	021774.00
XCSZ26	%8DD%BU,64,8,077777777777777777777	-SYNCH ONES WD	077777777777777777777	021775.00
XCSZ27	%8DD%BU,32,8,17777777600	-COMP FLD SYNCH ONES WD	17777777600	021776.00
XCSZ28	%8DD%BU,32,8,00000000000	-ZERO HALF WORD	00000000000	021776.40
XCSZ29	%8DD%BU,64,8,000000000000000000377	-EIGHT ONES	000000000000000000377	021777.00
XCSZ30	%8DD%BU,64,8,0000000000000000177777	-SIXTEEN ONES	0000000000000000177777	022000.00
XCSZ31	%8DD%BU,64,8,0000000000000077777777	-TWENTY-FOUR ONES	0000000000000077777777	022001.00
XCSZ32	%8DD%BU,64,8,000000000037777777777	-THIRTY-TWO ONES	000000000037777777777	022002.00
XCSZ33	%8DD%BU,64,8,000000017777777777777	-FORTY ONES	000000017777777777777	022003.00
XCSZ34	%8DD%BU,64,8,777 777 777 777 777 7		000000777777777777777	022004.00
XCSZ35	%8DD%BU,64,8,000377777777777777777	-FIFTY-SIX ONES	000377777777777777777	022005.00
XCSZ36	%8DD%BU,32,8,00017740000	-EIGHT ONES	00017740000	022006.00
XCSZ37	%8DD%BU,32,8,07777740000	-SIXTEEN ONES	07777740000	022006.40
XCSZ38	%8DD%BU,32,8,37777740000	-EIGHTEEN ONES	37777740000	022007.00
XCSZ39	%8DD%BU,32,8,00003740000	-SIX ONES	00003740000	022007.40
XCSZ40	%8DD%BU,32,8,01777740000	-FOURTEEN ONES	01777740000	022010.00
XCSZ41	%8DD%BU,32,8,00000000200	-ONE ONE	00000000200	022010.40
XCSZ42	%8DD%BU,32,8,00000177600	-NINE ONES	00000177600	022011.00
XCSZ43	%8DD%BU,32,8,00077777600	-SEVENTEEN ONES	00077777600	022011.40
XCSZA1	%8DD%BU,64,8,1637476371757637476163	-XTK-A,XF0	1637476371757637476163	022012.00
XCSZA2	%8DD%BU,32,8,36771740000	-XTK-A C COMP FLD	36771740000	022013.00
XCSZA3	%8DD%BU,32,8,23707140000	-XTK-A R COMP FLD	23707140000	022013.40
XCSZB1	%8DD%BU,64,8,1371763717174771747637	-XTK-B,XF1	1371763717174771747637	022014.00
XCSZB2	%8DD%BU,32,8,07637440000	-XTK-B C COMP FLD	07637440000	022015.00
XCSZB3	%8DD%BU,32,8,36371740000	-XTK-B R COMP FLD	36371740000	022015.40
XCSZC1	%8DD%BU,64,8,0727757275753767737172	-XTK-C,XF1	0727757275753767737172	022016.00
XCSZC2	%8DD%BU,32,8,36577340000	-XTK-C C COMP FLD	36577340000	022017.00
XCSZC3	%8DD%BU,32,8,35747500000	-XTK-C R COMPFLD	35747500000	022017.40
XCSZD1	%8DD%BU,64,8,1575377765537577365737	-XTK-D,XF1	1575377765537577365737	022020.00
XCSZD2	%8DD%BU,32,8,25767740000	-XTK-D C COMP FLD	25767740000	022021.00
XCSZD3	%8DD%BU,32,8,17275740000	-XTK-D R COMP FLD	17275740000	022021.40
XCSZE1	%8DD%BU,64,8,1776774777677376772757	-XTK-E,XF1	1776774777677376772757	022022.00
XCSZE2	%8DD%BU,32,8,33757700000	-XTK-E C COMP FLD	33757700000	022023.00
XCSZE3	%8DD%BU,32,8,37536740000	-XTK-E R COMP FLD	37536740000	022023.40
XCSZF1	%8DD%BU,64,8,1757737532767757517775	-XTK-F,XF0	1757737532767757517775	022024.00
XCSZF2	%8DD%BU,32,8,37376740000	-XTK-F C COMP FLD	37376740000	022025.00
XCSZF3	%8DD%BU,32,8,24777640000	-XTK-F R COMP FLD	24777640000	022025.40
XCS6	LX,\$X0,XCSZ1		21753.00 10	022026.00
	LX,\$X1,XCSZ1	-INITIALIZE BY SETTING	21753.02 10	022026.40
	LX,\$X2,XCSZ1		21753.04 10	022027.00
	LX,\$X3,XCSZ1	-ALL IX REGS TO ONES.	21753.06 10	022027.40
	LX,\$X4,XCSZ1		21753.10 10	022030.00
	LX,\$X5,XCSZ1	-PREPARE FOR	21753.12 10	022030.40
	LX,\$X6,XCSZ1		21753.14 10	022031.00
	LX,\$X7,XCSZ1	-HIGH ZERO TEST	21753.16 10	022031.40
	LX,\$X8,XCSZ1		21753.20 10	022032.00
	LX,\$X9,XCSZ1	-%UNDISTURBED	21753.22 10	022032.40
	LX,\$X10,XCSZ1		21753.24 10	022033.00
	LX,\$X11,XCSZ1		21753.26 10	022033.40
	LX,\$X12,XCSZ1		21753.30 10	022034.00
	LX,\$X13,XCSZ1		21753.32 10	022034.40
	LX,\$X14,XCSZ1		21753.34 10	022035.00
	LX,\$X15,XCSZ1		21753.36 10	022035.40
XCS6A	LX,\$X0,XCSZ1		21753.00 10	022036.00
	LX,\$X0,XCSZ1	-LOAD WITH ONES N TIMES %0	21753.00 10	022036.40

	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022037.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022037.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022040.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022040.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022041.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022041.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022042.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022042.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022043.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022043.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022044.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022044.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022045.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022045.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022046.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022046.40	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022047.00	
	LX,\$X0,XCSZ1	LX,\$X0,XCSZ1	21753.00	10	022047.40	
XCS6A1	LX,\$X0,XCSZ2		-LOAD ONCE WITH ZEROS	21754.00	10	022050.00
	KV,\$X0,XCSZ2		-TEST BITS 0-24	21754.00	90	022050.40
	SIC,SEN			1310.00	80	022051.00
		BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32	C4	022051.40
	KC,\$X0,XCSZ2		-TEST BITS 28-45	21754.01	90	022052.00
	SIC,SEN			1310.00	80	022052.40
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32	C0	022053.00
	SR,\$X0,XCSZ5		-REFILL TO WORK AREA	21756.01	70	022053.40
	SIC,SEN			1310.00	80	022054.00
		BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23	42	022054.40
	LC,\$X0,XCSZ5		-REFILL INTO COUNT FIELD	21756.00	50	022055.00
	KC,\$X0,XCSZ2		-TEST BITS 46-63	21754.01	90	022055.40
	SIC,SEN			1310.00	80	022056.00
		BZXE,SERS	-ERR IF BIT PICKED UP	1304.32	C0	022056.40
	LX,\$X0,XCSZ1			21753.00	10	022057.00
		NOP	-RESTORE IX REG.	0.30	00	022057.40
	NOP,0			0.30	00	022060.00
XCS6B	LX,\$X1,XCSZ1			21753.02	10	022060.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1	-LOAD WITH ONES N TIMES %1□	21753.02	10	022061.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022061.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022062.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022062.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022063.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022063.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022064.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022064.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022065.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022065.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022066.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022066.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022067.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022067.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022070.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022070.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022071.00
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022071.40
	LX,\$X1,XCSZ1	LX,\$X1,XCSZ1		21753.02	10	022072.00
XCS6B1	LX,\$X1,XCSZ2		-LOAD ONCE WITH ZEROS	21754.02	10	022072.40
	KV,\$X1,XCSZ2		-TEST BITS 0-24	21754.02	90	022073.00
	SIC,SEN			1310.00	80	022073.40
		BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32	C4	022074.00
	KC,\$X1,XCSZ2		-TEST BITS 28-45	21754.03	90	022074.40
	SIC,SEN			1310.00	80	022075.00
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32	C0	022075.40
	SR,\$X1,XCSZ5		-REFILL TO WORK AREA	21756.03	70	022076.00
	SIC,SEN			1310.00	80	022076.40
		BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23	42	022077.00
	LC,\$X1,XCSZ5		-REFILL INTO COUNT FIELD	21756.02	50	022077.40

	KC,\$X1,XCSZ2	-TEST BITS 46-63	21754.03 90	022100.90
	SIC,SEN		1310.00 80	022100.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022101.00
	LX,\$X1,XCSZ1		21753.02 10	022101.40
	NOP	-RESTORE IX REG.	0.30 00	022102.00
XCS6C	LX,\$X2,XCSZ1		0.30 00	022102.40
	LX,\$X2,XCSZ1	-LOAD WITH ONES N TIMES %2□	21753.04 10	022103.00
	LX,\$X2,XCSZ1		21753.04 10	022103.40
	LX,\$X2,XCSZ1		21753.04 10	022104.00
	LX,\$X2,XCSZ1		21753.04 10	022104.40
	LX,\$X2,XCSZ1		21753.04 10	022105.00
	LX,\$X2,XCSZ1		21753.04 10	022105.40
	LX,\$X2,XCSZ1		21753.04 10	022106.00
	LX,\$X2,XCSZ1		21753.04 10	022106.40
	LX,\$X2,XCSZ1		21753.04 10	022107.00
	LX,\$X2,XCSZ1		21753.04 10	022107.40
	LX,\$X2,XCSZ1		21753.04 10	022110.00
	LX,\$X2,XCSZ1		21753.04 10	022110.40
	LX,\$X2,XCSZ1		21753.04 10	022111.00
	LX,\$X2,XCSZ1		21753.04 10	022111.40
	LX,\$X2,XCSZ1		21753.04 10	022112.00
	LX,\$X2,XCSZ1		21753.04 10	022112.40
	LX,\$X2,XCSZ1		21753.04 10	022113.00
	LX,\$X2,XCSZ1		21753.04 10	022113.40
	LX,\$X2,XCSZ1		21753.04 10	022114.00
	LX,\$X2,XCSZ1		21753.04 10	022114.40
XCS6C1	LX,\$X2,XCSZ2	-LOAD ONCE WITH ZEROS	21754.04 10	022115.00
	KV,\$X2,XCSZ2	-TEST BITS 0-24	21754.04 90	022115.40
	SIC,SEN		1310.00 80	022116.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022116.40
	KC,\$X2,XCSZ2	-TEST BITS 28-45	21754.05 90	022117.00
	SIC,SEN		1310.00 80	022117.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	022120.00
	SR,\$X2,XCSZ5	-REFILL TO WORK AREA	21756.05 70	022120.40
	SIC,SEN		1310.00 80	022121.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022121.40
	LC,\$X2,XCSZ5	-REFILL INTO COUNT FIELD	21756.04 50	022122.00
	KC,\$X2,XCSZ2	-TEST BITS 46-63	21754.05 90	022122.40
	SIC,SEN		1310.00 80	022123.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022123.40
	LX,\$X2,XCSZ1		21753.04 10	022124.00
	NOP	-RESTORE IX REG.	0.30 00	022124.40
XCS6D	LX,\$X3,XCSZ1		0.30 00	022125.00
	LX,\$X3,XCSZ1	-LOAD WITH ONES N TIMES %3□	21753.06 10	022125.40
	LX,\$X3,XCSZ1		21753.06 10	022126.00
	LX,\$X3,XCSZ1		21753.06 10	022126.40
	LX,\$X3,XCSZ1		21753.06 10	022127.00
	LX,\$X3,XCSZ1		21753.06 10	022127.40
	LX,\$X3,XCSZ1		21753.06 10	022130.00
	LX,\$X3,XCSZ1		21753.06 10	022130.40
	LX,\$X3,XCSZ1		21753.06 10	022131.00
	LX,\$X3,XCSZ1		21753.06 10	022131.40
	LX,\$X3,XCSZ1		21753.06 10	022132.00
	LX,\$X3,XCSZ1		21753.06 10	022132.40
	LX,\$X3,XCSZ1		21753.06 10	022133.00
	LX,\$X3,XCSZ1		21753.06 10	022133.40
	LX,\$X3,XCSZ1		21753.06 10	022134.00
	LX,\$X3,XCSZ1		21753.06 10	022134.40
	LX,\$X3,XCSZ1		21753.06 10	022135.00
	LX,\$X3,XCSZ1		21753.06 10	022135.40
	LX,\$X3,XCSZ1		21753.06 10	022136.00
	LX,\$X3,XCSZ1		21753.06 10	022136.40
	LX,\$X3,XCSZ1		21753.06 10	022137.00
XCS6D1	LX,\$X3,XCSZ2	-LOAD ONCE WITH ZEROS	21754.06 10	022137.40
	KV,\$X3,XCSZ2	-TEST BITS 0-24	21754.06 90	022140.00
	SIC,SEN		1310.00 80	022140.40

	BZXEZ,SERS		-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022141.00
	KC,\$X3,XCSZ2		-TEST BITS 28-45	21754.07 90	022141.40
	SIC,SEN			1310.00 80	022142.00
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	022142.40
	SR,\$X3,XCSZ5		-REFILL TO WORK AREA	21756.07 70	022143.00
	SIC,SEN			1310.00 80	022143.40
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022144.00
	LC,\$X3,XCSZ5		-REFILL INTO COUNT FIELD	21756.06 50	022144.40
	KC,\$X3,XCSZ2		-TEST BITS 46-63	21754.07 90	022145.00
	SIC,SEN			1310.00 80	022145.40
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	022146.00
	LX,\$X3,XCSZ1			21753.06 10	022146.40
	NOP		-RESTORE IX REG.	0.30 00	022147.00
	NOP,0			0.30 00	022147.40
XCS6E	LX,\$X4,XCSZ1			21753.10 10	022150.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1	-LOAD WITH ONES N TIMES %4	21753.10 10	022150.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022151.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022151.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022152.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022152.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022153.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022153.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022154.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022154.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022155.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022155.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022156.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022156.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022157.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022157.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022160.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022160.40
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022161.00
	LX,\$X4,XCSZ1	LX,\$X4,XCSZ1		21753.10 10	022161.40
	LX,\$X4,XCSZ2		-LOAD ONCE WITH ZEROS	21754.10 10	022162.00
XCS6E1	KV,\$X4,XCSZ2		-TEST BITS 0-24	21754.10 90	022162.40
	SIC,SEN			1310.00 80	022163.00
	BZXEZ,SERS		-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022163.40
	KC,\$X4,XCSZ2		-TEST BITS 28-45	21754.11 90	022164.00
	SIC,SEN			1310.00 80	022164.40
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	022165.00
	SR,\$X4,XCSZ5		-REFILL TO WORK AREA	21756.11 70	022165.40
	SIC,SEN			1310.00 80	022166.00
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022166.40
	LC,\$X4,XCSZ5		-REFILL INTO COUNT FIELD	21756.10 50	022167.00
	KC,\$X4,XCSZ2		-TEST BITS 46-63	21754.11 90	022167.40
	SIC,SEN			1310.00 80	022170.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	022170.40
	LX,\$X4,XCSZ1			21753.10 10	022171.00
	NOP		-RESTORE IX REG.	0.30 00	022171.40
	NOP,0			0.30 00	022172.00
XCS6F	LX,\$X5,XCSZ1			21753.12 10	022172.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1	-LOAD WITH ONES N TIMES %5	21753.12 10	022173.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022173.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022174.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022174.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022175.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022175.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022176.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022176.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022177.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022177.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022200.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022200.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022201.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1		21753.12 10	022201.40

	LX,\$X5,XCSZ1		21753.12 10	022202.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1	21753.12 10	022202.40
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1	21753.12 10	022203.00
	LX,\$X5,XCSZ1	LX,\$X5,XCSZ1	21753.12 10	022203.40
	LX,\$X5,XCSZ2		21753.12 10	022204.00
XCS6F1	KV,\$X5,XCSZ2		21754.12 10	022204.40
	SIC,SEN		21754.12 90	022205.00
	BZXEZ,SERS		1310.00 80	022205.40
	KC,\$X5,XCSZ2		1304.32 C4	022206.00
	SIC,SEN		21754.13 90	022206.40
	BZXE,SERS		1310.00 80	022207.00
	SR,\$X5,XCSZ5		1304.32 C0	022207.40
	SIC,SEN		21756.13 70	022210.00
	BXF,SERS		1310.00 80	022210.40
	LC,\$X5,XCSZ5		1304.23 42	022211.00
	KC,\$X5,XCSZ2		21756.12 50	022211.40
	SIC,SEN		21754.13 90	022212.00
	BZXE,SERS		1310.00 80	022212.40
	LX,\$X5,XCSZ1		1304.32 C0	022213.00
	NOP		21753.12 10	022213.40
	NOP,0		0.30 00	022214.00
XCS6G	LX,\$X6,XCSZ1		0.30 00	022214.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022215.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022215.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022216.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022216.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022217.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022217.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022220.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022220.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022221.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022221.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022222.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022222.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022223.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022223.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022224.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022224.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022225.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022225.40
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022226.00
	LX,\$X6,XCSZ1	LX,\$X6,XCSZ1	21753.14 10	022226.40
XCS6G1	LX,\$X6,XCSZ2		21754.14 10	022227.00
	KV,\$X6,XCSZ2		21754.14 90	022227.40
	SIC,SEN		1310.00 80	022230.00
	BZXEZ,SERS		1304.32 C4	022230.40
	KC,\$X6,XCSZ2		21754.15 90	022231.00
	SIC,SEN		1310.00 80	022231.40
	BZXE,SERS		1304.32 C0	022232.00
	SR,\$X6,XCSZ5		21756.15 70	022232.40
	SIC,SEN		1310.00 80	022233.00
	BXF,SERS		1304.23 42	022233.40
	LC,\$X6,XCSZ5		21756.14 50	022234.00
	KC,\$X6,XCSZ2		21754.15 90	022234.40
	SIC,SEN		1310.00 80	022235.00
	BZXE,SERS		1304.32 C0	022235.40
	LX,\$X6,XCSZ1		21753.14 10	022236.00
	NOP		0.30 00	022236.40
	NOP,0		0.30 00	022237.00
XCS6H	LX,\$X7,XCSZ1		21753.16 10	022237.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022240.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022240.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022241.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022241.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022242.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022242.40

	LX,\$X7,XCSZ1		21753.16 10	022243.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022244.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022244.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022245.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022245.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022246.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022246.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022247.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022247.40
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022250.00
	LX,\$X7,XCSZ1	LX,\$X7,XCSZ1	21753.16 10	022250.40
	LX,\$X7,XCSZ2		21753.16 10	022251.00
XCS6H1	KV,\$X7,XCSZ2		21754.16 10	022251.40
	SIC,SEN		21754.16 90	022252.00
	BZXEZ,SERS		1310.00 80	022252.40
	KC,\$X7,XCSZ2		1304.32 C4	022253.00
	SIC,SEN		21754.17 90	022253.40
	BZXE,SERS		1310.00 80	022254.00
	SR,\$X7,XCSZ5		1304.32 C0	022254.40
	SIC,SEN		21756.17 70	022255.00
	BXF,SERS		1310.00 80	022255.40
	LC,\$X7,XCSZ5		1304.23 42	022256.00
	KC,\$X7,XCSZ2		21756.16 50	022256.40
	SIC,SEN		21754.17 90	022257.00
	BZXE,SERS		1310.00 80	022257.40
	LX,\$X7,XCSZ1		1304.32 C0	022260.00
	NOP		21753.16 10	022260.40
	NOP,0		0.30 00	022261.00
XCS6J	LX,\$X8,XCSZ1		0.30 00	022261.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022262.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022262.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022263.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022263.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022264.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022264.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022265.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022265.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022266.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022266.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022267.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022267.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022270.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022270.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022271.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022271.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022272.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022272.40
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022273.00
	LX,\$X8,XCSZ1	LX,\$X8,XCSZ1	21753.20 10	022273.40
	LX,\$X8,XCSZ2		21754.20 10	022274.00
XCS6J1	KV,\$X8,XCSZ2		21754.20 90	022274.40
	SIC,SEN		1310.00 80	022275.00
	BZXEZ,SERS		1304.32 C4	022275.40
	KC,\$X8,XCSZ2		21754.21 90	022276.00
	SIC,SEN		1310.00 80	022276.40
	BZXE,SERS		1304.32 C0	022277.00
	SR,\$X8,XCSZ5		21756.21 70	022277.40
	SIC,SEN		1310.00 80	022300.00
	BXF,SERS		1304.23 42	022300.40
	LC,\$X8,XCSZ5		21756.20 50	022301.00
	KC,\$X8,XCSZ2		21754.21 90	022301.40
	SIC,SEN		1310.00 80	022302.00
	BZXE,SERS		1304.32 C0	022302.40
	LX,\$X8,XCSZ1		21753.20 10	022303.00
	NOP		0.30 00	022303.40

	NOP,0		0.30 00	022304.00
XCS6K	LX,\$X9,XCSZ1		21753.22 10	022304.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022305.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022305.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022306.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022306.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022307.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022307.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022310.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022310.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022311.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022311.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022312.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022312.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022313.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022313.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022314.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022314.40
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022315.00
	LX,\$X9,XCSZ1	LX,\$X9,XCSZ1	21753.22 10	022315.40
	LX,\$X9,XCSZ2		21753.22 10	022316.00
XCS6K1	KV,\$X9,XCSZ2	-LOAD ONCE WITH ZEROS	21754.22 10	022316.40
	SIC,SEN	-TEST BITS 0-24	21754.22 90	022317.00
	BZXEZ,SERS		1310.00 80	022317.40
	KC,\$X9,XCSZ2	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022320.00
	SIC,SEN	-TEST BITS 28-45	21754.23 90	022320.40
	BZXE,SERS		1310.00 80	022321.00
	SR,\$X9,XCSZ5	-ERR IF IX BITS DONT COMPARE	1304.32 C0	022321.40
	SIC,SEN	-REFILL TO WORK AREA	21756.23 70	022322.00
	BXF,SERS		1310.00 80	022322.40
	LC,\$X9,XCSZ5	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022323.00
	KC,\$X9,XCSZ2	-REFILL INTO COUNT FIELD	21756.22 50	022323.40
	SIC,SEN	-TEST BITS 46-63	21754.23 90	022324.00
	BZXE,SERS		1310.00 80	022324.40
	LX,\$X9,XCSZ1	-ERR IF BIT PICKED UP	1304.32 C0	022325.00
	NOP		21753.22 10	022325.40
	NOP,0	-RESTORE IX REG.	0.30 00	022326.00
XCS6L	LX,\$X10,XCSZ1		0.30 00	022326.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022327.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022327.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022330.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022330.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022331.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022331.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022332.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022332.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022333.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022333.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022334.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022334.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022335.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022335.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022336.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022336.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022337.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022337.40
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022340.00
	LX,\$X10,XCSZ1	LX,\$X10,XCSZ1	21753.24 10	022340.40
XCS6L1	LX,\$X10,XCSZ2	-LOAD ONCE WITH ZEROS	21754.24 10	022341.00
	KV,\$X10,XCSZ2	-TEST BITS 0-24	21754.24 90	022341.40
	SIC,SEN		1310.00 80	022342.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022342.40
	KC,\$X10,XCSZ2	-TEST BITS 28-45	21754.25 90	022343.00
	SIC,SEN		1310.00 80	022343.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	022344.00
	SR,\$X10,XCSZ5	-REFILL TO WORK AREA	21756.25 70	022344.40

	SIC,SEN			1310.00 80	022345.00
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022345.40
	LC,\$X10,XCSZ5		-REFILL INTO COUNT FIELD	21756.24 50	022346.00
	KC,\$X10,XCSZ2		-TEST BITS 46-63	21754.25 90	022346.40
	SIC,SEN			1310.00 80	022347.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	022347.40
	LX,\$X10,XCSZ1			21753.24 10	022350.00
	NOP		-RESTORE IX REG.	0.30 00	022350.40
	NOP,0			0.30 00	022351.00
XCS6M	LX,\$X11,XCSZ1			21753.26 10	022351.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1	-LOAD WITH ONES N TIMES %11□	21753.26 10	022352.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022352.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022353.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022353.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022354.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022354.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022355.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022355.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022356.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022356.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022357.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022357.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022360.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022360.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022361.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022361.40
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022362.00
	LX,\$X11,XCSZ1	LX,\$X11,XCSZ1		21753.26 10	022362.40
	LX,\$X11,XCSZ2		-LOAD ONCE WITH ZEROS	21754.26 10	022363.00
XCS6M1	KV,\$X11,XCSZ2		-TEST BITS 0-24	21754.26 90	022363.40
	SIC,SEN			1310.00 80	022364.00
	BZXEZ,SERS		-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022364.40
	KC,\$X11,XCSZ2		-TEST BITS 28-45	21754.27 90	022365.00
	SIC,SEN			1310.00 80	022365.40
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	022366.00
	SR,\$X11,XCSZ5		-REFILL TO WORK AREA	21756.27 70	022366.40
	SIC,SEN			1310.00 80	022367.00
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022367.40
	LC,\$X11,XCSZ5		-REFILL INTO COUNT FIELD	21756.26 50	022370.00
	KC,\$X11,XCSZ2		-TEST BITS 46-63	21754.27 90	022370.40
	SIC,SEN			1310.00 80	022371.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	022371.40
	LX,\$X11,XCSZ1			21753.26 10	022372.00
	NOP		-RESTORE IX REG.	0.30 00	022372.40
	NOP,0			0.30 00	022373.00
XCS6N	LX,\$X12,XCSZ1			21753.30 10	022373.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1	-LOAD WITH ONES N TIMES%12□	21753.30 10	022374.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022374.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022375.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022375.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022376.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022376.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022377.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022377.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022400.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022400.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022401.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022401.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022402.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022402.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022403.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022403.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022404.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022404.40
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022405.00
	LX,\$X12,XCSZ1	LX,\$X12,XCSZ1		21753.30 10	022405.40

		LX,\$X12,XCSZ1		21753.30 10	022406.00
		LX,\$X12,XCSZ2		21753.30 10	022406.40
XCS6N1		KV,\$X12,XCSZ2	-LOAD ONCE WITH ZEROS	21754.30 10	022407.00
		SIC,SEN	-TEST BITS 0-24	21754.30 90	022407.40
		BZXEZ,SERS		1310.00 80	022410.00
		KC,\$X12,XCSZ2	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022410.40
		SIC,SEN	-TEST BITS 28-45	21754.31 90	022411.00
		BZXE,SERS		1310.00 80	022411.40
		SR,\$X12,XCSZ5	-ERR IF IX BITS DONT COMPARE	1304.32 C0	022412.00
		SIC,SEN	-REFILL TO WORK AREA	21756.31 70	022412.40
		BXF,SERS		1310.00 80	022413.00
		LC,\$X12,XCSZ5	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022413.40
		KC,\$X12,XCSZ2	-REFILL INTO COUNT FIELD	21756.30 50	022414.00
		SIC,SEN	-TEST BITS 46-63	21754.31 90	022414.40
		BZXE,SERS		1310.00 80	022415.00
		LX,\$X12,XCSZ1	-ERR IF BIT PICKED UP	1304.32 C0	022415.40
		NOP		21753.30 10	022416.00
		NOP,0	-RESTORE IX REG.	0.30 00	022416.40
XCS6P		LX,\$X13,XCSZ1		0.30 00	022417.00
		LX,\$X13,XCSZ1		21753.32 10	022417.40
		LX,\$X13,XCSZ1	-LOAD WITH ONES N TIMES %13□	21753.32 10	022420.00
		LX,\$X13,XCSZ1		21753.32 10	022420.40
		LX,\$X13,XCSZ1		21753.32 10	022421.00
		LX,\$X13,XCSZ1		21753.32 10	022421.40
		LX,\$X13,XCSZ1		21753.32 10	022422.00
		LX,\$X13,XCSZ1		21753.32 10	022422.40
		LX,\$X13,XCSZ1		21753.32 10	022423.00
		LX,\$X13,XCSZ1		21753.32 10	022423.40
		LX,\$X13,XCSZ1		21753.32 10	022424.00
		LX,\$X13,XCSZ1		21753.32 10	022424.40
		LX,\$X13,XCSZ1		21753.32 10	022425.00
		LX,\$X13,XCSZ1		21753.32 10	022425.40
		LX,\$X13,XCSZ1		21753.32 10	022426.00
		LX,\$X13,XCSZ1		21753.32 10	022426.40
		LX,\$X13,XCSZ1		21753.32 10	022427.00
		LX,\$X13,XCSZ1		21753.32 10	022427.40
		LX,\$X13,XCSZ1		21753.32 10	022430.00
		LX,\$X13,XCSZ1		21753.32 10	022430.40
		LX,\$X13,XCSZ1		21753.32 10	022431.00
XCS6P1		LX,\$X13,XCSZ2	-LOAD ONCE WITH ZEROS	21754.32 10	022431.40
		KV,\$X13,XCSZ2	-TEST BITS 0-24	21754.32 90	022432.00
		SIC,SEN		1310.00 80	022432.40
		BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	022433.00
		KC,\$X13,XCSZ2	-TEST BITS 28-45	21754.33 90	022433.40
		SIC,SEN		1310.00 80	022434.00
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	022434.40
		SR,\$X13,XCSZ5	-REFILL TO WORK AREA	21756.33 70	022435.00
		SIC,SEN		1310.00 80	022435.40
		BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022436.00
		LC,\$X13,XCSZ5	-REFILL INTO COUNT FIELD	21756.32 50	022436.40
		KC,\$X13,XCSZ2	-TEST BITS 46-63	21754.33 90	022437.00
		SIC,SEN		1310.00 80	022437.40
		BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022440.00
		LX,\$X13,XCSZ1		21753.32 10	022440.40
		NOP	-RESTORE IX REG.	0.30 00	022441.00
		NOP,0		0.30 00	022441.40
XCS6Q		LX,\$X14,XCSZ1		21753.34 10	022442.00
		LX,\$X14,XCSZ1	-LOAD WITH ONES N TIMES %14□	21753.34 10	022442.40
		LX,\$X14,XCSZ1		21753.34 10	022443.00
		LX,\$X14,XCSZ1		21753.34 10	022443.40
		LX,\$X14,XCSZ1		21753.34 10	022444.00
		LX,\$X14,XCSZ1		21753.34 10	022444.40
		LX,\$X14,XCSZ1		21753.34 10	022445.00
		LX,\$X14,XCSZ1		21753.34 10	022445.40
		LX,\$X14,XCSZ1		21753.34 10	022446.00
		LX,\$X14,XCSZ1		21753.34 10	022446.40

	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022447.00
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022447.40
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022450.00
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022450.40
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022451.00
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022451.40
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022452.00
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022452.40
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022453.00
	LX,\$X14,XCSZ1	LX,\$X14,XCSZ1		21753.34	10	022453.40
XCS6Q1	LX,\$X14,XCSZ2		-LOAD ONCE WITH ZEROS	21754.34	10	022454.00
	KV,\$X14,XCSZ2		-TEST BITS 0-24	21754.34	90	022454.40
	SIC,SEN			1310.00	80	022455.00
		BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32	C4	022455.40
	KC,\$X14,XCSZ2		-TEST BITS 28-45	21754.35	90	022456.00
	SIC,SEN			1310.00	80	022456.40
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32	C0	022457.00
	SR,\$X14,XCSZ5		-REFILL TO WORK AREA	21756.35	70	022457.40
	SIC,SEN			1310.00	80	022460.00
		BXF,SERS	-ERR IF XF NOT AS IT SHOULD BE %	1304.23	42	022460.40
	LC,\$X14,XCSZ5		-REFILL INTO COUNT FIELD	21756.34	50	022461.00
	KC,\$X14,XCSZ2		-TEST BITS 46-63	21754.35	90	022461.40
	SIC,SEN			1310.00	80	022462.00
		BZXE,SERS	-ERR IF BIT PICKED UP	1304.32	C0	022462.40
	LX,\$X14,XCSZ1			21753.34	10	022463.00
		NOP	-RESTORE IX REG.	0.30	00	022463.40
XCS6R	NOP,0			0.30	00	022464.00
	LX,\$X15,XCSZ1			21753.36	10	022464.40
		LX,\$X15,XCSZ1	-LOAD WITH ONES N TIMES %15□	21753.36	10	022465.00
	LX,\$X15,XCSZ1			21753.36	10	022465.40
		LX,\$X15,XCSZ1		21753.36	10	022466.00
	LX,\$X15,XCSZ1			21753.36	10	022466.40
		LX,\$X15,XCSZ1		21753.36	10	022467.00
	LX,\$X15,XCSZ1			21753.36	10	022467.40
		LX,\$X15,XCSZ1		21753.36	10	022470.00
	LX,\$X15,XCSZ1			21753.36	10	022470.40
		LX,\$X15,XCSZ1		21753.36	10	022471.00
	LX,\$X15,XCSZ1			21753.36	10	022471.40
		LX,\$X15,XCSZ1		21753.36	10	022472.00
	LX,\$X15,XCSZ1			21753.36	10	022472.40
		LX,\$X15,XCSZ1		21753.36	10	022473.00
	LX,\$X15,XCSZ1			21753.36	10	022473.40
		LX,\$X15,XCSZ1		21753.36	10	022474.00
	LX,\$X15,XCSZ1			21753.36	10	022474.40
		LX,\$X15,XCSZ1		21753.36	10	022475.00
	LX,\$X15,XCSZ1			21753.36	10	022475.40
		LX,\$X15,XCSZ1		21753.36	10	022476.00
XCS6R1	LX,\$X15,XCSZ2		-LOAD ONCE WITH ZEROS	21754.36	10	022476.40
	KV,\$X15,XCSZ2		-TEST BITS 0-24	21754.36	90	022477.00
	SIC,SEN			1310.00	80	022477.40
		BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32	C4	022500.00
	KC,\$X15,XCSZ2		-TEST BITS 28-45	21754.37	90	022500.40
	SIC,SEN			1310.00	80	022501.00
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32	C0	022501.40
	SR,\$X15,XCSZ5		-REFILL TO WORK AREA	21756.37	70	022502.00
	SIC,SEN			1310.00	80	022502.40
		BXF,SERS	-ERR IF XF NOT AS IT SHOULD BE %	1304.23	42	022503.00
	LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36	50	022503.40
	KC,\$X15,XCSZ2		-TEST BITS 46-63	21754.37	90	022504.00
	SIC,SEN			1310.00	80	022504.40
		BZXE,SERS	-ERR IF BIT PICKED UP	1304.32	C0	022505.00
	LX,\$X15,XCSZ1			21753.36	10	022505.40
		NOP	-RESTORE IX REG.	0.30	00	022506.00
	NOP,0			0.30	00	022506.40
	B,\$+1.0			22510.10	00	022507.00
	B,XCS6-TO LOOP IN HIGH ZERO TEST			22026.10	00	022507.40

	SIC,SEN0+,32	B,SSW-TEST SENSE SWITCHES	1311.40 80	022510.00
XCS7	LX,\$X0,XCSZ1		1301.10 00	022510.40
	LX,\$X2,XCSZ1	LX,\$X1,XCSZ1-INITIALIZE BY	21753.00 10	022511.00
	LX,\$X4,XCSZ1	LX,\$X3,XCSZ1-SETTING ALL IX	21753.02 10	022511.40
	LX,\$X6,XCSZ1	LX,\$X5,XCSZ1-REGS TO ONES	21753.04 10	022512.00
	LX,\$X8,XCSZ1	LX,\$X7,XCSZ1-PREPARE FOR	21753.06 10	022512.40
	LX,\$X10,XCSZ1	LX,\$X9,XCSZ1-HIGH ZERO	21753.10 10	022513.00
	LX,\$X12,XCSZ1	LX,\$X11,XCSZ1-%DISTURBED□	21753.12 10	022513.40
	LX,\$X14,XCSZ1	LX,\$X13,XCSZ1-TEST	21753.14 10	022514.00
		LX,\$X15,XCSZ1	21753.16 10	022514.40
XCS7A	LX,\$X0,XCSZ2-LOAD WITH ZEROS ONE TIME %0□		21753.20 10	022515.00
	SX,\$X0,XCSZ5		21753.22 10	022515.40
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5-READ IX REG N TIMES	21753.24 10	022516.00
	SX,\$X0,XCSZ5		21753.26 10	022516.40
	SX,\$X0,XCSZ5		21753.30 10	022517.00
	SX,\$X0,XCSZ5		21753.32 10	022517.40
	SX,\$X0,XCSZ5		21753.34 10	022520.00
	SX,\$X0,XCSZ5		21753.36 10	022520.40
	SX,\$X0,XCSZ5		21754.00 10	022521.00
	SX,\$X0,XCSZ5		21756.01 10	022521.40
	SX,\$X0,XCSZ5		21756.01 10	022522.00
	SX,\$X0,XCSZ5		21756.01 10	022522.40
	SX,\$X0,XCSZ5		21756.01 10	022523.00
	SX,\$X0,XCSZ5		21756.01 10	022523.40
	SX,\$X0,XCSZ5		21756.01 10	022524.00
	SX,\$X0,XCSZ5		21756.01 10	022524.40
	SX,\$X0,XCSZ5		21756.01 10	022525.00
	SX,\$X0,XCSZ5		21756.01 10	022525.40
	SX,\$X0,XCSZ5		21756.01 10	022526.00
	SX,\$X0,XCSZ5		21756.01 10	022526.40
	SX,\$X0,XCSZ5		21756.01 10	022527.00
	SX,\$X0,XCSZ5		21756.01 10	022527.40
	SX,\$X0,XCSZ5		21756.01 10	022530.00
	SX,\$X0,XCSZ5		21756.01 10	022530.40
	SX,\$X0,XCSZ5		21756.01 10	022531.00
	SX,\$X0,XCSZ5		21756.01 10	022531.40
	SX,\$X0,XCSZ5		21756.01 10	022532.00
	SX,\$X0,XCSZ5		21756.01 10	022532.40
	SX,\$X0,XCSZ5		21756.01 10	022533.00
XCS7A1	KV,\$X0,XCSZ2-TEST BITS 0-24		21754.00 90	022533.40
	SIC,SEN		1310.00 80	022534.00
		BZXEZ,SERS-ERR IF BIT PICKED UP	1304.32 C4	022534.40
	KC,\$X0,XCSZ2-TEST BITS 28-45		21754.01 90	022535.00
	SIC,SEN		1310.00 80	022535.40
		BZXE,SERS-BITS MUST COMPARE	1304.32 C0	022536.00
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01 70	022536.40
	SIC,SEN		1310.00 80	022537.00
		BXF,SERS-ERR IF XF NOT 0 AS IT SHOULD BE %25□	1304.23 42	022537.40
	LC,\$X0,XCSZ5-REFILL INTO COUNT FIELD		21756.00 50	022540.00
	KC,\$X0,XCSZ2-TEST BITS 46-63		21754.01 90	022540.40
	SIC,SEN		1310.00 80	022541.00
		BZXE,SERS-ERR IF BIT PICKED UP	1304.32 C0	022541.40
	LX,\$X0,XCSZ1-RESTORE IX REG.		21753.00 10	022542.00
	NOP,0		0.30 00	022542.40
	NOP,0		0.30 00	022543.00
XCS7B	LX,\$X1,XCSZ2-LOAD WITH ZEROS ONE TIME %1□		21754.02 10	022543.40
	SX,\$X1,XCSZ5		21756.03 10	022544.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5-READ IX REG N TIMES	21756.03 10	022544.40
	SX,\$X1,XCSZ5		21756.03 10	022545.00
	SX,\$X1,XCSZ5		21756.03 10	022545.40
	SX,\$X1,XCSZ5		21756.03 10	022546.00
	SX,\$X1,XCSZ5		21756.03 10	022546.40
	SX,\$X1,XCSZ5		21756.03 10	022547.00
	SX,\$X1,XCSZ5		21756.03 10	022547.40
	SX,\$X1,XCSZ5		21756.03 10	022550.00
	SX,\$X1,XCSZ5		21756.03 10	022550.40

	SX,\$X1,XCSZ5		21756.03	10	022551.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022551.40
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022552.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022552.40
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022553.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022553.40
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022554.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022554.40
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022555.00
	SX,\$X1,XCSZ5	SX,\$X1,XCSZ5	21756.03	10	022555.40
XCS7B1	KV,\$X1,XCSZ2-TEST BITS 0-24		21754.02	90	022556.00
	SIC,SEN		1310.00	80	022556.40
		BZXEZ,SERS-ERR IF BIT PICKED UP	1304.32	C4	022557.00
	KC,\$X1,XCSZ2-TEST BITS 28-45		21754.03	90	022557.40
	SIC,SEN		1310.00	80	022560.00
		BZXE,SERS-BITS MUST COMPARE	1304.32	C0	022560.40
	SR,\$X1,XCSZ5-REFILL TO WORK AREA		21756.03	70	022561.00
	SIC,SEN		1310.00	80	022561.40
		BXF,SERS-ERR IF XF NOT 0 AS IT SHOULD BE %25	1304.23	42	022562.00
	LC,\$X1,XCSZ5-REFILL INTO COUNT FIELD		21756.02	50	022562.40
	KC,\$X1,XCSZ2-TEST BITS 46-63		21754.03	90	022563.00
	SIC,SEN		1310.00	80	022563.40
		BZXE,SERS-ERR IF BIT PICKED UP	1304.32	C0	022564.00
	LX,\$X1,XCSZ1-RESTORE IX REG.		21753.02	10	022564.40
	NOP,0		0.30	00	022565.00
	NOP,0		0.30	00	022565.40
XCS7C	LX,\$X2,XCSZ2	-LOAD WITH ZEROS ONE TIME %2	21754.04	10	022566.00
	SX,\$X2,XCSZ5		21756.05	10	022566.40
		SX,\$X2,XCSZ5 -READ IX REG N TIMES	21756.05	10	022567.00
	SX,\$X2,XCSZ5		21756.05	10	022567.40
		SX,\$X2,XCSZ5	21756.05	10	022570.00
	SX,\$X2,XCSZ5		21756.05	10	022570.40
		SX,\$X2,XCSZ5	21756.05	10	022571.00
	SX,\$X2,XCSZ5		21756.05	10	022571.40
		SX,\$X2,XCSZ5	21756.05	10	022572.00
	SX,\$X2,XCSZ5		21756.05	10	022572.40
		SX,\$X2,XCSZ5	21756.05	10	022573.00
	SX,\$X2,XCSZ5		21756.05	10	022573.40
		SX,\$X2,XCSZ5	21756.05	10	022574.00
	SX,\$X2,XCSZ5		21756.05	10	022574.40
		SX,\$X2,XCSZ5	21756.05	10	022575.00
	SX,\$X2,XCSZ5		21756.05	10	022575.40
		SX,\$X2,XCSZ5	21756.05	10	022576.00
	SX,\$X2,XCSZ5		21756.05	10	022576.40
		SX,\$X2,XCSZ5	21756.05	10	022577.00
	SX,\$X2,XCSZ5		21756.05	10	022577.40
XCS7C1	KV,\$X2,XCSZ2	-TEST BITS 0-24	21756.05	10	022600.00
	SIC,SEN		21754.04	90	022600.40
		BZXEZ,SERS -ERR IF BIT PICKED UP	1310.00	80	022601.00
			1304.32	C4	022601.40
	KC,\$X2,XCSZ2	-TEST BITS 28-45	21754.05	90	022602.00
	SIC,SEN		1310.00	80	022602.40
		BZXE,SERS -BITS MUST COMPARE	1304.32	C0	022603.00
	SR,\$X2,XCSZ5	-REFILL TO WORK AREA	21756.05	70	022603.40
	SIC,SEN		1310.00	80	022604.00
		BXF,SERS -ERR IF XF NOT 0 AS IT SHOULD BE	1304.23	42	022604.40
	LC,\$X2,XCSZ5	-REFILL INTO COUNT FIELD	21756.04	50	022605.00
	KC,\$X2,XCSZ2	-TEST BITS 46-63	21754.05	90	022605.40
	SIC,SEN		1310.00	80	022606.00
		BZXE,SERS -ERR IF BIT PICKED UP	1304.32	C0	022606.40
	LX,\$X2,XCSZ1	-RESTORE IX REG.	21753.04	10	022607.00
	NOP,0		0.30	00	022607.40
	NOP,0		0.30	00	022610.00
XCS7D	LX,\$X3,XCSZ2	-LOAD WITH ZEROS ONE TIME %3	21754.06	10	022610.40
	SX,\$X3,XCSZ5		21756.07	10	022611.00
		SX,\$X3,XCSZ5 -READ IX REG N TIMES	21756.07	10	022611.40

	SX,\$X3,XCSZ5		21756.07 10	022612.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022612.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022613.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022613.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022614.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022614.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022615.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022615.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022616.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022616.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022617.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022617.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022620.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022620.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022621.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022621.40
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022622.00
	SX,\$X3,XCSZ5	SX,\$X3,XCSZ5	21756.07 10	022622.40
XCS7D1	KV,\$X3,XCSZ2	-TEST BITS 0-24	21754.06 90	022623.00
	SIC,SEN		1310.00 80	022623.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP/LOST	1304.32 C4	022624.00
	KC,\$X3,XCSZ2	-TEST BITS 28-45	21754.07 90	022624.40
	SIC,SEN		1310.00 80	022625.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022625.40
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	022626.00
	SIC,SEN		1310.00 80	022626.40
	BXF,SERS	-ERR IF XF NOT AS IT SHOULD BE %	1304.23 42	022627.00
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	022627.40
	KC,\$X3,XCSZ2	-TEST BITS 46-63	21754.07 90	022630.00
	SIC,SEN		1310.00 80	022630.40
	BZXE,SERS	-ERR IF BIT PICKED UP LOST	1304.32 C0	022631.00
	LX,\$X3,XCSZ1	-RESTORE IX REG.	21753.06 10	022631.40
	NOP,0		0.30 00	022632.00
	NOP,0		0.30 00	022632.40
XCS7E	LX,\$X4,XCSZ2	-LOAD WITH ZEROS ONE TIME %4□	21754.10 10	022633.00
	SX,\$X4,XCSZ5		21756.11 10	022633.40
	SX,\$X4,XCSZ5	-READ IX REG N TIMES	21756.11 10	022634.00
	SX,\$X4,XCSZ5		21756.11 10	022634.40
	SX,\$X4,XCSZ5		21756.11 10	022635.00
	SX,\$X4,XCSZ5		21756.11 10	022635.40
	SX,\$X4,XCSZ5		21756.11 10	022636.00
	SX,\$X4,XCSZ5		21756.11 10	022636.40
	SX,\$X4,XCSZ5		21756.11 10	022637.00
	SX,\$X4,XCSZ5		21756.11 10	022637.40
	SX,\$X4,XCSZ5		21756.11 10	022640.00
	SX,\$X4,XCSZ5		21756.11 10	022640.40
	SX,\$X4,XCSZ5		21756.11 10	022641.00
	SX,\$X4,XCSZ5		21756.11 10	022641.40
	SX,\$X4,XCSZ5		21756.11 10	022642.00
	SX,\$X4,XCSZ5		21756.11 10	022642.40
	SX,\$X4,XCSZ5		21756.11 10	022643.00
	SX,\$X4,XCSZ5		21756.11 10	022643.40
	SX,\$X4,XCSZ5		21756.11 10	022644.00
	SX,\$X4,XCSZ5		21756.11 10	022644.40
XCS7E1	KV,\$X4,XCSZ2	-TEST BITS 0-24	21754.10 90	022645.00
	SIC,SEN		1310.00 80	022645.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	022646.00
	KC,\$X4,XCSZ2	-TEST BITS 28-45	21754.11 90	022646.40
	SIC,SEN		1310.00 80	022647.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022647.40
	SR,\$X4,XCSZ5	-REFILL TO WORK AREA	21756.11 70	022650.00
	SIC,SEN		1310.00 80	022650.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022651.00
	LC,\$X4,XCSZ5	-REFILL INTO COUNT FIELD	21756.10 50	022651.40
	KC,\$X4,XCSZ2	-TEST BITS 46-63	21754.11 90	022652.00
				022652.40

	SIC,SEN		1310.00 80	022653.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022653.40
	LX,\$X4,XCSZ1	-RESTORE IX REG.	21753.10 10	022654.00
	NOP,0		0.30 00	022654.40
	NOP,0		0.30 00	022655.00
XCS7F	LX,\$X5,XCSZ2	-LOAD WITH ZEROS ONE TIME %5	21754.12 10	022655.40
	SX,\$X5,XCSZ5		21756.13 10	022656.00
	SX,\$X5,XCSZ5	-READ IX REG N TIMES	21756.13 10	022656.40
	SX,\$X5,XCSZ5		21756.13 10	022657.00
	SX,\$X5,XCSZ5		21756.13 10	022657.40
	SX,\$X5,XCSZ5		21756.13 10	022660.00
	SX,\$X5,XCSZ5		21756.13 10	022660.40
	SX,\$X5,XCSZ5		21756.13 10	022661.00
	SX,\$X5,XCSZ5		21756.13 10	022661.40
	SX,\$X5,XCSZ5		21756.13 10	022662.00
	SX,\$X5,XCSZ5		21756.13 10	022662.40
	SX,\$X5,XCSZ5		21756.13 10	022663.00
	SX,\$X5,XCSZ5		21756.13 10	022663.40
	SX,\$X5,XCSZ5		21756.13 10	022664.00
	SX,\$X5,XCSZ5		21756.13 10	022664.40
	SX,\$X5,XCSZ5		21756.13 10	022665.00
	SX,\$X5,XCSZ5		21756.13 10	022665.40
	SX,\$X5,XCSZ5		21756.13 10	022666.00
	SX,\$X5,XCSZ5		21756.13 10	022666.40
	SX,\$X5,XCSZ5		21756.13 10	022667.00
	SX,\$X5,XCSZ5		21756.13 10	022667.40
XCS7F1	KV,\$X5,XCSZ2	-TEST BITS 0-24	21754.12 90	022670.00
	SIC,SEN		1310.00 80	022670.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	022671.00
	KC,\$X5,XCSZ2	-TEST BITS 28-45	21754.13 90	022671.40
	SIC,SEN		1310.00 80	022672.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022672.40
	SR,\$X5,XCSZ5	-REFILL TO WORK AREA	21756.13 70	022673.00
	SIC,SEN		1310.00 80	022673.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022674.00
	LC,\$X5,XCSZ5	-REFILL INTO COUNT FIELD	21756.12 50	022674.40
	KC,\$X5,XCSZ2	-TEST BITS 46-63	21754.13 90	022675.00
	SIC,SEN		1310.00 80	022675.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022676.00
	LX,\$X5,XCSZ1	-RESTORE IX REG.	21753.12 10	022676.40
	NOP,0		0.30 00	022677.00
	NOP,0		0.30 00	022677.40
XCS7G	LX,\$X6,XCSZ2	-LOAD WITH ZEROS ONE TIME %6	21754.14 10	022700.00
	SX,\$X6,XCSZ5		21756.15 10	022700.40
	SX,\$X6,XCSZ5	-READ IX REG N TIMES	21756.15 10	022701.00
	SX,\$X6,XCSZ5		21756.15 10	022701.40
	SX,\$X6,XCSZ5		21756.15 10	022702.00
	SX,\$X6,XCSZ5		21756.15 10	022702.40
	SX,\$X6,XCSZ5		21756.15 10	022703.00
	SX,\$X6,XCSZ5		21756.15 10	022703.40
	SX,\$X6,XCSZ5		21756.15 10	022704.00
	SX,\$X6,XCSZ5		21756.15 10	022704.40
	SX,\$X6,XCSZ5		21756.15 10	022705.00
	SX,\$X6,XCSZ5		21756.15 10	022705.40
	SX,\$X6,XCSZ5		21756.15 10	022706.00
	SX,\$X6,XCSZ5		21756.15 10	022706.40
	SX,\$X6,XCSZ5		21756.15 10	022707.00
	SX,\$X6,XCSZ5		21756.15 10	022707.40
	SX,\$X6,XCSZ5		21756.15 10	022710.00
	SX,\$X6,XCSZ5		21756.15 10	022710.40
	SX,\$X6,XCSZ5		21756.15 10	022711.00
	SX,\$X6,XCSZ5		21756.15 10	022711.40
XCS7G1	KV,\$X6,XCSZ2	-TEST BITS 0-24	21754.14 90	022712.00
	SIC,SEN		1310.00 80	022713.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	022713.40

	KC,\$X6,XCSZ2	-TEST BITS 28-45	21754.15 90	022714.00
	SIC,SEN		1310.00 80	022714.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022715.00
	SR,\$X6,XCSZ5	-REFILL TO WORK AREA	21756.15 70	022715.40
	SIC,SEN		1310.00 80	022716.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022716.40
	LC,\$X6,XCSZ5	-REFILL INTO COUNT FIELD	21756.14 50	022717.00
	KC,\$X6,XCSZ2	-TEST BITS 46-63	21754.15 90	022717.40
	SIC,SEN		1310.00 80	022720.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022720.40
	LX,\$X6,XCSZ1	-RESTORE IX REG.	21753.14 10	022721.00
	NOP,0		0.30 00	022721.40
	NOP,0		0.30 00	022722.00
XCS7H	LX,\$X7,XCSZ2	-LOAD WITH ZEROS ONE TIME %7	21754.16 10	022722.40
	SX,\$X7,XCSZ5		21756.17 10	022723.00
	SX,\$X7,XCSZ5	-READ IX REG N TIMES	21756.17 10	022723.40
	SX,\$X7,XCSZ5		21756.17 10	022724.00
	SX,\$X7,XCSZ5		21756.17 10	022724.40
	SX,\$X7,XCSZ5		21756.17 10	022725.00
	SX,\$X7,XCSZ5		21756.17 10	022725.40
	SX,\$X7,XCSZ5		21756.17 10	022726.00
	SX,\$X7,XCSZ5		21756.17 10	022726.40
	SX,\$X7,XCSZ5		21756.17 10	022727.00
	SX,\$X7,XCSZ5		21756.17 10	022727.40
	SX,\$X7,XCSZ5		21756.17 10	022730.00
	SX,\$X7,XCSZ5		21756.17 10	022730.40
	SX,\$X7,XCSZ5		21756.17 10	022731.00
	SX,\$X7,XCSZ5		21756.17 10	022731.40
	SX,\$X7,XCSZ5		21756.17 10	022732.00
	SX,\$X7,XCSZ5		21756.17 10	022732.40
	SX,\$X7,XCSZ5		21756.17 10	022733.00
	SX,\$X7,XCSZ5		21756.17 10	022733.40
	SX,\$X7,XCSZ5		21756.17 10	022734.00
	SX,\$X7,XCSZ5		21756.17 10	022734.40
XCS7H1	KV,\$X7,XCSZ2	-TEST BITS 0-24	21754.16 90	022735.00
	SIC,SEN		1310.00 80	022735.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	022736.00
	KC,\$X7,XCSZ2	-TEST BITS 28-45	21754.17 90	022736.40
	SIC,SEN		1310.00 80	022737.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022737.40
	SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17 70	022740.00
	SIC,SEN		1310.00 80	022740.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022741.00
	LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16 50	022741.40
	KC,\$X7,XCSZ2	-TEST BITS 46-63	21754.17 90	022742.00
	SIC,SEN		1310.00 80	022742.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022743.00
	LX,\$X7,XCSZ1	-RESTORE IX REG.	21753.16 10	022743.40
	NOP,0		0.30 00	022744.00
	NOP,0		0.30 00	022744.40
XCS7J	LX,\$X8,XCSZ2	-LOAD WITH ZEROS ONE TIME %8	21754.20 10	022745.00
	SX,\$X8,XCSZ5		21756.21 10	022745.40
	SX,\$X8,XCSZ5	-READ IX REG N TIMES	21756.21 10	022746.00
	SX,\$X8,XCSZ5		21756.21 10	022746.40
	SX,\$X8,XCSZ5		21756.21 10	022747.00
	SX,\$X8,XCSZ5		21756.21 10	022747.40
	SX,\$X8,XCSZ5		21756.21 10	022750.00
	SX,\$X8,XCSZ5		21756.21 10	022750.40
	SX,\$X8,XCSZ5		21756.21 10	022751.00
	SX,\$X8,XCSZ5		21756.21 10	022751.40
	SX,\$X8,XCSZ5		21756.21 10	022752.00
	SX,\$X8,XCSZ5		21756.21 10	022752.40
	SX,\$X8,XCSZ5		21756.21 10	022753.00
	SX,\$X8,XCSZ5		21756.21 10	022753.40
	SX,\$X8,XCSZ5		21756.21 10	022754.00
	SX,\$X8,XCSZ5		21756.21 10	022754.40

	SX,\$X8,XCSZ5		21756.21 10	022755.00
		SX,\$X8,XCSZ5	21756.21 10	022756.00
	SX,\$X8,XCSZ5		21756.21 10	022756.40
		SX,\$X8,XCSZ5	21756.21 10	022757.00
XCS7J1	KV,\$X8,XCSZ2		21754.20 90	022757.40
	SIC,SEN	-TEST BITS 0-24	1310.00 80	022760.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	022760.40
	KC,\$X8,XCSZ2	-TEST BITS 28-45	21754.21 90	022761.00
	SIC,SEN		1310.00 80	022761.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	022762.00
	SR,\$X8,XCSZ5	-REFILL TO WORK AREA	21756.21 70	022762.40
	SIC,SEN		1310.00 80	022763.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	022763.40
	LC,\$X8,XCSZ5	-REFILL INTO COUNT FIELD	21756.20 50	022764.00
	KC,\$X8,XCSZ2	-TEST BITS 46-63	21754.21 90	022764.40
	SIC,SEN		1310.00 80	022765.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	022765.40
	LX,\$X8,XCSZ1	-RESTORE IX REG.	21753.20 10	022766.00
	NOP,0		0.30 00	022766.40
	NOP,0		0.30 00	022767.00
XCS7K	LX,\$X9,XCSZ2	-LOAD WITH ZEROS ONE TIME %9	21754.22 10	022767.40
	SX,\$X9,XCSZ5		21756.23 10	022770.00
		SX,\$X9,XCSZ5	21756.23 10	022770.40
	SX,\$X9,XCSZ5	-READ IX REG N TIMES	21756.23 10	022771.00
			21756.23 10	022771.40
	SX,\$X9,XCSZ5		21756.23 10	022772.00
		SX,\$X9,XCSZ5	21756.23 10	022772.40
	SX,\$X9,XCSZ5		21756.23 10	022773.00
		SX,\$X9,XCSZ5	21756.23 10	022773.40
	SX,\$X9,XCSZ5		21756.23 10	022774.00
		SX,\$X9,XCSZ5	21756.23 10	022774.40
	SX,\$X9,XCSZ5		21756.23 10	022775.00
		SX,\$X9,XCSZ5	21756.23 10	022775.40
	SX,\$X9,XCSZ5		21756.23 10	022776.00
		SX,\$X9,XCSZ5	21756.23 10	022776.40
	SX,\$X9,XCSZ5		21756.23 10	022777.00
		SX,\$X9,XCSZ5	21756.23 10	022777.40
	SX,\$X9,XCSZ5		21756.23 10	023000.00
		SX,\$X9,XCSZ5	21756.23 10	023000.40
	SX,\$X9,XCSZ5		21756.23 10	023001.00
		SX,\$X9,XCSZ5	21756.23 10	023001.40
XCS7K1	KV,\$X9,XCSZ2	-TEST BITS 0-24	21754.22 90	023002.00
	SIC,SEN		1310.00 80	023002.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	023003.00
	KC,\$X9,XCSZ2	-TEST BITS 28-45	21754.23 90	023003.40
	SIC,SEN		1310.00 80	023004.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023004.40
	SR,\$X9,XCSZ5	-REFILL TO WORK AREA	21756.23 70	023005.00
	SIC,SEN		1310.00 80	023005.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023006.00
	LC,\$X9,XCSZ5	-REFILL INTO COUNT FIELD	21756.22 50	023006.40
	KC,\$X9,XCSZ2	-TEST BITS 46-63	21754.23 90	023007.00
	SIC,SEN		1310.00 80	023007.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023010.00
	LX,\$X9,XCSZ1	-RESTORE IX REG.	21753.22 10	023010.40
	NOP,0		0.30 00	023011.00
	NOP,0		0.30 00	023011.40
XCS7L	LX,\$X10,XCSZ2	-LOAD WITH ZEROS ONE TIME %10	21754.24 10	023012.00
	SX,\$X10,XCSZ5		21756.25 10	023012.40
		SX,\$X10,XCSZ5	21756.25 10	023013.00
	SX,\$X10,XCSZ5	-READ IX REG N TIMES	21756.25 10	023013.40
			21756.25 10	023014.00
	SX,\$X10,XCSZ5		21756.25 10	023014.40
		SX,\$X10,XCSZ5	21756.25 10	023015.00
	SX,\$X10,XCSZ5		21756.25 10	023015.40

	SX,\$X10,XCSZ5		21756.25 10	023016.00
	SX,\$X10,XCSZ5		21756.25 10	023016.40
	SX,\$X10,XCSZ5		21756.25 10	023017.00
	SX,\$X10,XCSZ5		21756.25 10	023017.40
	SX,\$X10,XCSZ5		21756.25 10	023017.40
	SX,\$X10,XCSZ5		21756.25 10	023020.00
	SX,\$X10,XCSZ5		21756.25 10	023020.40
	SX,\$X10,XCSZ5		21756.25 10	023021.00
	SX,\$X10,XCSZ5		21756.25 10	023021.40
	SX,\$X10,XCSZ5		21756.25 10	023022.00
	SX,\$X10,XCSZ5		21756.25 10	023022.40
	SX,\$X10,XCSZ5		21756.25 10	023023.00
	SX,\$X10,XCSZ5		21756.25 10	023023.40
	SX,\$X10,XCSZ5		21756.25 10	023024.00
XCS7L1	KV,\$X10,XCSZ2	-TEST BITS 0-24	21754.24 90	023024.40
	SIC,SEN		1310.00 80	023025.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	023025.40
	KC,\$X10,XCSZ2	-TEST BITS 28-45	21754.25 90	023026.00
	SIC,SEN		1310.00 80	023026.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023027.00
	SR,\$X10,XCSZ5	-REFILL TO WORK AREA	21756.25 70	023027.40
	SIC,SEN		1310.00 80	023030.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023030.40
	LC,\$X10,XCSZ5	-REFILL INTO COUNT FIELD	21756.24 50	023031.00
	KC,\$X10,XCSZ2	-TEST BITS 46-63	21754.25 90	023031.40
	SIC,SEN		1310.00 80	023032.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023032.40
	LX,\$X10,XCSZ1	-RESTORE IX REG.	21753.24 10	023033.00
	NOP,0		0.30 00	023033.40
	NOP,0		0.30 00	023034.00
XCS7M	LX,\$X11,XCSZ2	-LOAD WITH ZEROS ONE TIME %11□	21754.26 10	023034.40
	SX,\$X11,XCSZ5		21756.27 10	023035.00
	SX,\$X11,XCSZ5	-READ IX REG N TIMES	21756.27 10	023035.40
	SX,\$X11,XCSZ5		21756.27 10	023036.00
	SX,\$X11,XCSZ5		21756.27 10	023036.40
	SX,\$X11,XCSZ5		21756.27 10	023037.00
	SX,\$X11,XCSZ5		21756.27 10	023037.40
	SX,\$X11,XCSZ5		21756.27 10	023040.00
	SX,\$X11,XCSZ5		21756.27 10	023040.40
	SX,\$X11,XCSZ5		21756.27 10	023041.00
	SX,\$X11,XCSZ5		21756.27 10	023041.40
	SX,\$X11,XCSZ5		21756.27 10	023042.00
	SX,\$X11,XCSZ5		21756.27 10	023042.40
	SX,\$X11,XCSZ5		21756.27 10	023043.00
	SX,\$X11,XCSZ5		21756.27 10	023043.40
	SX,\$X11,XCSZ5		21756.27 10	023044.00
	SX,\$X11,XCSZ5		21756.27 10	023044.40
	SX,\$X11,XCSZ5		21756.27 10	023045.00
	SX,\$X11,XCSZ5		21756.27 10	023045.40
	SX,\$X11,XCSZ5		21756.27 10	023046.00
	SX,\$X11,XCSZ5		21756.27 10	023046.40
XCS7M1	KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26 90	023047.00
	SIC,SEN		1310.00 80	023047.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	023050.00
	KC,\$X11,XCSZ2	-TEST BITS 28-45	21754.27 90	023050.40
	SIC,SEN		1310.00 80	023051.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023051.40
	SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27 70	023052.00
	SIC,SEN		1310.00 80	023052.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023053.00
	LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26 50	023053.40
	KC,\$X11,XCSZ2	-TEST BITS 46-63	21754.27 90	023054.00
	SIC,SEN		1310.00 80	023054.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023055.00
	LX,\$X11,XCSZ1	-RESTORE IX REG.	21753.26 10	023055.40
	NOP,0		0.30 00	023056.00
	NOP,0		0.30 00	023056.40

XCS7N	LX,\$X12,XCSZ2	-LOAD WITH ZEROS ONE TIME %12	21754.30 10	023057.00
	SX,\$X12,XCSZ5		21756.31 10	023057.40
	SX,\$X12,XCSZ5	-READ IX REG N TIMES	21756.31 10	023060.00
	SX,\$X12,XCSZ5		21756.31 10	023060.40
	SX,\$X12,XCSZ5		21756.31 10	023061.00
	SX,\$X12,XCSZ5		21756.31 10	023061.40
	SX,\$X12,XCSZ5		21756.31 10	023062.00
	SX,\$X12,XCSZ5		21756.31 10	023062.40
	SX,\$X12,XCSZ5		21756.31 10	023063.00
	SX,\$X12,XCSZ5		21756.31 10	023063.40
	SX,\$X12,XCSZ5		21756.31 10	023064.00
	SX,\$X12,XCSZ5		21756.31 10	023064.40
	SX,\$X12,XCSZ5		21756.31 10	023065.00
	SX,\$X12,XCSZ5		21756.31 10	023065.40
	SX,\$X12,XCSZ5		21756.31 10	023066.00
	SX,\$X12,XCSZ5		21756.31 10	023066.40
	SX,\$X12,XCSZ5		21756.31 10	023067.00
	SX,\$X12,XCSZ5		21756.31 10	023067.40
	SX,\$X12,XCSZ5		21756.31 10	023070.00
	SX,\$X12,XCSZ5		21756.31 10	023070.40
XCS7N1	KV,\$X12,XCSZ2	-TEST BITS 0-24	21756.31 10	023071.00
	SIC,SEN		21754.30 90	023071.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1310.00 80	023072.00
	KC,\$X12,XCSZ2	-TEST BITS 28-45	1304.32 C4	023072.40
	SIC,SEN		21754.31 90	023073.00
	BZXE,SERS	-BITS MUST COMPARE	1310.00 80	023073.40
	SR,\$X12,XCSZ5	-REFILL TO WORK AREA	1304.32 C0	023074.00
	SIC,SEN		21756.31 70	023074.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1310.00 80	023075.00
	LC,\$X12,XCSZ5	-REFILL INTO COUNT FIELD	1304.23 42	023075.40
	KC,\$X12,XCSZ2	-TEST BITS 46-63	21756.30 50	023076.00
	SIC,SEN		21754.31 90	023076.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1310.00 80	023077.00
	LX,\$X12,XCSZ1	-RESTORE IX REG.	1304.32 C0	023077.40
	NOP,0		21753.30 10	023100.00
	NOP,0		0.30 00	023100.40
	NOP,0		0.30 00	023101.00
XCS7P	LX,\$X13,XCSZ2	-LOAD WITH ZEROS ONE TIME %13	21754.32 10	023101.40
	SX,\$X13,XCSZ5		21756.33 10	023102.00
	SX,\$X13,XCSZ5	-READ IX REG N TIMES	21756.33 10	023102.40
	SX,\$X13,XCSZ5		21756.33 10	023103.00
	SX,\$X13,XCSZ5		21756.33 10	023103.40
	SX,\$X13,XCSZ5		21756.33 10	023104.00
	SX,\$X13,XCSZ5		21756.33 10	023104.40
	SX,\$X13,XCSZ5		21756.33 10	023105.00
	SX,\$X13,XCSZ5		21756.33 10	023105.40
	SX,\$X13,XCSZ5		21756.33 10	023106.00
	SX,\$X13,XCSZ5		21756.33 10	023106.40
	SX,\$X13,XCSZ5		21756.33 10	023107.00
	SX,\$X13,XCSZ5		21756.33 10	023107.40
	SX,\$X13,XCSZ5		21756.33 10	023110.00
	SX,\$X13,XCSZ5		21756.33 10	023110.40
	SX,\$X13,XCSZ5		21756.33 10	023111.00
	SX,\$X13,XCSZ5		21756.33 10	023111.40
	SX,\$X13,XCSZ5		21756.33 10	023112.00
	SX,\$X13,XCSZ5		21756.33 10	023112.40
	SX,\$X13,XCSZ5		21756.33 10	023113.00
	SX,\$X13,XCSZ5		21756.33 10	023113.40
XCS7P1	KV,\$X13,XCSZ2	-TEST BITS 0-24	21756.33 10	023113.40
	SIC,SEN		21754.32 90	023114.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1310.00 80	023114.40
	KC,\$X13,XCSZ2	-TEST BITS 28-45	1304.32 C4	023115.00
	SIC,SEN		21754.33 90	023115.40
	BZXE,SERS	-BITS MUST COMPARE	1310.00 80	023116.00
	SR,\$X13,XCSZ5	-REFILL TO WORK AREA	1304.32 C0	023116.40
	SIC,SEN		21756.33 70	023117.00
			1310.00 80	023117.40

	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023120.00
	LC,\$X13,XCSZ5	-REFILL INTO COUNT FIELD	21756.32 50	023120.40
	KC,\$X13,XCSZ2	-TEST BITS 46-63	21754.33 90	023121.00
	SIC,SEN		1310.00 80	023121.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023122.00
	LX,\$X13,XCSZ1	-RESTORE IX REG.	21753.32 10	023122.40
	NOP,0		0.30 00	023123.00
	NOP,0		0.30 00	023123.40
XCS7Q	LX,\$X14,XCSZ2	-LOAD WITH ZEROS ONE TIME %14	21754.34 10	023124.00
	SX,\$X14,XCSZ5		21756.35 10	023124.40
	SX,\$X14,XCSZ5	-READ IX REG N TIMES	21756.35 10	023125.00
	SX,\$X14,XCSZ5		21756.35 10	023125.40
	SX,\$X14,XCSZ5		21756.35 10	023126.00
	SX,\$X14,XCSZ5		21756.35 10	023126.40
	SX,\$X14,XCSZ5		21756.35 10	023127.00
	SX,\$X14,XCSZ5		21756.35 10	023127.40
	SX,\$X14,XCSZ5		21756.35 10	023130.00
	SX,\$X14,XCSZ5		21756.35 10	023130.40
	SX,\$X14,XCSZ5		21756.35 10	023131.00
	SX,\$X14,XCSZ5		21756.35 10	023131.40
	SX,\$X14,XCSZ5		21756.35 10	023132.00
	SX,\$X14,XCSZ5		21756.35 10	023132.40
	SX,\$X14,XCSZ5		21756.35 10	023133.00
	SX,\$X14,XCSZ5		21756.35 10	023133.40
	SX,\$X14,XCSZ5		21756.35 10	023134.00
	SX,\$X14,XCSZ5		21756.35 10	023134.40
	SX,\$X14,XCSZ5		21756.35 10	023135.00
	SX,\$X14,XCSZ5		21756.35 10	023135.40
	SX,\$X14,XCSZ5		21756.35 10	023136.00
XCS7Q1	KV,\$X14,XCSZ2	-TEST BITS 0-24	21754.34 90	023136.40
	SIC,SEN		1310.00 80	023137.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	023137.40
	KC,\$X14,XCSZ2	-TEST BITS 28-45	21754.35 90	023140.00
	SIC,SEN		1310.00 80	023140.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023141.00
	SR,\$X14,XCSZ5	-REFILL TO WORK AREA	21756.35 70	023141.40
	SIC,SEN		1310.00 80	023142.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023142.40
	LC,\$X14,XCSZ5	-REFILL INTO COUNT FIELD	21756.34 50	023143.00
	KC,\$X14,XCSZ2	-TEST BITS 46-63	21754.35 90	023143.40
	SIC,SEN		1310.00 80	023144.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023144.40
	LX,\$X14,XCSZ1	-RESTORE IX REG.	21753.34 10	023145.00
	NOP,0		0.30 00	023145.40
	NOP,0		0.30 00	023146.00
XCS7R	LX,\$X15,XCSZ2	-LOAD WITH ZEROS ONE TIME %15	21754.36 10	023146.40
	SX,\$X15,XCSZ5		21756.37 10	023147.00
	SX,\$X15,XCSZ5	-READ IX REG N TIMES	21756.37 10	023147.40
	SX,\$X15,XCSZ5		21756.37 10	023150.00
	SX,\$X15,XCSZ5		21756.37 10	023150.40
	SX,\$X15,XCSZ5		21756.37 10	023151.00
	SX,\$X15,XCSZ5		21756.37 10	023151.40
	SX,\$X15,XCSZ5		21756.37 10	023152.00
	SX,\$X15,XCSZ5		21756.37 10	023152.40
	SX,\$X15,XCSZ5		21756.37 10	023153.00
	SX,\$X15,XCSZ5		21756.37 10	023153.40
	SX,\$X15,XCSZ5		21756.37 10	023154.00
	SX,\$X15,XCSZ5		21756.37 10	023154.40
	SX,\$X15,XCSZ5		21756.37 10	023155.00
	SX,\$X15,XCSZ5		21756.37 10	023155.40
	SX,\$X15,XCSZ5		21756.37 10	023156.00
	SX,\$X15,XCSZ5		21756.37 10	023156.40
	SX,\$X15,XCSZ5		21756.37 10	023157.00
	SX,\$X15,XCSZ5		21756.37 10	023157.40
	SX,\$X15,XCSZ5		21756.37 10	023160.00
	SX,\$X15,XCSZ5		21756.37 10	023160.40

XCS7R1	KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36 90	023161.00
	SIC,SEN		1310.00 80	023161.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	023162.00
	KC,\$X15,XCSZ2	-TEST BITS 28-45	21754.37 90	023162.40
	SIC,SEN		1310.00 80	023163.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023163.40
	SR,\$X15,XCSZ5	-REFILL TO WORK AREA	21756.37 70	023164.00
	SIC,SEN		1310.00 80	023164.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE	1304.23 42	023165.00
	LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36 50	023165.40
	KC,\$X15,XCSZ2	-TEST BITS 46-63	21754.37 90	023166.00
	SIC,SEN		1310.00 80	023166.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	023167.00
	LX,\$X15,XCSZ1	-RESTORE IX REG.	21753.36 10	023167.40
	NOP,0		0.30 00	023170.00
	NOP,0		0.30 00	023170.40
	B,\$+1.0		23172.10 00	023171.00
	B,XCS7	-TO LOOP IN LOW ONES TEST	22511.10 00	023171.40
	SIC,SEN0+.32		1311.40 80	023172.00
	B,SSW	-TEST SENSE SWITCHES	1301.10 00	023172.40
XCS8	LX,\$X0,XCSZ2		21754.00 10	023173.00
	LX,\$X1,XCSZ2	-INITIALIZE BY	21754.02 10	023173.40
	LX,\$X2,XCSZ2		21754.04 10	023174.00
	LX,\$X3,XCSZ2	-SETTING ALL IX	21754.06 10	023174.40
	LX,\$X4,XCSZ2		21754.10 10	023175.00
	LX,\$X5,XCSZ2	-REGS TO ZEROS	21754.12 10	023175.40
	LX,\$X6,XCSZ2		21754.14 10	023176.00
	LX,\$X7,XCSZ2	-PREPARE FOR	21754.16 10	023176.40
	LX,\$X8,XCSZ2		21754.20 10	023177.00
	LX,\$X9,XCSZ2	-LOW ONE	21754.22 10	023177.40
	LX,\$X10,XCSZ2		21754.24 10	023200.00
	LX,\$X11,XCSZ2	-%UNDISTURBED□	21754.26 10	023200.40
	LX,\$X12,XCSZ2		21754.30 10	023201.00
	LX,\$X13,XCSZ2	-TEST	21754.32 10	023201.40
	LX,\$X14,XCSZ2		21754.34 10	023202.00
	LX,\$X15,XCSZ2		21754.36 10	023202.40
XCS8A	LX,\$X0,XCSZ2		21754.00 10	023203.00
	LX,\$X0,XCSZ2	-LOAD WITH ZEROS N TIMES %0□	21754.00 10	023203.40
	LX,\$X0,XCSZ2		21754.00 10	023204.00
	LX,\$X0,XCSZ2		21754.00 10	023204.40
	LX,\$X0,XCSZ2		21754.00 10	023205.00
	LX,\$X0,XCSZ2		21754.00 10	023205.40
	LX,\$X0,XCSZ2		21754.00 10	023206.00
	LX,\$X0,XCSZ2		21754.00 10	023206.40
	LX,\$X0,XCSZ2		21754.00 10	023207.00
	LX,\$X0,XCSZ2		21754.00 10	023207.40
	LX,\$X0,XCSZ2		21754.00 10	023210.00
	LX,\$X0,XCSZ2		21754.00 10	023210.40
	LX,\$X0,XCSZ2		21754.00 10	023211.00
	LX,\$X0,XCSZ2		21754.00 10	023211.40
	LX,\$X0,XCSZ2		21754.00 10	023212.00
	LX,\$X0,XCSZ2		21754.00 10	023212.40
	LX,\$X0,XCSZ2		21754.00 10	023213.00
	LX,\$X0,XCSZ2		21754.00 10	023213.40
	LX,\$X0,XCSZ2		21754.00 10	023214.00
	LX,\$X0,XCSZ2		21754.00 10	023214.40
XCS8A1	LX,\$X0,XCSZ1	-LOAD ONCE WITH ONES	21753.00 10	023215.00
	KV,\$X0,XCSZ1	-TEST BITS 0-24	21753.00 90	023215.40
	SIC,SEN		1310.00 80	023216.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023216.40
	KC,\$X0,XCSZ1	-TEST BITS 28-45	21753.01 90	023217.00
	SIC,SEN		1310.00 80	023217.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023220.00
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01 70	023220.40
	SIC,SEN		1310.00 80	023221.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023221.40

	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00 50	023222.00
	KC,\$X0,XCSZ1	-TEST BITS 46-63	21753.01 90	023222.40
	SIC,SEN		1310.00 80	023223.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023223.40
	LX,\$X0,XCSZ2		21754.00 10	023224.00
	NOP	-RESTORE IX REG.	0.30 00	023224.40
	NOP,0		0.30 00	023225.00
XCS8B	LX,\$X1,XCSZ2		21754.02 10	023225.40
	LX,\$X1,XCSZ2	-LOAD WITH ZEROS N TIMES %1□	21754.02 10	023226.00
	LX,\$X1,XCSZ2		21754.02 10	023226.40
	LX,\$X1,XCSZ2		21754.02 10	023227.00
	LX,\$X1,XCSZ2		21754.02 10	023227.40
	LX,\$X1,XCSZ2		21754.02 10	023230.00
	LX,\$X1,XCSZ2		21754.02 10	023230.40
	LX,\$X1,XCSZ2		21754.02 10	023231.00
	LX,\$X1,XCSZ2		21754.02 10	023231.40
	LX,\$X1,XCSZ2		21754.02 10	023232.00
	LX,\$X1,XCSZ2		21754.02 10	023232.40
	LX,\$X1,XCSZ2		21754.02 10	023233.00
	LX,\$X1,XCSZ2		21754.02 10	023233.40
	LX,\$X1,XCSZ2		21754.02 10	023234.00
	LX,\$X1,XCSZ2		21754.02 10	023234.40
	LX,\$X1,XCSZ2		21754.02 10	023235.00
	LX,\$X1,XCSZ2		21754.02 10	023235.40
	LX,\$X1,XCSZ2		21754.02 10	023236.00
	LX,\$X1,XCSZ2		21754.02 10	023236.40
	LX,\$X1,XCSZ2		21754.02 10	023237.00
XCS8B1	LX,\$X1,XCSZ1	-LOAD ONCE WITH ONES	21753.02 10	023237.40
	KV,\$X1,XCSZ1	-TEST BITS 0-24	21753.02 90	023240.00
	SIC,SEN		1310.00 80	023240.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023241.00
	KC,\$X1,XCSZ1	-TEST BITS 28-45	21753.03 90	023241.40
	SIC,SEN		1310.00 80	023242.00
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023242.40
	SR,\$X1,XCSZ5	-REFILL TO WORK AREA	21756.03 70	023243.00
	SIC,SEN		1310.00 80	023243.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023244.00
	LC,\$X1,XCSZ5	-REFILL INTO COUNT FIELD	21756.02 50	023244.40
	KC,\$X1,XCSZ1	-TEST BITS 46-63	21753.03 90	023245.00
	SIC,SEN		1310.00 80	023245.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023246.00
	LX,\$X1,XCSZ2		21754.02 10	023246.40
	NOP	-RESTORE IX REG.	0.30 00	023247.00
	NOP,0		0.30 00	023247.40
XCS8C	LX,\$X2,XCSZ2		21754.04 10	023250.00
	LX,\$X2,XCSZ2	-LOAD WITH ZEROS N TIMES %2□	21754.04 10	023250.40
	LX,\$X2,XCSZ2		21754.04 10	023251.00
	LX,\$X2,XCSZ2		21754.04 10	023251.40
	LX,\$X2,XCSZ2		21754.04 10	023252.00
	LX,\$X2,XCSZ2		21754.04 10	023252.40
	LX,\$X2,XCSZ2		21754.04 10	023253.00
	LX,\$X2,XCSZ2		21754.04 10	023253.40
	LX,\$X2,XCSZ2		21754.04 10	023254.00
	LX,\$X2,XCSZ2		21754.04 10	023254.40
	LX,\$X2,XCSZ2		21754.04 10	023255.00
	LX,\$X2,XCSZ2		21754.04 10	023255.40
	LX,\$X2,XCSZ2		21754.04 10	023256.00
	LX,\$X2,XCSZ2		21754.04 10	023256.40
	LX,\$X2,XCSZ2		21754.04 10	023257.00
	LX,\$X2,XCSZ2		21754.04 10	023257.40
	LX,\$X2,XCSZ2		21754.04 10	023260.00
	LX,\$X2,XCSZ2		21754.04 10	023260.40
	LX,\$X2,XCSZ2		21754.04 10	023261.00
	LX,\$X2,XCSZ2		21754.04 10	023261.40
XCS8C1	LX,\$X2,XCSZ1	-LOAD ONCE WITH ONES	21753.04 10	023262.00
	KV,\$X2,XCSZ1	-TEST BITS 0-24	21753.04 90	023262.40

	SIC,SEN		1310.00 80	023263.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023263.40
	KC,\$X2,XCSZ1	-TEST BITS 28-45	21753.05 90	023264.00
	SIC,SEN		1310.00 80	023264.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023265.00
	SR,\$X2,XCSZ5	-REFILL TO WORK AREA	21756.05 70	023265.40
	SIC,SEN		1310.00 80	023266.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023266.40
	LC,\$X2,XCSZ5	-REFILL INTO COUNT FIELD	21756.04 50	023267.00
	KC,\$X2,XCSZ1	-TEST BITS 46-63	21753.05 90	023267.40
	SIC,SEN		1310.00 80	023270.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023270.40
	LX,\$X2,XCSZ2		21754.04 10	023271.00
	NOP	-RESTORE IX REG.	0.30 00	023271.40
	NOP,0		0.30 00	023272.00
XCS8D	LX,\$X3,XCSZ2		21754.06 10	023272.40
	LX,\$X3,XCSZ2	-LOAD WITH ZEROS N TIMES %3	21754.06 10	023273.00
	LX,\$X3,XCSZ2		21754.06 10	023273.40
	LX,\$X3,XCSZ2		21754.06 10	023274.00
	LX,\$X3,XCSZ2		21754.06 10	023274.40
	LX,\$X3,XCSZ2		21754.06 10	023275.00
	LX,\$X3,XCSZ2		21754.06 10	023275.40
	LX,\$X3,XCSZ2		21754.06 10	023276.00
	LX,\$X3,XCSZ2		21754.06 10	023276.40
	LX,\$X3,XCSZ2		21754.06 10	023277.00
	LX,\$X3,XCSZ2		21754.06 10	023277.40
	LX,\$X3,XCSZ2		21754.06 10	023300.00
	LX,\$X3,XCSZ2		21754.06 10	023300.40
	LX,\$X3,XCSZ2		21754.06 10	023301.00
	LX,\$X3,XCSZ2		21754.06 10	023301.40
	LX,\$X3,XCSZ2		21754.06 10	023302.00
	LX,\$X3,XCSZ2		21754.06 10	023302.40
	LX,\$X3,XCSZ2		21754.06 10	023303.00
	LX,\$X3,XCSZ2		21754.06 10	023303.40
	LX,\$X3,XCSZ2		21754.06 10	023304.00
XCS8D1	LX,\$X3,XCSZ1	-LOAD ONCE WITH ONES	21753.06 10	023304.40
	KV,\$X3,XCSZ1	-TEST BITS 0-24	21753.06 90	023305.00
	SIC,SEN		1310.00 80	023305.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023306.00
	KC,\$X3,XCSZ1	-TEST BITS 28-45	21753.07 90	023306.40
	SIC,SEN		1310.00 80	023307.00
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023307.40
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	023310.00
	SIC,SEN		1310.00 80	023310.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023311.00
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	023311.40
	KC,\$X3,XCSZ1	-TEST BITS 46-63	21753.07 90	023312.00
	SIC,SEN		1310.00 80	023312.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023313.00
	LX,\$X3,XCSZ2		21754.06 10	023313.40
	NOP	-RESTORE IX REG.	0.30 00	023314.00
	NOP,0		0.30 00	023314.40
XCS8E	LX,\$X4,XCSZ2		21754.10 10	023315.00
	LX,\$X4,XCSZ2	-LOAD WITH ZEROS N TIMES %4	21754.10 10	023315.40
	LX,\$X4,XCSZ2		21754.10 10	023316.00
	LX,\$X4,XCSZ2		21754.10 10	023316.40
	LX,\$X4,XCSZ2		21754.10 10	023317.00
	LX,\$X4,XCSZ2		21754.10 10	023317.40
	LX,\$X4,XCSZ2		21754.10 10	023320.00
	LX,\$X4,XCSZ2		21754.10 10	023320.40
	LX,\$X4,XCSZ2		21754.10 10	023321.00
	LX,\$X4,XCSZ2		21754.10 10	023321.40
	LX,\$X4,XCSZ2		21754.10 10	023322.00
	LX,\$X4,XCSZ2		21754.10 10	023322.40
	LX,\$X4,XCSZ2		21754.10 10	023323.00
	LX,\$X4,XCSZ2		21754.10 10	023323.40

	LX,\$X4,XCSZ2		21754.10 10	023324.00
	LX,\$X4,XCSZ2		21754.10 10	023325.00
	LX,\$X4,XCSZ2		21754.10 10	023325.40
	LX,\$X4,XCSZ2		21754.10 10	023326.00
	LX,\$X4,XCSZ2		21754.10 10	023326.40
	LX,\$X4,XCSZ1		21753.10 10	023327.00
XCS8E1	KV,\$X4,XCSZ1	-LOAD ONCE WITH ONES	21753.10 90	023327.40
	SIC,SEN	-TEST BITS 0-24	1310.00 80	023330.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP/OR LOST	1304.32 C4	023330.40
	KC,\$X4,XCSZ1	-TEST BITS 28-45	21753.11 90	023331.00
	SIC,SEN		1310.00 80	023331.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023332.00
	SR,\$X4,XCSZ5	-REFILL TO WORK AREA	21756.11 70	023332.40
	SIC,SEN		1310.00 80	023333.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023333.40
	LC,\$X4,XCSZ5	-REFILL INTO COUNT FIELD	21756.10 50	023334.00
	KC,\$X4,XCSZ1	-TEST BITS 46-63	21753.11 90	023334.40
	SIC,SEN		1310.00 80	023335.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023335.40
	LX,\$X4,XCSZ2		21754.10 10	023336.00
	NOP	-RESTORE IX REG.	0.30 00	023336.40
	NOP,0		0.30 00	023337.00
XCS8F	LX,\$X5,XCSZ2		21754.12 10	023337.40
	LX,\$X5,XCSZ2	-LOAD WITH ZEROS N TIMES %5□	21754.12 10	023340.00
	LX,\$X5,XCSZ2		21754.12 10	023340.40
	LX,\$X5,XCSZ2		21754.12 10	023341.00
	LX,\$X5,XCSZ2		21754.12 10	023341.40
	LX,\$X5,XCSZ2		21754.12 10	023342.00
	LX,\$X5,XCSZ2		21754.12 10	023342.40
	LX,\$X5,XCSZ2		21754.12 10	023343.00
	LX,\$X5,XCSZ2		21754.12 10	023343.40
	LX,\$X5,XCSZ2		21754.12 10	023344.00
	LX,\$X5,XCSZ2		21754.12 10	023344.40
	LX,\$X5,XCSZ2		21754.12 10	023345.00
	LX,\$X5,XCSZ2		21754.12 10	023345.40
	LX,\$X5,XCSZ2		21754.12 10	023346.00
	LX,\$X5,XCSZ2		21754.12 10	023346.40
	LX,\$X5,XCSZ2		21754.12 10	023347.00
	LX,\$X5,XCSZ2		21754.12 10	023347.40
	LX,\$X5,XCSZ2		21754.12 10	023350.00
	LX,\$X5,XCSZ2		21754.12 10	023350.40
	LX,\$X5,XCSZ2		21754.12 10	023351.00
	LX,\$X5,XCSZ1	-LOAD ONCE WITH ONES	21753.12 10	023351.40
XCS8F1	KV,\$X5,XCSZ1	-TEST BITS 0-24	21753.12 90	023352.00
	SIC,SEN		1310.00 80	023352.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023353.00
	KC,\$X5,XCSZ1	-TEST BITS 28-45	21753.13 90	023353.40
	SIC,SEN		1310.00 80	023354.00
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023354.40
	SR,\$X5,XCSZ5	-REFILL TO WORK AREA	21756.13 70	023355.00
	SIC,SEN		1310.00 80	023355.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023356.00
	LC,\$X5,XCSZ5	-REFILL INTO COUNT FIELD	21756.12 50	023356.40
	KC,\$X5,XCSZ1	-TEST BITS 46-63	21753.13 90	023357.00
	SIC,SEN		1310.00 80	023357.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023360.00
	LX,\$X5,XCSZ2		21754.12 10	023360.40
	NOP	-RESTORE IX REG.	0.30 00	023361.00
	NOP,0		0.30 00	023361.40
XCS8G	LX,\$X6,XCSZ2		21754.14 10	023362.00
	LX,\$X6,XCSZ2	-LOAD WITH ZEROS N TIMES %6□	21754.14 10	023362.40
	LX,\$X6,XCSZ2		21754.14 10	023363.00
	LX,\$X6,XCSZ2		21754.14 10	023363.40
	LX,\$X6,XCSZ2		21754.14 10	023364.00
	LX,\$X6,XCSZ2		21754.14 10	023364.40

	LX,\$X6,XCSZ2			21754.14 10	023365.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023366.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023366.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023367.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023367.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023370.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023371.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023371.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023372.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023372.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023373.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ2		21754.14 10	023373.40
XCS8G1	LX,\$X6,XCSZ1		-LOAD ONCE WITH ONES	21753.14 10	023374.00
	KV,\$X6,XCSZ1		-TEST BITS 0-24	21753.14 90	023374.40
	SIC,SEN			1310.00 80	023375.00
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	023375.40
	KC,\$X6,XCSZ1		-TEST BITS 28-45	21753.15 90	023376.00
	SIC,SEN			1310.00 80	023376.40
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	023377.00
	SR,\$X6,XCSZ5		-REFILL TO WORK AREA	21756.15 70	023377.40
	SIC,SEN			1310.00 80	023400.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023400.40
	LC,\$X6,XCSZ5		-REFILL INTO COUNT FIELD	21756.14 50	023401.00
	KC,\$X6,XCSZ1		-TEST BITS 46-63	21753.15 90	023401.40
	SIC,SEN			1310.00 80	023402.00
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	023402.40
	LX,\$X6,XCSZ2			21754.14 10	023403.00
	NOP		-RESTORE IX REG.	0.30 00	023403.40
XCS8H	NOP,0			0.30 00	023404.00
	LX,\$X7,XCSZ2			21754.16 10	023404.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2	-LOAD WITH ZEROS N TIMES %7	21754.16 10	023405.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023405.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023406.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023406.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023407.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023407.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023410.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023410.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023411.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023411.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023412.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023412.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023413.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023413.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023414.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023414.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023415.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023415.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ2		21754.16 10	023416.00
XCS8H1	LX,\$X7,XCSZ1		-LOAD ONCE WITH ONES	21753.16 10	023416.40
	KV,\$X7,XCSZ1		-TEST BITS 0-24	21753.16 90	023417.00
	SIC,SEN			1310.00 80	023417.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	023420.00
	KC,\$X7,XCSZ1		-TEST BITS 28-45	21753.17 90	023420.40
	SIC,SEN			1310.00 80	023421.00
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	023421.40
	SR,\$X7,XCSZ5		-REFILL TO WORK AREA	21756.17 70	023422.00
	SIC,SEN			1310.00 80	023422.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023423.00
	LC,\$X7,XCSZ5		-REFILL INTO COUNT FIELD	21756.16 50	023423.40
	KC,\$X7,XCSZ1		-TEST BITS 46-63	21753.17 90	023424.00
	SIC,SEN			1310.00 80	023424.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	023425.00
	LX,\$X7,XCSZ2			21754.16 10	023425.40

	NOP,0	NOP	-RESTORE IX REG.	0.30 00	023426.00
XCS8J	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		0.30 00	023426.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2	-LOAD WITH ZEROS N TIMES %8□	21754.20 10	023427.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023427.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023430.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023430.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023431.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023431.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023432.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023432.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023433.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023433.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023434.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023434.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023435.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023435.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023436.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023436.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023437.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023437.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023440.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ2		21754.20 10	023440.40
XCS8J1	LX,\$X8,XCSZ1		-LOAD ONCE WITH ONES	21753.20 10	023441.00
	KV,\$X8,XCSZ1		-TEST BITS 0-24	21753.20 90	023441.40
	SIC,SEN			1310.00 80	023442.00
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	023442.40
	KC,\$X8,XCSZ1		-TEST BITS 28-45	21753.21 90	023443.00
	SIC,SEN			1310.00 80	023443.40
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	023444.00
	SR,\$X8,XCSZ5		-REFILL TO WORK AREA	21756.21 70	023444.40
	SIC,SEN			1310.00 80	023445.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023445.40
	LC,\$X8,XCSZ5		-REFILL INTO COUNT FIELD	21756.20 50	023446.00
	KC,\$X8,XCSZ1		-TEST BITS 46-63	21753.21 90	023446.40
	SIC,SEN			1310.00 80	023447.00
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	023447.40
	LX,\$X8,XCSZ2			21754.20 10	023450.00
	NOP,0	NOP	-RESTORE IX REG.	0.30 00	023450.40
XCS8K	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		0.30 00	023451.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2	-LOAD WITH ZEROS N TIMES %9□	21754.22 10	023451.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023452.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023452.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023453.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023453.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023454.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023454.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023455.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023455.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023456.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023456.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023457.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023457.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023460.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023460.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023461.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023461.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023462.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023462.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ2		21754.22 10	023463.00
XCS8K1	LX,\$X9,XCSZ1		-LOAD ONCE WITH ONES.	21753.22 10	023463.40
	KV,\$X9,XCSZ1		-TEST BITS 0-24	21753.22 90	023464.00
	SIC,SEN			1310.00 80	023464.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	023465.00
	KC,\$X9,XCSZ1		-TEST BITS 28-45	21753.23 90	023465.40
	SIC,SEN			1310.00 80	023466.00
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32 C0	023466.40

	SR,\$X9,XCSZ5		-REFILL TO WORK AREA	21756.23	70	023467.00
	SIC,SEN			1310.00	80	023467.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	023470.00
	LC,\$X9,XCSZ5		-REFILL INTO COUNT FIELD	21756.22	50	023470.40
	KC,\$X9,XCSZ1		-TEST BITS 46-63	21753.23	90	023471.00
	SIC,SEN			1310.00	80	023471.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32	C0	023472.00
	LX,\$X9,XCSZ2			21754.22	10	023472.40
	NOP		-RESTORE IX REG.	0.30	00	023473.00
	NOP,0			0.30	00	023473.40
XCS8L	LX,\$X10,XCSZ2			21754.24	10	023474.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2	-LOAD WITH ZEROS N TIMES %10□	21754.24	10	023474.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023475.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023475.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023476.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023476.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023477.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023477.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023500.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023500.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023501.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023501.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023502.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023502.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023503.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023503.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023504.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023504.40
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023505.00
	LX,\$X10,XCSZ2	LX,\$X10,XCSZ2		21754.24	10	023505.40
	LX,\$X10,XCSZ1		-LOAD ONCE WITH ONES	21753.24	10	023506.00
XCS8L1	KV,\$X10,XCSZ1		-TEST BITS 0-24	21753.24	90	023506.40
	SIC,SEN			1310.00	80	023507.00
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32	C4	023507.40
	KC,\$X10,XCSZ1		-TEST BITS 28-45	21753.25	90	023510.00
	SIC,SEN			1310.00	80	023510.40
	BZXE,SERS		-ERR IF IX BITS DONT COMPARE	1304.32	C0	023511.00
	SR,\$X10,XCSZ5		-REFILL TO WORK AREA	21756.25	70	023511.40
	SIC,SEN			1310.00	80	023512.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	023512.40
	LC,\$X10,XCSZ5		-REFILL INTO COUNT FIELD	21756.24	50	023513.00
	KC,\$X10,XCSZ1		-TEST BITS 46-63	21753.25	90	023513.40
	SIC,SEN			1310.00	80	023514.00
	BZXE,SERS		-ERR IF BIT LOST	1304.32	C0	023514.40
	LX,\$X10,XCSZ2			21754.24	10	023515.00
	NOP		-RESTORE IX REG.	0.30	00	023515.40
	NOP,0			0.30	00	023516.00
XCS8M	LX,\$X11,XCSZ2			21754.26	10	023516.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2	-LOAD WITH ZEROS N TIMES %11□	21754.26	10	023517.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023517.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023520.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023520.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023521.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023521.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023522.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023522.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023523.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023523.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023524.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023524.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023525.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023525.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023526.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023526.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023527.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ2		21754.26	10	023527.40

		LX,\$X11,XCSZ2		21754.26 10	023530.00
XCS8M1		LX,\$X11,XCSZ1	-LOAD ONCE WITH ONES	21753.26 10	023530.40
		KV,\$X11,XCSZ1	-TEST BITS 0-24	21753.26 90	023531.00
		SIC,SEN		1310.00 80	023531.40
		BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023532.00
		KC,\$X11,XCSZ1	-TEST BITS 28-45	21753.27 90	023532.40
		SIC,SEN		1310.00 80	023533.00
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023533.40
		SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27 70	023534.00
		SIC,SEN		1310.00 80	023534.40
		BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023535.00
		LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26 50	023535.40
		KC,\$X11,XCSZ1	-TEST BITS 46-63	21753.27 90	023536.00
		SIC,SEN		1310.00 80	023536.40
		BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023537.00
		LX,\$X11,XCSZ2		21754.26 10	023537.40
		NOP	-RESTORE IX REG.	0.30 00	023540.00
		NOP,0		0.30 00	023540.40
XCS8N		LX,\$X12,XCSZ2		21754.30 10	023541.00
		LX,\$X12,XCSZ2	-LOAD WITH ZEROS N TIMES %12□	21754.30 10	023541.40
		LX,\$X12,XCSZ2		21754.30 10	023542.00
		LX,\$X12,XCSZ2		21754.30 10	023542.40
		LX,\$X12,XCSZ2		21754.30 10	023543.00
		LX,\$X12,XCSZ2		21754.30 10	023543.40
		LX,\$X12,XCSZ2		21754.30 10	023544.00
		LX,\$X12,XCSZ2		21754.30 10	023544.40
		LX,\$X12,XCSZ2		21754.30 10	023545.00
		LX,\$X12,XCSZ2		21754.30 10	023545.40
		LX,\$X12,XCSZ2		21754.30 10	023546.00
		LX,\$X12,XCSZ2		21754.30 10	023546.40
		LX,\$X12,XCSZ2		21754.30 10	023547.00
		LX,\$X12,XCSZ2		21754.30 10	023547.40
		LX,\$X12,XCSZ2		21754.30 10	023550.00
		LX,\$X12,XCSZ2		21754.30 10	023550.40
		LX,\$X12,XCSZ2		21754.30 10	023551.00
		LX,\$X12,XCSZ2		21754.30 10	023551.40
		LX,\$X12,XCSZ2		21754.30 10	023552.00
		LX,\$X12,XCSZ2		21754.30 10	023552.40
XCS8N1		LX,\$X12,XCSZ1	-LOAD ONCE WITH ONES	21753.30 10	023553.00
		KV,\$X12,XCSZ1	-TEST BITS 0-24	21753.30 90	023553.40
		SIC,SEN		1310.00 80	023554.00
		BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023554.40
		KC,\$X12,XCSZ1	-TEST BITS 28-45	21753.31 90	023555.00
		SIC,SEN		1310.00 80	023555.40
		BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023556.00
		SR,\$X12,XCSZ5	-REFILL TO WORK AREA	21756.31 70	023556.40
		SIC,SEN		1310.00 80	023557.00
		BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023557.40
		LC,\$X12,XCSZ5	-REFILL INTO COUNT FIELD	21756.30 50	023560.00
		KC,\$X12,XCSZ1	-TEST BITS 46-63	21753.31 90	023560.40
		SIC,SEN		1310.00 80	023561.00
		BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023561.40
		LX,\$X12,XCSZ2		21754.30 10	023562.00
		NOP	-RESTORE IX REG.	0.30 00	023562.40
		NOP,0		0.30 00	023563.00
XCS8P		LX,\$X13,XCSZ2		21754.32 10	023563.40
		LX,\$X13,XCSZ2	-LOAD WITH ZEROS N TIMES %13□	21754.32 10	023564.00
		LX,\$X13,XCSZ2		21754.32 10	023564.40
		LX,\$X13,XCSZ2		21754.32 10	023565.00
		LX,\$X13,XCSZ2		21754.32 10	023565.40
		LX,\$X13,XCSZ2		21754.32 10	023566.00
		LX,\$X13,XCSZ2		21754.32 10	023566.40
		LX,\$X13,XCSZ2		21754.32 10	023567.00
		LX,\$X13,XCSZ2		21754.32 10	023567.40
		LX,\$X13,XCSZ2		21754.32 10	023570.00
		LX,\$X13,XCSZ2		21754.32 10	023570.40

	LX,\$X13,XCSZ2		21754.32 10	023571.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023571.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023572.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023572.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023573.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023573.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023574.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ2	21754.32 10	023574.40
	LX,\$X13,XCSZ1		21754.32 10	023575.00
XCS8P1	KV,\$X13,XCSZ1	-LOAD ONCE WITH ONES	21753.32 10	023575.40
	SIC,SEN	-TEST BITS 0-24	21753.32 90	023576.00
	BZXEZ,SERS		1310.00 80	023576.40
	KC,\$X13,XCSZ1	-ERR IF BIT LOST	1304.32 C4	023577.00
	SIC,SEN	-TEST BITS 28-45	21753.33 90	023577.40
	BZXE,SERS		1310.00 80	023600.00
	SR,\$X13,XCSZ5	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023600.40
	SIC,SEN	-REFILL TO WORK AREA	21756.33 70	023601.00
	BZXF,SERS		1310.00 80	023601.40
	LC,\$X13,XCSZ5	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023602.00
	KC,\$X13,XCSZ1	-REFILL INTO COUNT FIELD	21756.32 50	023602.40
	SIC,SEN	-TEST BITS 46-63	21753.33 90	023603.00
	BZXE,SERS		1310.00 80	023603.40
	LX,\$X13,XCSZ2	-ERR IF BIT LOST	1304.32 C0	023604.00
	NOP		21754.32 10	023604.40
	NOP,0		0.30 00	023605.00
XCS8Q	LX,\$X14,XCSZ2		0.30 00	023605.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ2	21754.34 10	023606.00
	LX,\$X14,XCSZ2	-LOAD WITH ZEROS N TIMES %14□	21754.34 10	023606.40
	LX,\$X14,XCSZ2		21754.34 10	023607.00
	LX,\$X14,XCSZ2		21754.34 10	023607.40
	LX,\$X14,XCSZ2		21754.34 10	023610.00
	LX,\$X14,XCSZ2		21754.34 10	023610.40
	LX,\$X14,XCSZ2		21754.34 10	023611.00
	LX,\$X14,XCSZ2		21754.34 10	023611.40
	LX,\$X14,XCSZ2		21754.34 10	023612.00
	LX,\$X14,XCSZ2		21754.34 10	023612.40
	LX,\$X14,XCSZ2		21754.34 10	023613.00
	LX,\$X14,XCSZ2		21754.34 10	023613.40
	LX,\$X14,XCSZ2		21754.34 10	023614.00
	LX,\$X14,XCSZ2		21754.34 10	023614.40
	LX,\$X14,XCSZ2		21754.34 10	023615.00
	LX,\$X14,XCSZ2		21754.34 10	023615.40
	LX,\$X14,XCSZ2		21754.34 10	023616.00
	LX,\$X14,XCSZ2		21754.34 10	023616.40
	LX,\$X14,XCSZ2		21754.34 10	023617.00
	LX,\$X14,XCSZ2		21754.34 10	023617.40
XCS8Q1	LX,\$X14,XCSZ1	-LOAD ONCE WITH ONES	21753.34 10	023620.00
	KV,\$X14,XCSZ1	-TEST BITS 0-24	21753.34 90	023620.40
	SIC,SEN		1310.00 80	023621.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023621.40
	KC,\$X14,XCSZ1	-TEST BITS 28-45	21753.35 90	023622.00
	SIC,SEN		1310.00 80	023622.40
	BZXE,SERS	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023623.00
	SR,\$X14,XCSZ5	-REFILL TO WORK AREA	21756.35 70	023623.40
	SIC,SEN		1310.00 80	023624.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023624.40
	LC,\$X14,XCSZ5	-REFILL INTO COUNT FIELD	21756.34 50	023625.00
	KC,\$X14,XCSZ1	-TEST BITS 46-63	21753.35 90	023625.40
	SIC,SEN		1310.00 80	023626.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023626.40
	LX,\$X14,XCSZ2		21754.34 10	023627.00
	NOP		0.30 00	023627.40
	NOP,0		0.30 00	023630.00
XCS8R	LX,\$X15,XCSZ2		21754.36 10	023630.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023631.00
	LX,\$X15,XCSZ2	-LOAD WITH ZEROS N TIMES %15□	21754.36 10	023631.40

	LX,\$X15,XCSZ2		21754.36 10	023632.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023632.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023633.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023633.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023634.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023634.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023635.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023635.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023636.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023636.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023637.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023637.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023640.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ2	21754.36 10	023640.40
	LX,\$X15,XCSZ1		21754.36 10	023641.00
XCS8R1	KV,\$X15,XCSZ1		21754.36 10	023641.40
	SIC,SEN		21754.36 10	023642.00
	BZXEZ,SERS		21754.36 10	023642.40
	KC,\$X15,XCSZ1	-LOAD ONCE WITH ONES	21753.36 10	023642.40
	SIC,SEN	-TEST BITS 0-24	21753.36 90	023643.00
	BZXE,SERS		1310.00 80	023643.40
	SR,\$X15,XCSZ5	-ERR IF BIT LOST	1304.32 C4	023644.00
	SIC,SEN	-TEST BITS 28-45	21753.37 90	023644.40
	BZXF,SERS		1310.00 80	023645.00
	LC,\$X15,XCSZ5	-ERR IF IX BITS DONT COMPARE	1304.32 C0	023645.40
	KC,\$X15,XCSZ1	-REFILL TO WORK AREA	21756.37 70	023646.00
	SIC,SEN		1310.00 80	023646.40
	BZXE,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023647.00
	LX,\$X15,XCSZ2	-REFILL INTO COUNT FIELD	21756.36 50	023647.40
	NOP	-TEST BITS 46-63	21753.37 90	023650.00
	NOP,0		1310.00 80	023650.40
	B,\$+1.0	-ERR IF BIT LOST	1304.32 C0	023651.00
	B,XCS8		21754.36 10	023651.40
	SIC,SEN0+.32	-RESTORE IX REG.	0.30 00	023652.00
	B,SSW		0.30 00	023652.40
XCS9	LX,\$X0,XCSZ2		23654.10 00	023653.00
	LX,\$X2,XCSZ2	-TO LOOP IN LOW ONES TEST	23173.10 00	023653.40
	LX,\$X4,XCSZ2		1311.40 80	023654.00
	LX,\$X6,XCSZ2	-TEST SENSE SWITCHES	1301.10 00	023654.40
	LX,\$X8,XCSZ2		21754.00 10	023655.00
	LX,\$X10,XCSZ2	-INITIALIZE BY	21754.02 10	023655.40
	LX,\$X12,XCSZ2		21754.04 10	023656.00
	LX,\$X14,XCSZ2	-SETTING ALL IX	21754.06 10	023656.40
	LX,\$X15,XCSZ2		21754.10 10	023657.00
	LX,\$X0,XCSZ1	-REGS TO ZEROS	21754.12 10	023657.40
	SX,\$X0,XCSZ5		21754.14 10	023660.00
	SX,\$X0,XCSZ5	-PREPARE FOR	21754.16 10	023660.40
	SX,\$X0,XCSZ5		21754.20 10	023661.00
	SX,\$X0,XCSZ5	-LOW ONE	21754.22 10	023661.40
	SX,\$X0,XCSZ5		21754.24 10	023662.00
	SX,\$X0,XCSZ5	-%DISTURBED	21754.26 10	023662.40
	SX,\$X0,XCSZ5		21754.30 10	023663.00
	SX,\$X0,XCSZ5	-TEST	21754.32 10	023663.40
	SX,\$X0,XCSZ5		21754.34 10	023664.00
	SX,\$X0,XCSZ5		21754.36 10	023664.40
CS9A	LX,\$X0,XCSZ1	-LOAD WITH ONES ONE TIME %0	21753.00 10	023665.00
	SX,\$X0,XCSZ5		21756.01 10	023665.40
	SX,\$X0,XCSZ5	-READ IX REG N TIMES	21756.01 10	023666.00
	SX,\$X0,XCSZ5		21756.01 10	023666.40
	SX,\$X0,XCSZ5		21756.01 10	023667.00
	SX,\$X0,XCSZ5		21756.01 10	023667.40
	SX,\$X0,XCSZ5		21756.01 10	023670.00
	SX,\$X0,XCSZ5		21756.01 10	023670.40
	SX,\$X0,XCSZ5		21756.01 10	023671.00
	SX,\$X0,XCSZ5		21756.01 10	023671.40
	SX,\$X0,XCSZ5		21756.01 10	023672.00
	SX,\$X0,XCSZ5		21756.01 10	023672.40

	SX,\$X0,XCSZ5		21756.01	10	023673.00
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023674.00
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023674.40
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023675.00
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023675.40
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023676.00
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023676.40
	SX,\$X0,XCSZ5	SX,\$X0,XCSZ5	21756.01	10	023677.00
XCS9A1	KV,\$X0,XCSZ1		21753.00	90	023677.40
	SIC,SEN	-TEST BITS 0-24	1310.00	80	023700.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32	C4	023700.40
	KC,\$X0,XCSZ1	-TEST BITS 28-45	21753.01	90	023701.00
	SIC,SEN		1310.00	80	023701.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32	C0	023702.00
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	023702.40
	SIC,SEN		1310.00	80	023703.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	023703.40
	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	023704.00
	KC,\$X0,XCSZ1	-TEST BITS 46-63	21753.01	90	023704.40
	SIC,SEN		1310.00	80	023705.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32	C0	023705.40
	LX,\$X0,XCSZ2	-RESTORE IX REG.	21754.00	10	023706.00
	NOP,0		0.30	00	023706.40
	NOP,0		0.30	00	023707.00
XCS9B	LX,\$X1,XCSZ1	-LOAD WITH ONES ONE TIME %1□	21753.02	10	023707.40
	SX,\$X1,XCSZ5		21756.03	10	023710.00
	SX,\$X1,XCSZ5	-READ IX REG N TIMES	21756.03	10	023710.40
	SX,\$X1,XCSZ5		21756.03	10	023711.00
	SX,\$X1,XCSZ5		21756.03	10	023711.40
	SX,\$X1,XCSZ5		21756.03	10	023712.00
	SX,\$X1,XCSZ5		21756.03	10	023712.40
	SX,\$X1,XCSZ5		21756.03	10	023713.00
	SX,\$X1,XCSZ5		21756.03	10	023713.40
	SX,\$X1,XCSZ5		21756.03	10	023714.00
	SX,\$X1,XCSZ5		21756.03	10	023714.40
	SX,\$X1,XCSZ5		21756.03	10	023715.00
	SX,\$X1,XCSZ5		21756.03	10	023715.40
	SX,\$X1,XCSZ5		21756.03	10	023716.00
	SX,\$X1,XCSZ5		21756.03	10	023716.40
	SX,\$X1,XCSZ5		21756.03	10	023717.00
	SX,\$X1,XCSZ5		21756.03	10	023717.40
	SX,\$X1,XCSZ5		21756.03	10	023720.00
	SX,\$X1,XCSZ5		21756.03	10	023720.40
	SX,\$X1,XCSZ5		21756.03	10	023721.00
	SX,\$X1,XCSZ5		21756.03	10	023721.40
XCS9B1	KV,\$X1,XCSZ1	-TEST BITS 0-24	21753.02	90	023722.00
	SIC,SEN		1310.00	80	023722.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32	C4	023723.00
	KC,\$X1,XCSZ1	-TEST BITS 28-45	21753.03	90	023723.40
	SIC,SEN		1310.00	80	023724.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32	C0	023724.40
	SR,\$X1,XCSZ5	-REFILL TO WORK AREA	21756.03	70	023725.00
	SIC,SEN		1310.00	80	023725.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	023726.00
	LC,\$X1,XCSZ5	-REFILL INTO COUNT FIELD	21756.02	50	023726.40
	KC,\$X1,XCSZ1	-TEST BITS 46-63	21753.03	90	023727.00
	SIC,SEN		1310.00	80	023727.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32	C0	023730.00
	LX,\$X1,XCSZ2	-RESTORE IX REG.	21754.02	10	023730.40
	NOP,0		0.30	00	023731.00
	NOP,0		0.30	00	023731.40
XCS9C	LX,\$X2,XCSZ1	-LOAD WITH ONES ONE TIME %2□	21753.04	10	023732.00
	SX,\$X2,XCSZ5		21756.05	10	023732.40
	SX,\$X2,XCSZ5	-READ IX REG N TIMES	21756.05	10	023733.00
	SX,\$X2,XCSZ5		21756.05	10	023733.40

	SX,\$X2,XCSZ5		21756.05 10	023734.00
	SX,\$X2,XCSZ5		21756.05 10	023735.00
	SX,\$X2,XCSZ5		21756.05 10	023735.40
	SX,\$X2,XCSZ5		21756.05 10	023736.00
	SX,\$X2,XCSZ5		21756.05 10	023736.40
	SX,\$X2,XCSZ5		21756.05 10	023737.00
	SX,\$X2,XCSZ5		21756.05 10	023737.40
	SX,\$X2,XCSZ5		21756.05 10	023740.00
	SX,\$X2,XCSZ5		21756.05 10	023740.40
	SX,\$X2,XCSZ5		21756.05 10	023741.00
	SX,\$X2,XCSZ5		21756.05 10	023741.40
	SX,\$X2,XCSZ5		21756.05 10	023742.00
	SX,\$X2,XCSZ5		21756.05 10	023742.40
	SX,\$X2,XCSZ5		21756.05 10	023743.00
	SX,\$X2,XCSZ5		21756.05 10	023743.40
	SX,\$X2,XCSZ5		21756.05 10	023744.00
XCS9C1	KV,\$X2,XCSZ1	-TEST BITS 0-24	21753.04 90	023744.40
	SIC,SEN		1310.00 80	023745.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023745.40
	KC,\$X2,XCSZ1	-TEST BITS 28-45	21753.05 90	023746.00
	SIC,SEN		1310.00 80	023746.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023747.00
	SR,\$X2,XCSZ5	-REFILL TO WORK AREA	21756.05 70	023747.40
	SIC,SEN		1310.00 80	023750.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023750.40
	LC,\$X2,XCSZ5	-REFILL INTO COUNT FIELD	21756.04 50	023751.00
	KC,\$X2,XCSZ1	-TEST BITS 46-63	21753.05 90	023751.40
	SIC,SEN		1310.00 80	023752.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023752.40
	LX,\$X2,XCSZ2	-RESTORE IX REG.	21754.04 10	023753.00
	NOP,0		0.30 00	023753.40
	NOP,0		0.30 00	023754.00
XCS9D	LX,\$X3,XCSZ1	-LOAD WITH ONES ONE TIME %3□	21753.06 10	023754.40
	SX,\$X3,XCSZ5		21756.07 10	023755.00
	SX,\$X3,XCSZ5	-READ IX REG N TIMES	21756.07 10	023755.40
	SX,\$X3,XCSZ5		21756.07 10	023756.00
	SX,\$X3,XCSZ5		21756.07 10	023756.40
	SX,\$X3,XCSZ5		21756.07 10	023757.00
	SX,\$X3,XCSZ5		21756.07 10	023757.40
	SX,\$X3,XCSZ5		21756.07 10	023760.00
	SX,\$X3,XCSZ5		21756.07 10	023760.40
	SX,\$X3,XCSZ5		21756.07 10	023761.00
	SX,\$X3,XCSZ5		21756.07 10	023761.40
	SX,\$X3,XCSZ5		21756.07 10	023762.00
	SX,\$X3,XCSZ5		21756.07 10	023762.40
	SX,\$X3,XCSZ5		21756.07 10	023763.00
	SX,\$X3,XCSZ5		21756.07 10	023763.40
	SX,\$X3,XCSZ5		21756.07 10	023764.00
	SX,\$X3,XCSZ5		21756.07 10	023764.40
	SX,\$X3,XCSZ5		21756.07 10	023765.00
	SX,\$X3,XCSZ5		21756.07 10	023765.40
	SX,\$X3,XCSZ5		21756.07 10	023766.00
	SX,\$X3,XCSZ5		21756.07 10	023766.40
XCSD91	KV,\$X3,XCSZ1	-TEST BITS 0-24	21753.06 90	023767.00
	SIC,SEN		1310.00 80	023767.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	023770.00
	KC,\$X3,XCSZ1	-TEST BITS 28-45	21753.07 90	023770.40
	SIC,SEN		1310.00 80	023771.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	023771.40
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	023772.00
	SIC,SEN		1310.00 80	023772.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	023773.00
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	023773.40
	KC,\$X3,XCSZ1	-TEST BITS 46-63	21753.07 90	023774.00
	SIC,SEN		1310.00 80	023774.40

	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	023775.00
	LX,\$X3,XCSZ2	-RESTORE IX REG.	21754.06 10	023775.40
	NOP,0		0.30 00	023776.00
	NOP,0		0.30 00	023776.40
XCS9E	LX,\$X4,XCSZ1	-LOAD WITH ONES ONE TIME %4	21753.10 10	023777.00
	SX,\$X4,XCSZ5		21756.11 10	023777.40
	SX,\$X4,XCSZ5	-READ IX REG N TIMES	21756.11 10	024000.00
	SX,\$X4,XCSZ5		21756.11 10	024000.40
	SX,\$X4,XCSZ5		21756.11 10	024001.00
	SX,\$X4,XCSZ5		21756.11 10	024001.40
	SX,\$X4,XCSZ5		21756.11 10	024002.00
	SX,\$X4,XCSZ5		21756.11 10	024002.40
	SX,\$X4,XCSZ5		21756.11 10	024003.00
	SX,\$X4,XCSZ5		21756.11 10	024003.40
	SX,\$X4,XCSZ5		21756.11 10	024004.00
	SX,\$X4,XCSZ5		21756.11 10	024004.40
	SX,\$X4,XCSZ5		21756.11 10	024005.00
	SX,\$X4,XCSZ5		21756.11 10	024005.40
	SX,\$X4,XCSZ5		21756.11 10	024006.00
	SX,\$X4,XCSZ5		21756.11 10	024006.40
	SX,\$X4,XCSZ5		21756.11 10	024007.00
	SX,\$X4,XCSZ5		21756.11 10	024007.40
	SX,\$X4,XCSZ5		21756.11 10	024010.00
	SX,\$X4,XCSZ5		21756.11 10	024010.40
	SX,\$X4,XCSZ5		21756.11 10	024011.00
XCS9E1	KV,\$X4,XCSZ1	-TEST BITS 0-24	21753.10 90	024011.40
	SIC,SEN		1310.00 80	024012.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024012.40
	KC,\$X4,XCSZ1	-TEST BITS 28-45	21753.11 90	024013.00
	SIC,SEN		1310.00 80	024013.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024014.00
	SR,\$X4,XCSZ5	-REFILL TO WORK AREA	21756.11 70	024014.40
	SIC,SEN		1310.00 80	024015.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024015.40
	LC,\$X4,XCSZ5	-REFILL INTO COUNT FIELD	21756.10 50	024016.00
	KC,\$X4,XCSZ1	-TEST BITS 46-63	21753.11 90	024016.40
	SIC,SEN		1310.00 80	024017.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024017.40
	LX,\$X4,XCSZ2	-RESTORE IX REG.	21754.10 10	024020.00
	NOP,0		0.30 00	024020.40
	NOP,0		0.30 00	024021.00
XCS9F	LX,\$X5,XCSZ1	-LOAD WITH ONES ONE TIME %5	21753.12 10	024021.40
	SX,\$X5,XCSZ5		21756.13 10	024022.00
	SX,\$X5,XCSZ5	-READ IX REG N TIMES	21756.13 10	024022.40
	SX,\$X5,XCSZ5		21756.13 10	024023.00
	SX,\$X5,XCSZ5		21756.13 10	024023.40
	SX,\$X5,XCSZ5		21756.13 10	024024.00
	SX,\$X5,XCSZ5		21756.13 10	024024.40
	SX,\$X5,XCSZ5		21756.13 10	024025.00
	SX,\$X5,XCSZ5		21756.13 10	024025.40
	SX,\$X5,XCSZ5		21756.13 10	024026.00
	SX,\$X5,XCSZ5		21756.13 10	024026.40
	SX,\$X5,XCSZ5		21756.13 10	024027.00
	SX,\$X5,XCSZ5		21756.13 10	024027.40
	SX,\$X5,XCSZ5		21756.13 10	024030.00
	SX,\$X5,XCSZ5		21756.13 10	024030.40
	SX,\$X5,XCSZ5		21756.13 10	024031.00
	SX,\$X5,XCSZ5		21756.13 10	024031.40
	SX,\$X5,XCSZ5		21756.13 10	024032.00
	SX,\$X5,XCSZ5		21756.13 10	024032.40
	SX,\$X5,XCSZ5		21756.13 10	024033.00
	SX,\$X5,XCSZ5		21756.13 10	024033.40
XCS9F1	KV,\$X5,XCSZ1	-TEST BITS 0-24	21753.12 90	024034.00
	SIC,SEN		1310.00 80	024034.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024035.00
	KC,\$X5,XCSZ1	-TEST BITS 28-45	21753.13 90	024035.40

	SIC,SEN		1310.00 80	024036.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024036.40
	SR,\$X5,XCSZ5	-REFILL TO WORK AREA	21756.13 70	024037.00
	SIC,SEN		1310.00 80	024037.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024040.00
	LC,\$X5,XCSZ5	-REFILL INTO COUNT FIELD	21756.12 50	024040.40
	KC,\$X5,XCSZ1	-TEST BITS 46-63	21753.13 90	024041.00
	SIC,SEN		1310.00 80	024041.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024042.00
	LX,\$X5,XCSZ2	-RESTORE IX REG.	21754.12 10	024042.40
	NOP,0		0.30 00	024043.00
	NOP,0		0.30 00	024043.40
XCS9G	LX,\$X6,XCSZ1	-LOAD WITH ONES ONE TIME %6□	21753.14 10	024044.00
	SX,\$X6,XCSZ5		21756.15 10	024044.40
	SX,\$X6,XCSZ5	-READ IX REG N TIMES	21756.15 10	024045.00
	SX,\$X6,XCSZ5		21756.15 10	024045.40
	SX,\$X6,XCSZ5		21756.15 10	024046.00
	SX,\$X6,XCSZ5		21756.15 10	024046.40
	SX,\$X6,XCSZ5		21756.15 10	024047.00
	SX,\$X6,XCSZ5		21756.15 10	024047.40
	SX,\$X6,XCSZ5		21756.15 10	024050.00
	SX,\$X6,XCSZ5		21756.15 10	024050.40
	SX,\$X6,XCSZ5		21756.15 10	024051.00
	SX,\$X6,XCSZ5		21756.15 10	024051.40
	SX,\$X6,XCSZ5		21756.15 10	024052.00
	SX,\$X6,XCSZ5		21756.15 10	024052.40
	SX,\$X6,XCSZ5		21756.15 10	024053.00
	SX,\$X6,XCSZ5		21756.15 10	024053.40
	SX,\$X6,XCSZ5		21756.15 10	024054.00
	SX,\$X6,XCSZ5		21756.15 10	024054.40
	SX,\$X6,XCSZ5		21756.15 10	024055.00
	SX,\$X6,XCSZ5		21756.15 10	024055.40
	SX,\$X6,XCSZ5		21756.15 10	024056.00
XCS9G1	KV,\$X6,XCSZ1	-TEST BITS 0-24	21753.14 90	024056.40
	SIC,SEN		1310.00 80	024057.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024057.40
	KC,\$X6,XCSZ1	-TEST BITS 28-45	21753.15 90	024060.00
	SIC,SEN		1310.00 80	024060.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024061.00
	SR,\$X6,XCSZ5	-REFILL TO WORK AREA	21756.15 70	024061.40
	SIC,SEN		1310.00 80	024062.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024062.40
	LC,\$X6,XCSZ5	-REFILL INTO COUNT FIELD	21756.14 50	024063.00
	KC,\$X6,XCSZ1	-TEST BITS 46-63	21753.15 90	024063.40
	SIC,SEN		1310.00 80	024064.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024064.40
	LX,\$X6,XCSZ2	-RESTORE IX REG.	21754.14 10	024065.00
	NOP,0		0.30 00	024065.40
	NOP,0		0.30 00	024066.00
XCS9H	LX,\$X7,XCSZ1	-LOAD WITH ONES ONE TIME %7□	21753.16 10	024066.40
	SX,\$X7,XCSZ5		21756.17 10	024067.00
	SX,\$X7,XCSZ5	-READ IX REG N TIMES	21756.17 10	024067.40
	SX,\$X7,XCSZ5		21756.17 10	024070.00
	SX,\$X7,XCSZ5		21756.17 10	024070.40
	SX,\$X7,XCSZ5		21756.17 10	024071.00
	SX,\$X7,XCSZ5		21756.17 10	024071.40
	SX,\$X7,XCSZ5		21756.17 10	024072.00
	SX,\$X7,XCSZ5		21756.17 10	024072.40
	SX,\$X7,XCSZ5		21756.17 10	024073.00
	SX,\$X7,XCSZ5		21756.17 10	024073.40
	SX,\$X7,XCSZ5		21756.17 10	024074.00
	SX,\$X7,XCSZ5		21756.17 10	024074.40
	SX,\$X7,XCSZ5		21756.17 10	024075.00
	SX,\$X7,XCSZ5		21756.17 10	024075.40
	SX,\$X7,XCSZ5		21756.17 10	024076.00
	SX,\$X7,XCSZ5		21756.17 10	024076.40

	SX,\$X7,XCSZ5		21756.17 10	024077.00
	SX,\$X7,XCSZ5		21756.17 10	024100.00
	SX,\$X7,XCSZ5		21756.17 10	024100.40
XCS9H1	KV,\$X7,XCSZ1	-TEST BITS 0-24	21753.16 90	024101.00
	SIC,SEN		1310.00 80	024101.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024102.00
	KC,\$X7,XCSZ1	-TEST BITS 28-45	21753.17 90	024102.40
	SIC,SEN		1310.00 80	024103.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024103.40
	SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17 70	024104.00
	SIC,SEN		1310.00 80	024104.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024105.00
	LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16 50	024105.40
	KC,\$X7,XCSZ1	-TEST BITS 46-63	21753.17 90	024106.00
	SIC,SEN		1310.00 80	024106.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024107.00
	LX,\$X7,XCSZ2	-RESTORE IX REG.	21754.16 10	024107.40
	NOP,0		0.30 00	024110.00
	NOP,0		0.30 00	024110.40
XCS9J	LX,\$X8,XCSZ1	-LOAD WITH ONES ONE TIME %8□	21753.20 10	024111.00
	SX,\$X8,XCSZ5		21756.21 10	024111.40
	SX,\$X8,XCSZ5	-READ IX REG N TIMES	21756.21 10	024112.00
	SX,\$X8,XCSZ5		21756.21 10	024112.40
	SX,\$X8,XCSZ5		21756.21 10	024113.00
	SX,\$X8,XCSZ5		21756.21 10	024113.40
	SX,\$X8,XCSZ5		21756.21 10	024114.00
	SX,\$X8,XCSZ5		21756.21 10	024114.40
	SX,\$X8,XCSZ5		21756.21 10	024115.00
	SX,\$X8,XCSZ5		21756.21 10	024115.40
	SX,\$X8,XCSZ5		21756.21 10	024116.00
	SX,\$X8,XCSZ5		21756.21 10	024116.40
	SX,\$X8,XCSZ5		21756.21 10	024117.00
	SX,\$X8,XCSZ5		21756.21 10	024117.40
	SX,\$X8,XCSZ5		21756.21 10	024120.00
	SX,\$X8,XCSZ5		21756.21 10	024120.40
	SX,\$X8,XCSZ5		21756.21 10	024121.00
	SX,\$X8,XCSZ5		21756.21 10	024121.40
	SX,\$X8,XCSZ5		21756.21 10	024122.00
	SX,\$X8,XCSZ5		21756.21 10	024122.40
	SX,\$X8,XCSZ5		21756.21 10	024123.00
XCS9J1	KV,\$X8,XCSZ1	-TEST BITS 0-24	21753.20 90	024123.40
	SIC,SEN		1310.00 80	024124.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024124.40
	KC,\$X8,XCSZ1	-TEST BITS 28-45	21753.21 90	024125.00
	SIC,SEN		1310.00 80	024125.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024126.00
	SR,\$X8,XCSZ5	-REFILL TO WORK AREA	21756.21 70	024126.40
	SIC,SEN		1310.00 80	024127.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024127.40
	LC,\$X8,XCSZ5	-REFILL INTO COUNT FIELD	21756.20 50	024130.00
	KC,\$X8,XCSZ1	-TEST BITS 46-63	21753.21 90	024130.40
	SIC,SEN		1310.00 80	024131.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024131.40
	LX,\$X8,XCSZ2	-RESTORE IX REG.	21754.20 10	024132.00
	NOP,0		0.30 00	024132.40
	NOP,0		0.30 00	024133.00
XCS9K	LX,\$X9,XCSZ1	-LOAD WITH ONES ONE TIME %9□	21753.22 10	024133.40
	SX,\$X9,XCSZ5		21756.23 10	024134.00
	SX,\$X9,XCSZ5	-READ IX REG N TIMES	21756.23 10	024134.40
	SX,\$X9,XCSZ5		21756.23 10	024135.00
	SX,\$X9,XCSZ5		21756.23 10	024135.40
	SX,\$X9,XCSZ5		21756.23 10	024136.00
	SX,\$X9,XCSZ5		21756.23 10	024136.40
	SX,\$X9,XCSZ5		21756.23 10	024137.00
	SX,\$X9,XCSZ5		21756.23 10	024137.40

	SX,\$X9,XCSZ5		21756.23	10	024140.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024140.40
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024141.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024141.40
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024142.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024142.40
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024143.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024143.40
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024144.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024144.40
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024145.00
	SX,\$X9,XCSZ5	SX,\$X9,XCSZ5	21756.23	10	024145.40
XCS9K1	KV,\$X9,XCSZ1	-TEST BITS 0-24	21753.22	90	024146.00
	SIC,SEN		1310.00	80	024146.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32	C4	024147.00
	KC,\$X9,XCSZ1	-TEST BITS 28-45	21753.23	90	024147.40
	SIC,SEN		1310.00	80	024150.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32	C0	024150.40
	SR,\$X9,XCSZ5	-REFILL TO WORK AREA	21756.23	70	024151.00
	SIC,SEN		1310.00	80	024151.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	024152.00
	LC,\$X9,XCSZ5	-REFILL INTO COUNT FIELD	21756.22	50	024152.40
	KC,\$X9,XCSZ1	-TEST BITS 46-63	21753.23	90	024153.00
	SIC,SEN		1310.00	80	024153.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32	C0	024154.00
	LX,\$X9,XCSZ2	-RESTORE IX REG.	21754.22	10	024154.40
	NOP,0		0.30	00	024155.00
	NOP,0		0.30	00	024155.40
XCS9L	LX,\$X10,XCSZ1	-LOAD WITH ONES ONE TIME %10□	21753.24	10	024156.00
	SX,\$X10,XCSZ5		21756.25	10	024156.40
	SX,\$X10,XCSZ5	-READ IX REG N. TIMES	21756.25	10	024157.00
	SX,\$X10,XCSZ5		21756.25	10	024157.40
	SX,\$X10,XCSZ5		21756.25	10	024160.00
	SX,\$X10,XCSZ5		21756.25	10	024160.40
	SX,\$X10,XCSZ5		21756.25	10	024161.00
	SX,\$X10,XCSZ5		21756.25	10	024161.40
	SX,\$X10,XCSZ5		21756.25	10	024162.00
	SX,\$X10,XCSZ5		21756.25	10	024162.40
	SX,\$X10,XCSZ5		21756.25	10	024163.00
	SX,\$X10,XCSZ5		21756.25	10	024163.40
	SX,\$X10,XCSZ5		21756.25	10	024164.00
	SX,\$X10,XCSZ5		21756.25	10	024164.40
	SX,\$X10,XCSZ5		21756.25	10	024165.00
	SX,\$X10,XCSZ5		21756.25	10	024165.40
	SX,\$X10,XCSZ5		21756.25	10	024166.00
	SX,\$X10,XCSZ5		21756.25	10	024166.40
	SX,\$X10,XCSZ5		21756.25	10	024167.00
	SX,\$X10,XCSZ5		21756.25	10	024167.40
	SX,\$X10,XCSZ5		21756.25	10	024170.00
XCS9L1	KV,\$X10,XCSZ1	-TEST BITS 0-24	21753.24	90	024170.40
	SIC,SEN		1310.00	80	024171.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32	C4	024171.40
	KC,\$X10,XCSZ1	-TEST BITS 28-45	21753.25	90	024172.00
	SIC,SEN		1310.00	80	024172.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32	C0	024173.00
	SR,\$X10,XCSZ5	-REFILL TO WORK AREA	21756.25	70	024173.40
	SIC,SEN		1310.00	80	024174.00
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23	40	024174.40
	LC,\$X10,XCSZ5	-REFILL INTO COUNT FIELD	21756.24	50	024175.00
	KC,\$X10,XCSZ1	-TEST BITS 46-63	21753.25	90	024175.40
	SIC,SEN		1310.00	80	024176.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32	C0	024176.40
	LX,\$X10,XCSZ2	-RESTORE IX REG.	21754.24	10	024177.00
	NOP,0		0.30	00	024177.40
	NOP,0		0.30	00	024200.00
XCS9M	LX,\$X11,XCSZ1	-LOAD WITH ONES ONE TIME %11□	21753.26	10	024200.40

	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5	-READ IX REG N TIMES	21756.27 10	024201.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024202.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024202.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024203.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024203.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024204.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024204.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024205.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024205.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024206.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024206.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024207.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024207.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024210.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024210.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024211.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024211.40
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024212.00
	SX,\$X11,XCSZ5	SX,\$X11,XCSZ5		21756.27 10	024212.40
XCS9M1	KV,\$X11,XCSZ1		-TEST BITS 0-24	21753.26 90	024213.00
	SIC,SEN			1310.00 80	024213.40
		BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024214.00
	KC,\$X11,XCSZ1		-TEST BITS 28-45	21753.27 90	024214.40
	SIC,SEN			1310.00 80	024215.00
		BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024215.40
	SR,\$X11,XCSZ5		-REFILL TO WORK AREA	21756.27 70	024216.00
	SIC,SEN			1310.00 80	024216.40
		BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024217.00
	LC,\$X11,XCSZ5		-REFILL INTO COUNT FIELD	21756.26 50	024217.40
	KC,\$X11,XCSZ1		-TEST BITS 46-63	21753.27 90	024220.00
	SIC,SEN			1310.00 80	024220.40
		BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024221.00
	LX,\$X11,XCSZ2		-RESTORE IX REG.	21754.26 10	024221.40
	NOP,0			0.30 00	024222.00
		NOP,0		0.30 00	024222.40
XCS9N	LX,\$X12,XCSZ1		-LOAD WITH ONES ONE TIME%12□	21753.30 10	024223.00
	SX,\$X12,XCSZ5			21756.31 10	024223.40
		SX,\$X12,XCSZ5	-READ IX REG N TIMES	21756.31 10	024224.00
	SX,\$X12,XCSZ5			21756.31 10	024224.40
		SX,\$X12,XCSZ5		21756.31 10	024225.00
	SX,\$X12,XCSZ5			21756.31 10	024225.40
		SX,\$X12,XCSZ5		21756.31 10	024226.00
	SX,\$X12,XCSZ5			21756.31 10	024226.40
		SX,\$X12,XCSZ5		21756.31 10	024227.00
	SX,\$X12,XCSZ5			21756.31 10	024227.40
		SX,\$X12,XCSZ5		21756.31 10	024230.00
	SX,\$X12,XCSZ5			21756.31 10	024230.40
		SX,\$X12,XCSZ5		21756.31 10	024231.00
	SX,\$X12,XCSZ5			21756.31 10	024231.40
		SX,\$X12,XCSZ5		21756.31 10	024232.00
	SX,\$X12,XCSZ5			21756.31 10	024232.40
		SX,\$X12,XCSZ5		21756.31 10	024233.00
	SX,\$X12,XCSZ5			21756.31 10	024233.40
		SX,\$X12,XCSZ5		21756.31 10	024234.00
	SX,\$X12,XCSZ5			21756.31 10	024234.40
		SX,\$X12,XCSZ5		21756.31 10	024235.00
XCS9N1	KV,\$X12,XCSZ1		-TEST BITS 0-24	21753.30 90	024235.40
	SIC,SEN			1310.00 80	024236.00
		BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024236.40
	KC,\$X12,XCSZ1		-TEST BITS 28-45	21753.31 90	024237.00
	SIC,SEN			1310.00 80	024237.40
		BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024240.00
	SR,\$X12,XCSZ5		-REFILL TO WORK AREA	21756.31 70	024240.40
	SIC,SEN			1310.00 80	024241.00
		BZXF,SERS	-ERR IF XF NOT1 AS IT SHOULD BE	1304.23 40	024241.40

	LC,\$X12,XCSZ5	-REFILL INTO COUNT FIELD	21756.30 50	024242.00
	KC,\$X12,XCSZ1	-TEST BITS 46-63	21753.31 90	024242.40
	SIC,SEN		1310.00 80	024243.00
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024243.40
	LX,\$X12,XCSZ2	-RESTORE IX REG.	21754.30 10	024244.00
	NOP,0		0.30 00	024244.40
	NOP,0		0.30 00	024245.00
XCS9P	LX,\$X13,XCSZ1	-LOAD WITH ONES ONE TIME %13□	21753.32 10	024245.40
	SX,\$X13,XCSZ5		21756.33 10	024246.00
	SX,\$X13,XCSZ5	-READ IX REG N TIMES	21756.33 10	024246.40
	SX,\$X13,XCSZ5		21756.33 10	024247.00
	SX,\$X13,XCSZ5		21756.33 10	024247.40
	SX,\$X13,XCSZ5		21756.33 10	024250.00
	SX,\$X13,XCSZ5		21756.33 10	024250.40
	SX,\$X13,XCSZ5		21756.33 10	024251.00
	SX,\$X13,XCSZ5		21756.33 10	024251.40
	SX,\$X13,XCSZ5		21756.33 10	024252.00
	SX,\$X13,XCSZ5		21756.33 10	024252.40
	SX,\$X13,XCSZ5		21756.33 10	024253.00
	SX,\$X13,XCSZ5		21756.33 10	024253.40
	SX,\$X13,XCSZ5		21756.33 10	024254.00
	SX,\$X13,XCSZ5		21756.33 10	024254.40
	SX,\$X13,XCSZ5		21756.33 10	024255.00
	SX,\$X13,XCSZ5		21756.33 10	024255.40
XCS9P1	KV,\$X13,XCSZ1	-TEST BITS 0-24	21753.32 90	024256.00
	SIC,SEN		1310.00 80	024256.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024257.00
	KC,\$X13,XCSZ1	-TEST BITS 28-45	21753.33 90	024257.40
	SIC,SEN		1310.00 80	024260.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024260.40
	SR,\$X13,XCSZ5	-REFILL TO WORK AREA	21756.33 70	024261.00
	SIC,SEN		1310.00 80	024261.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024262.00
	LC,\$X13,XCSZ5	-REFILL INTO COUNT FIELD	21756.32 50	024262.40
	KC,\$X13,XCSZ1	-TEST BITS 46-63	21753.33 90	024263.00
	SIC,SEN		1310.00 80	024263.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	024264.00
	LX,\$X13,XCSZ2	-RESTORE IX REG.	21754.32 10	024264.40
	NOP,0		0.30 00	024265.00
	NOP,0		0.30 00	024265.40
XCS9Q	LX,\$X14,XCSZ1	-LOAD WITH ONES ONE TIME %14□	21753.34 10	024266.00
	SX,\$X14,XCSZ5		21756.35 10	024266.40
	SX,\$X14,XCSZ5	-READ IX REG N TIMES	21756.35 10	024267.00
	SX,\$X14,XCSZ5		21756.35 10	024267.40
	SX,\$X14,XCSZ5		21756.35 10	024270.00
	SX,\$X14,XCSZ5		21756.35 10	024270.40
	SX,\$X14,XCSZ5		21756.35 10	024271.00
	SX,\$X14,XCSZ5		21756.35 10	024271.40
	SX,\$X14,XCSZ5		21756.35 10	024272.00
	SX,\$X14,XCSZ5		21756.35 10	024272.40
	SX,\$X14,XCSZ5		21756.35 10	024273.00
	SX,\$X14,XCSZ5		21756.35 10	024273.40
	SX,\$X14,XCSZ5		21756.35 10	024274.00
	SX,\$X14,XCSZ5		21756.35 10	024274.40
	SX,\$X14,XCSZ5		21756.35 10	024275.00
	SX,\$X14,XCSZ5		21756.35 10	024275.40
	SX,\$X14,XCSZ5		21756.35 10	024276.00
	SX,\$X14,XCSZ5		21756.35 10	024276.40
	SX,\$X14,XCSZ5		21756.35 10	024277.00
	SX,\$X14,XCSZ5		21756.35 10	024277.40
	SX,\$X14,XCSZ5		21756.35 10	024300.00
XCS9Q1	KV,\$X14,XCSZ1	-TEST BITS 0-24	21753.34 90	024300.40
	SIC,SEN		1310.00 80	024301.00
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	024301.40
	KC,\$X14,XCSZ1	-TEST BITS 28-45	21753.35 90	024302.00
	SIC,SEN		1310.00 80	024302.40

	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	024303.00
	SR,\$X14,XCSZ5		-REFILL TO WORK AREA	21756.35 70	024303.40
	SIC,SEN			1310.00 80	024304.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024304.40
	LC,\$X14,XCSZ5		-REFILL INTO COUNT FIELD	21756.34 50	024305.00
	KC,\$X14,XCSZ1		-TEST BITS 46-63	21753.35 90	024305.40
	SIC,SEN			1310.00 80	024306.00
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	024306.40
	LX,\$X14,XCSZ2		-RESTORE IX REG.	21754.34 10	024307.00
	NOP,0			0.30 00	024307.40
	NOP,0			0.30 00	024310.00
XCS9R	LX,\$X15,XCSZ1		-LOAD WITH ONES ONE TIME %15	21753.36 10	024310.40
	SX,\$X15,XCSZ5			21756.37 10	024311.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5	-READ IX REG N TIMES	21756.37 10	024311.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024312.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024312.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024313.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024313.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024314.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024314.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024315.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024315.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024316.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024316.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024317.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024317.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024320.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024320.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024321.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024321.40
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024322.00
	SX,\$X15,XCSZ5	SX,\$X15,XCSZ5		21756.37 10	024322.40
XCS9R1	KV,\$X15,XCSZ1		-TEST BITS 0-24	21753.36 90	024323.00
	SIC,SEN			1310.00 80	024323.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	024324.00
	KC,\$X15,XCSZ1		-TEST BITS 28-45	21753.37 90	024324.40
	SIC,SEN			1310.00 80	024325.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	024325.40
	SR,\$X15,XCSZ5		-REFILL TO WORK AREA	21756.37 70	024326.00
	SIC,SEN			1310.00 80	024326.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHOULD BE	1304.23 40	024327.00
	LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36 50	024327.40
	KC,\$X15,XCSZ1		-TEST BITS 46-63	21753.37 90	024330.00
	SIC,SEN			1310.00 80	024330.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	024331.00
	LX,\$X15,XCSZ2		-RESTORE IX REG.	21754.36 10	024331.40
	NOP,0			0.30 00	024332.00
	NOP,0			0.30 00	024332.40
	B,\$+1.0			24334.10 00	024333.00
	B,XCS9		-TO LOOP IN LOW ONE DIST TEST	23655.10 00	024333.40
	SIC,SEN0+.32			1311.40 80	024334.00
	B,SSW		-TEST SENSE SWITCHES	1301.10 00	024334.40
XCS10	LX,\$X0,XCSZ1			21753.00 10	024335.00
	LX,\$X1,XCSZ1		-INITIALIZE BY	21753.02 10	024335.40
	LX,\$X2,XCSZ1			21753.04 10	024336.00
	LX,\$X3,XCSZ1		-SETTING ALL IX	21753.06 10	024336.40
	LX,\$X4,XCSZ1			21753.10 10	024337.00
	LX,\$X5,XCSZ1		-REGS TO ONES	21753.12 10	024337.40
	LX,\$X6,XCSZ1			21753.14 10	024340.00
	LX,\$X7,XCSZ1		-PREPARE FOR	21753.16 10	024340.40
	LX,\$X8,XCSZ1			21753.20 10	024341.00
	LX,\$X9,XCSZ1		-ALTERNATE ZERO	21753.22 10	024341.40
	LX,\$X10,XCSZ1			21753.24 10	024342.00
	LX,\$X11,XCSZ1		-WORD AND	21753.26 10	024342.40
	LX,\$X12,XCSZ1			21753.30 10	024343.00
	LX,\$X13,XCSZ1		-ALL ONES WORD	21753.32 10	024343.40

	LX,\$X14,XCSZ1	LX,\$X15,XCSZ1	-TEST	21753.36 10	024344.00
XCS10A	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1	-ALTERN WITH 0 AND ONES %0	21754.00 10	024344.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024345.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024345.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024346.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024346.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024347.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024347.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024350.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024350.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024351.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024351.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024352.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024352.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024353.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024353.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024354.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024354.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024355.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024355.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21753.00 10	024356.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		21754.00 10	024356.40
	LX,\$X0,XCSZ2		-ALL ZEROS IN %0	21754.00 10	024357.00
	KV,\$X0,XCSZ2		-TEST BITS 0-24	21754.00 90	024357.40
	SIC,SEN			1310.00 80	024360.00
		BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	024360.40
	KC,\$X0,XCSZ2		-TEST BITS 28-45	21754.01 90	024361.00
	SIC,SEN			1310.00 80	024361.40
		BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024362.00
	SR,\$X0,XCSZ5		-REFILL TO WORK AREA	21756.01 70	024362.40
	SIC,SEN			1310.00 80	024363.00
		BXF,SERS	-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024363.40
	LC,\$X0,XCSZ5		-REFILL INTO COUNT FIELD	21756.00 50	024364.00
	KC,\$X0,XCSZ2		-TEST BITS 46-63	21754.01 90	024364.40
	SIC,SEN			1310.00 80	024365.00
		BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	024365.40
	LX,\$X0,XCSZ1		-RESTORE IX REG.	21753.00 10	024366.00
	NOP,0			0.30 00	024366.40
		NOP,0		0.30 00	024367.00
XCS10B	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1	-ALTERN WITH 0 AND ONES %1	21754.02 10	024367.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024370.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024370.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024371.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024371.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024372.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024372.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024373.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024373.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024374.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024374.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024375.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024375.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024376.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024376.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024377.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024377.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024400.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21754.02 10	024400.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		21753.02 10	024401.00
	LX,\$X1,XCSZ2		-ALL ZEROS IN %1	21754.02 10	024401.40
	KV,\$X1,XCSZ2		-TEST BITS 0-24	21754.02 90	024402.00
	SIC,SEN			1310.00 80	024402.40
		BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	024403.00
	KC,\$X1,XCSZ2		-TEST BITS 28-45	21754.03 90	024403.40
	SIC,SEN			1310.00 80	024404.00
		BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024404.40

	SR,\$X1,XCSZ5		-REFILL TO WORK AREA	21756.03	70	024405.00
	SIC,SEN			1310.00	80	024405.40
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHD BE	1304.23	42	024406.00
	LC,\$X1,XCSZ5		-REFILL INTO COUNT FIELD	21756.02	50	024406.40
	KC,\$X1,XCSZ2		-TEST BITS 46-63	21754.03	90	024407.00
	SIC,SEN			1310.00	80	024407.40
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32	C0	024410.00
	LX,\$X1,XCSZ1		-RESTORE IX REG.	21753.02	10	024410.40
	NOP,0			0.30	00	024411.00
	NOP,0			0.30	00	024411.40
CS10C	LX,\$X2,XCSZ2			21754.04	10	024412.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1	-ALTERN WITH 0 AND ONES%2	21753.04	10	024412.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024413.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024413.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024414.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024414.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024415.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024415.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024416.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024416.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024417.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024417.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024420.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024420.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024421.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024421.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024422.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024422.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04	10	024423.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04	10	024423.40
	LX,\$X2,XCSZ2		-ALL ZEROS IN %2	21754.04	10	024424.00
	KV,\$X2,XCSZ2		-TEST BITS 0-24	21754.04	90	024424.40
	SIC,SEN			1310.00	80	024425.00
	BZXEZ,SERS		-ERR IF BIT PICKED UP	1304.32	C4	024425.40
	KC,\$X2,XCSZ2		-TEST BITS 28-45	21754.05	90	024426.00
	SIC,SEN			1310.00	80	024426.40
	BZXE,SERS		-BITS MUST COMPARE	1304.32	C0	024427.00
	SR,\$X2,XCSZ5		-REFILL TO WORK AREA	21756.05	70	024427.40
	SIC,SEN			1310.00	80	024430.00
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHD BE	1304.23	42	024430.40
	LC,\$X2,XCSZ5		-REFILL INTO COUNT FIELD	21756.04	50	024431.00
	KC,\$X2,XCSZ2		-TEST BITS 46-63	21754.05	90	024431.40
	SIC,SEN			1310.00	80	024432.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32	C0	024432.40
	LX,\$X2,XCSZ1		-RESTORE IX REG.	21753.04	10	024433.00
	NOP,0			0.30	00	024433.40
	NOP,0			0.30	00	024434.00
CS10D	LX,\$X3,XCSZ2			21754.06	10	024434.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1	-ALTERN WITH 0 AND ONES %3	21753.06	10	024435.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024435.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024436.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024436.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024437.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024437.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024440.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024440.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024441.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024441.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024442.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024442.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024443.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024443.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024444.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024444.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06	10	024445.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06	10	024445.40

	LX,\$X3,XCSZ1		21753.06 10	024446.00
	LX,\$X3,XCSZ2	-ALL ZEROS IN %3□	21754.06 10	024446.40
	KV,\$X3,XCSZ2	-TEST BITS 0-24	21754.06 90	024447.00
	SIC,SEN		1310.00 80	024447.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	024450.00
	KC,\$X3,XCSZ2	-TEST BITS 28-45	21754.07 90	024450.40
	SIC,SEN		1310.00 80	024451.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024451.40
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	024452.00
	SIC,SEN		1310.00 80	024452.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024453.00
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	024453.40
	KC,\$X3,XCSZ2	-TEST BITS 46-63	21754.07 90	024454.00
	SIC,SEN		1310.00 80	024454.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	024455.00
	LX,\$X3,XCSZ1	-RESTORE IX REG.	21753.06 10	024455.40
	NOP,0		0.30 00	024456.00
	NOP,0		0.30 00	024456.40
CS10E	LX,\$X4,XCSZ2		21754.10 10	024457.00
	LX,\$X4,XCSZ1	-ALTERN WITH 0 AND ONES %4□	21753.10 10	024457.40
	LX,\$X4,XCSZ2		21754.10 10	024460.00
	LX,\$X4,XCSZ1		21753.10 10	024460.40
	LX,\$X4,XCSZ2		21754.10 10	024461.00
	LX,\$X4,XCSZ1		21753.10 10	024461.40
	LX,\$X4,XCSZ2		21754.10 10	024462.00
	LX,\$X4,XCSZ1		21753.10 10	024462.40
	LX,\$X4,XCSZ2		21754.10 10	024463.00
	LX,\$X4,XCSZ1		21753.10 10	024463.40
	LX,\$X4,XCSZ2		21754.10 10	024464.00
	LX,\$X4,XCSZ1		21753.10 10	024464.40
	LX,\$X4,XCSZ2		21754.10 10	024465.00
	LX,\$X4,XCSZ1		21753.10 10	024465.40
	LX,\$X4,XCSZ2		21754.10 10	024466.00
	LX,\$X4,XCSZ1		21753.10 10	024466.40
	LX,\$X4,XCSZ2		21754.10 10	024467.00
	LX,\$X4,XCSZ1		21753.10 10	024467.40
	LX,\$X4,XCSZ2		21754.10 10	024470.00
	LX,\$X4,XCSZ1		21753.10 10	024470.40
	LX,\$X4,XCSZ2	-ALL ZEROS IN %4□	21754.10 10	024471.00
	KV,\$X4,XCSZ2	-TEST BITS 0-24	21754.10 90	024471.40
	SIC,SEN		1310.00 80	024472.00
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1304.32 C4	024472.40
	KC,\$X4,XCSZ2	-TEST BITS 28-45	21754.11 90	024473.00
	SIC,SEN		1310.00 80	024473.40
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	024474.00
	SR,\$X4,XCSZ5	-REFILL TO WORK AREA	21756.11 70	024474.40
	SIC,SEN		1310.00 80	024475.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024475.40
	LC,\$X4,XCSZ5	-REFILL INTO COUNT FIELD	21756.10 50	024476.00
	KC,\$X4,XCSZ2	-TEST BITS 46-63	21754.11 90	024476.40
	SIC,SEN		1310.00 80	024477.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1304.32 C0	024477.40
	LX,\$X4,XCSZ1	-RESTORE IX REG.	21753.10 10	024500.00
	NOP,0		0.30 00	024500.40
	NOP,0		0.30 00	024501.00
CS20F	LX,\$X5,XCSZ2		21754.12 10	024501.40
	LX,\$X5,XCSZ1	-ALTERN WITH 0 AND ONES %5□	21753.12 10	024502.00
	LX,\$X5,XCSZ2		21754.12 10	024502.40
	LX,\$X5,XCSZ1		21753.12 10	024503.00
	LX,\$X5,XCSZ2		21754.12 10	024503.40
	LX,\$X5,XCSZ1		21753.12 10	024504.00
	LX,\$X5,XCSZ2		21754.12 10	024504.40
	LX,\$X5,XCSZ1		21753.12 10	024505.00
	LX,\$X5,XCSZ2		21754.12 10	024505.40
	LX,\$X5,XCSZ1		21753.12 10	024506.00
	LX,\$X5,XCSZ2		21754.12 10	024506.40

	LX,\$X5,XCSZ1		21753.12 10	024507.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21754.12 10	024507.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21753.12 10	024510.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21754.12 10	024510.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21753.12 10	024511.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21754.12 10	024511.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21753.12 10	024512.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	21754.12 10	024512.40
	LX,\$X5,XCSZ2		21753.12 10	024513.00
	KV,\$X5,XCSZ2		21754.12 10	024513.40
	SIC,SEN	-ALL ZEROS IN %5□	21754.12 90	024514.00
	BZXEZ,SERS	-TEST BITS 0-24	1310.00 80	024514.40
	KC,\$X5,XCSZ2	-ERR IF BIT PICKED UP	1304.32 C4	024515.00
	SIC,SEN	-TEST BITS 28-45	21754.13 90	024515.40
	BZXE,SERS	-BITS MUST COMPARE	1310.00 80	024516.00
	SR,\$X5,XCSZ5	-REFILL TO WORK AREA	1304.32 C0	024516.40
	SIC,SEN		21756.13 70	024517.00
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHD BE	1310.00 80	024517.40
	LC,\$X5,XCSZ5	-REFILL INTO COUNT FIELD	1304.23 42	024520.00
	KC,\$X5,XCSZ2	-TEST BITS 46-63	21756.12 50	024520.40
	SIC,SEN		21754.13 90	024521.00
	BZXE,SERS	-ERR IF BIT PICKED UP	1310.00 80	024521.40
	LX,\$X5,XCSZ1	-RESTORE IX REG.	1304.32 C0	024522.00
	NOP,0		21753.12 10	024522.40
	NOP,0		0.30 00	024523.00
CS10G	LX,\$X6,XCSZ2		0.30 00	024523.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024524.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024524.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024525.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024525.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024526.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024526.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024527.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024527.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024530.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024530.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024531.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024531.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024532.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024532.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024533.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024533.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024534.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024534.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024535.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024535.40
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21754.14 10	024536.00
	LX,\$X6,XCSZ2	LX,\$X6,XCSZ1	21753.14 10	024536.40
	KV,\$X6,XCSZ2	-ALL ZEROS IN %6□	21754.14 10	024537.00
	SIC,SEN	-TEST BITS 0-24	21754.14 90	024537.40
	BZXEZ,SERS	-ERR IF BIT PICKED UP	1310.00 80	024540.00
	KC,\$X6,XCSZ2	-TEST BITS 28-45	1304.32 C4	024540.40
	SIC,SEN		21754.15 90	024541.00
	BZXE,SERS	-BITS MUST COMPARE	1310.00 80	024541.40
	SR,\$X6,XCSZ5	-REFILL TO WORK AREA	1304.32 C0	024542.00
	SIC,SEN		21756.15 70	024542.40
	BXF,SERS	-ERR IF XF NOT 0 AS IT SHD BE	1310.00 80	024543.00
	LC,\$X6,XCSZ5	-REFILL INTO COUNT FIELD	1304.23 42	024543.40
	KC,\$X6,XCSZ2	-TEST BITS 46-63	21756.14 50	024544.00
	SIC,SEN		21754.15 90	024544.40
	BZXE,SERS	-ERR IF BIT PICKED UP	1310.00 80	024545.00
	LX,\$X6,XCSZ1	-RESTORE IX REG.	1304.32 C0	024545.40
	NOP,0		21753.14 10	024546.00
	NOP,0		0.30 00	024546.40
	NOP,0		0.30 00	024547.00
XCS10H	LX,\$X7,XCSZ2		21754.16 10	024547.40

	LX,\$X7,XCSZ2			21753.16 10	024550.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024550.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024551.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024551.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024552.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024552.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024553.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024553.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024554.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024554.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024555.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024555.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024556.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024556.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024557.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024557.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024560.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	024560.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	024561.00
	KV,\$X7,XCSZ2		-ALL ZEROS IN %7□	21754.16 10	024561.40
	SIC,SEN		-TEST BITS 0-24	21754.16 90	024562.00
	BZXEZ,SERS			1310.00 80	024562.40
	KC,\$X7,XCSZ2		-ERR IF BIT PICKED UP	1304.32 C4	024563.00
	SIC,SEN		-TEST BITS 28-45	21754.17 90	024563.40
	BZXE,SERS			1310.00 80	024564.00
	SR,\$X7,XCSZ5		-BITS MUST COMPARE	1304.32 C0	024564.40
	SIC,SEN		-REFILL TO WORK AREA	21756.17 70	024565.00
	BXF,SERS			1310.00 80	024565.40
	LC,\$X7,XCSZ5		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024566.00
	KC,\$X7,XCSZ2		-REFILL INTO COUNT FIELD	21756.16 50	024566.40
	SIC,SEN		-TEST BITS 46-63	21754.17 90	024567.00
	BZXE,SERS			1310.00 80	024567.40
	LX,\$X7,XCSZ1		-ERR IF BIT PICKED UP	1304.32 C0	024570.00
	NOP,0		-RESTORE IX REG.	21753.16 10	024570.40
	NOP,0			0.30 00	024571.00
XCS10J	LX,\$X8,XCSZ2			0.30 00	024571.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1	-ALTERN WITH 0 AND ONES %8□	21754.20 10	024572.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024572.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024573.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024573.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024574.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024574.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024575.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024575.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024576.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024576.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024577.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024577.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024600.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024600.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024601.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024601.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024602.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024602.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	024603.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	024603.40
	KV,\$X8,XCSZ2		-ALL ZEROS IN %8□	21754.20 10	024604.00
	SIC,SEN		-TEST BITS 0-24	21754.20 90	024604.40
	BZXEZ,SERS			1310.00 80	024605.00
	KC,\$X8,XCSZ2		-ERR IF BIT PICKED UP	1304.32 C4	024605.40
	SIC,SEN		-TEST BITS 28-45	21754.21 90	024606.00
	BZXE,SERS			1310.00 80	024606.40
	SR,\$X8,XCSZ5		-BITS MUST COMPARE	1304.32 C0	024607.00
	SIC,SEN		-REFILL TO WORK AREA	21756.21 70	024607.40
	BXF,SERS			1310.00 80	024610.00
			-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024610.40

	LC,\$X8,XCSZ5		-REFILL INTO COUNT FIELD	21756.20 50	024611.00
	KC,\$X8,XCSZ2		-TEST BITS 46-63	21754.21 90	024611.40
	SIC,SEN			1310.00 80	024612.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	024612.40
	LX,\$X8,XCSZ1		-RESTORE IX REG.	21753.20 10	024613.00
	NOP,0			0.30 00	024613.40
	NOP,0			0.30 00	024614.00
XCS10K	LX,\$X9,XCSZ2			21754.22 10	024614.40
	LX,\$X9,XCSZ1		-ALTERN WITH 0 AND ONES %9□	21753.22 10	024615.00
	LX,\$X9,XCSZ2			21754.22 10	024615.40
	LX,\$X9,XCSZ1			21753.22 10	024616.00
	LX,\$X9,XCSZ2			21754.22 10	024616.40
	LX,\$X9,XCSZ1			21753.22 10	024617.00
	LX,\$X9,XCSZ2			21754.22 10	024617.40
	LX,\$X9,XCSZ1			21753.22 10	024620.00
	LX,\$X9,XCSZ2			21754.22 10	024620.40
	LX,\$X9,XCSZ1			21753.22 10	024621.00
	LX,\$X9,XCSZ2			21754.22 10	024621.40
	LX,\$X9,XCSZ1			21753.22 10	024622.00
	LX,\$X9,XCSZ2			21754.22 10	024622.40
	LX,\$X9,XCSZ1			21753.22 10	024623.00
	LX,\$X9,XCSZ2			21754.22 10	024623.40
	LX,\$X9,XCSZ1			21753.22 10	024624.00
	LX,\$X9,XCSZ2			21754.22 10	024624.40
	LX,\$X9,XCSZ1			21753.22 10	024625.00
	LX,\$X9,XCSZ2			21754.22 10	024625.40
	LX,\$X9,XCSZ1			21753.22 10	024626.00
	LX,\$X9,XCSZ2		-ALL ZEROS IN %9□	21754.22 10	024626.40
	KV,\$X9,XCSZ2		-TEST BITS 0-24	21754.22 90	024627.00
	SIC,SEN			1310.00 80	024627.40
	BZXEZ,SERS		-ERR IF BIT PICKED UP	1304.32 C4	024630.00
	KC,\$X9,XCSZ2		-TEST BITS 28-45	21754.23 90	024630.40
	SIC,SEN			1310.00 80	024631.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	024631.40
	SR,\$X9,XCSZ5		-REFILL TO WORK AREA	21756.23 70	024632.00
	SIC,SEN			1310.00 80	024632.40
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024633.00
	LC,\$X9,XCSZ5		-REFILL INTO COUNT FIELD	21756.22 50	024633.40
	KC,\$X9,XCSZ2		-TEST BITS 46-63	21754.23 90	024634.00
	SIC,SEN			1310.00 80	024634.40
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	024635.00
	LX,\$X9,XCSZ1		-RESTORE IX REG.	21753.22 10	024635.40
	NOP,0			0.30 00	024636.00
	NOP,0			0.30 00	024636.40
XCS10L	LX,\$X10,XCSZ2			21754.24 10	024637.00
	LX,\$X10,XCSZ1		-ALTERN WITH 0 AND ONES %10□	21753.24 10	024637.40
	LX,\$X10,XCSZ2			21754.24 10	024640.00
	LX,\$X10,XCSZ1			21753.24 10	024640.40
	LX,\$X10,XCSZ2			21754.24 10	024641.00
	LX,\$X10,XCSZ1			21753.24 10	024641.40
	LX,\$X10,XCSZ2			21754.24 10	024642.00
	LX,\$X10,XCSZ1			21753.24 10	024642.40
	LX,\$X10,XCSZ2			21754.24 10	024643.00
	LX,\$X10,XCSZ1			21753.24 10	024643.40
	LX,\$X10,XCSZ2			21754.24 10	024644.00
	LX,\$X10,XCSZ1			21753.24 10	024644.40
	LX,\$X10,XCSZ2			21754.24 10	024645.00
	LX,\$X10,XCSZ1			21753.24 10	024645.40
	LX,\$X10,XCSZ2			21754.24 10	024646.00
	LX,\$X10,XCSZ1			21753.24 10	024646.40
	LX,\$X10,XCSZ2			21754.24 10	024647.00
	LX,\$X10,XCSZ1			21753.24 10	024647.40
	LX,\$X10,XCSZ2			21754.24 10	024650.00
	LX,\$X10,XCSZ1			21753.24 10	024650.40
	LX,\$X10,XCSZ2		-ALL ZEROS IN %10□	21754.24 10	024651.00
	KV,\$X10,XCSZ2		-TEST BITS 0-24	21754.24 90	024651.40

	SIC,SEN		-ERR IF BIT PICKED UP	1310.00 80	024652.00
	BZXEZ,SERS		-TEST BITS 28-45	1304.32 C4	024652.40
	KC,\$X10,XCSZ2			21754.25 90	024653.00
	SIC,SEN			1310.00 80	024653.40
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	024654.00
	SR,\$X10,XCSZ5		-REFILL TO WORK AREA	21756.25 70	024654.40
	SIC,SEN			1310.00 80	024655.00
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024655.40
	LC,\$X10,XCSZ5		-REFILL INTO COUNT FIELD	21756.24 50	024656.00
	KC,\$X10,XCSZ2		-TEST BITS 46-63	21754.25 90	024656.40
	SIC,SEN			1310.00 80	024657.00
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	024657.40
	LX,\$X10,XCSZ1		-RESTORE IX REG.	21753.24 10	024660.00
	NOP,0			0.30 00	024660.40
	NOP,0			0.30 00	024661.00
XCS10M	LX,\$X11,XCSZ2			21754.26 10	024661.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1	-ALTERN WITH 0 AND ONES %11□	21753.26 10	024662.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024662.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024663.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024663.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024664.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024664.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024665.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024665.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024666.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024666.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024667.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024667.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024670.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024670.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024671.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024671.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024672.00
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21754.26 10	024672.40
	LX,\$X11,XCSZ2	LX,\$X11,XCSZ1		21753.26 10	024673.00
	LX,\$X11,XCSZ2		-ALL ZEROS IN %11□	21754.26 10	024673.40
	KV,\$X11,XCSZ2		-TEST BITS 0-24	21754.26 90	024674.00
	SIC,SEN			1310.00 80	024674.40
	BZXEZ,SERS		-ERR IF BIT PICKED UP	1304.32 C4	024675.00
	KC,\$X11,XCSZ2		-TEST BITS 28-45	21754.27 90	024675.40
	SIC,SEN			1310.00 80	024676.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	024676.40
	SR,\$X11,XCSZ5		-REFILL TO WORK AREA	21756.27 70	024677.00
	SIC,SEN			1310.00 80	024677.40
	BXF,SERS		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024700.00
	LC,\$X11,XCSZ5		-REFILL INTO COUNT FIELD	21756.26 50	024700.40
	KC,\$X11,XCSZ2		-TEST BITS 46-63	21754.27 90	024701.00
	SIC,SEN			1310.00 80	024701.40
	BZXE,SERS		-ERR IF BIT PICKED UP	1304.32 C0	024702.00
	LX,\$X11,XCSZ1		-RESTORE IX REG.	21753.26 10	024702.40
	NOP,0			0.30 00	024703.00
	NOP,0			0.30 00	024703.40
XCS10N	LX,\$X12,XCSZ2			21754.30 10	024704.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1	-ALTERN WITH 0 AND ONES %12□	21753.30 10	024704.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024705.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024705.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024706.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024706.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024707.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024707.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024710.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024710.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024711.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024711.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30 10	024712.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30 10	024712.40

	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30	10	024713.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30	10	024713.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30	10	024714.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30	10	024714.40
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21754.30	10	024715.00
	LX,\$X12,XCSZ2	LX,\$X12,XCSZ1		21753.30	10	024715.40
	KV,\$X12,XCSZ2		-ALL ZEROS IN %12□	21754.30	10	024716.00
	SIC,SEN		-TEST BITS 0-24	21754.30	90	024716.40
		BZXEZ,SERS		1310.00	80	024717.00
	KC,\$X12,XCSZ2		-ERR IF BIT PICKED UP	1304.32	C4	024717.40
	SIC,SEN		-TEST BITS 28-45	21754.31	90	024720.00
		BZXE,SERS		1310.00	80	024720.40
	SR,\$X12,XCSZ5		-BITS MUST COMPARE	1304.32	C0	024721.00
	SIC,SEN		-REFILL TO WORK AREA	21756.31	70	024721.40
		BXF,SERS		1310.00	80	024722.00
	LC,\$X12,XCSZ5		-ERR IF XF NOT 0 AS IT SHD BE	1304.23	42	024722.40
	KC,\$X12,XCSZ2		-REFILL INTO COUNT FIELD	21756.30	50	024723.00
	SIC,SEN		-TEST BITS 46-63	21754.31	90	024723.40
		BZXE,SERS		1310.00	80	024724.00
	LX,\$X12,XCSZ1		-ERR IF BIT PICKED UP	1304.32	C0	024724.40
	NOP,0		-RESTORE IX REG.	21753.30	10	024725.00
		NOP,0		0.30	00	024725.40
XCS10P	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		0.30	00	024726.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1	-ALTERN WITH 0 AND ONES %13□	21754.32	10	024726.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024727.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024727.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024730.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024730.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024731.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024731.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024732.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024732.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024733.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024733.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024734.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024734.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024735.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024735.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024736.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024736.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024737.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32	10	024737.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32	10	024740.00
	KV,\$X13,XCSZ2		-ALL ZEROS IN %12□	21754.32	10	024740.40
	SIC,SEN		-TEST BITS 0-24	21754.32	90	024741.00
		BZXEZ,SERS		1310.00	80	024741.40
	KC,\$X13,XCSZ2		-ERR IF BIT PICKED UP	1304.32	C4	024742.00
	SIC,SEN		-TEST BITS 28-45	21754.33	90	024742.40
		BZXE,SERS		1310.00	80	024743.00
	SR,\$X13,XCSZ5		-BITS MUST COMPARE	1304.32	C0	024743.40
	SIC,SEN		-REFILL TO WORK AREA	21756.33	70	024744.00
		BXF,SERS		1310.00	80	024744.40
	LC,\$X13,XCSZ5		-ERR IF XF NOT 0 AS IT SHD BE	1304.23	42	024745.00
	KC,\$X13,XCSZ2		-REFILL INTO COUNT FIELD	21756.32	50	024745.40
	SIC,SEN		-TEST BITS 46-63	21754.33	90	024746.00
		BZXE,SERS		1310.00	80	024746.40
	LX,\$X13,XCSZ1		-ERR IF BIT PICKED UP	1304.32	C0	024747.00
	NOP,0		-RESTORE IX REG.	21753.32	10	024747.40
		NOP,0		0.30	00	024750.00
XCS10Q	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		0.30	00	024750.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1	-ALTERN WITH 0 AND ONES %14□	21754.34	10	024751.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34	10	024751.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34	10	024752.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34	10	024752.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34	10	024753.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34	10	024753.40

	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024754.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024754.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024755.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024755.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024756.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024756.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024757.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024757.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024760.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024760.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024761.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024761.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	024762.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	024762.40
	KV,\$X14,XCSZ2		-ALL ZEROS IN %14□	21754.34 10	024763.00
	SIC,SEN		-TEST BITS 0-24	21754.34 90	024763.40
	BZXEZ,SERS			1310.00 80	024764.00
	KC,\$X14,XCSZ2		-ERR IF BIT PICKED UP	1304.32 C4	024764.40
	SIC,SEN		-TEST BITS 28-45	21754.35 90	024765.00
	BZXE,SERS			1310.00 80	024765.40
	SR,\$X14,XCSZ5		-BITS MUST COMPARE	1304.32 C0	024766.00
	SIC,SEN		-REFILL TO WORK AREA	21756.35 70	024766.40
	BXF,SERS			1310.00 80	024767.00
	LC,\$X14,XCSZ5		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	024767.40
	KC,\$X14,XCSZ2		-REFILL INTO COUNT FIELD	21756.34 50	024770.00
	SIC,SEN		-TEST BITS 46-63	21754.35 90	024770.40
	BZXE,SERS			1310.00 80	024771.00
	LX,\$X14,XCSZ1		-ERR IF BIT PICKED UP	1304.32 C0	024771.40
	NOP,0		-RESTORE IX REG.	21753.34 10	024772.00
	NOP,0			0.30 00	024772.40
XCS10R	LX,\$X15,XCSZ2			0.30 00	024773.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1	-ALTERN WITH 0 AND ONES %15□	21754.36 10	024773.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	024774.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	024774.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	024775.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	024775.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	024776.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	024776.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	024777.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	024777.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025000.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	025000.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025001.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	025001.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025002.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	025002.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025003.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	025003.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025004.00
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21754.36 10	025004.40
	LX,\$X15,XCSZ2	LX,\$X15,XCSZ1		21753.36 10	025005.00
	KV,\$X15,XCSZ2		-ALL ZEROS IN %15□	21754.36 10	025005.40
	SIC,SEN		-TEST BITS 0-24	21754.36 90	025006.00
	BZXEZ,SERS			1310.00 80	025006.40
	KC,\$X15,XCSZ2		-ERR IF BIT PICKED UP	1304.32 C4	025007.00
	SIC,SEN		-TEST BITS 28-45	21754.37 90	025007.40
	BZXE,SERS			1310.00 80	025010.00
	SR,\$X15,XCSZ5		-BITS MUST COMPARE	1304.32 C0	025010.40
	SIC,SEN		-REFILL TO WORK AREA	21756.37 70	025011.00
	BXF,SERS			1310.00 80	025011.40
	LC,\$X15,XCSZ5		-ERR IF XF NOT 0 AS IT SHD BE	1304.23 42	025012.00
	KC,\$X15,XCSZ2		-REFILL INTO COUNT FIELD	21756.36 50	025012.40
	SIC,SEN		-TEST BITS 46-63	21754.37 90	025013.00
	BZXE,SERS			1310.00 80	025013.40
	LX,\$X15,XCSZ1		-ERR IF BIT PICKED UP	1304.32 C0	025014.00
			-RESTORE IX REG.	21753.36 10	025014.40

	NOP,0		0.30 00	025015.00
XCS11A	LX,\$X0,XCSZ2		21754.00 10	025015.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1	-ALTERN WITH 0 AND ONES %0□	025016.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025016.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025017.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025017.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025020.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025020.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025021.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025021.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025022.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025022.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025023.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025023.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025024.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025024.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025025.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025025.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025026.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025026.40
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025027.00
	LX,\$X0,XCSZ2	LX,\$X0,XCSZ1		025027.40
	KV,\$X0,XCSZ1		-ALL ONES IN %0□	025030.00
	SIC,SEN		-TEST BITS 0-24	025030.40
			1310.00 80	025031.00
	BZXEZ,SERS		-ERR IF BIT LOST	025031.40
	KC,\$X0,XCSZ1		-TEST BITS 28-45	025032.00
	SIC,SEN			025032.40
			1304.32 C4	025033.00
	BZXE,SERS		-BITS MUST COMPARE	025033.40
	SR,\$X0,XCSZ5		-REFILL TO WORK AREA	025033.80
	SIC,SEN			025034.00
			1310.00 80	025034.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	025034.80
	LC,\$X0,XCSZ5		-REFILL INTO COUNT FIELD	025035.00
	KC,\$X0,XCSZ1		-TEST BITS 46-63	025035.40
	SIC,SEN			025036.00
			1310.00 80	025036.40
	BZXE,SERS		-ERR IF BIT LOST	025037.00
	LX,\$X0,XCSZ1		-RESTORE IX REG.	025037.40
	NOP,0		0.30 00	025040.00
XCS11B	LX,\$X1,XCSZ2		21754.02 10	025040.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1	-ALTERN WITH 0 AND ONES %1□	025041.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025041.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025042.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025042.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025043.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025043.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025044.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025044.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025045.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025045.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025046.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025046.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025047.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025047.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025050.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025050.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025051.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025051.40
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025052.00
	LX,\$X1,XCSZ2	LX,\$X1,XCSZ1		025052.40
	KV,\$X1,XCSZ1		-ALL ONES IN %1□	025053.00
	SIC,SEN		-TEST BITS 0-24	025053.40
			1310.00 80	025054.00
	BZXEZ,SERS		-ERR IF BIT LOST	025054.40
	KC,\$X1,XCSZ1		-TEST BITS 28-45	025055.00
	SIC,SEN			025055.40
			1304.32 C4	
	BZXE,SERS		-BITS MUST COMPARE	
	SR,\$X1,XCSZ5		-REFILL TO WORK AREA	
	SIC,SEN			
			1310.00 80	

	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025056.00
	LC,\$X1,XCSZ5		-REFILL INTO COUNT FIELD	21756.02 50	025056.40
	KC,\$X1,XCSZ1		-TEST BITS 46-63	21753.03 90	025057.00
	SIC,SEN			1310.00 80	025057.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	025060.00
	LX,\$X1,XCSZ1		-RESTORE IX REG.	21753.02 10	025060.40
	NOP,0			0.30 00	025061.00
	NOP,0			0.30 00	025061.40
XCS11C	LX,\$X2,XCSZ2			21754.04 10	025062.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1	-ALTERN WITH 0 AND ONES %2	21753.04 10	025062.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025063.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025063.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025064.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025064.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025065.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025065.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025066.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025066.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025067.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025067.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025070.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025070.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025071.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025071.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21754.04 10	025072.00
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1		21753.04 10	025072.40
	LX,\$X2,XCSZ2	LX,\$X2,XCSZ1	-ALL ONES IN %2	21754.04 10	025073.00
	KV,\$X2,XCSZ1		-TEST BITS 0-24	21753.04 10	025073.40
	SIC,SEN			21753.04 90	025074.00
	BZXEZ,SERS		-ERR IF BIT LOST	1310.00 80	025074.40
	KC,\$X2,XCSZ1		-TEST BITS 28-45	1304.32 C4	025075.00
	SIC,SEN			21753.05 90	025075.40
	BZXE,SERS		-BITS MUST COMPARE	1310.00 80	025076.00
	SR,\$X2,XCSZ5		-REFILL TO WORK AREA	1304.32 C0	025076.40
	SIC,SEN			21756.05 70	025077.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1310.00 80	025077.40
	LC,\$X2,XCSZ5		-REFILL INTO COUNT FIELD	1304.23 40	025100.00
	KC,\$X2,XCSZ1		-TEST BITS 46-63	21756.04 50	025100.40
	SIC,SEN			21753.05 90	025101.00
	BZXE,SERS		-ERR IF BIT LOST	1310.00 80	025101.40
	LX,\$X2,XCSZ1		-RESTORE IX REG.	1304.32 C0	025102.00
	NOP,0			21753.04 10	025102.40
	NOP,0			0.30 00	025103.00
	NOP,0			0.30 00	025103.40
XCS11D	LX,\$X3,XCSZ2			21754.06 10	025104.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1	-ALTERN WITH 0 + ONES %3	21753.06 10	025104.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025105.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025105.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025106.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025106.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025107.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025107.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025110.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025110.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025111.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025111.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025112.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025112.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025113.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025113.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025114.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025114.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21754.06 10	025115.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025115.40
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1	-ALL ONES IN %3	21754.06 10	025116.00
	LX,\$X3,XCSZ2	LX,\$X3,XCSZ1		21753.06 10	025116.40

	KV,\$X3,XCSZ1		-TEST BITS 0-24	21753.00 90	025117.90
	SIC,SEN			1310.00 80	025117.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	025120.00
	KC,\$X3,XCSZ1		-TEST BITS 28-45	21753.07 90	025120.40
	SIC,SEN			1310.00 80	025121.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	025121.40
	SR,\$X3,XCSZ5		-REFILL TO WORK AREA	21756.07 70	025122.00
	SIC,SEN			1310.00 80	025122.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025123.00
	LC,\$X3,XCSZ5		-REFILL INTO COUNT FIELD	21756.06 50	025123.40
	KC,\$X3,XCSZ1		-TEST BITS 46-63	21753.07 90	025124.00
	SIC,SEN			1310.00 80	025124.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	025125.00
	LX,\$X3,XCSZ1		-RESTORE IX REG.	21753.06 10	025125.40
	NOP,0			0.30 00	025126.00
	NOP,0			0.30 00	025126.40
XCS11E	LX,\$X4,XCSZ2			21754.10 10	025127.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1	-ALTERN WITH 0 AND ONES %4□	21753.10 10	025127.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025130.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025130.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025131.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025131.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025132.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025132.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025133.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025133.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025134.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025134.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025135.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025135.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025136.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025136.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025137.00
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21753.10 10	025137.40
	LX,\$X4,XCSZ2	LX,\$X4,XCSZ1		21754.10 10	025140.00
	KV,\$X4,XCSZ1		-ALL ONES IN %4□	21753.10 10	025140.40
	SIC,SEN		-TEST BITS 0-24	21753.10 90	025141.00
	BZXEZ,SERS		-ERR IF BIT LOST	1310.00 80	025141.40
	KC,\$X4,XCSZ1		-TEST BITS 28-45	1304.32 C4	025142.00
	SIC,SEN			21753.11 90	025142.40
	BZXE,SERS		-BITS MUST COMPARE	1310.00 80	025143.00
	SR,\$X4,XCSZ5		-REFILL TO WORK AREA	1304.32 C0	025143.40
	SIC,SEN			21756.11 70	025144.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1310.00 80	025144.40
	LC,\$X4,XCSZ5		-REFILL INTO COUNT FIELD	1304.23 40	025145.00
	KC,\$X4,XCSZ1		-TEST BITS 46-63	21756.10 50	025145.40
	SIC,SEN			21753.11 90	025146.00
	BZXE,SERS		-ERR IF BIT LOST	1310.00 80	025146.40
	LX,\$X4,XCSZ1		-RESTORE IX REG.	1304.32 C0	025147.00
	NOP,0			21753.10 10	025147.40
	NOP,0			0.30 00	025150.00
	NOP,0			0.30 00	025150.40
XCS11F	LX,\$X5,XCSZ2			21754.12 10	025151.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1	-ALTERN WITH 0 AND ONES %5□	21753.12 10	025151.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025152.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025152.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025153.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025153.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025154.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025154.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025155.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025155.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025156.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025156.40
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21754.12 10	025157.00
	LX,\$X5,XCSZ2	LX,\$X5,XCSZ1		21753.12 10	025157.40

		LX,\$X5,XCSZ1	21754.12 10	025160.40
		LX,\$X5,XCSZ2	21753.12 10	025160.40
		LX,\$X5,XCSZ1	21754.12 10	025161.00
		LX,\$X5,XCSZ2	21753.12 10	025161.40
		LX,\$X5,XCSZ1	21754.12 10	025161.40
		KV,\$X5,XCSZ1	21753.12 10	025162.00
		SIC,SEN	21753.12 90	025162.40
		BZXEZ,SERS	1310.00 80	025163.00
		KC,\$X5,XCSZ1	1304.32 C4	025163.40
		SIC,SEN	21753.13 90	025164.00
		BZXE,SERS	1310.00 80	025164.40
		SR,\$X5,XCSZ5	1304.32 C0	025165.00
		SIC,SEN	21756.13 70	025165.40
		BZXF,SERS	1310.00 80	025166.00
		LC,\$X5,XCSZ5	1304.23 40	025166.40
		KC,\$X5,XCSZ1	21756.12 50	025167.00
		SIC,SEN	21753.13 90	025167.40
		BZXE,SERS	1310.00 80	025170.00
		LX,\$X5,XCSZ1	1304.32 C0	025170.40
		NOP,0	21753.12 10	025171.00
		NOP,0	0.30 00	025171.40
XCS11G		LX,\$X6,XCSZ2	0.30 00	025172.00
		LX,\$X6,XCSZ1	0.30 00	025172.40
		LX,\$X6,XCSZ2	21754.14 10	025173.00
		LX,\$X6,XCSZ1	21753.14 10	025173.40
		LX,\$X6,XCSZ2	21754.14 10	025174.00
		LX,\$X6,XCSZ1	21753.14 10	025174.40
		LX,\$X6,XCSZ2	21754.14 10	025175.00
		LX,\$X6,XCSZ1	21753.14 10	025175.40
		LX,\$X6,XCSZ2	21754.14 10	025176.00
		LX,\$X6,XCSZ1	21753.14 10	025176.40
		LX,\$X6,XCSZ2	21754.14 10	025177.00
		LX,\$X6,XCSZ1	21753.14 10	025177.40
		LX,\$X6,XCSZ2	21754.14 10	025200.00
		LX,\$X6,XCSZ1	21753.14 10	025200.40
		LX,\$X6,XCSZ2	21754.14 10	025201.00
		LX,\$X6,XCSZ1	21753.14 10	025201.40
		LX,\$X6,XCSZ2	21754.14 10	025202.00
		LX,\$X6,XCSZ1	21753.14 10	025202.40
		LX,\$X6,XCSZ2	21754.14 10	025203.00
		LX,\$X6,XCSZ1	21753.14 10	025203.40
		LX,\$X6,XCSZ2	21754.14 10	025204.00
		LX,\$X6,XCSZ1	21753.14 10	025204.40
		KV,\$X6,XCSZ1	21753.14 90	025205.00
		SIC,SEN	1310.00 80	025205.40
		BZXEZ,SERS	1304.32 C4	025206.00
		KC,\$X6,XCSZ1	21753.15 90	025206.40
		SIC,SEN	1310.00 80	025207.00
		BZXE,SERS	1304.32 C0	025207.40
		SR,\$X6,XCSZ5	21756.15 70	025210.00
		SIC,SEN	1310.00 80	025210.40
		BZXF,SERS	1304.23 40	025211.00
		LC,\$X6,XCSZ5	21756.14 50	025211.40
		KC,\$X6,XCSZ1	21753.15 90	025212.00
		SIC,SEN	1310.00 80	025212.40
		BZXE,SERS	1304.32 C0	025213.00
		LX,\$X6,XCSZ1	21753.14 10	025213.40
		NOP,0	0.30 00	025214.00
		NOP,0	0.30 00	025214.40
XCS11H		LX,\$X7,XCSZ2	21754.16 10	025215.00
		LX,\$X7,XCSZ1	21753.16 10	025215.40
		LX,\$X7,XCSZ2	21754.16 10	025216.00
		LX,\$X7,XCSZ1	21753.16 10	025216.40
		LX,\$X7,XCSZ2	21754.16 10	025217.00
		LX,\$X7,XCSZ1	21753.16 10	025217.40
		LX,\$X7,XCSZ2	21754.16 10	025220.00
		LX,\$X7,XCSZ1	21753.16 10	025220.40

	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025221.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	025221.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025222.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	025222.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025223.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	025223.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025224.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	025224.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025225.00
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21753.16 10	025225.40
	LX,\$X7,XCSZ2	LX,\$X7,XCSZ1		21754.16 10	025226.00
	KV,\$X7,XCSZ1		-ALL ONES IN %7□	21753.16 10	025226.40
	SIC,SEN		-TEST BITS 0-24	21753.16 90	025227.00
	BZXEZ,SERS			1310.00 80	025227.40
	KC,\$X7,XCSZ1		-ERR IF BIT LOST	1304.32 C4	025230.00
	SIC,SEN		-TEST BITS 28-45	21753.17 90	025230.40
	BZXE,SERS			1310.00 80	025231.00
	SR,\$X7,XCSZ5		-BITS MUST COMPARE	1304.32 C0	025231.40
	SIC,SEN		-REFILL TO WORK AREA	21756.17 70	025232.00
	BZXF,SERS			1310.00 80	025232.40
	LC,\$X7,XCSZ5		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025233.00
	KC,\$X7,XCSZ1		-REFILL INTO COUNT FIELD	21756.16 50	025233.40
	SIC,SEN		-TEST BITS 46-63	21753.17 90	025234.00
	BZXE,SERS			1310.00 80	025234.40
	LX,\$X7,XCSZ1		-ERR IF BIT LOST	1304.32 C0	025235.00
	NOP,0		-RESTORE IX REG.	21753.16 10	025235.40
	NOP,0			0.30 00	025236.00
XCS11J	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		0.30 00	025236.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1	-ALTERN WITH 0 AND ONES %8□	21754.20 10	025237.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025237.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025240.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025240.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025241.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025241.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025242.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025242.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025243.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025243.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025244.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025244.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025245.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025245.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025246.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025246.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025247.00
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21753.20 10	025247.40
	LX,\$X8,XCSZ2	LX,\$X8,XCSZ1		21754.20 10	025250.00
	KV,\$X8,XCSZ1		-ALL ONES IN %8□	21753.20 10	025250.40
	SIC,SEN		-TEST BITS 0-24	21753.20 90	025251.00
	BZXEZ,SERS			1310.00 80	025251.40
	KC,\$X8,XCSZ1		-ERR IF BIT LOST	1304.32 C4	025252.00
	SIC,SEN		-TEST BITS 28-45	21753.21 90	025252.40
	BZXE,SERS			1310.00 80	025253.00
	SR,\$X8,XCSZ5		-BITS MUST COMPARE	1304.32 C0	025253.40
	SIC,SEN		-REFILL TO WORK AREA	21756.21 70	025254.00
	BZXF,SERS			1310.00 80	025254.40
	LC,\$X8,XCSZ5		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025255.00
	KC,\$X8,XCSZ1		-REFILL INTO COUNT FIELD	21756.20 50	025255.40
	SIC,SEN		-TEST BITS 46-63	21753.21 90	025256.00
	BZXE,SERS			1310.00 80	025256.40
	LX,\$X8,XCSZ1		-ERR IF BIT LOST	1304.32 C0	025257.00
	NOP,0		-RESTORE IX REG.	21753.20 10	025257.40
	NOP,0			0.30 00	025260.00
XCS11K	LX,\$X9,XCSZ2	LX,\$X9,XCSZ1		0.30 00	025260.40
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ1	-ALTERN WITH 0 AND ONES %9□	21754.22 10	025261.00
	LX,\$X9,XCSZ2	LX,\$X9,XCSZ1		21753.22 10	025261.40

	LX,\$X9,XCSZ2		21754.22 10	025262.00
	LX,\$X9,XCSZ1		21753.22 10	025262.40
	LX,\$X9,XCSZ2		21754.22 10	025263.00
	LX,\$X9,XCSZ1		21753.22 10	025263.40
	LX,\$X9,XCSZ2		21754.22 10	025264.00
	LX,\$X9,XCSZ1		21753.22 10	025264.40
	LX,\$X9,XCSZ2		21754.22 10	025265.00
	LX,\$X9,XCSZ1		21753.22 10	025265.40
	LX,\$X9,XCSZ2		21754.22 10	025266.00
	LX,\$X9,XCSZ1		21753.22 10	025266.40
	LX,\$X9,XCSZ2		21754.22 10	025267.00
	LX,\$X9,XCSZ1		21753.22 10	025267.40
	LX,\$X9,XCSZ2		21754.22 10	025270.00
	LX,\$X9,XCSZ1		21753.22 10	025270.40
	LX,\$X9,XCSZ2		21754.22 10	025271.00
	LX,\$X9,XCSZ1		21753.22 10	025271.40
	LX,\$X9,XCSZ2		21754.22 10	025272.00
	LX,\$X9,XCSZ1		21753.22 10	025272.40
	KV,\$X9,XCSZ1	-ALL ONES IN %9□	21753.22 90	025273.00
	SIC,SEN	-TEST BITS 0-24	1310.00 80	025273.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	025274.00
	KC,\$X9,XCSZ1	-TEST BITS 28-45	21753.23 90	025274.40
	SIC,SEN		1310.00 80	025275.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	025275.40
	SR,\$X9,XCSZ5	-REFILL TO WORK AREA	21756.23 70	025276.00
	SIC,SEN		1310.00 80	025276.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025277.00
	LC,\$X9,XCSZ5	-REFILL INTO COUNT FIELD	21756.22 50	025277.40
	KC,\$X9,XCSZ1	-TEST BITS 46-63	21753.23 90	025300.00
	SIC,SEN		1310.00 80	025300.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	025301.00
	LX,\$X9,XCSZ1	-RESTORE IX REG.	21753.22 10	025301.40
	NOP,0		0.30 00	025302.00
	NOP,0		0.30 00	025302.40
XCS11L	LX,\$X10,XCSZ2		21754.24 10	025303.00
	LX,\$X10,XCSZ1	-ALTERN WITH 0 AND ONES %10□	21753.24 10	025303.40
	LX,\$X10,XCSZ2		21754.24 10	025304.00
	LX,\$X10,XCSZ1		21753.24 10	025304.40
	LX,\$X10,XCSZ2		21754.24 10	025305.00
	LX,\$X10,XCSZ1		21753.24 10	025305.40
	LX,\$X10,XCSZ2		21754.24 10	025306.00
	LX,\$X10,XCSZ1		21753.24 10	025306.40
	LX,\$X10,XCSZ2		21754.24 10	025307.00
	LX,\$X10,XCSZ1		21753.24 10	025307.40
	LX,\$X10,XCSZ2		21754.24 10	025310.00
	LX,\$X10,XCSZ1		21753.24 10	025310.40
	LX,\$X10,XCSZ2		21754.24 10	025311.00
	LX,\$X10,XCSZ1		21753.24 10	025311.40
	LX,\$X10,XCSZ2		21754.24 10	025312.00
	LX,\$X10,XCSZ1		21753.24 10	025312.40
	LX,\$X10,XCSZ2		21754.24 10	025313.00
	LX,\$X10,XCSZ1		21753.24 10	025313.40
	LX,\$X10,XCSZ2		21754.24 10	025314.00
	LX,\$X10,XCSZ1		21753.24 10	025314.40
	KV,\$X10,XCSZ1	-ALL ONES IN %10□	21753.24 90	025315.00
	SIC,SEN	-TEST BITS 0-24	1310.00 80	025315.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	025316.00
	KC,\$X10,XCSZ1	-TEST BITS 28-45	21753.25 90	025316.40
	SIC,SEN		1310.00 80	025317.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	025317.40
	SR,\$X10,XCSZ5	-REFILL TO WORK AREA	21756.25 70	025320.00
	SIC,SEN		1310.00 80	025320.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025321.00
	LC,\$X10,XCSZ5	-REFILL INTO COUNT FIELD	21756.24 50	025321.40
	KC,\$X10,XCSZ1	-TEST BITS 46-63	21753.25 90	025322.00
	SIC,SEN		1310.00 80	025322.40

	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	025323.00
	LX,\$X10,XCSZ1	-RESTORE IX REG.	21753.24 10	025323.40
	NOP,0		0.30 00	025324.00
	NOP,0		0.30 00	025324.40
XCS11M	LX,\$X11,XCSZ2		21754.26 10	025325.00
	LX,\$X11,XCSZ1	-ALTERN WITH 0 AND ONES %11□	21753.26 10	025325.40
	LX,\$X11,XCSZ2		21754.26 10	025326.00
	LX,\$X11,XCSZ1		21753.26 10	025326.40
	LX,\$X11,XCSZ2		21754.26 10	025327.00
	LX,\$X11,XCSZ1		21753.26 10	025327.40
	LX,\$X11,XCSZ2		21754.26 10	025330.00
	LX,\$X11,XCSZ1		21753.26 10	025330.40
	LX,\$X11,XCSZ2		21754.26 10	025331.00
	LX,\$X11,XCSZ1		21753.26 10	025331.40
	LX,\$X11,XCSZ2		21754.26 10	025332.00
	LX,\$X11,XCSZ1		21753.26 10	025332.40
	LX,\$X11,XCSZ2		21754.26 10	025333.00
	LX,\$X11,XCSZ1		21753.26 10	025333.40
	LX,\$X11,XCSZ2		21754.26 10	025334.00
	LX,\$X11,XCSZ1		21753.26 10	025334.40
	LX,\$X11,XCSZ2		21754.26 10	025335.00
	LX,\$X11,XCSZ1		21753.26 10	025335.40
	LX,\$X11,XCSZ2		21754.26 10	025336.00
	LX,\$X11,XCSZ1	-ALL ONES IN %11□	21753.26 10	025336.40
	KV,\$X11,XCSZ1	-TEST BITS 0-24	21753.26 90	025337.00
	SIC,SEN		1310.00 80	025337.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	025340.00
	KC,\$X11,XCSZ1	-TEST BITS 28-45	21753.27 90	025340.40
	SIC,SEN		1310.00 80	025341.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	025341.40
	SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27 70	025342.00
	SIC,SEN		1310.00 80	025342.40
	BZXF,SERS	-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025343.00
	LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26 50	025343.40
	KC,\$X11,XCSZ1	-TEST BITS 46-63	21753.27 90	025344.00
	SIC,SEN		1310.00 80	025344.40
	BZXE,SERS	-ERR IF BIT LOST	1304.32 C0	025345.00
	LX,\$X11,XCSZ1	-RESTORE IX REG.	21753.26 10	025345.40
	NOP,0		0.30 00	025346.00
	NOP,0		0.30 00	025346.40
XCS11N	LX,\$X12,XCSZ2		21754.30 10	025347.00
	LX,\$X12,XCSZ1	-ALTERN WITH 0 AND ONES %12□	21753.30 10	025347.40
	LX,\$X12,XCSZ2		21754.30 10	025350.00
	LX,\$X12,XCSZ1		21753.30 10	025350.40
	LX,\$X12,XCSZ2		21754.30 10	025351.00
	LX,\$X12,XCSZ1		21753.30 10	025351.40
	LX,\$X12,XCSZ2		21754.30 10	025352.00
	LX,\$X12,XCSZ1		21753.30 10	025352.40
	LX,\$X12,XCSZ2		21754.30 10	025353.00
	LX,\$X12,XCSZ1		21753.30 10	025353.40
	LX,\$X12,XCSZ2		21754.30 10	025354.00
	LX,\$X12,XCSZ1		21753.30 10	025354.40
	LX,\$X12,XCSZ2		21754.30 10	025355.00
	LX,\$X12,XCSZ1		21753.30 10	025355.40
	LX,\$X12,XCSZ2		21754.30 10	025356.00
	LX,\$X12,XCSZ1		21753.30 10	025356.40
	LX,\$X12,XCSZ2		21754.30 10	025357.00
	LX,\$X12,XCSZ1		21753.30 10	025357.40
	LX,\$X12,XCSZ2		21754.30 10	025360.00
	LX,\$X12,XCSZ1	-ALL ONES IN %12□	21753.30 10	025360.40
	KV,\$X12,XCSZ1	-TEST BITS 0-24	21753.30 90	025361.00
	SIC,SEN		1310.00 80	025361.40
	BZXEZ,SERS	-ERR IF BIT LOST	1304.32 C4	025362.00
	KC,\$X12,XCSZ1	-TEST BITS 28-45	21753.31 90	025362.40
	SIC,SEN		1310.00 80	025363.00
	BZXE,SERS	-BITS MUST COMPARE	1304.32 C0	025363.40

	SR,\$X12,XCSZ5		-REFILL TO WORK AREA	21756.31 70	025364.00
	SIC,SEN			1310.00 80	025364.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025365.00
	LC,\$X12,XCSZ5		-REFILL INTO COUNT FIELD	21756.30 50	025365.40
	KC,\$X12,XCSZ1		-TEST BITS 46-63	21753.31 90	025366.00
	SIC,SEN			1310.00 80	025366.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	025367.00
	LX,\$X12,XCSZ1		-RESTORE IX REG.	21753.30 10	025367.40
	NOP,0			0.30 00	025370.00
	NOP,0			0.30 00	025370.40
XCS11P	LX,\$X13,XCSZ2			21754.32 10	025371.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1	-ALTERN WITH 0 AND ONES %13□	21753.32 10	025371.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025372.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025372.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025373.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025373.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025374.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025374.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025375.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025375.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025376.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025376.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025377.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025377.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025400.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025400.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025401.00
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21753.32 10	025401.40
	LX,\$X13,XCSZ2	LX,\$X13,XCSZ1		21754.32 10	025402.00
	KV,\$X13,XCSZ1		-ALL ONES IN %13□	21753.32 10	025402.40
	SIC,SEN		-TEST BITS 0-24	21753.32 90	025403.00
	BZXEZ,SERS		-ERR IF BIT LOST	1310.00 80	025403.40
	KC,\$X13,XCSZ1		-TEST BITS 28-45	1304.32 C4	025404.00
	SIC,SEN			21753.33 90	025404.40
	BZXE,SERS		-BITS MUST COMPARE	1310.00 80	025405.00
	SR,\$X13,XCSZ5		-REFILL TO WORK AREA	1304.32 C0	025405.40
	SIC,SEN			21756.33 70	025406.00
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1310.00 80	025406.40
	LC,\$X13,XCSZ5		-REFILL INTO COUNT FIELD	1304.23 40	025407.00
	KC,\$X13,XCSZ1		-TEST BITS 46-63	21756.32 50	025407.40
	SIC,SEN			21753.33 90	025410.00
	BZXE,SERS		-ERR IF BIT LOST	1310.00 80	025410.40
	LX,\$X13,XCSZ1		-RESTORE IX REG.	1304.32 C0	025411.00
	NOP,0			21753.32 10	025411.40
	NOP,0			0.30 00	025412.00
	NOP,0			0.30 00	025412.40
XCS11Q	LX,\$X14,XCSZ2			21754.34 10	025413.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1	-ALTERN WITH 0 AND ONES %14□	21753.34 10	025413.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025414.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025414.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025415.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025415.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025416.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025416.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025417.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025417.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025420.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025420.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025421.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025421.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025422.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025422.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025423.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21753.34 10	025423.40
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1		21754.34 10	025424.00
	LX,\$X14,XCSZ2	LX,\$X14,XCSZ1	-ALL ONES IN %14□	21753.34 10	025424.40

	KV,\$X14,XCSZ1		-TEST BITS 0-24	21753.34 90	025425.00
	SIC,SEN			1310.00 80	025425.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	025426.00
	KC,\$X14,XCSZ1		-TEST BITS 28-45	21753.35 90	025426.40
	SIC,SEN			1310.00 80	025427.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	025427.40
	SR,\$X14,XCSZ5		-REFILL TO WORK AREA	21756.35 70	025430.00
	SIC,SEN			1310.00 80	025430.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025431.00
	LC,\$X14,XCSZ5		-REFILL INTO COUNT FIELD	21756.34 50	025431.40
	KC,\$X14,XCSZ1		-TEST BITS 46-63	21753.35 90	025432.00
	SIC,SEN			1310.00 80	025432.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	025433.00
	LX,\$X14,XCSZ1		-RESTORE IX REG.	21753.34 10	025433.40
	NOP,0			0.30 00	025434.00
	NOP,0			0.30 00	025434.40
XCS11R	LX,\$X15,XCSZ2			21754.36 10	025435.00
	LX,\$X15,XCSZ1		-ALTERN WITH 0 AND ONES %15□	21753.36 10	025435.40
	LX,\$X15,XCSZ2			21754.36 10	025436.00
	LX,\$X15,XCSZ1			21753.36 10	025436.40
	LX,\$X15,XCSZ2			21754.36 10	025437.00
	LX,\$X15,XCSZ1			21753.36 10	025437.40
	LX,\$X15,XCSZ2			21754.36 10	025440.00
	LX,\$X15,XCSZ1			21753.36 10	025440.40
	LX,\$X15,XCSZ2			21754.36 10	025441.00
	LX,\$X15,XCSZ1			21753.36 10	025441.40
	LX,\$X15,XCSZ2			21754.36 10	025442.00
	LX,\$X15,XCSZ1			21753.36 10	025442.40
	LX,\$X15,XCSZ2			21754.36 10	025443.00
	LX,\$X15,XCSZ1			21753.36 10	025443.40
	LX,\$X15,XCSZ2			21754.36 10	025444.00
	LX,\$X15,XCSZ1			21753.36 10	025444.40
	LX,\$X15,XCSZ2			21754.36 10	025445.00
	LX,\$X15,XCSZ1			21753.36 10	025445.40
	LX,\$X15,XCSZ2			21754.36 10	025446.00
	LX,\$X15,XCSZ1		-ALL ONES IN %15□	21753.36 10	025446.40
	KV,\$X15,XCSZ1		-TEST BITS 0-24	21753.36 90	025447.00
	SIC,SEN			1310.00 80	025447.40
	BZXEZ,SERS		-ERR IF BIT LOST	1304.32 C4	025450.00
	KC,\$X15,XCSZ1		-TEST BITS 28-45	21753.37 90	025450.40
	SIC,SEN			1310.00 80	025451.00
	BZXE,SERS		-BITS MUST COMPARE	1304.32 C0	025451.40
	SR,\$X15,XCSZ5		-REFILL TO WORK AREA	21756.37 70	025452.00
	SIC,SEN			1310.00 80	025452.40
	BZXF,SERS		-ERR IF XF NOT 1 AS IT SHD BE	1304.23 40	025453.00
	LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36 50	025453.40
	KC,\$X15,XCSZ1		-TEST BITS 46-63	21753.37 90	025454.00
	SIC,SEN			1310.00 80	025454.40
	BZXE,SERS		-ERR IF BIT LOST	1304.32 C0	025455.00
	LX,\$X15,XCSZ1		-RESTORE IX REG.	21753.36 10	025455.40
	NOP,0			0.30 00	025456.00
	NOP,0			0.30 00	025456.40
	B,\$+1.0			25460.10 00	025457.00
	B,XCS10		-TO LOOP IN ALTERN 1 AND 0 TEST	24335.10 00	025457.40
	SIC,SEN0+.32			1311.40 80	025460.00
	B,SSW		-TEST SENSE SWITCHES	1301.10 00	025460.40
XCS12	LX,\$X0,XCSZ1			21753.00 10	025461.00
	LX,\$X1,XCSZ1		-INITIALIZE BY	21753.02 10	025461.40
	LX,\$X2,XCSZ1			21753.04 10	025462.00
	LX,\$X3,XCSZ1		-SETTING ALL IX	21753.06 10	025462.40
	LX,\$X4,XCSZ1			21753.10 10	025463.00
	LX,\$X5,XCSZ1		-REGS TO ONES	21753.12 10	025463.40
	LX,\$X6,XCSZ1			21753.14 10	025464.00
	LX,\$X7,XCSZ1		-PREPARE FOR	21753.16 10	025464.40
	LX,\$X8,XCSZ1			21753.20 10	025465.00
	LX,\$X9,XCSZ1		-XCS TO XCS	21753.22 10	025465.40

	LX,\$X10,XCSZ1	LX,\$X11,XCSZ1	-PATTERN TRANSFER	21753.24 10	025466.00
	LX,\$X12,XCSZ1			21753.26 10	025466.40
		LX,\$X13,XCSZ1	-TEST	21753.30 10	025467.00
	LX,\$X14,XCSZ1			21753.32 10	025467.40
		LX,\$X15,XCSZ1		21753.34 10	025470.00
				21753.36 10	025470.40
XCS12A	LX,\$X0,XCSZ20		-X0 WITH 10101 PATTERN	21770.00 10	025471.00
	LX,\$X1,16.0		-X1 FROM X0	20.02 10	025471.40
	LX,\$X2,17.0		-X2 FROM X1	21.04 10	025472.00
	LX,\$X3,18.0		-X3 FROM X2	22.06 10	025472.40
	LX,\$X4,19.0		-X4 FROM X3	23.10 10	025473.00
	LX,\$X5,20.0		-X5 FROM X4	24.12 10	025473.40
	LX,\$X6,21.0		-X6 FROM X5	25.14 10	025474.00
	LX,\$X7,22.0		-X7 FROM X6	26.16 10	025474.40
	LX,\$X8,23.0		-X8 FROM X7	27.20 10	025475.00
	LX,\$X9,24.0		-X9 FROM X8	30.22 10	025475.40
	LX,\$X10,25.0		-X10 FROM X9	31.24 10	025476.00
	LX,\$X11,26.0		-X11 FROM X10	32.26 10	025476.40
	LX,\$X12,27.0		-X12 FROM X11	33.30 10	025477.00
	LX,\$X13,28.0		-X13 FROM X12	34.32 10	025477.40
	LX,\$X14,29.0		-X14 FROM X13	35.34 10	025500.00
	LX,\$X15,30.0		-X15 FROM X14	36.36 10	025500.40
XCS12B	KV,\$X15,XCSZ22		-WITH 10101 COMP PATTERN	21772.36 90	025501.00
	BZXE,XCS12V		-ERR IN VAL FLD OF X15 0-24	25506.72 C0	025501.40
XCS12C	KC,\$X15,XCSZ22		-WITH 10101 COMP PATTERN	21772.37 90	025502.00
	BZXE,XCS12W		-ERR IN CNT FLD OF X15 28-45	25544.72 C0	025502.40
XCS12D	SR,\$X15,XCSZ5		-REFILL INTO WORK AREA	21756.37 70	025503.00
	BXF,XCS12X		-ERR XF %SHD BE 0 IN X 15 25	25602.63 42	025503.40
XCS12E	SR,\$X15,XCSZ5		-REFILL INTO WORK AREA	21756.37 70	025504.00
	LC,\$X15,XCSZ5		-REFILL INTO CNT FLD	21756.36 50	025504.40
	KC,\$X15,XCSZ22		-WITH 10101 COMP PATTERN	21772.37 90	025505.00
	BZXE,XCS12Y		-ERR IN REF FLD OF X15 46-63	25640.72 C0	025505.40
XCS12F	B,XCS13		-TO NEXT TEST SECTION	25715.50 00	025506.00
XCS12V	KV,\$X0,XCSZ22		-TEST V OF X0 WITH 10101 PATTERN	21772.00 90	025506.40
	SIC,SEN			1310.00 80	025507.00
	BZXE,SERS		-STOP V TEST WHEN FIRST	1304.32 C0	025507.40
			-ERR LOCATED		
	BZXE,XCS12C		-V OF X0 WAS ERR NOW TEST COUNTS	25502.32 C0	025510.00
	KV,\$X1,XCSZ22		-TEST V OF X1, 10101 PATTERN	21772.02 90	025510.40
	SIC,SEN			1310.00 80	025511.00
	BZXE,SERS		-V OF X1 WAS ERR NOW TEST COUNTS	1304.32 C0	025511.40
	BZXE,XCS12C			25502.32 C0	025512.00
	KV,\$X2,XCSZ22		-TEST V OF X2, 10101 PATTERN	21772.04 90	025512.40
	SIC,SEN			1310.00 80	025513.00
	BZXE,SERS		-V OF X2 WAS IN ERR	1304.32 C0	025513.40
	BZXE,XCS12C			25502.32 C0	025514.00
	KV,\$X3,XCSZ22		-V OF X3 WITH 10101	21772.06 90	025514.40
	SIC,SEN			1310.00 80	025515.00
	BZXE,SERS		-V OF X3 WAS IN ERR	1304.32 C0	025515.40
	BZXE,XCS12C			25502.32 C0	025516.00
	KV,\$X4,XCSZ22		-V OF X4 WITH 10101	21772.10 90	025516.40
	SIC,SEN			1310.00 80	025517.00
	BZXE,SERS		-V OF X4 WAS IN ERR	1304.32 C0	025517.40
	BZXE,XCS12C			25502.32 C0	025520.00
	KV,\$X5,XCSZ22		-V OF X5 WITH 10101	21772.12 90	025520.40
	SIC,SEN			1310.00 80	025521.00
	BZXE,SERS		-V OF X5 WAS IN ERR	1304.32 C0	025521.40
	BZXE,XCS12C			25502.32 C0	025522.00
	KV,\$X6,XCSZ22		-V OF X3 WITH 10101	21772.14 90	025522.40
	SIC,SEN			1310.00 80	025523.00
	BZXE,SERS		-V OF X6 WAS IN ERR	1304.32 C0	025523.40
	BZXE,XCS12C			25502.32 C0	025524.00
	KV,\$X7,XCSZ22		-V OF X7 WITH 10101	21772.16 90	025524.40
	SIC,SEN			1310.00 80	025525.00
	BZXE,SERS		-V OF X7 WAS IN ERR	1304.32 C0	025525.40
	BZXE,XCS12C			25502.32 C0	025526.00

	KV,\$X8,XCSZ22	-V OF X8 WITH 10101	21772.26 90	025526.40
	SIC,SEN		1310.00 80	025527.00
	BZXE,SERS		1304.32 C0	025527.40
	BZXE,XCS12C		25502.32 C0	025530.00
	KV,\$X9,XCSZ22	-V OF X9 WITH 10101	21772.22 90	025530.40
	SIC,SEN		1310.00 80	025531.00
	BZXE,SERS		1304.32 C0	025531.40
	BZXE,XCS12C		25502.32 C0	025532.00
	KV,\$X10,XCSZ22	-V OF X10 WITH 10101	21772.24 90	025532.40
	SIC,SEN		1310.00 80	025533.00
	BZXE,SERS		1304.32 C0	025533.40
	BZXE,XCS12C		25502.32 C0	025534.00
	KV,\$X11,XCSZ22	-V OF X11 WITH 10101	21772.26 90	025534.40
	SIC,SEN		1310.00 80	025535.00
	BZXE,SERS		1304.32 C0	025535.40
	BZXE,XCS12C		25502.32 C0	025536.00
	KV,\$X12,XCSZ22	-V OF X12 WITH 10101	21772.30 90	025536.40
	SIC,SEN		1310.00 80	025537.00
	BZXE,SERS		1304.32 C0	025537.40
	BZXE,XCS12C		25502.32 C0	025540.00
	KV,\$X14,XCSZ22	-V OF X14 WITH 10101	21772.34 90	025540.40
	SIC,SEN		1310.00 80	025541.00
	BZXE,SERS		1304.32 C0	025541.40
	BZXE,XCS12C		25502.32 C0	025542.00
	KV,\$X14,XCSZ22	-V OF X15 WITH 10101 AGAIN	21772.34 90	025542.40
	SIC,SEN		1310.00 80	025543.00
	BZXE,SERS		1304.32 C0	025543.40
	B,XCS12C		25502.10 00	025544.00
XCS12W	KC,\$X0,XCSZ22	-C OF X0 WITH 10101	21772.01 90	025544.40
	SIC,SEN		1310.00 80	025545.00
	BZXE,SERS		1304.32 C0	025545.40
	BZXE,XCS12D		25503.32 C0	025546.00
	KC,\$X1,XCSZ22	-C OF X1 WITH 10101	21772.03 90	025546.40
	SIC,SEN		1310.00 80	025547.00
	BZXE,SERS		1304.32 C0	025547.40
	BZXE,XCS12D		25503.32 C0	025550.00
	KC,\$X2,XCSZ22	-C OF X2 WITH 10101	21772.05 90	025550.40
	SIC,SEN		1310.00 80	025551.00
	BZXE,SERS		1304.32 C0	025551.40
	BZXE,XCS12D		25503.32 C0	025552.00
	KC,\$X3,XCSZ22	-C OF X3 WITH 10101	21772.07 90	025552.40
	SIC,SEN		1310.00 80	025553.00
	BZXE,SERS		1304.32 C0	025553.40
	BZXE,XCS12D		25503.32 C0	025554.00
	KC,\$X4,XCSZ22	-C OF X4 WITH 10101	21772.11 90	025554.40
	SIC,SEN		1310.00 80	025555.00
	BZXE,SERS		1304.32 C0	025555.40
	BZXE,XCS12D		25503.32 C0	025556.00
	KC,\$X5,XCSZ22	-C OF X5 WITH 10101	21772.13 90	025556.40
	SIC,SEN		1310.00 80	025557.00
	BZXE,SERS		1304.32 C0	025557.40
	BZXE,XCS12D		25503.32 C0	025560.00
	KC,\$X6,XCSZ22	-C OF X6 WITH 10101	21772.15 90	025560.40
	SIC,SEN		1310.00 80	025561.00
	BZXE,SERS		1304.32 C0	025561.40
	BZXE,XCS12D		25503.32 C0	025562.00
	KC,\$X7,XCSZ22	-C OF X7 WITH 10101	21772.17 90	025562.40
	SIC,SEN		1310.00 80	025563.00
	BZXE,SERS		1304.32 C0	025563.40
	BZXE,XCS12D		25503.32 C0	025564.00
	KC,\$X8,XCSZ22	-C OF X8 WITH 10101	21772.21 90	025564.40
	SIC,SEN		1310.00 80	025565.00
	BZXE,SERS		1304.32 C0	025565.40
	BZXE,XCS12D		25503.32 C0	025566.00
	KC,\$X9,XCSZ22	-C OF X9 WITH 10101	21772.23 90	025566.40
	SIC,SEN		1310.00 80	025567.00

	BZXE,SERS	-C OF X9 WAS IN ERR	1304.32 CO	025567.40
	BZXE,XCS12D		25503.32 CO	025570.00
	KC,\$X10,XCSZ22	-C OF X10 WITH 10101	21772.25 90	025570.40
	SIC,SEN		1310.00 80	025571.00
	BZXE,SERS	-C OF X10 WAS IN ERR	1304.32 CO	025571.40
	BZXE,XCS12D		25503.32 CO	025572.00
	KC,\$X11,XCSZ22	-C OF X11 WITH 10101	21772.27 90	025572.40
	SIC,SEN		1310.00 80	025573.00
	BZXE,SERS	-C OF X11 WAS IN ERR	1304.32 CO	025573.40
	BZXE,XCS12D		25503.32 CO	025574.00
	KC,\$X12,XCSZ22	-C OF X12 WITH 10101	21772.31 90	025574.40
	SIC,SEN		1310.00 80	025575.00
	BZXE,SERS	-C OF X12 WAS IN ERR	1304.32 CO	025575.40
	BZXE,XCS12D		25503.32 CO	025576.00
	KC,\$X14,XCSZ22	-C OF X14 WITH 10101	21772.35 90	025576.40
	BZXE,XCS12D		25503.32 CO	025577.00
	SIC,SEN		1310.00 80	025577.40
	BZXE,SERS	-C OF X13 OR X14 WAS 2N ERR	1304.32 CO	025600.00
	SIC,SEN		1310.00 80	025600.40
	BZXE,SERS	-C OF X15 WAS IN ERR	1304.32 CO	025601.00
	KC,\$X15,XCSZ22	-C OF X15 WITH 10101	21772.37 90	025601.40
	B,XCS12D		25503.10 00	025602.00
XCS12X	SX,\$X0,XCSZ5		21756.01 10	025602.40
	SIC,SEN		1310.00 80	025603.00
	BXF,SERS	-X0 ERR IF XF IS ONE	1304.23 42	025603.40
	BXF,XCS12E		25504.23 42	025604.00
	SX,\$X1,XCSZ5		21756.03 10	025604.40
	SIC,SEN		1310.00 80	025605.00
	BXF,SERS	-X1 ERR IF XF IS ONE	1304.23 42	025605.40
	BXF,XCS12E		25504.23 42	025606.00
	SX,\$X2,XCSZ5		21756.05 10	025606.40
	SIC,SEN		1310.00 80	025607.00
	BXF,SERS	-X2 ERR IF XF IS ONE	1304.23 42	025607.40
	BXF,XCS12E		25504.23 42	025610.00
	SX,\$X3,XCSZ5		21756.07 10	025610.40
	SIC,SEN		1310.00 80	025611.00
	BXF,SERS	-X3 ERR IF XF IS ONE	1304.23 42	025611.40
	BXF,XCS12E		25504.23 42	025612.00
	SX,\$X4,XCSZ5		21756.11 10	025612.40
	SIC,SEN		1310.00 80	025613.00
	BXF,SERS	-X4 ERR IF XF IS ONE	1304.23 42	025613.40
	BXF,XCS12E		25504.23 42	025614.00
	SX,\$X5,XCSZ5		21756.13 10	025614.40
	SIC,SEN		1310.00 80	025615.00
	BXF,SERS	-X5 ERR IF XF IS ONE	1304.23 42	025615.40
	BXF,XCS12E		25504.23 42	025616.00
	SX,\$X6,XCSZ5		21756.15 10	025616.40
	SIC,SEN		1310.00 80	025617.00
	BXF,SERS	-X6 ERR IF XF IS ONE	1304.23 42	025617.40
	BXF,XCS12E		25504.23 42	025620.00
	SX,\$X7,XCSZ5		21756.17 10	025620.40
	SIC,SEN		1310.00 80	025621.00
	BXF,SERS	-X7 ERR IF XF IS ONE	1304.23 42	025621.40
	BXF,XCS12E		25504.23 42	025622.00
	SX,\$X8,XCSZ5		21756.21 10	025622.40
	SIC,SEN		1310.00 80	025623.00
	BXF,SERS	-X8 ERR IF XF IS ONE	1304.23 42	025623.40
	BXF,XCS12E		25504.23 42	025624.00
	SX,\$X9,XCSZ5		21756.23 10	025624.40
	SIC,SEN		1310.00 80	025625.00
	BXF,SERS	-X9 ERR IF XF IS ONE	1304.23 42	025625.40
	BXF,XCS12E		25504.23 42	025626.00
	SX,\$X10,XCSZ5		21756.25 10	025626.40
	SIC,SEN		1310.00 80	025627.00
	BXF,SERS	-X10 ERR IF XF IS ONE	1304.23 42	025627.40
	BXF,XCS12E		25504.23 42	025630.00

	SX,\$X11,XCSZ5		21756.27	10	025630.40
	SIC,SEN		1310.00	80	025631.00
	BXF,SERS	-X11 ERR IF XF IS ONE	1304.23	42	025631.40
	BXF,XCS12E		25504.23	42	025632.00
	SX,\$X12,XCSZ5		21756.31	10	025632.40
	SIC,SEN		1310.00	80	025633.00
	BXF,SERS	-X12 ERR IF XF IS ONE	1304.23	42	025633.40
	BXF,XCS12E		25504.23	42	025634.00
	SX,\$X14,XCSZ5		21756.35	10	025634.40
	SIC,SEN		1310.00	80	025635.00
	BXF,SERS	-X13 OR X14 ERR IF XF IS ONE	1304.23	42	025635.40
	BXF,XCS12E		25504.23	42	025636.00
	SX,\$X15,XCSZ5		21756.37	10	025636.40
	SIC,SEN		1310.00	80	025637.00
	BXF,SERS	-X15 ERR IF XF IS ONE	1304.23	42	025637.40
XCS12Y	B,XCS12E		25504.10	00	025640.00
	SR,\$X0,XCSZ5		21756.01	70	025640.40
	LC,\$X0,XCSZ5		21756.00	50	025641.00
	KC,\$X0,XCSZ22	-WITH 10101 PATTERN	21772.01	90	025641.40
	SIC,SEN		1310.00	80	025642.00
	BZXE,SERS	-X0 REFILL ERR	1304.32	C0	025642.40
	BZXE,XCS13		25715.72	C0	025643.00
	SR,\$X1,XCSZ5		21756.03	70	025643.40
	LC,\$X1,XCSZ5		21756.02	50	025644.00
	KC,\$X1,XCSZ22	-WITH 10101 PATTERN	21772.03	90	025644.40
	SIC,SEN		1310.00	80	025645.00
	BZXE,SERS	-X1 REFILL ERR	1304.32	C0	025645.40
	BZXE,XCS13		25715.72	C0	025646.00
	SR,\$X2,XCSZ5		21756.05	70	025646.40
	LC,\$X2,XCSZ5		21756.04	50	025647.00
	KC,\$X2,XCSZ22	-WITH 10101 PATTERN	21772.05	90	025647.40
	SIC,SEN		1310.00	80	025650.00
	BZXE,SERS	-X2 REFILL ERR	1304.32	C0	025650.40
	BZXE,XCS13		25715.72	C0	025651.00
	SR,\$X3,XCSZ5		21756.07	70	025651.40
	LC,\$X3,XCSZ5		21756.06	50	025652.00
	KC,\$X3,XCSZ22	-WITH 10101 PATTERN	21772.07	90	025652.40
	SIC,SEN		1310.00	80	025653.00
	BZXE,SERS	-X3 REFILL ERR	1304.32	C0	025653.40
	BZXE,XCS13		25715.72	C0	025654.00
	SR,\$X4,XCSZ5		21756.11	70	025654.40
	LC,\$X4,XCSZ5		21756.10	50	025655.00
	KC,\$X4,XCSZ22	-WITH 10101 PATTERN	21772.11	90	025655.40
	SIC,SEN		1310.00	80	025656.00
	BZXE,SERS	-X4 REFILL ERR	1304.32	C0	025656.40
	BZXE,XCS13	-TO TEST REFILL FIELDS	25715.72	C0	025657.00
	SR,\$X5,XCSZ5		21756.13	70	025657.40
	LC,\$X5,XCSZ5		21756.12	50	025660.00
	KC,\$X5,XCSZ22	-WITH 10101 PATTERN	21772.13	90	025660.40
	SIC,SEN		1310.00	80	025661.00
	BZXE,SERS	-X5 REFILL ERR	1304.32	C0	025661.40
	BZXE,XCS13		25715.72	C0	025662.00
	SR,\$X6,XCSZ5		21756.15	70	025662.40
	LC,\$X6,XCSZ5		21756.14	50	025663.00
	KC,\$X6,XCSZ22	-WITH 10101 PATTERN	21772.15	90	025663.40
	SIC,SEN		1310.00	80	025664.00
	BZXE,SERS	-X6 REFILL ERR	1304.32	C0	025664.40
	BZXE,XCS13		25715.72	C0	025665.00
	SR,\$X7,XCSZ5		21756.17	70	025665.40
	LC,\$X7,XCSZ5		21756.16	50	025666.00
	KC,\$X7,XCSZ22	-WITH 10101 PATTERN	21772.17	90	025666.40
	SIC,SEN		1310.00	80	025667.00
	BZXE,SERS	-X7 REFILL ERR	1304.32	C0	025667.40
	BZXE,XCS13		25715.72	C0	025670.00
	SR,\$X8,XCSZ5		21756.21	70	025670.40
	LC,\$X8,XCSZ5		21756.20	50	025671.00

	KC,\$X8,XCSZ22		-WITH 10101 PATTERN	21772.21	90	025671.40
	SIC,SEN			1310.00	80	025672.00
	BZXE,SERS			1304.32	C0	025672.40
	BZXE,XCS13			25715.72	C0	025673.00
	SR,\$X9,XCSZ5			21756.23	70	025673.40
	LC,\$X9,XCSZ5			21756.22	50	025674.00
	KC,\$X9,XCSZ22		-WITH 10101 PATTERN	21772.23	90	025674.40
	SIC,SEN			1310.00	80	025675.00
	BZXE,SERS			1304.32	C0	025675.40
	BZXE,XCS13			25715.72	C0	025676.00
	SR,\$X10,XCSZ5			21756.25	70	025676.40
	LC,\$X10,XCSZ5			21756.24	50	025677.00
	KC,\$X10,XCSZ22		-WITH 10101 PATTERN	21772.25	90	025677.40
	SIC,SEN			1310.00	80	025700.00
	BZXE,SERS			1304.32	C0	025700.40
	BZXE,XCS13			25715.72	C0	025701.00
	SR,\$X11,XCSZ5			21756.27	70	025701.40
	LC,\$X11,XCSZ5			21756.26	50	025702.00
	KC,\$X11,XCSZ22		-WITH 10101 PATTERN	21772.27	90	025702.40
	SIC,SEN			1310.00	80	025703.00
	BZXE,SERS			1304.32	C0	025703.40
	BZXE,XCS13			25715.72	C0	025704.00
	SR,\$X12,XCSZ5			21756.31	70	025704.40
	LC,\$X12,XCSZ5			21756.30	50	025705.00
	KC,\$X12,XCSZ22		-WITH 10101 PATTERN	21772.31	90	025705.40
	SIC,SEN			1310.00	80	025706.00
	BZXE,SERS			1304.32	C0	025706.40
	BZXE,XCS13			25715.72	C0	025707.00
	SR,\$X14,XCSZ5			21756.35	70	025707.40
	LC,\$X14,XCSZ5			21756.34	50	025710.00
	KC,\$X14,XCSZ22		-WITH 10101 PATTERN	21772.35	90	025710.40
	SIC,SEN			1310.00	80	025711.00
	BZXE,SERS			1304.32	C0	025711.40
	BZXE,XCS13			25715.72	C0	025712.00
	SR,\$X15,XCSZ5			21756.37	70	025712.40
	LC,\$X15,XCSZ5			21756.36	50	025713.00
	KC,\$X15,XCSZ22		-WITH 10101 PATTERN	21772.37	90	025713.40
	SIC,SEN			1310.00	80	025714.00
	BZXE,SERS			1304.32	C0	025714.40
	BZXE,XCS13			25715.72	C0	025715.00
XCS13	LX,\$X0,XCSZ1			21753.00	10	025715.40
	LX,\$X1,XCSZ1		-INITIALIZE BY	21753.02	10	025716.00
	LX,\$X2,XCSZ1			21753.04	10	025716.40
	LX,\$X3,XCSZ1		-SETTING ALL IX	21753.06	10	025717.00
	LX,\$X4,XCSZ1			21753.10	10	025717.40
	LX,\$X5,XCSZ1		-REGS TO ONES	21753.12	10	025720.00
	LX,\$X6,XCSZ1			21753.14	10	025720.40
	LX,\$X7,XCSZ1		-PREPARE FOR	21753.16	10	025721.00
	LX,\$X8,XCSZ1			21753.20	10	025721.40
	LX,\$X9,XCSZ1		-XCS TO XCS	21753.22	10	025722.00
	LX,\$X10,XCSZ1			21753.24	10	025722.40
	LX,\$X11,XCSZ1		-PATTERN TRANSFER	21753.26	10	025723.00
	LX,\$X12,XCSZ1			21753.30	10	025723.40
	LX,\$X13,XCSZ1		-STORE	21753.32	10	025724.00
	LX,\$X14,XCSZ1			21753.34	10	025724.40
	LX,\$X15,XCSZ1		-TEST	21753.36	10	025725.00
XCS13A	LX,\$X15,XCSZ21		-X15 WITH 01010 PATTERN	21771.36	10	025725.40
	SX,\$X15,30.0		-X14 FROM X15	36.37	10	025726.00
	SX,\$X14,29.0		-X13 FROM X14	35.35	10	025726.40
	SX,\$X13,28.0		-X12 FROM X13	34.33	10	025727.00
	SX,\$X12,27.0		-X11 FROM X12	33.31	10	025727.40
	SX,\$X11,26.0		-X10 FROM X11	32.27	10	025730.00
	SX,\$X10,25.0		-X9 FROM X10	31.25	10	025730.40
	SX,\$X9,24.0		-X8 FROM X9	30.23	10	025731.00
	SX,\$X8,23.0		-X7 FROM X8	27.21	10	025731.40
	SX,\$X7,22.0		-X6 FROM X7	26.17	10	025732.00

	SX,\$X6,21.0	-X5 FROM X6	25.15 10	025732.40
	SX,\$X5,20.0	-X4 FROM X5	24.13 10	025733.00
	SX,\$X4,19.0	-X3 FROM X4	23.11 10	025733.40
	SX,\$X3,18.0	-X2 FROM X3	22.07 10	025734.00
	SX,\$X2,17.0	-X1 FROM X2	21.05 10	025734.40
	SX,\$X1,16.0	-X0 FROM X1	20.03 10	025735.00
XCS13B	KV,\$X0,XCSZ23	-WITH 01010 COMP ATTERN	21772.40 90	025735.40
	BZXE,XCS13V	-ERR IN V OF X0 0-24	25747.72 C0	025736.00
XCS13C	KC,\$X0,XCSZ23	-WITH 01010 COMP PATTERN	21772.41 90	025736.40
	BZXE,XCS13W	-ERR IN C OF X0 28-45	26005.72 C0	025737.00
XCS13D	SR,\$X0,XCSZ5	-REFILL INTO WORK AREA	21756.01 70	025737.40
	BZXF,XCS13X	-ERR IF XF IS 0 25	26043.63 40	025740.00
XCS13E	SR,\$X0,XCSZ5	-REFILL INTO WORK AREA	21756.01 70	025740.40
	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00 50	025741.00
	KC,\$X0,XCSZ23	-WITH 01010 COMP PATTERN	21772.41 90	025741.40
	BZXE,XCS13Y	-ERR IN REF 46-63	26101.32 C0	025742.00
XCS13F	NOP,0		0.30 00	025742.40
	NOP,0		0.30 00	025743.00
	B,\$+1.0		25744.50 00	025743.40
	B,XCS12	--TO TEST FOR LOOPING	25461.10 00	025744.00
	SIC,SEN0+.32		1311.40 80	025744.40
	B,SSW		1301.10 00	025745.00
	B,XCS14	-OR TO PROCEED TO NEXT TEST	26156.10 00	025745.40
	NOP,0	-TO NEXT TEST	0.30 00	025746.00
	NOP,0		0.30 00	025746.40
	NOP,0		0.30 00	025747.00
XCS13V	KV,\$X15,XCSZ23	-V OF X15 WITH 01010 PATTERN	21772.76 90	025747.40
	SIC,SEN		1310.00 80	025750.00
	BZXE,SERS	-ERR IN V OF X15	1304.32 C0	025750.40
	BZXE,XCS13C		25736.72 C0	025751.00
	KV,\$X14,XCSZ23	-V OF X14 WITH 01010 PATTERN	21772.74 90	025751.40
	SIC,SEN		1310.00 80	025752.00
	BZXE,SERS	-ERR IN V OF X14	1304.32 C0	025752.40
	BZXE,XCS13C		25736.72 C0	025753.00
	KV,\$X12,XCSZ23	-V OF X12 WITH 01010 PATTERN	21772.70 90	025753.40
	SIC,SEN		1310.00 80	025754.00
	BZXE,SERS	-ERR IN V OF X13 OR X14	1304.32 C0	025754.40
	BZXE,XCS13C		25736.72 C0	025755.00
	KV,\$X11,XCSZ23	-V OF X11 WITH 01010 PATTERN	21772.66 90	025755.40
	SIC,SEN		1310.00 80	025756.00
	BZXE,SERS	-ERR IN V OF X11	1304.32 C0	025756.40
	BZXE,XCS13C		25736.72 C0	025757.00
	KV,\$X10,XCSZ23	-V OF X10 WITH 01010 PATTERN	21772.64 90	025757.40
	SIC,SEN		1310.00 80	025760.00
	BZXE,SERS	-ERR IN V OF X10	1304.32 C0	025760.40
	BZXE,XCS13C		25736.72 C0	025761.00
	KV,\$X9,XCSZ23	-V OF X9 WITH 01010 PATTERN	21772.62 90	025761.40
	SIC,SEN		1310.00 80	025762.00
	BZXE,SERS	-ERR IN V OF X9	1304.32 C0	025762.40
	BZXE,XCS13C		25736.72 C0	025763.00
	KV,\$X8,XCSZ23	-V OF X8 WITH 01010 PATTERN	21772.60 90	025763.40
	SIC,SEN		1310.00 80	025764.00
	BZXE,SERS	-ERR IN V OF X8	1304.32 C0	025764.40
	BZXE,XCS13C		25736.72 C0	025765.00
	KV,\$X7,XCSZ23	-V OF X7 WITH 01010 PATTERN	21772.56 90	025765.40
	SIC,SEN		1310.00 80	025766.00
	BZXE,SERS	-ERR IN V OF X7	1304.32 C0	025766.40
	BZXE,XCS13C		25736.72 C0	025767.00
	KV,\$X6,XCSZ23	-V OF X6 WITH 01010 PATTERN	21772.54 90	025767.40
	SIC,SEN		1310.00 80	025770.00
	BZXE,SERS	-ERR IN V OF X6	1304.32 C0	025770.40
	BZXE,XCS13C		25736.72 C0	025771.00
	KV,\$X5,XCSZ23	-V OF X5 WITH 01010 PATTERN	21772.52 90	025771.40
	SIC,SEN		1310.00 80	025772.00
	BZXE,SERS	-ERR IN V OF X5	1304.32 C0	025772.40
	BZXE,XCS13C		25736.72 C0	025773.00

	KV,\$X4,XCSZ23	-V OF X4 WITH 01010 PATTERN	21772.50 90	025773.40
	SIC,SEN		1310.00 80	025774.00
	BZXE,SERS	-ERR IN V OF X4	1304.32 C0	025774.40
	BZXE,XCS13C		25736.72 C0	025775.00
	KV,\$X3,XCSZ23	-V OF X3 WITH 01010 PATTERN	21772.46 90	025775.40
	SIC,SEN		1310.00 80	025776.00
	BZXE,SERS	-ERR IN V OF X3	1304.32 C0	025776.40
	BZXE,XCS13C		25736.72 C0	025777.00
	KV,\$X2,XCSZ23	-V OF X2 WITH 01010 PATTERN	21772.44 90	025777.40
	SIC,SEN		1310.00 80	026000.00
	BZXE,SERS	-ERR IN V OF X2	1304.32 C0	026000.40
	BZXE,XCS13C		25736.72 C0	026001.00
	KV,\$X1,XCSZ23	-V OF X1 WITH 01010 PATTERN	21772.42 90	026001.40
	SIC,SEN		1310.00 80	026002.00
	BZXE,SERS	-ERR IN V OF X1	1304.32 C0	026002.40
	BZXE,XCS13C		25736.72 C0	026003.00
	KV,\$X0,XCSZ23	-V OF X0 WITH 01010 PATTERN	21772.40 90	026003.40
	SIC,SEN		1310.00 80	026004.00
	BZXE,SERS	-ERR IN V OF X0	1304.32 C0	026004.40
	BZXE,XCS13C	-NOW TEST COUNT FIELDS	25736.72 C0	026005.00
XCS13W	KC,\$X15,XCSZ23	-C OF X15 WITH 01010 PATTERN	21772.77 90	026005.40
	SIC,SEN		1310.00 80	026006.00
	BZXE,SERS	-ERR IN C OF X15	1304.32 C0	026006.40
	BZXE,XCS13D		25737.72 C0	026007.00
	KC,\$X14,XCSZ23	-C OF X14 WITH 01010 PATTERN	21772.75 90	026007.40
	SIC,SEN		1310.00 80	026010.00
	BZXE,SERS	-ERR IN C OF X14	1304.32 C0	026010.40
	BZXE,XCS13D		25737.72 C0	026011.00
	KC,\$X12,XCSZ23	-C OF X12 WITH 01010 PATTERN	21772.71 90	026011.40
	SIC,SEN		1310.00 80	026012.00
	BZXE,SERS	-ERR IN C OF X13 OR X12	1304.32 C0	026012.40
	BZXE,XCS13D		25737.72 C0	026013.00
	KC,\$X11,XCSZ23	-C OF X11 WITH 01010 PATTERN	21772.67 90	026013.40
	SIC,SEN		1310.00 80	026014.00
	BZXE,SERS	-ERR IN C OF X11	1304.32 C0	026014.40
	BZXE,XCS13D		25737.72 C0	026015.00
	KC,\$X10,XCSZ23	-C OF X10 WITH 01010 PATTERN	21772.65 90	026015.40
	SIC,SEN		1310.00 80	026016.00
	BZXE,SERS	-ERR IN C OF X10	1304.32 C0	026016.40
	BZXE,XCS13D		25737.72 C0	026017.00
	KC,\$X9,XCSZ23	-C OF X9 WITH 01010 PATTERN	21772.63 90	026017.40
	SIC,SEN		1310.00 80	026020.00
	BZXE,SERS	-ERR IN C OF X9	1304.32 C0	026020.40
	BZXE,XCS13D		25737.72 C0	026021.00
	KC,\$X8,XCSZ23	-C OF X8 WITH 01010 PATTERN	21772.61 90	026021.40
	SIC,SEN		1310.00 80	026022.00
	BZXE,SERS	-ERR IN C OF X8	1304.32 C0	026022.40
	BZXE,XCS13D		25737.72 C0	026023.00
	KC,\$X7,XCSZ23	-C OF X7 WITH 01010 PATTERN	21772.57 90	026023.40
	SIC,SEN		1310.00 80	026024.00
	BZXE,SERS	-ERR IN C OF X7	1304.32 C0	026024.40
	BZXE,XCS13D		25737.72 C0	026025.00
	KC,\$X6,XCSZ23	-C OF X6 WITH 01010 PATTERN	21772.55 90	026025.40
	SIC,SEN		1310.00 80	026026.00
	BZXE,SERS	-ERR IN C OF X6	1304.32 C0	026026.40
	BZXE,XCS13D		25737.72 C0	026027.00
	KC,\$X5,XCSZ23	-C OF X5 WITH 01010 PATTERN	21772.53 90	026027.40
	SIC,SEN		1310.00 80	026030.00
	BZXE,SERS	-ERR IN C OF X5	1304.32 C0	026030.40
	BZXE,XCS13D		25737.72 C0	026031.00
	KC,\$X4,XCSZ23	-C OF X4 WITH 01010 PATTERN	21772.51 90	026031.40
	SIC,SEN		1310.00 80	026032.00
	BZXE,SERS	-ERR IN C OF X3	1304.32 C0	026032.40
	BZXE,XCS13D		25737.72 C0	026033.00
	KC,\$X3,XCSZ23	-C OF X3 WITH 01010 PATTERN	21772.47 90	026033.40
	SIC,SEN		1310.00 80	026034.00

	BZXE,SERS	-ERR IN C OF X2	1304.32 C0	026034.40
	BZXE,XCS13D		25737.72 C0	026035.00
	KC,\$X2,XCSZ23	-C OF X2 WITH 01010 PATTERN	21772.45 90	026035.40
	SIC,SEN		1310.00 80	026036.00
	BZXE,SERS	-ERR IN C OF X1	1304.32 C0	026036.40
	BZXE,XCS13D		25737.72 C0	026037.00
	KC,\$X1,XCSZ23	-C OF X1 WITH 01010 PATTERN	21772.43 90	026037.40
	SIC,SEN		1310.00 80	026040.00
	BZXE,SERS	-ERR IN C OF X0	1304.32 C0	026040.40
	BZXE,XCS13D		25737.72 C0	026041.00
	KC,\$X0,XCSZ23	-C OF X0 WITH 01010 PATTERN	21772.41 90	026041.40
	SIC,SEN		1310.00 80	026042.00
	BZXE,SERS	-ERR IN C OF X0	1304.32 C0	026042.40
	B,XCS13D	-NOW TEST INDEX FLAG BIT	25737.50 00	026043.00
XCS13X	SX,\$X15,XCSZ5		21756.37 10	026043.40
	SIC,SEN		1310.00 80	026044.00
	BZXF,SERS	-ERR IF XF OF X15 NOT 1	1304.23 40	026044.40
	BZXF,XCS13E		25740.63 40	026045.00
	SX,\$X14,XCSZ5		21756.35 10	026045.40
	SIC,SEN		1310.00 80	026046.00
	BZXF,SERS	-ERR IF XF OF X14 NOT 1	1304.23 40	026046.40
	BZXF,XCS13E		25740.63 40	026047.00
	SX,\$X12,XCSZ5		21756.31 10	026047.40
	SIC,SEN		1310.00 80	026050.00
	BZXF,SERS	-ERR IN X13 OR X12 IF XF NOT 1	1304.23 40	026050.40
	BZXF,XCS13E		25740.63 40	026051.00
	SX,\$X11,XCSZ5		21756.27 10	026051.40
	SIC,SEN		1310.00 80	026052.00
	BZXF,SERS	-ERR IF XF OF X11 NOT 1	1304.23 40	026052.40
	BZXF,XCS13E		25740.63 40	026053.00
	SX,\$X10,XCSZ5		21756.25 10	026053.40
	SIC,SEN		1310.00 80	026054.00
	BZXF,SERS	-ERR IF XF OF X10 NOT 1	1304.23 40	026054.40
	BZXF,XCS13E		25740.63 40	026055.00
	SX,\$X9,XCSZ5		21756.23 10	026055.40
	SIC,SEN		1310.00 80	026056.00
	BZXF,SERS	-ERR IF XF OF X9 NOT 1	1304.23 40	026056.40
	BZXF,XCS13E		25740.63 40	026057.00
	SX,\$X8,XCSZ5		21756.21 10	026057.40
	SIC,SEN		1310.00 80	026060.00
	BZXF,SERS	-ERR IF XF OF X8 NOT 1	1304.23 40	026060.40
	BZXF,XCS13E		25740.63 40	026061.00
	SX,\$X7,XCSZ5		21756.17 10	026061.40
	SIC,SEN		1310.00 80	026062.00
	BZXF,SERS	-ERR IF XF OF X7 NOT 1	1304.23 40	026062.40
	BZXF,XCS13E		25740.63 40	026063.00
	SX,\$X6,XCSZ5		21756.15 10	026063.40
	SIC,SEN		1310.00 80	026064.00
	BZXF,SERS	-ERR IF XF OF X6 NOT 1	1304.23 40	026064.40
	BZXF,XCS13E		25740.63 40	026065.00
	SX,\$X5,XCSZ5		21756.13 10	026065.40
	SIC,SEN		1310.00 80	026066.00
	BZXF,SERS	-ERR IF XF OF X5 NOT 1	1304.23 40	026066.40
	BZXF,XCS13E		25740.63 40	026067.00
	SX,\$X4,XCSZ5		21756.11 10	026067.40
	SIC,SEN		1310.00 80	026070.00
	BZXF,SERS	-ERR IF XF OF X4 NOT 1	1304.23 40	026070.40
	BZXF,XCS13E		25740.63 40	026071.00
	SX,\$X3,XCSZ5		21756.07 10	026071.40
	SIC,SEN		1310.00 80	026072.00
	BZXF,SERS	-ERR IF XF OF X3 NOT 1	1304.23 40	026072.40
	BZXF,XCS13E		25740.63 40	026073.00
	SX,\$X2,XCSZ5		21756.05 10	026073.40
	SIC,SEN		1310.00 80	026074.00
	BZXF,SERS	-ERR IF XF OF X2 NOT 1	1304.23 40	026074.40
	BZXF,XCS13E		25740.63 40	026075.00

	SX,\$X1,XCSZ5		21756.03 40	026075.40
	SIC,SEN		1310.00 80	026076.00
	BZXF,SERS	-ERR IF XF OF X1 NOT 1	1304.23 40	026076.40
	SX,\$X0,XCSZ5		21756.01 10	026077.00
	SIC,SEN		1310.00 80	026077.40
	BZXF,SERS	-ERR IF XF OF X0 NOT 1	1304.23 40	026100.00
	B,XCS13E		25740.50 00	026100.40
XCS13Y	SR,\$X15,XCSZ5		21756.37 70	026101.00
	LC,\$X15,XCSZ5		21756.36 50	026101.40
	KC,\$X15,XCSZ23	-R OF X15 WITH 01010 PATTERN	21772.77 90	026102.00
	SIC,SEN		1310.00 80	026102.40
	BZXE,SERS	-ERR IN R OF X15	1304.32 C0	026103.00
	BZXE,XCS13F		25742.72 C0	026103.40
	SR,\$X14,XCSZ5		21756.35 70	026104.00
	LC,\$X14,XCSZ5		21756.34 50	026104.40
	KC,\$X14,XCSZ23	-R OF X14 WITH 01010 PATTERN	21772.75 90	026105.00
	SIC,SEN		1310.00 80	026105.40
	BZXE,SERS	-ERR IN R OF X14	1304.32 C0	026106.00
	BZXE,XCS13F		25742.72 C0	026106.40
	SR,\$X12,XCSZ5		21756.31 70	026107.00
	LC,\$X12,XCSZ5		21756.30 50	026107.40
	KC,\$X12,XCSZ23	-R OF X12 WITH 01010 PATTERN	21772.71 90	026110.00
	SIC,SEN		1310.00 80	026110.40
	BZXE,SERS	-ERR IN R OF X12 OR X13	1304.32 C0	026111.00
	BZXE,XCS13F		25742.72 C0	026111.40
	SR,\$X11,XCSZ5		21756.27 70	026112.00
	LC,\$X11,XCSZ5		21756.26 50	026112.40
	KC,\$X11,XCSZ23	-R OF X11 WITH 01010 PATTERN	21772.67 90	026113.00
	SIC,SEN		1310.00 80	026113.40
	BZXE,SERS	-ERR IN R OF X11	1304.32 C0	026114.00
	BZXE,XCS13F		25742.72 C0	026114.40
	SR,\$X10,XCSZ5		21756.25 70	026115.00
	LC,\$X10,XCSZ5		21756.24 50	026115.40
	KC,\$X10,XCSZ23	-R OF X10 WITH 01010 PATTERN	21772.65 90	026116.00
	SIC,SEN		1310.00 80	026116.40
	BZXE,SERS	-ERR IN R OF X10	1304.32 C0	026117.00
	BZXE,XCS13F		25742.72 C0	026117.40
	SR,\$X9,XCSZ5		21756.23 70	026120.00
	LC,\$X9,XCSZ5		21756.22 50	026120.40
	KC,\$X9,XCSZ23	-R OF X9 WITH 01010 PATTERN	21772.63 90	026121.00
	SIC,SEN		1310.00 80	026121.40
	BZXE,SERS	-ERR IN R OF X9	1304.32 C0	026122.00
	BZXE,XCS13F		25742.72 C0	026122.40
	SR,\$X8,XCSZ5		21756.21 70	026123.00
	LC,\$X8,XCSZ5		21756.20 50	026123.40
	KC,\$X8,XCSZ23	-R OF X8 WITH 01010 PATTERN	21772.61 90	026124.00
	SIC,SEN		1310.00 80	026124.40
	BZXE,SERS	-ERR IN R OF X8	1304.32 C0	026125.00
	BZXE,XCS13F		25742.72 C0	026125.40
	SR,\$X7,XCSZ5		21756.17 70	026126.00
	LC,\$X7,XCSZ5		21756.16 50	026126.40
	KC,\$X7,XCSZ23	-R OF X7 WITH 01010 PATTERN	21772.57 90	026127.00
	SIC,SEN		1310.00 80	026127.40
	BZXE,SERS	-ERR IN R OF X7	1304.32 C0	026130.00
	BZXE,XCS13F		25742.72 C0	026130.40
	SR,\$X6,XCSZ5		21756.15 70	026131.00
	LC,\$X6,XCSZ5		21756.14 50	026131.40
	KC,\$X6,XCSZ23	-R OF X6 WITH 01010 PATTERN	21772.55 90	026132.00
	SIC,SEN		1310.00 80	026132.40
	BZXE,SERS	-ERR IN R OF X6	1304.32 C0	026133.00
	BZXE,XCS13F		25742.72 C0	026133.40
	SR,\$X5,XCSZ5		21756.13 70	026134.00
	LC,\$X5,XCSZ5		21756.12 50	026134.40
	KC,\$X5,XCSZ23	-R OF X5 WITH 01010 PATTERN	21772.53 90	026135.00
	SIC,SEN		1310.00 80	026135.40
	BZXE,SERS	-ERR IN R OF X5	1304.32 C0	026136.00

	BZXE,XCS13F		25742.72	C0	026136.40
	SR,\$X4,XCSZ5		21756.11	70	026137.00
		LC,\$X4,XCSZ5	21756.10	50	026137.40
	KC,\$X4,XCSZ23		21772.51	90	026140.00
	SIC,SEN	-R OF X4 WITH 01010 PATTERN	1310.00	80	026140.40
		BZXE,SERS	1304.32	C0	026141.00
	BZXE,XCS13F		25742.72	C0	026141.40
	SR,\$X3,XCSZ5		21756.07	70	026142.00
		LC,\$X3,XCSZ5	21756.06	50	026142.40
	KC,\$X3,XCSZ23		21772.47	90	026143.00
	SIC,SEN	-R OF X3 WITH 01010 PATTERN	1310.00	80	026143.40
		BZXE,SERS	1304.32	C0	026144.00
	BZXE,XCS13F		25742.72	C0	026144.40
	SR,\$X2,XCSZ5		21756.05	70	026145.00
		LC,\$X2,XCSZ5	21756.04	50	026145.40
	KC,\$X2,XCSZ23		21772.45	90	026146.00
	SIC,SEN	-R OF X2 WITH 01010 PATTERN	1310.00	80	026146.40
		BZXE,SERS	1304.32	C0	026147.00
	BZXE,XCS13F		25742.72	C0	026147.40
	SR,\$X1,XCSZ5		21756.03	70	026150.00
		LC,\$X1,XCSZ5	21756.02	50	026150.40
	KC,\$X1,XCSZ23		21772.43	90	026151.00
	SIC,SEN	-R OF X1 WITH 01010 PATTERN	1310.00	80	026151.40
		BZXE,SERS	1304.32	C0	026152.00
	BZXE,XCS13F		25742.72	C0	026152.40
	SR,\$X0,XCSZ5		21756.01	70	026153.00
		LC,\$X0,XCSZ5	21756.00	50	026153.40
	KC,\$X0,XCSZ23		21772.41	90	026154.00
	SIC,SEN	-R OF X0 WITH 01010 PATTERN	1310.00	80	026154.40
		BZXE,SERS	1304.32	C0	026155.00
	B,XCS13F		25742.50	00	026155.40
XCS14	BD,\$+1.0		26157.04	00	026156.00
		NOP,0	0.30	00	026156.40
	LX,\$X4,BIT45		33406.10	10	026157.00
	LX,\$X1,XCSZ1		21753.02	10	026157.40
		-TIME CLOCK HIGH ZERO TEST			
		-SET 1ST PASS INDICATOR			
		-ALL ONES WORD INTO X1			
	SV,\$X1,1.0		1.03	30	026160.00
		SV,\$X1,1.0	1.03	30	026160.40
	SV,\$X1,1.0		1.03	30	026161.00
		SV,\$X1,1.0	1.03	30	026161.40
	SV,\$X1,1.0		1.03	30	026162.00
		SV,\$X1,1.0	1.03	30	026162.40
	SV,\$X1,1.0		1.03	30	026163.00
		SV,\$X1,1.0	1.03	30	026163.40
	SV,\$X1,1.0		1.03	30	026164.00
		SV,\$X1,1.0	1.03	30	026164.40
	SV,\$X1,1.0		1.03	30	026165.00
		SV,\$X1,1.0	1.03	30	026165.40
	SV,\$X1,1.0		1.03	30	026166.00
		SV,\$X1,1.0	1.03	30	026166.40
	SV,\$X1,1.0		1.03	30	026167.00
		SV,\$X1,1.0	1.03	30	026167.40
	SV,\$X1,1.0		1.03	30	026170.00
		SV,\$X1,1.0	1.03	30	026170.40
	SV,\$X1,1.0		1.03	30	026171.00
		SV,\$X1,1.0	1.03	30	026171.40
	LX,\$X2,XCSZ24		21773.04	10	026172.00
	SV,\$X2,1.0		1.05	30	026172.40
	LV,\$X3,1.0		1.06	30	026173.00
	KV,\$X3,XCSZ2		21754.06	90	026173.40
	SIC,SEN		1310.00	80	026174.00
	BZXE,XCS14B		26205.72	C0	026174.40
		-ERR 0-18, 1ST READ			
		-OCCASIONAL ERRORS ARE TO BE EXPECTED			
		-THE TIME CAN LEGITIMATELY CHANGE			
		-DURING THE COURSE OF THE TEST.			
	LV,\$X3,1.0		1.06	30	026175.00
		-TIME REG INTO V OF X3			

	KV,\$X3,XCSZ2	-WITH ZEROS	21754.06	90	026175.40
	SIC,SEN		1310.00	80	026176.00
	BZXE,XCS14B	-ERR 0-18, 2ND READ	26205.72	C0	026176.40
	LV,\$X3,1.0	-IT INTO V OF X3	1.06	30	026177.00
	KV,\$X3,XCSZ2	-WITH ZEROS	21754.06	90	026177.40
	SIC,SEN		1310.00	80	026200.00
	BZXE,XCS14B	-ERR 0-18, 3RD READ	26205.72	C0	026200.40
	LV,\$X3,1.0	-IT INTO V OF X3	1.06	30	026201.00
	KV,\$X3,XCSZ2	-WITH ZEROS	21754.06	90	026201.40
	SIC,SEN		1310.00	80	026202.00
	BZXE,XCS14B	-ERR 0-18, 4TH READ	26205.72	C0	026202.40
	LV,\$X3,1.0	-IT INTO V OF X3	1.06	30	026203.00
	KV,\$X3,XCSZ2	-WITH ZEROS	21754.06	90	026203.40
	SIC,SEN		1310.00	80	026204.00
	BZXE,XCS14B	-ERR 0-18, 5TH READ	26205.72	C0	026204.40
	B,\$+2.32		26207.50	00	026205.00
XCS14B	KC,\$X4,BIT44		33405.11	90	026205.40
	BXE,SERS	-BRANCH IF SECOND PASS	1304.32	C2	026206.00
	LX,\$X4,BIT44	-MODIFY INDEX WORD	33405.10	10	026206.40
	B,XCS14+1.32	-1ST PASS TRY AGAIN	26157.50	00	026207.00
	NOP,0		0.30	00	026207.40
	NOP,0		0.30	00	026210.00
	B,\$+1.0		26211.50	00	026210.40
	B,XCS14	-FOR LOOP	26156.10	00	026211.00
	SIC,SEN0+.32		1311.40	80	026211.40
	B,SSW		1301.10	00	026212.00
	BD,XCS14A		26213.04	00	026212.40
XCS14A	LX,\$X2,XCSZ26	-SYNCH ONES WD, START	21775.04	10	026213.00
	NOP,0	-LOW ONES TEST	0.30	00	026213.40
	LX,\$X4,BIT45	-SET 1ST PASS INDICATOR	33406.10	10	026214.00
	LX,\$X1,XCSZ25	-NON SYNCH ZERO WD	21774.02	10	026214.40
	SV,\$X1,1.0		1.03	30	026215.00
	SV,\$X1,1.0	-STORE N TIMES INTO TIME REG	1.03	30	026215.40
	SV,\$X1,1.0		1.03	30	026216.00
	SV,\$X1,1.0		1.03	30	026216.40
	SV,\$X1,1.0		1.03	30	026217.00
	SV,\$X1,1.0		1.03	30	026217.40
	SV,\$X1,1.0		1.03	30	026220.00
	SV,\$X1,1.0		1.03	30	026220.40
	SV,\$X1,1.0		1.03	30	026221.00
	SV,\$X1,1.0		1.03	30	026221.40
	SV,\$X1,1.0		1.03	30	026222.00
	SV,\$X1,1.0		1.03	30	026222.40
	SV,\$X1,1.0		1.03	30	026223.00
	SV,\$X1,1.0		1.03	30	026223.40
	SV,\$X1,1.0		1.03	30	026224.00
	SV,\$X1,1.0		1.03	30	026224.40
	SV,\$X1,1.0		1.03	30	026225.00
	SV,\$X1,1.0		1.03	30	026225.40
	SV,\$X1,1.0		1.03	30	026226.00
	SV,\$X1,1.0		1.03	30	026226.40
	SV,\$X2,1.0		1.05	30	026227.00
	LV,\$X3,1.0	-IT INTO V OF X3	1.06	30	026227.40
	KV,\$X3,XCSZ27	-WITH SP SYNCH ONES FLD	21776.06	90	026230.00
	SIC,SEN		1310.00	80	026230.40
	BZXE,XCS14C	-ERR 0-18, 1ST READ	26242.32	C0	026231.00
	LV,\$X3,1.0	-IT INTO V OF X3	1.06	30	026231.40
	KV,\$X3,XCSZ27	-WITH SP SYNCH ONES FLD	21776.06	90	026232.00
	SIC,SEN		1310.00	80	026232.40
	BZXE,XCS14C	-ERR 0-18, 2ND READ	26242.32	C0	026233.00
	LV,\$X3,1.0	-IT INOT V OF X3	1.06	30	026233.40
	KV,\$X3,XCSZ27	-WITH SP SYNCH ONES FLD	21776.06	90	026234.00
	SIC,SEN		1310.00	80	026234.40
	BZXE,XCS14C	-ERR 0-18, 3RD READ	26242.32	C0	026235.00
	LV,\$X3,1.0	-IT INOT V OF X3	1.06	30	026235.40
	KV,\$X3,XCSZ27	-WITH SP SYNCH ONES FLD	21776.06	90	026236.00

	SIC,SEN		1310.00 80	026236.40
	BZXE,XCS14C	-ERR 0-18, 4TH READ	26242.32 C0	026237.00
	LV,\$X3,1.0	-IT INOT V OF X3	1.06 30	026237.40
	KV,\$X3,XCSZ27	-WITH SP SYNCH ONES FLD	21776.06 90	026240.00
	SIC,SEN		1310.00 80	026240.40
	BZXE,XCS14C	-ERR 0-18, 5TH READ	26242.32 C0	026241.00
	B,\$+2.32		26244.10 00	026241.40
XCS14C	KC,\$X4,BIT44		33405.11 90	026242.00
	BXE,SERS	-BRANCH IF SECOND PASS	1304.32 C2	026242.40
	LX,\$X4,BIT44	-MODIFY INDEX WORD	33405.10 10	026243.00
	B,XCS14A+1.32	-1ST PASS TRY AGAIN	26214.50 00	026243.40
	B,\$+1.0		26245.10 00	026244.00
	B,XCS14A		26213.10 00	026244.40
	SIC,SEN0+.32		1311.40 80	026245.00
	B,SSW	-FOR LOOP	1301.10 00	026245.40
	B,XCS15		26246.50 00	026246.00
		-OCCASIONAL ERRORS ARE TO BE EZPECTED		
		-THE TIME CAN LEGITIMATELY CHANGE		
		-DURING THE COURSE OF THE TEST		
XCS15	LX,\$X0,XCSZ1		21753.00 10	026246.40
	LX,\$X1,XCSZ1	-INITIALIZE BY	21753.02 10	026247.00
	LX,\$X2,XCSZ1		21753.04 10	026247.40
	LX,\$X3,XCSZ1	-SETTING ALL IX	21753.06 10	026250.00
	LX,\$X4,XCSZ1		21753.10 10	026250.40
	LX,\$X5,XCSZ1	-REGS TO ONES	21753.12 10	026251.00
	LX,\$X6,XCSZ1		21753.14 10	026251.40
	LX,\$X7,XCSZ1	-PREPARE FOR	21753.16 10	026252.00
	LX,\$X8,XCSZ1		21753.20 10	026252.40
	LX,\$X9,XCSZ1	-SENSE AMPLIFIER	21753.22 10	026253.00
	LX,\$X10,XCSZ1		21753.24 10	026253.40
	LX,\$X11,XCSZ1	-CROSSTALK TEST	21753.26 10	026254.00
	LX,\$X12,XCSZ1		21753.30 10	026254.40
	LX,\$X13,XCSZ1	-UTILIZING	21753.32 10	026255.00
	LX,\$X14,XCSZ1		21753.34 10	026255.40
	LX,\$X15,XCSZ1	-SPECIAL BIT	21753.36 10	026256.00
	NOP,0		0.30 00	026256.40
	NOP,0		0.30 00	026257.00
XCS15A	LX,\$X1,XCSZA1	-PATTERNS	22012.02 10	026257.40
	SX,\$X1,XCSZ5	-XTK-A IN TX 1	21756.03 10	026260.00
	LX,\$X2,XCSZ5	-READ OUT X1	21756.04 10	026260.40
	KV,\$X2,XCSZA1	-BRING READ OUT BACK FOR TESTING	22012.04 90	026261.00
	SIC,SEN	-COMP. WITH ORIGINAL 0-24	1310.00 80	026261.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32 C0	026262.00
	KC,\$X2,XCSZA2	-WITH COUNT PATTERN 28-45	22013.05 90	026262.40
	SIC,SEN		1310.00 80	026263.00
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32 C0	026263.40
	SR,\$X2,XCSZ28	-STORE REFILL WORK AREA	21776.45 70	026264.00
	SIC,SEN		1310.00 80	026264.40
	BXF,SERS	-ERR IF XF IS ONE 25	1304.23 42	026265.00
	LC,\$X2,XCSZ28	-REFILL TO COUNT FLD	21776.44 50	026265.40
	KC,\$X2,XCSZA3	-WITH REFILL PATTERN 46-63	22013.45 90	026266.00
	SIC,SEN		1310.00 80	026266.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32 C0	026267.00
	NOP,		0.30 00	026267.40
	NOP,0		0.30 00	026270.00
	LX,\$X1,XCSZ1		21753.02 10	026270.40
	LX,\$X2,XCSZ1	-RESTORE IX REGS	21753.04 10	026271.00
XCS15B	LX,\$X3,XCSZB1	-XTK-B IN X3	22014.06 10	026271.40
	SX,\$X3,XCSZ5		21756.07 10	026272.00
	LX,\$X4,XCSZ5	-RD OUT, BRING BACK FOR TESTING	21756.10 10	026272.40
	KV,\$X4,XCSZB1	-COMP WITH ORIGINAL 0-24	22014.10 90	026273.00
	SIC,SEN		1310.00 80	026273.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32 C0	026274.00
	KC,\$X4,XCSZB2	-WITH COUNT PATTERN 28-45	22015.11 90	026274.40
	SIC,SEN		1310.00 80	026275.00
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32 C0	026275.40

	SR,\$X4,XCSZ28	-REFILL TO WORK AREA	21776.51	70	026276.00
	SIC,SEN		1310.00	80	026276.40
	BZXF,SERS	-ERR IF XF IS 0 25	1304.23	40	026277.00
	LC,\$X4,XCSZ28		21776.50	50	026277.40
	KC,\$X4,XCSZB3	-WITH REFILL PATTERN 46-63	22015.51	90	026300.00
	SIC,SEN		1310.00	80	026300.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026301.00
	NOP,0		0.30	00	026301.40
	NOP,0		0.30	00	026302.00
	LX,\$X3,XCSZ1		21753.06	10	026302.40
	LX,\$X4,XCSZ1	-RESTORE IX REGS	21753.10	10	026303.00
XCS15C	LX,\$X5,XCSZC1	-XTK-C IN X5	22016.12	10	026303.40
	SX,\$X5,XCSZ5		21756.13	10	026304.00
	LX,\$X6,XCSZ5	-RD OUT, BRING BACK FOR TESTING	21756.14	10	026304.40
	KV,\$X6,XCSZC1	-COMP WITH ORIGINAL 0-24	22016.14	90	026305.00
	SIC,SEN		1310.00	80	026305.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026306.00
	KC,\$X6,XCSZC2	-WITH COUNT PATTERN 28-45	22017.15	90	026306.40
	SIC,SEN		1310.00	80	026307.00
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026307.40
	SR,\$X6,XCSZ28	-REFILL TO WORK AREA	21776.55	70	026310.00
	SIC,SEN		1310.00	80	026310.40
	BZXF,SERS	-ERR IF XF IS 0 25	1304.23	40	026311.00
	LC,\$X6,XCSZ28		21776.54	50	026311.40
	KC,\$X6,XCSZC3	-WITH REFILL PATTERN 46-63	22017.55	90	026312.00
	SIC,SEN		1310.00	80	026312.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026313.00
	NOP,0		0.30	00	026313.40
	NOP,0		0.30	00	026314.00
	LX,\$X5,XCSZ1		21753.12	10	026314.40
	LX,\$X6,XCSZ1	-RESTORE IX REGS	21753.14	10	026315.00
XCS15D	LX,\$X7,XCSZD1	-XTK-D IN X7	22020.16	10	026315.40
	SX,\$X7,XCSZ5		21756.17	10	026316.00
	LX,\$X8,XCSZ5	-RD OUT, BRING BACK FOR TESTING	21756.20	10	026316.40
	KV,\$X8,XCSZD1	-COMP WITH ORIGINAL 0-24	22020.20	90	026317.00
	SIC,SEN		1310.00	80	026317.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026320.00
	KC,\$X8,XCSZD2	-WITH COUNT PATTERN 28-45	22021.21	90	026320.40
	SIC,SEN		1310.00	80	026321.00
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026321.40
	SR,\$X8,XCSZ28	-REFILL TO WORK AREA	21776.61	70	026322.00
	SIC,SEN		1310.00	80	026322.40
	BZXF,SERS	-ERR IF XF IS 0 25	1304.23	40	026323.00
	LC,\$X8,XCSZ28		21776.60	50	026323.40
	KC,\$X8,XCSZD3	-WITH REFILL PATTERN 46-63	22021.61	90	026324.00
	SIC,SEN		1310.00	80	026324.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026325.00
	NOP,0		0.30	00	026325.40
	NOP,0		0.30	00	026326.00
	LX,\$X7,XCSZ1		21753.16	10	026326.40
	LX,\$X8,XCSZ1	-RESTORE IX REGS	21753.20	10	026327.00
XCS15E	LX,\$X9,XCSZE1	-XTK-E IN X9	22022.22	10	026327.40
	SX,\$X9,XCSZ5		21756.23	10	026330.00
	LX,\$X10,XCSZ5	-RD OUT BRING BACK FOR TESTION	21756.24	10	026330.40
	KV,\$X10,XCSZE1	-COMP WITH ORIGINAL 0-24	22022.24	90	026331.00
	SIC,SEN		1310.00	80	026331.40
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026332.00
	KC,\$X10,XCSZE2	-WITH COUNT PATTERN 28-45	22023.25	90	026332.40
	SIC,SEN		1310.00	80	026333.00
	BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026333.40
	SR,\$X10,XCSZ28	-REFILL TO WORK AREA	21776.65	70	026334.00
	SIC,SEN		1310.00	80	026334.40
	BZXF,SERS	-ERR IF XF IS 0 25	1304.23	40	026335.00
	LC,\$X10,XCSZ28		21776.64	50	026335.40
	KC,\$X10,XCSZE3	-WITH REFILL PATTERN 46-63	22023.65	90	026336.00
	SIC,SEN		1310.00	80	026336.40

		BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026337.00
		NOP,0		0.30	00	026337.40
		NOP,0		0.30	00	026340.00
		LX,\$X9,XCSZ1		21753.22	10	026340.40
		LX,\$X10,XCSZ1	-RESTORE IX REGS	21753.24	10	026341.00
XCS15F		LX,\$X11,XCSZF1	-XTK-F IN X11	22024.26	10	026341.40
		SX,\$X11,XCSZ5		21756.27	10	026342.00
		LX,\$X12,XCSZ5	-RD OUT BRING BACK FOR TESTING	21756.30	10	026342.40
		KV,\$X12,XCSZF1	-COMP WITH ORIGINAL	22024.30	90	026343.00
		SIC,SEN		1310.00	80	026343.40
		BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026344.00
		KC,\$X12,XCSZF2	-WITH COUNT PATTERN 28-45	22025.31	90	026344.40
		SIC,SEN		1310.00	80	026345.00
		BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026345.40
		SR,\$X12,XCSZ28	-REFILL TO WORK AREA	21776.71	70	026346.00
		SIC,SEN		1310.00	80	026346.40
		BXF,SERS	-ERR IF XF IS 1 25	1304.23	42	026347.00
		LC,\$X12,XCSZ28		21776.70	50	026347.40
		KC,\$X12,XCSZF3	-WITH REFILL PATTERN 46-63	22025.71	90	026350.00
		SIC,SEN		1310.00	80	026350.40
		BZXE,SERS	-ERR IF PATTERN ALTERED	1304.32	C0	026351.00
		NOP,0		0.30	00	026351.40
		NOP,0		0.30	00	026352.00
		LX,\$X11,XCSZ1		21753.26	10	026352.40
		LX,\$X12,XCSZ1	-RESTORE IX REGS	21753.30	10	026353.00
		B,\$+1.0		26354.50	00	026353.40
		B,XCS15	-TO LOOP	26246.50	00	026354.00
		SIC,SEN0+.32		1311.40	80	026354.40
		B,SSW	-CROSS TALK TESTS	1301.10	00	026355.00
		B,\$+1.0		26356.50	00	026355.40
		B,XCS1	-TO LOOP	20171.50	00	026356.00
		SIC,SEN0+.32		1311.40	80	026356.40
		B,SSW	-ALL XCS TESTS	1301.10	00	026357.00
		-TEST,ONES TRAVEL LEFT TOWARDS ZERO IMPEDANCE				
XCS16		LX,\$X0,XCSZ1		21753.00	10	026357.40
		LX,\$X1,XCSZ1	-INITIALIZE BY	21753.02	10	026360.00
		LX,\$X2,XCSZ1		21753.04	10	026360.40
		LX,\$X3,XCSZ1	-SETTING ALL IX	21753.06	10	026361.00
		LX,\$X4,XCSZ1		21753.10	10	026361.40
		LX,\$X5,XCSZ1	-REGS TO ONES	21753.12	10	026362.00
		LX,\$X6,XCSZ1		21753.14	10	026362.40
		LX,\$X7,XCSZ1	-PREPARE FOR	21753.16	10	026363.00
		LX,\$X8,XCSZ1		21753.20	10	026363.40
		LX,\$X9,XCSZ1	-ONES TRAVEL	21753.22	10	026364.00
		LX,\$X10,XCSZ1	-LEFT TOWARD	21753.24	10	026364.40
		LX,\$X11,XCSZ1		21753.26	10	026365.00
		LX,\$X12,XCSZ1		21753.30	10	026365.40
		LX,\$X13,XCSZ1	-ZERO IMPEDANCE	21753.32	10	026366.00
		LX,\$X14,XCSZ1		21753.34	10	026366.40
		LX,\$X15,XCSZ1	-TEST	21753.36	10	026367.00
XCS16A		LX,\$X0,XCSZ2	-LOAD WITH ZEROS ONE TIME	21754.00	10	026367.40
		KV,\$X0,XCSZ2	-TEST BITS 0-24	21754.00	90	026370.00
		SIC,SEN		1310.00	80	026370.40
		BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026371.00
		KC,\$X0,XCSZ2	-TEST BITS 28-45	21754.01	90	026371.40
		SIC,SEN		1310.00	80	026372.00
		BZXE,SERS	-COUNT FLDS MUST COMPARE	1304.32	C0	026372.40
		SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	026373.00
		SIC,SEN		1310.00	80	026373.40
		BXF,SERS	-ERR IF XF NOT 0 AS IT SHOULD BE %25	1304.23	42	026374.00
		LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	026374.40
		KC,\$X0,XCSZ2	-TEST REFILL BITS 46-63	21754.01	90	026375.00
		SIC,SEN		1310.00	80	026375.40
		BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026376.00
		NOP,0		0.30	00	026376.40
		NOP,0		0.30	00	026377.00

LX,\$X0,XCSZ29	-EIGHT RIGHT HAND ONES	21777.00	10	026377.40
KV,\$X0,XCSZ2	-TEST BITS 0-24	21754.00	90	026400.00
SIC,SEN		1310.00	80	026400.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026401.00
KC,\$X0,XCSZ2	-TEST BITS 28-45	21754.01	90	026401.40
SIC,SEN		1310.00	80	026402.00
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS	1304.32	C0	026402.40
SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	026403.00
SIC,SEN		1310.00	80	026403.40
BXF,SERS	-ERR IF XF NOT ZERO	1304.23	42	026404.00
LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	026404.40
KC,\$X0,XCSZ36	-TEST REFILL FOR 8 ONES 46-63	22006.01	90	026405.00
SIC,SEN		1310.00	80	026405.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026406.00
NOP,0		0.30	00	026406.40
NOP,0		0.30	00	026407.00
LX,\$X0,XCSZ30	-16 RIGHT HAND ONES	22000.00	10	026407.40
KV,\$X0,XCSZ2	-TEST BITS 0-24	21754.00	90	026410.00
SIC,SEN		1310.00	80	026410.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026411.00
KC,\$X0,XCSZ2	-TEST BITS 28-45	21754.01	90	026411.40
SIC,SEN		1310.00	80	026412.00
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS	1304.32	C0	026412.40
SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	026413.00
SIC,SEN		1310.00	80	026413.40
BXF,SERS	-ERR IF AF NOT ZERO	1304.23	42	026414.00
LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	026414.40
KC,\$X0,XCSZ37	-TEST REFILL FOR 16 ONES 46-63	22006.41	90	026415.00
SIC,SEN		1310.00	80	026415.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026416.00
NOP,0		0.30	00	026416.40
NOP,0		0.30	00	026417.00
LX,\$X0,XCSZ31	-24 RIGHT HAND ONES	22001.00	10	026417.40
KV,\$X0,XCSZ2	-TEST BITS 0-24	21754.00	90	026420.00
SIC,SEN		1310.00	80	026420.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026421.00
KC,\$X0,XCSZ39	-TEST 28-45 FOR 6 ONES	22007.41	90	026421.40
SIC,SEN		1310.00	80	026422.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026422.40
SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	026423.00
SIC,SEN		1310.00	80	026423.40
BXF,SERS	-ERR IF XF NOT ZERO	1304.23	42	026424.00
LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	026424.40
KC,\$X0,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.01	90	026425.00
SIC,SEN		1310.00	80	026425.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026426.00
NOP,0		0.30	00	026426.40
NOP,0		0.30	00	026427.00
LX,\$X0,XCSZ32	-32 RIGHT HAND ONES	22002.00	10	026427.40
KV,\$X0,XCSZ2	-TEST BITS 0-24	21754.00	90	026430.00
SIC,SEN		1310.00	80	026430.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026431.00
KC,\$X0,XCSZ40	-TEST 28-45 FOR 14 ONES	22010.01	90	026431.40
SIC,SEN		1310.00	80	026432.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026432.40
SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01	70	026433.00
SIC,SEN		1310.00	80	026433.40
BXF,SERS	-ERR IF XF NOT ZERO	1304.23	42	026434.00
LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00	50	026434.40
KC,\$X0,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.01	90	026435.00
SIC,SEN		1310.00	80	026435.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026436.00
NOP,0		0.30	00	026436.40
NOP,0		0.30	00	026437.00
LX,\$X0,XCSZ33	-40 RIGHT HAND ONES	22003.00	10	026437.40
KV,\$X0,XCSZ41	-TEST BITS 0-24	22010.40	90	026440.00

	SIC,SEN		1310.00 80	026440.40
	BZXEZ,SERS	-ERR IF NO COMP EQUAL	1304.32 C4	026441.00
	KC,\$X0,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.01 90	026441.40
	SIC,SEN		1310.00 80	026442.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026442.40
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01 70	026443.00
	SIC,SEN		1310.00 80	026443.40
	BZXF,SERS	-ERR IF XF NOT ONE	1304.23 40	026444.00
	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00 50	026444.40
	KC,\$X0,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.01 90	026445.00
	SIC,SEN		1310.00 80	026445.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026446.00
	NOP,0		0.30 00	026446.40
	NOP,0		0.30 00	026447.00
	LX,\$X0,XCSZ34	-48 RIGHT HAND ONES	22004.00 10	026447.40
	KV,\$X0,XCSZ42	-TEST BITS 0-24	22011.00 90	026450.00
	SIC,SEN		1310.00 80	026450.40
	BZXEZ,SERS	-ERR IF NO COMP EQUAL	1304.32 C4	026451.00
	KC,\$X0,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.01 90	026451.40
	SIC,SEN		1310.00 80	026452.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026452.40
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01 70	026453.00
	SIC,SEN		1310.00 80	026453.40
	BZXF,SERS	-ERR IF XF NOT ONE	1304.23 40	026454.00
	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00 50	026454.40
	KC,\$X0,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.01 90	026455.00
	SIC,SEN		1310.00 80	026455.40
	BZXE,SERS	-ERR IF NOT COMP EQUAL	1304.32 C0	026456.00
	NOP,0		0.30 00	026456.40
	NOP,0		0.30 00	026457.00
	LX,\$X0,XCSZ35	-56 RIGHT HAND ONES	22005.00 10	026457.40
	KV,\$X0,XCSZ43	-TEST BITS 0-24	22011.40 90	026460.00
	SIC,SEN		1310.00 80	026460.40
	BZXEZ,SERS	-ERR IF NO COMP EQUAL	1304.32 C4	026461.00
	KC,\$X0,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.01 90	026461.40
	SIC,SEN		1310.00 80	026462.00
	BZXE,SERS	-COUNT FILD MUST BE ALL ONES	1304.32 C0	026462.40
	SR,\$X0,XCSZ5	-REFILL TO WORK AREA	21756.01 70	026463.00
	SIC,SEN		1310.00 80	026463.40
	BZXF,SERS	-ERR IF XF NOT ONE	1304.23 40	026464.00
	LC,\$X0,XCSZ5	-REFILL INTO COUNT FIELD	21756.00 50	026464.40
	KC,\$X0,XCSZ38	-TEST REFILL FOR 18 ONES	22007.01 90	026465.00
	SIC,SEN		1310.00 80	026465.40
	BZXE,SERS	-ERR IF NOT COMP EQUAL	1304.32 C0	026466.00
	NOP,0		0.30 00	026466.40
	NOP,0		0.30 00	026467.00
	LX,\$X0,XCSZ1		21753.00 10	026467.40
	B,\$+1.0	-RESTORE X0 TO ALL ONES	26471.10 00	026470.00
	B,XCS16A	-TO LOOP IN X0 MARCHING ONES TEST	26367.50 00	026470.40
	SIC,SEN0+.32		1311.40 80	026471.00
	B,SSW	-TO TEST SENSE SWITCHES	1301.10 00	026471.40
XCS16D	LX,\$X3,XCSZ2	-LOAD WITH ZEROS ONE TIME	21754.06 10	026472.00
	KV,\$X3,XCSZ2	-TEST BITS 0-24	21754.06 90	026472.40
	SIC,SEN		1310.00 80	026473.00
	BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32 C4	026473.40
	KC,\$X3,XCSZ2	-TEST BITS 28-45	21754.07 90	026474.00
	SIC,SEN		1310.00 80	026474.40
	BZXE,SERS	-COUNT FIELDS MUST COMPARE	1304.32 C0	026475.00
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	026475.40
	SIC,SEN		1310.00 80	026476.00
	BXF,SERS	-ERR IF XF IS ONE	1304.23 42	026476.40
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	026477.00
	KC,\$X3,XCSZ2	-TEST REFILL BITS 46-63	21754.07 90	026477.40
	SIC,SEN		1310.00 80	026500.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026500.40
	NOP,0		0.30 00	026501.00

NOP,0			0.30 00	026501.40
LX,\$X3,XCSZ29	-EIGHT RIGHT HAND ONES		21777.06 10	026502.00
KV,\$X3,XCSZ2	-TEST BITS 0-24		21754.06 90	026502.40
SIC,SEN			1310.00 80	026503.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP		1304.32 C4	026503.40
KC,\$X3,XCSZ2	-TEST BITS 28-45		21754.07 90	026504.00
SIC,SEN			1310.00 80	026504.40
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS		1304.32 C0	026505.00
SR,\$X3,XCSZ5	-REFILL TO WORK AREA		21756.07 70	026505.40
SIC,SEN			1310.00 80	026506.00
BXF,SERS	-ERR IF XF NOT ZERO		1304.23 42	026506.40
LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD		21756.06 50	026507.00
KC,\$X3,XCSZ36	-TEST REFILL FOR 8 ONES 46-63		22006.07 90	026507.40
SIC,SEN			1310.00 80	026510.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL		1304.32 C0	026510.40
NOP,0			0.30 00	026511.00
NOP,0			0.30 00	026511.40
LX,\$X3,XCSZ30	-16 RIGHT HAND ONES		22000.06 10	026512.00
KV,\$X3,XCSZ2	-TEST BITS 0-24		21754.06 90	026512.40
SIC,SEN			1310.00 80	026513.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP		1304.32 C4	026513.40
KC,\$X3,XCSZ2	-TEST BITS 28-45		21754.07 90	026514.00
SIC,SEN			1310.00 80	026514.40
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS		1304.32 C0	026515.00
SR,\$X3,XCSZ5	-REFILL TO WORK AREA		21756.07 70	026515.40
SIC,SEN			1310.00 80	026516.00
BXF,SERS	-ERR IF XF IS ONE		1304.23 42	026516.40
LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD		21756.06 50	026517.00
KC,\$X3,XCSZ37	-TEST REFILL FOR 16 ONES 46-63		22006.47 90	026517.40
SIC,SEN			1310.00 80	026520.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL		1304.32 C0	026520.40
NOP,0			0.30 00	026521.00
NOP,0			0.30 00	026521.40
LX,\$X3,XCSZ31	-24 RIGHT HAND ONES		22001.06 10	026522.00
KV,\$X3,XCSZ2	-TEST BITS 0-24		21754.06 90	026522.40
SIC,SEN			1310.00 80	026523.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP		1304.32 C4	026523.40
KC,\$X3,XCSZ39	-TEST 28-45 FOR 6 ONES		22007.47 90	026524.00
SIC,SEN			1310.00 80	026524.40
BZXE,SERS	-COUNT FIELD MUST COMPARE		1304.32 C0	026525.00
SR,\$X3,XCSZ5	-REFILL TO WORK AREA		21756.07 70	026525.40
SIC,SEN			1310.00 80	026526.00
BXF,SERS	-ERR IF XF IS ONE		1304.23 42	026526.40
LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD		21756.06 50	026527.00
KC,\$X3,XCSZ38	-TEST REFILL FOR 18 ONES 46-63		22007.07 90	026527.40
SIC,SEN			1310.00 80	026530.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL		1304.32 C0	026530.40
NOP,0			0.30 00	026531.00
NOP,0			0.30 00	026531.40
LX,\$X3,XCSZ32	-32 RIGHT HAND ONES		22002.06 10	026532.00
KV,\$X3,XCSZ2	-TEST BITS 0-24		21754.06 90	026532.40
SIC,SEN			1310.00 80	026533.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP		1304.32 C4	026533.40
KC,\$X3,XCSZ40	-TEST 28-45 FOR 14 ONES		22010.07 90	026534.00
SIC,SEN			1310.00 80	026534.40
BZXE,SERS	-COUNT FIELD MUST COMPARE		1304.32 C0	026535.00
SR,\$X3,XCSZ5	-REFILL TO WORK AREA		21756.07 70	026535.40
SIC,SEN			1310.00 80	026536.00
BXF,SERS	-ERR IF XF IS ONE		1304.23 42	026536.40
LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD		21756.06 50	026537.00
KC,\$X3,XCSZ38	-TEST REFILL FOR 18 ONES 46-63		22007.07 90	026537.40
SIC,SEN			1310.00 80	026540.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL		1304.32 C0	026540.40
LX,\$X3,XCSZ33	-40 RIGHT HAND ONES		22003.06 10	026541.00
KV,\$X3,XCSZ41	-TEST BITS 0-24		22010.46 90	026541.40
SIC,SEN			1310.00 80	026542.00

	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026542.40
	KC,\$X3,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.07 90	026543.00
	SIC,SEN		1310.00 80	026543.40
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026544.00
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	026544.40
	SIC,SEN		1310.00 80	026545.00
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026545.40
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	026546.00
	KC,\$X3,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.07 90	026546.40
	SIC,SEN		1310.00 80	026547.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026547.40
	NOP,0		0.30 00	026550.00
	NOP,0		0.30 00	026550.40
	LX,\$X3,XCSZ34	-48 RIGHT HAND ONES	22004.06 10	026551.00
	KV,\$X3,XCSZ42	-TEST BITS 0-24	22011.06 90	026551.40
	SIC,SEN		1310.00 80	026552.00
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026552.40
	KC,\$X3,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.07 90	026553.00
	SIC,SEN		1310.00 80	026553.40
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026554.00
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	026554.40
	SIC,SEN		1310.00 80	026555.00
	BZXF,SERS	-ERR IF XF NOT ONE	1304.23 40	026555.40
	LC,\$X3,XCSZ5	-REFILL INTO COUNT FIELD	21756.06 50	026556.00
	KC,\$X3,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.07 90	026556.40
	SIC,SEN		1310.00 80	026557.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026557.40
	NOP,0		0.30 00	026560.00
	NOP,0		0.30 00	026560.40
	LX,\$X3,XCSZ35	-56 RIGHT HAND ONES	22005.06 10	026561.00
	KV,\$X3,XCSZ43	-TEST BITS 0-24	22011.46 90	026561.40
	SIC,SEN		1310.00 80	026562.00
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026562.40
	KC,\$X3,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.07 90	026563.00
	SIC,SEN		1310.00 80	026563.40
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026564.00
	SR,\$X3,XCSZ5	-REFILL TO WORK AREA	21756.07 70	026564.40
	SIC,SEN		1310.00 80	026565.00
	BZXF,SERS	-ERR IF XF NOT ONE	1304.23 40	026565.40
	LC,\$X3,XCSZ5	-REFILL TO COUNT FIELD	21756.06 50	026566.00
	KC,\$X3,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.07 90	026566.40
	SIC,SEN		1310.00 80	026567.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026567.40
	NOP,0		0.30 00	026570.00
	NOP,0		0.30 00	026570.40
	LX,\$X3,XCSZ1	-RESTORE X3 TO ALL ONES	21753.06 10	026571.00
	B,\$+1.0		26572.50 00	026571.40
	B,XCS16D	-TO LOOP IN X3 MARCHING ONES	26472.10 00	026572.00
	SIC,SEN0+.32		1311.40 80	026572.40
	B,SSW	-TO TEST SENSE SWITCHES	1301.10 00	026573.00
XCS16H	LX,\$X7,XCSZ2	-LOAD WITH ZEROS ONE TIME	21754.16 10	026573.40
	KV,\$X7,XCSZ2	-TEST BITS 0-24	21754.16 90	026574.00
	SIC,SEN		1310.00 80	026574.40
	BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32 C4	026575.00
	KC,\$X7,XCSZ2	-TEST BITS 28-45	21754.17 90	026575.40
	SIC,SEN		1310.00 80	026576.00
	BZXE,SERS	-COUNT FIELDS MUST COMPARE	1304.32 C0	026576.40
	SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17 70	026577.00
	SIC,SEN		1310.00 80	026577.40
	BXF,SERS	-ERR IF XF IS ONE	1304.23 42	026600.00
	LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16 50	026600.40
	KC,\$X7,XCSZ2	-TEST REFILL BITS 46-63	21754.17 90	026601.00
	SIC,SEN		1310.00 80	026601.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026602.00
	NOP,0		0.30 00	026602.40
	NOP,0		0.30 00	026603.00

LX,\$X7,XCSZ29	-EIGHT RIGHT HAND ONES	2177.16	10	026603.40
KV,\$X7,XCSZ2	-TEST BITS 0-24	21754.16	90	026604.00
SIC,SEN		1310.00	80	026604.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026605.00
KC,\$X7,XCSZ2	-TEST BITS 28-45	21754.17	90	026605.40
SIC,SEN		1310.00	80	026606.00
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS	1304.32	C0	026606.40
SR,\$X7,XCSZ5	-REFILL INTO WORK AREA	21756.17	70	026607.00
SIC,SEN		1310.00	80	026607.40
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026610.00
LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16	50	026610.40
KC,\$X7,XCSZ36	-TEST REFILL FOR 8 ONES 46-63	22006.17	90	026611.00
SIC,SEN		1310.00	80	026611.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026612.00
NOP,0		0.30	00	026612.40
NOP,0		0.30	00	026613.00
LX,\$X7,XCSZ30	-16 RIGHT HAND ONES	22000.16	10	026613.40
KV,\$X7,XCSZ2	-TEST BITS 0-24.	21754.16	90	026614.00
SIC,SEN		1310.00	80	026614.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026615.00
KC,\$X7,XCSZ2	-TEST BITS 28-45	21754.17	90	026615.40
SIC,SEN		1310.00	80	026616.00
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS	1304.32	C0	026616.40
SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17	70	026617.00
SIC,SEN		1310.00	80	026617.40
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026620.00
LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16	50	026620.40
KC,\$X7,XCSZ37	-TEST REFILL FOR 16 ONES 46-63	22006.57	90	026621.00
SIC,SEN		1310.00	80	026621.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026622.00
NOP,0		0.30	00	026622.40
NOP,0		0.30	00	026623.00
LX,\$X7,XCSZ31	-24 RIGHT HAND ONES	22001.16	10	026623.40
KV,\$X7,XCSZ2	-TEST BITS 0-24	21754.16	90	026624.00
SIC,SEN		1310.00	80	026624.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026625.00
KC,\$X7,XCSZ39	-TEST 28-45 FOR 6 ONES	22007.57	90	026625.40
SIC,SEN		1310.00	80	026626.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026626.40
SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17	70	026627.00
SIC,SEN		1310.00	80	026627.40
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026630.00
LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16	50	026630.40
KC,\$X7,XCSZ38	-TEST REFILL FOR 18 ONES	22007.17	90	026631.00
SIC,SEN		1310.00	80	026631.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026632.00
NOP,0		0.30	00	026632.40
NOP,0		0.30	00	026633.00
LX,\$X7,XCSZ32	-32 RIGHT HAND ONES	22002.16	10	026633.40
KV,\$X7,XCSZ2	-TEST BITS 0-24	21754.16	90	026634.00
SIC,SEN		1310.00	80	026634.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026635.00
KC,\$X7,XCSZ40	-TEST 28-45 FOR 14 ONES	22010.17	90	026635.40
SIC,SEN		1310.00	80	026636.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026636.40
SR,\$X7,XCSZ5	-REFILL TO WORK AREA	21756.17	70	026637.00
SIC,SEN		1310.00	80	026637.40
BXF,SERS	-ERROR IF XF IS ONE	1304.23	42	026640.00
LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16	50	026640.40
KC,\$X7,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.17	90	026641.00
SIC,SEN		1310.00	80	026641.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026642.00
NOP,0		0.30	00	026642.40
NOP,0		0.30	00	026643.00
LX,\$X7,XCSZ33	-40 RIGHT HAND ONES	22003.16	10	026643.40
KV,\$X7,XCSZ41	-TEST BITS 0-24	22010.56	90	026644.00

	SIC,SEN		1310.00 80	026644.40
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026645.00
	KC,\$X7,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.17 90	026645.40
	SIC,SEN		1310.00 80	026646.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026646.40
	SR,\$X7,XCSZ5	-REFILL INTO WORK AREA	21756.17 70	026647.00
	SIC,SEN		1310.00 80	026647.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026650.00
	LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16 50	026650.40
	KC,\$X7,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.17 90	026651.00
	SIC,SEN		1310.00 80	026651.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026652.00
	NOP,0		0.30 00	026652.40
	NOP,0		0.30 00	026653.00
	LX,\$X7,XCSZ34	-48 RIGHT HAND ONES	22004.16 10	026653.40
	KV,\$X7,XCSZ42	-TEST BITS 0-24	22011.16 90	026654.00
	SIC,SEN		1310.00 80	026654.40
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026655.00
	KC,\$X7,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.17 90	026655.40
	SIC,SEN		1310.00 80	026656.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026656.40
	SR,\$X7,XCSZ5	-REFILL INTO WORK AREA	21756.17 70	026657.00
	SIC,SEN		1310.00 80	026657.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026660.00
	LC,\$X7,XCSZ5	-REFILL INTO COUNT FIELD	21756.16 50	026660.40
	KC,\$X7,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.17 90	026661.00
	SIC,SEN		1310.00 80	026661.40
	BZXE,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C0	026662.00
	NOP,0		0.30 00	026662.40
	NOP,0		0.30 00	026663.00
	LX,\$X7,XCSZ35	-56 RIGHT HAND ONES	22005.16 10	026663.40
	KV,\$X7,XCSZ43	-TEST BITS 0-24	22011.56 90	026664.00
	SIC,SEN		1310.00 80	026664.40
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026665.00
	KC,\$X7,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.17 90	026665.40
	SIC,SEN		1310.00 80	026666.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026666.40
	SR,\$X7,XCSZ5	-REFILL INTO WORK AREA	21756.17 70	026667.00
	SIC,SEN		1310.00 80	026667.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026670.00
	LC,\$X7,XCSZ5	-REFILL TO COUNT FIELD	21756.16 50	026670.40
	KC,\$X7,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.17 90	026671.00
	SIC,SEN		1310.00 80	026671.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026672.00
	NOP,0		0.30 00	026672.40
	NOP,0		0.30 00	026673.00
	LX,\$X7,XCSZ1	-RESTORE X7 TO ALL ONES	21753.16 10	026673.40
	B,\$+1.0		26675.10 00	026674.00
	B,XCS16H	-TO LOOP IN X7 MARCHING ONES	26573.50 00	026674.40
	SIC,SEN0+.32		1311.40 80	026675.00
	B,SSW	-TO TEST SENSE SWITCHES	1301.10 00	026675.40
XCS16L	LX,\$X11,XCSZ2	-LOAD WITH ZEROS	21754.26 10	026676.00
	KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26 90	026676.40
	SIC,SEN		1310.00 80	026677.00
	BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32 C4	026677.40
	KC,\$X11,XCSZ2	-TEST BITS 28-45	21754.27 90	026700.00
	SIC,SEN		1310.00 80	026700.40
	BZXE,SERS	-COUNT FIELD MUST COMPAREA	1304.32 C0	026701.00
	SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27 70	026701.40
	SIC,SEN		1310.00 80	026702.00
	BXF,SERS	-ERR IF XF IS ONE	1304.23 42	026702.40
	LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26 50	026703.00
	KC,\$X11,XCSZ2	-TEST REFILL BITS 46-63	21754.27 90	026703.40
	SIC,SEN		1310.00 80	026704.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026704.40
	NOP,0		0.30 00	026705.00

NOP,0		0.30	00	026705.40
LX,\$X11,XCSZ29	-8 RIGHT HAND ONES	21777.26	10	026706.00
KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26	90	026706.40
SIC,SEN		1310.00	80	026707.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026707.40
KC,\$X11,XCSZ2	-TEST BITS 28-45	21754.27	90	026710.00
SIC,SEN		1310.00	80	026710.40
BZXE,SERS	-COUNT FIELD MUST BE ALL ZEROS	1304.32	C0	026711.00
SR,\$X11,XCSZ5	-REFILL INTO WORK AREA	21756.27	70	026711.40
SIC,SEN		1310.00	80	026712.00
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026712.40
LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26	50	026713.00
KC,\$X11,XCSZ36	-TEST REFILL FOR 8 ONES 46-63	22006.27	90	026713.40
SIC,SEN		1310.00	80	026714.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026714.40
NOP,0		0.30	00	026715.00
NOP,0		0.30	00	026715.40
LX,\$X11,XCSZ30	-16 RIGHT HAND ONES	22000.26	10	026716.00
KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26	90	026716.40
SIC,SEN		1310.00	80	026717.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026717.40
KC,\$X11,XCSZ2	-TEST BITS 28-45	21754.27	90	026720.00
SIC,SEN		1310.00	80	026720.40
BZXE,SERS	-COUNT FIELD ALL ZEROS	1304.32	C0	026721.00
SR,\$X11,XCSZ5	-REFILL INTO WORK AREA	21756.27	70	026721.40
SIC,SEN		1310.00	80	026722.00
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026722.40
LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26	50	026723.00
KC,\$X11,XCSZ37	-TEST REFILL FOR 16 ONES 46-63	22006.67	90	026723.40
SIC,SEN		1310.00	80	026724.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026724.40
NOP,0		0.30	00	026725.00
NOP,0		0.30	00	026725.40
LX,\$X11,XCSZ31	-24 RIGHT HAND ONES	22001.26	10	026726.00
KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26	90	026726.40
SIC,SEN		1310.00	80	026727.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026727.40
KC,\$X11,XCSZ39	-TEST 28-45 FOR 6 ONES	22007.67	90	026730.00
SIC,SEN		1310.00	80	026730.40
KC,\$X11,XCSZ39	-TEST 28-45 FOR 6 ONES	22007.67	90	026730.00
SIC,SEN		1310.00	80	026730.40
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026731.00
SR,\$X11,XCSZ5	-REFILL INTO WORK AREA	21756.27	70	026731.40
SIC,SEN		1310.00	80	026732.00
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	026732.40
LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26	50	026733.00
KC,\$X11,XCSZ38	-TEST REFILL FOR 18 ONES	22007.27	90	026733.40
SIC,SEN		1310.00	80	026734.00
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	026734.40
NOP,0		0.30	00	026735.00
NOP,0		0.30	00	026735.40
LX,\$X11,XCSZ32	-32 RIGHT HAND ONES	22002.26	10	026736.00
KV,\$X11,XCSZ2	-TEST BITS 0-24	21754.26	90	026736.40
SIC,SEN		1310.00	80	026737.00
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	026737.40
KC,\$X11,XCSZ40	-TEST 28-45 FOR 14 ONES	22010.27	90	026740.00
SIC,SEN		1310.00	80	026740.40
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	026741.00
SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27	70	026741.40
SIC,SEN		1310.00	80	026742.00
BXF,SERS	-ERROR IF XF IS ONE	1304.23	42	026742.40
LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26	50	026743.00
KC,\$X11,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.27	90	026743.40
SIC,SEN		1310.00	80	026744.00

	BZXE,SERS	-ERROR IF NOT COMPARE EQUAL	1304.32 C0	026744.40
	NOP,0		0.30 00	026745.00
	NOP,0		0.30 00	026745.40
	LX,\$X11,XCSZ33	-40 RIGHT HAND ONES	22003.26 10	026746.00
	KV,\$X11,XCSZ41	-TEST BITS 0-24	22010.66 90	026746.40
	SIC,SEN		1310.00 80	026747.00
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026747.40
	KC,\$X11,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.27 90	026750.00
	SIC,SEN		1310.00 80	026750.40
	BZXE,SERS	-COUNTFIELD MUST BE ALL ONES	1304.32 C0	026751.00
	SR,\$X11,XCSZ5	-REFILL TO WORK AREA	21756.27 70	026751.40
	SIC,SEN		1310.00 80	026752.00
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026752.40
	LC,\$X11,XCSZ5	-REFILL INTO COUNTFIELD	21756.26 50	026753.00
	KC,\$X11,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.27 90	026753.40
	SIC,SEN		1310.00 80	026754.00
	BZXE,SERS	-ERROR IF NOT COMPARE EQUAL	1304.32 C0	026754.40
	NOP,0		0.30 00	026755.00
	NOP,0		0.30 00	026755.40
	LX,\$X11,XCSZ34	-48 RIGHT HAND ONES	22004.26 10	026756.00
	KV,\$X11,XCSZ42	-TEST BITS 0-24	22011.26 90	026756.40
	SIC,SEN		1310.00 80	026757.00
	BZXEZ,SERS	-ERROR IF NO COMPARE EQUAL	1304.32 C4	026757.40
	KC,\$X11,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.27 90	026760.00
	SIC,SEN		1310.00 80	026760.40
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026761.00
	SR,\$X11,XCSZ5	-REFILL INTO WORK AREA	21756.27 70	026761.40
	SIC,SEN		1310.00 80	026762.00
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026762.40
	LC,\$X11,XCSZ5	-REFILL INTO COUNT FIELD	21756.26 50	026763.00
	KC,\$X11,XCSZ38	-TEST FRFILL FOR 18 ONES 46-63	22007.27 90	026763.40
	SIC,SEN		1310.00 80	026764.00
	BZXE,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C0	026764.40
	NOP,0		0.30 00	026765.00
	NOP,0		0.30 00	026765.40
	LX,\$X11,XCSZ35	-56 RIGHT HAND ONES	22005.26 10	026766.00
	KV,\$X11,XCSZ43	-TEST BITS 0-24.	22011.66 90	026766.40
	SIC,SEN		1310.00 80	026767.00
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	026767.40
	KC,\$X11,XCSZ38	-TEST 28-45 FOR 18 ONES	22007.27 90	026770.00
	SIC,SEN		1310.00 80	026770.40
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	026771.00
	SR,\$X11,XCSZ5	-REFILL INTO WORK AREA	21756.27 70	026771.40
	SIC,SEN		1310.00 80	026772.00
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	026772.40
	LC,\$X11,XCSZ5	-REFILL INTO COUNTFIELD	21756.26 50	026773.00
	KC,\$X11,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.27 90	026773.40
	SIC,SEN		1310.00 80	026774.00
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	026774.40
	NOP,0		0.30 00	026775.00
	NOP,0		0.30 00	026775.40
	LX,\$X11,XCSZ1	-RESTORE X11 TO ALL ONES	21753.26 10	026776.00
	B,\$+1.0		26777.50 00	026776.40
	B,XCS16L	-TO LOOP IN X11 MARCHING ONES	26676.10 00	026777.00
	SIC,SEN0+.32		1311.40 80	026777.40
	B,SSW	-TO TEST SENSE SWITCHES	1301.10 00	027000.00
XCS160	LX,\$X15,XCSZ2	-LOAD WITH ZEROS	21754.36 10	027000.40
	KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36 90	027001.00
	SIC,SEN		1310.00 80	027001.40
	BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32 C4	027002.00
	KC,\$X15,XCSZ2	-TEST BITS 28-45	21754.37 90	027002.40
	SIC,SEN		1310.00 80	027003.00
	BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32 C0	027003.40
	SR,\$X15,XCSZ5	-REFILL TO WORK AREA	21756.37 70	027004.00
	SIC,SEN		1310.00 80	027004.40
	BXF,SERS	-ERR IF XF IS ONE	1304.23 42	027005.00

LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36	50	027005.40
KC,\$X15,XCSZ2	-TEST REFILL BITS 46-63	21754.37	90	027006.00
SIC,SEN		1310.00	80	027006.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	027007.00
NOP,0		0.30	00	027007.40
NOP,0		0.30	00	027010.00
LX,\$X15,XCSZ29	-8 RIGHT HAND ONES	21777.36	10	027010.40
KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36	90	027011.00
SIC,SEN		1310.00	80	027011.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	027012.00
KC,\$X15,XCSZ2	-TEST BITS 28-45	21754.37	90	027012.40
SIC,SEN		1310.00	80	027013.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	027013.40
SR,\$X15,XCSZ5	-REFILL TO WORK AREA	21756.37	70	027014.00
SIC,SEN		1310.00	80	027014.40
BXF,SERS	-ERROR IF XF IS ONE	1304.23	42	027015.00
LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36	50	027015.40
KC,\$X15,XCSZ36	-TEST REFILL FOR 8 ONES 46-63	22006.37	90	027016.00
SIC,SEN		1310.00	80	027016.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	027017.00
NOP,0		0.30	00	027017.40
NOP,0		0.30	00	027020.00
LX,\$X15,XCSZ30	-16 RIGHT HAND ONES	22000.36	10	027020.40
KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36	90	027021.00
SIC,SEN		1310.00	80	027021.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	027022.00
KC,\$X15,XCSZ2	-TEST BITS 28-45	21754.37	90	027022.40
SIC,SEN		1310.00	80	027023.00
BZXE,SERS	-COUNT FIELD SHOULD BE ZEROS	1304.32	C0	027023.40
SR,\$X15,XCSZ5	-REFILL TO WORK AREA	21756.37	70	027024.00
SIC,SEN		1310.00	80	027024.40
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	027025.00
LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36	50	027025.40
KC,\$X15,XCSZ37	-TEST REFILL FOR 16 ONES 46-63	22006.77	90	027026.00
NOP,0		0.30	00	027026.40
NOP,0		0.30	00	027027.00
SIC,SEN		1310.00	80	027027.40
BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32	C0	027030.00
LX,\$X15,XCSZ31	-24 RIGHT HAND ONES	22001.36	10	027030.40
KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36	90	027031.00
SIC,SEN		1310.00	80	027031.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	027032.00
KC,\$X15,XCSZ39	-TEST 28-45 FOR 6 ONES	22007.77	90	027032.40
SIC,SEN		1310.00	80	027033.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	027033.40
SR,\$X15,XCSZ5	-REFILL INTO WORK AREA	21756.37	70	027034.00
SIC,SEN		1310.00	80	027034.40
BXF,SERS	-ERR IF XF IS ONE	1304.23	42	027035.00
LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36	50	027035.40
KC,\$X15,XCSZ38	-TEST REFILL FOR 18 ONES	22007.37	90	027036.00
SIC,SEN		1310.00	80	027036.40
BZXE,SERS	-ERROR IF NOT COMPARE EQUAL	1304.32	C0	027037.00
NOP,0		0.30	00	027037.40
NOP,0		0.30	00	027040.00
LX,\$X15,XCSZ32	-32 RIGHT HAND ONES	22002.36	10	027040.40
KV,\$X15,XCSZ2	-TEST BITS 0-24	21754.36	90	027041.00
SIC,SEN		1310.00	80	027041.40
BZXEZ,SERS	-ERROR IF BIT PICKED UP	1304.32	C4	027042.00
KC,\$X15,XCSZ40	-TEST 28-45 FOR 14 ONES.	22010.37	90	027042.40
SIC,SEN		1310.00	80	027043.00
BZXE,SERS	-COUNT FIELD MUST COMPARE	1304.32	C0	027043.40
SR,\$X15,XCSZ5	-REFILL TO WORK AREA	21756.37	70	027044.00
SIC,SEN		1310.00	80	027044.40
BXF,SERS	-ERROR IF XF IS ONE	1304.23	42	027045.00
LC,\$X15,XCSZ5	-REFILL INTO COUNT FIELD	21756.36	50	027045.40
KC,\$X15,XCSZ38	-TEST REFILL FOR 18 ONES 46-63	22007.37	90	027046.00

SIC,SEN	BZXE,SERS	-ERROR IF NOT COMPARE EQUAL	1310.00 80	027046.40
NOP,0			1304.32 C0	027047.00
	NOP,0		0.30 00	027047.40
	NOP,0		0.30 00	027050.00
LX,\$X15,XCSZ33		-40 RIGHT HAND ONES.	22003.36 10	027050.40
KV,\$X15,XCSZ41		-TEST BITS 0-24.	22010.76 90	027051.00
SIC,SEN			1310.00 80	027051.40
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	027052.00
KC,\$X15,XCSZ38		-TEST 28-45 FOR 18 ONES	22007.37 90	027052.40
SIC,SEN			1310.00 80	027053.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	027053.40
SR,\$X15,XCSZ5		-REFILL TO WORK AREA	21756.37 70	027054.00
SIC,SEN			1310.00 80	027054.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	027055.00
LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36 50	027055.40
KC,\$X15,XCSZ38		-TEST REFILL FOR 18 ONES 46-63	22007.37 90	027056.00
SIC,SEN			1310.00 80	027056.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	027057.00
NOP,0			0.30 00	027057.40
	NOP,0		0.30 00	027060.00
LX,\$X15,XCSZ34		-48 RIGHT HAND ONES	22004.36 10	027060.40
KV,\$X15,XCSZ42		-TEST BITS 0-24	22011.36 90	027061.00
SIC,SEN			1310.00 80	027061.40
	BZXEZ,SERS	-ERROR IF NO COMPARE EQUAL	1304.32 C4	027062.00
KC,\$X15,XCSZ38		-TEST 28-45 FOR 18 ONES	22007.37 90	027062.40
SIC,SEN			1310.00 80	027063.00
	BZXE,SERS	-COUNT FIELD MUST BE ALL ONES	1304.32 C0	027063.40
SR,\$X15,XCSZ5		-REFILL INTO WORK AREA	21756.37 70	027064.00
SIC,SEN			1310.00 80	027064.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	027065.00
LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36 50	027065.40
KC,\$X15,XCSZ38		-TEST REFILL FOR 18 ONES 46-63	22007.37 90	027066.00
SIC,SEN			1310.00 80	027066.40
	BZXE,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C0	027067.00
NOP,0			0.30 00	027067.40
	NOP,0		0.30 00	027070.00
LX,\$X15,XCSZ35		-56 RIGHT HAND ONES	22005.36 10	027070.40
KV,\$X15,XCSZ43		-TEST BITS 0-24	22011.76 90	027071.00
SIC,SEN			1310.00 80	027071.40
	BZXEZ,SERS	-ERR IF NO COMPARE EQUAL	1304.32 C4	027072.00
KC,\$X15,XCSZ38		-TEST 28-45 FOR 18 ONES	22007.37 90	027072.40
SIC,SEN			1310.00 80	027073.00
	BZXE,SERS	-COUNT FIELD MUST BE ONES	1304.32 C0	027073.40
SR,\$X15,XCSZ5		-REFILL INTO WORK AREA	21756.37 70	027074.00
SIC,SEN			1310.00 80	027074.40
	BZXF,SERS	-ERR IF XF IS ZERO	1304.23 40	027075.00
LC,\$X15,XCSZ5		-REFILL INTO COUNT FIELD	21756.36 50	027075.40
KC,\$X15,XCSZ38		-TEST REFILL FOR 18 ONES 46-63	22007.37 90	027076.00
SIC,SEN			1310.00 80	027076.40
	BZXE,SERS	-ERR IF NOT COMPARE EQUAL	1304.32 C0	027077.00
NOP,0			0.30 00	027077.40
	NOP,0		0.30 00	027100.00
LX,\$X15,XCSZ1		-RESTORE X15 TO ALL ONES	21753.36 10	027100.40
B,\$+1.0			27102.10 00	027101.00
	B,XCS16Q	-TO LOOP IN X15 MARCHING ONES	27000.50 00	027101.40
SIC,SEN0+.32			1311.40 80	027102.00
	B,SSW	-TO TEST SENSE SWITCHES	1301.10 00	027102.40
	----I222----	CHECK SV AND SVA.		
		-THIS TEST CHECKS THE SV AND SVA INSTRUCTIONS		
		-FOR DATA AND THE STORING OF THE CORRECT		
		-NUMBER OF BITS BY SVA.		
122	LX,\$X1,I221D	-UPDATE IDENT	27106.02 10	027103.00
	SX,\$X1,DPET13		1437.03 10	027103.40
	SIC,RET		1306.40 80	027104.00

ID	Code	Description	Value	Unit	Code
	Z,IC222		1443.10	00	027104.40
	BD,I221		31251.22	00	027105.00
	CNOP		27107.44	00	027105.40
I221D	%IQSZ=DD%BU,64,8=,I 222	Z			027106.00
I221	LX,\$X0,BIT0	-CHECK SV BIT 00 TO 3 MEMORIES.	33331.00	10	027107.40
	Z,I22DMP		31324.22	00	027110.00
	Z,\$R		11.22	00	027110.40
	Z,\$X1		21.22	00	027111.00
	SV,\$X0,I22DMP		31324.01	30	027111.40
	SV,\$X0,\$R		11.01	30	027112.00
	SV,\$X0,\$X1		21.01	30	027112.40
	KV,\$X0,I22DMP		31324.00	90	027113.00
	BXE,\$+1.32		27115.32	C2	027113.40
	SIC,SEN		1310.00	80	027114.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10	00	027114.40
	KV,\$X0,\$R		11.00	90	027115.00
	BXE,\$+1.32		27117.32	C2	027115.40
	SIC,SEN		1310.00	80	027116.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10	00	027116.40
	KV,\$X0,\$X1		21.00	90	027117.00
	BXE,\$+1.32		27121.32	C2	027117.40
	SIC,SEN		1310.00	80	027120.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10	00	027120.40
	LX,\$X0,BIT1	-CHECK SV BIT 01 TO 3 MEMORIES.	33332.00	10	027121.00
	Z,I22DMP		31324.22	00	027121.40
	Z,\$R		11.22	00	027122.00
	Z,\$X1		21.22	00	027122.40
	SV,\$X0,I22DMP		31324.01	30	027123.00
	SV,\$X0,\$R		11.01	30	027123.40
	SV,\$X0,\$X1		21.01	30	027124.00
	KV,\$X0,I22DMP		31324.00	90	027124.40
	BXE,\$+1.32		27126.72	C2	027125.00
	SIC,SEN		1310.00	80	027125.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10	00	027126.00
	KV,\$X0,\$R		11.00	90	027126.40
	BXE,\$+1.32		27130.72	C2	027127.00
	SIC,SEN		1310.00	80	027127.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10	00	027130.00
	KV,\$X0,\$X1		21.00	90	027130.40
	BXE,\$+1.32		27132.72	C2	027131.00
	SIC,SEN		1310.00	80	027131.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10	00	027132.00

	LX,\$X0,BIT2	-CHECK SV BIT 02 TO 3 MEMORIES.	33333.00 10	027132.40
	Z,I22DMP		31324.22 00	027133.00
	Z,\$R		11.22 00	027133.40
	Z,\$X1		21.22 00	027134.00
	SV,\$X0,I22DMP		31324.01 30	027134.40
	SV,\$X0,\$R		11.01 30	027135.00
	SV,\$X0,\$X1		21.01 30	027135.40
	KV,\$X0,I22DMP		31324.00 90	027136.00
	BXE,\$+1.32		27140.32 C2	027136.40
	SIC,SEN		1310.00 80	027137.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027137.40
			-	
	KV,\$X0,\$R		11.00 90	027140.00
	BXE,\$+1.32		27142.32 C2	027140.40
	SIC,SEN		1310.00 80	027141.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027141.40
			-	
	KV,\$X0,\$X1		21.00 90	027142.00
	BXE,\$+1.32		27144.32 C2	027142.40
	SIC,SEN		1310.00 80	027143.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027143.40
			-	
	B,\$+1.0		27145.10 00	027144.00
	BD,I221		27107.44 00	027144.40
	SIC,SEN0+.32		1311.40 80	027145.00
	B,SSW	-TO SSIP.	1301.10 00	027145.40
	BD,\$+.32		27146.44 00	027146.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027146.40
	V+,\$X13,BIT0		33331.32 B0	027147.00
	SX,\$X13,IC222		31251.33 10	027147.40
I222	LX,\$X0,BIT3	-CHECK SV BIT 03 TO 3 MEMORIES.	33334.00 10	027150.00
	Z,I22DMP		31324.22 00	027150.40
	Z,\$R		11.22 00	027151.00
	Z,\$X1		21.22 00	027151.40
	SV,\$X0,I22DMP		31324.01 30	027152.00
	SV,\$X0,\$R		11.01 30	027152.40
	SV,\$X0,\$X1		21.01 30	027153.00
	KV,\$X0,I22DMP		31324.00 90	027153.40
	BXE,\$+1.32		27155.72 C2	027154.00
	SIC,SEN		1310.00 80	027154.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027155.00
			-	
	KV,\$X0,\$R		11.00 90	027155.40
	BXE,\$+1.32		27157.72 C2	027156.00
	SIC,SEN		1310.00 80	027156.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027157.00
			-	
	KV,\$X0,\$X1		21.00 90	027157.40
	BXE,\$+1.32		27161.72 C2	027160.00
	SIC,SEN		1310.00 80	027160.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027161.00

LX,\$X0,BIT4	-CHECK SV BIT 04 TO 3 MEMORIES.	33335.00 10	027161.40
Z,I22DMP		31324.22 00	027162.00
Z,\$R		11.22 00	027162.40
Z,\$X1		21.22 00	027163.00
SV,\$X0,I22DMP		31324.01 30	027163.40
SV,\$X0,\$R		11.01 30	027164.00
SV,\$X0,\$X1		21.01 30	027164.40
KV,\$X0,I22DMP		31324.00 90	027165.00
BXE,\$+1.32		27167.32 C2	027165.40
SIC,SEN		1310.00 80	027166.00
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027166.40
-			
KV,\$X0,\$R		11.00 90	027167.00
BXE,\$+1.32		27171.32 C2	027167.40
SIC,SEN		1310.00 80	027170.00
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027170.40
-			
KV,\$X0,\$X1		21.00 90	027171.00
BXE,\$+1.32		27173.32 C2	027171.40
SIC,SEN		1310.00 80	027172.00
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027172.40
-			
LX,\$X0,BIT5	-CHECK SV BIT 05 TO 3 MEMORIES.	33336.00 10	027173.00
Z,I22DMP		31324.22 00	027173.40
Z,\$R		11.22 00	027174.00
Z,\$X1		21.22 00	027174.40
SV,\$X0,I22DMP		31324.01 30	027175.00
SV,\$X0,\$R		11.01 30	027175.40
SV,\$X0,\$X1		21.01 30	027176.00
KV,\$X0,I22DMP		31324.00 90	027176.40
BXE,\$+1.32		27200.72 C2	027177.00
SIC,SEN		1310.00 80	027177.40
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027200.00
-			
KV,\$X0,\$R		11.00 90	027200.40
BXE,\$+1.32		27202.72 C2	027201.00
SIC,SEN		1310.00 80	027201.40
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027202.00
-			
KV,\$X0,\$X1		21.00 90	027202.40
BXE,\$+1.32		27204.72 C2	027203.00
SIC,SEN		1310.00 80	027203.40
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027204.00
-			
B,\$+1.0		27205.50 00	027204.40
BD,I222		27150.04 00	027205.00
SIC,SEN0+.32		1311.40 80	027205.40
B,SSW	-TO SSIP.	1301.10 00	027206.00
BD,\$+.32		27207.04 00	027206.40
-			
LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027207.00
V+,\$X13,BIT1		33332.32 B0	027207.40
SX,\$X13,IC222		31251.33 10	027210.00

1223	LX,\$X0,BIT6	-CHECK SV BIT 06 TO 3 MEMORIES.	33337.00 10	027210.40
	Z,I22DMP		31324.22 00	027211.00
	Z,\$R		11.22 00	027211.40
	Z,\$X1		21.22 00	027212.00
	SV,\$X0,I22DMP		31324.01 30	027212.40
	SV,\$X0,\$R		11.01 30	027213.00
	SV,\$X0,\$X1		21.01 30	027213.40
	KV,\$X0,I22DMP		31324.00 90	027214.00
	BXE,\$+1.32		27216.32 C2	027214.40
	SIC,SEN		1310.00 80	027215.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027215.40
			-	
	KV,\$X0,\$R		11.00 90	027216.00
	BXE,\$+1.32		27220.32 C2	027216.40
	SIC,SEN		1310.00 80	027217.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027217.40
			-	
	KV,\$X0,\$X1		21.00 90	027220.00
	BXE,\$+1.32		27222.32 C2	027220.40
	SIC,SEN		1310.00 80	027221.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027221.40
	LX,\$X0,BIT7	-CHECK SV BIT 07 TO 3 MEMORIES.	33340.00 10	027222.00
	Z,I22DMP		31324.22 00	027222.40
	Z,\$R		11.22 00	027223.00
	Z,\$X1		21.22 00	027223.40
	SV,\$X0,I22DMP		31324.01 30	027224.00
	SV,\$X0,\$R		11.01 30	027224.40
	SV,\$X0,\$X1		21.01 30	027225.00
	KV,\$X0,I22DMP		31324.00 90	027225.40
	BXE,\$+1.32		27227.72 C2	027226.00
	SIC,SEN		1310.00 80	027226.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027227.00
			-	
	KV,\$X0,\$R		11.00 90	027227.40
	BXE,\$+1.32		27231.72 C2	027230.00
	SIC,SEN		1310.00 80	027230.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027231.00
			-	
	KV,\$X0,\$X1		21.00 90	027231.40
	BXE,\$+1.32		27233.72 C2	027232.00
	SIC,SEN		1310.00 80	027232.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027233.00

	LX,\$X0,BIT8	-CHECK SV BIT 08 TO 3 MEMORIES.	33341.00 10	027233.40
	Z,I22DMP		31324.22 00	027234.00
	Z,\$R		11.22 00	027234.40
	Z,\$X1		21.22 00	027235.00
	SV,\$X0,I22DMP		31324.01 30	027235.40
	SV,\$X0,\$R		11.01 30	027236.00
	SV,\$X0,\$X1		21.01 30	027236.40
	KV,\$X0,I22DMP		31324.00 90	027237.00
	BXE,\$+1.32		27241.32 C2	027237.40
	SIC,SEN		1310.00 80	027240.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027240.40
			-	
	KV,\$X0,\$R		11.00 90	027241.00
	BXE,\$+1.32		27243.32 C2	027241.40
	SIC,SEN		1310.00 80	027242.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027242.40
			-	
	KV,\$X0,\$X1		21.00 90	027243.00
	BXE,\$+1.32		27245.32 C2	027243.40
	SIC,SEN		1310.00 80	027244.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027244.40
			-	
	B,\$+1.0		27246.10 00	027245.00
	BD,I223		27210.44 00	027245.40
	SIC,SEN0+.32		1311.40 80	027246.00
	B,SSW	-TO SSIP.	1301.10 00	027246.40
	BD,\$+.32		27247.44 00	027247.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027247.40
	V+,\$X13,BIT2		33333.32 B0	027250.00
	SX,\$X13,IC222		31251.33 10	027250.40
			-	
1224	LX,\$X0,BIT9	-CHECK SV BIT 09 TO 3 MEMORIES.	33342.00 10	027251.00
	Z,I22DMP		31324.22 00	027251.40
	Z,\$R		11.22 00	027252.00
	Z,\$X1		21.22 00	027252.40
	SV,\$X0,I22DMP		31324.01 30	027253.00
	SV,\$X0,\$R		11.01 30	027253.40
	SV,\$X0,\$X1		21.01 30	027254.00
	KV,\$X0,I22DMP		31324.00 90	027254.40
	BXE,\$+1.32		27256.72 C2	027255.00
	SIC,SEN		1310.00 80	027255.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027256.00
			-	
	KV,\$X0,\$R		11.00 90	027256.40
	BXE,\$+1.32		27260.72 C2	027257.00
	SIC,SEN		1310.00 80	027257.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027260.00
			-	
	KV,\$X0,\$X1		21.00 90	027260.40
	BXE,\$+1.32		27262.72 C2	027261.00
	SIC,SEN		1310.00 80	027261.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027262.00
			-	

LX,\$X0,BIT10	-CHECK SV BIT 10 TO 3 MEMORIES.	33343.00 10	027262.40
Z,I22DMP		31324.22 00	027263.00
Z,\$R		11.22 00	027263.40
Z,\$X1		21.22 00	027264.00
SV,\$X0,I22DMP		31324.01 30	027264.40
SV,\$X0,\$R		11.01 30	027265.00
SV,\$X0,\$X1		21.01 30	027265.40
KV,\$X0,I22DMP		31324.00 90	027266.00
BXE,\$+1.32		27270.32 C2	027266.40
SIC,SEN		1310.00 80	027267.00
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027267.40
-			
KV,\$X0,\$R		11.00 90	027270.00
BXE,\$+1.32		27272.32 C2	027270.40
SIC,SEN		1310.00 80	027271.00
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027271.40
-			
KV,\$X0,\$X1		21.00 90	027272.00
BXE,\$+1.32		27274.32 C2	027272.40
SIC,SEN		1310.00 80	027273.00
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027273.40
-			
LX,\$X0,BIT11	-CHECK SV BIT 11 TO 3 MEMORIES.	33344.00 10	027274.00
Z,I22DMP		31324.22 00	027274.40
Z,\$R		11.22 00	027275.00
Z,\$X1		21.22 00	027275.40
SV,\$X0,I22DMP		31324.01 30	027276.00
SV,\$X0,\$R		11.01 30	027276.40
SV,\$X0,\$X1		21.01 30	027277.00
KV,\$X0,I22DMP		31324.00 90	027277.40
BXE,\$+1.32		27301.72 C2	027300.00
SIC,SEN		1310.00 80	027300.40
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027301.00
-			
KV,\$X0,\$R		11.00 90	027301.40
BXE,\$+1.32		27303.72 C2	027302.00
SIC,SEN		1310.00 80	027302.40
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027303.00
-			
KV,\$X0,\$X1		21.00 90	027303.40
BXE,\$+1.32		27305.72 C2	027304.00
SIC,SEN		1310.00 80	027304.40
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027305.00
-			
B,\$+1.0		27306.50 00	027305.40
BD,I224		27251.04 00	027306.00
SIC,SEN0+.32		1311.40 80	027306.40
B,SSW	-TO SSIP.	1301.10 00	027307.00
BD,\$+.32		27310.04 00	027307.40
-			
LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027310.00
V+,\$X13,BIT3		33334.32 B0	027310.40
SX,\$X13,IC222		31251.33 10	027311.00

1225	LX,\$X0,BIT12	-CHECK SV BIT 12 TO 3 MEMORIES.	33345.00 10	027311.40
	Z,I22DMP		31324.22 00	027312.00
	Z,\$R		11.22 00	027312.40
	Z,\$X1		21.22 00	027313.00
	SV,\$X0,I22DMP		31324.01 30	027313.40
	SV,\$X0,\$R		11.01 30	027314.00
	SV,\$X0,\$X1		21.01 30	027314.40
	KV,\$X0,I22DMP		31324.00 90	027315.00
	BXE,\$+1.32		27317.32 C2	027315.40
	SIC,SEN		1310.00 80	027316.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027316.40
			-	
	KV,\$X0,\$R		11.00 90	027317.00
	BXE,\$+1.32		27321.32 C2	027317.40
	SIC,SEN		1310.00 80	027320.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027320.40
			-	
	KV,\$X0,\$X1		21.00 90	027321.00
	BXE,\$+1.32		27323.32 C2	027321.40
	SIC,SEN		1310.00 80	027322.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027322.40
			-	
	LX,\$X0,BIT13	-CHECK SV BIT 13 TO 3 MEMORIES.	33346.00 10	027323.00
	Z,I22DMP		31324.22 00	027323.40
	Z,\$R		11.22 00	027324.00
	Z,\$X1		21.22 00	027324.40
	SV,\$X0,I22DMP		31324.01 30	027325.00
	SV,\$X0,\$R		11.01 30	027325.40
	SV,\$X0,\$X1		21.01 30	027326.00
	KV,\$X0,I22DMP		31324.00 90	027326.40
	BXE,\$+1.32		27330.72 C2	027327.00
	SIC,SEN		1310.00 80	027327.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027330.00
			-	
	KV,\$X0,\$R		11.00 90	027330.40
	BXE,\$+1.32		27332.72 C2	027331.00
	SIC,SEN		1310.00 80	027331.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027332.00
			-	
	KV,\$X0,\$X1		21.00 90	027332.40
	BXE,\$+1.32		27334.72 C2	027333.00
	SIC,SEN		1310.00 80	027333.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027334.00
			-	

	LX,\$X0,BIT14	-CHECK SV BIT 14 TO 3 MEMORIES.	33347.00 10	027334.40
	Z,I22DMP		31324.22 00	027335.00
	Z,\$R		11.22 00	027335.40
	Z,\$X1		21.22 00	027336.00
	SV,\$X0,I22DMP		31324.01 30	027336.40
	SV,\$X0,\$R		11.01 30	027337.00
	SV,\$X0,\$X1		21.01 30	027337.40
	KV,\$X0,I22DMP		31324.00 90	027340.00
	BXE,\$+1.32		27342.32 C2	027340.40
	SIC,SEN		1310.00 80	027341.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027341.40
			-	
	KV,\$X0,\$R		11.00 90	027342.00
	BXE,\$+1.32		27344.32 C2	027342.40
	SIC,SEN		1310.00 80	027343.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027343.40
			-	
	KV,\$X0,\$X1		21.00 90	027344.00
	BXE,\$+1.32		27346.32 C2	027344.40
	SIC,SEN		1310.00 80	027345.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027345.40
			-	
	B,\$+1.0		27347.10 00	027346.00
	BD,I225		27311.44 00	027346.40
	SIC,SEN0+.32		1311.40 80	027347.00
	B,SSW	-TO SSIP.	1301.10 00	027347.40
	BD,\$+.32		27350.44 00	027350.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027350.40
	V+,\$X13,BIT4		33335.32 B0	027351.00
	SX,\$X13,IC222		31251.33 10	027351.40
			-	
1226	LX,\$X0,BIT15	-CHECK SV BIT 15 TO 3 MEMORIES.	33350.00 10	027352.00
	Z,I22DMP		31324.22 00	027352.40
	Z,\$R		11.22 00	027353.00
	Z,\$X1		21.22 00	027353.40
	SV,\$X0,I22DMP		31324.01 30	027354.00
	SV,\$X0,\$R		11.01 30	027354.40
	SV,\$X0,\$X1		21.01 30	027355.00
	KV,\$X0,I22DMP		31324.00 90	027355.40
	BXE,\$+1.32		27357.72 C2	027356.00
	SIC,SEN		1310.00 80	027356.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027357.00
			-	
	KV,\$X0,\$R		11.00 90	027357.40
	BXE,\$+1.32		27361.72 C2	027360.00
	SIC,SEN		1310.00 80	027360.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027361.00
			-	
	KV,\$X0,\$X1		21.00 90	027361.40
	BXE,\$+1.32		27363.72 C2	027362.00
	SIC,SEN		1310.00 80	027362.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027363.00

LX,\$X0,BIT16	-CHECK SV BIT 16 TO 3 MEMORIES.	33351.00 10	027363.40
Z,I22DMP		31324.22 00	027364.00
Z,\$R		11.22 00	027364.40
Z,\$X1		21.22 00	027365.00
SV,\$X0,I22DMP		31324.01 30	027365.40
SV,\$X0,\$R		11.01 30	027366.00
SV,\$X0,\$X1		21.01 30	027366.40
KV,\$X0,I22DMP		31324.00 90	027367.00
BXE,\$+1.32		27371.32 C2	027367.40
SIC,SEN		1310.00 80	027370.00
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027370.40
-			
KV,\$X0,\$R		11.00 90	027371.00
BXE,\$+1.32		27373.32 C2	027371.40
SIC,SEN		1310.00 80	027372.00
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027372.40
-			
KV,\$X0,\$X1		21.00 90	027373.00
BXE,\$+1.32		27375.32 C2	027373.40
SIC,SEN		1310.00 80	027374.00
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027374.40
-			
LX,\$X0,BIT17	-CHECK SV BIT 17 TO 3 MEMORIES.	33352.00 10	027375.00
Z,I22DMP		31324.22 00	027375.40
Z,\$R		11.22 00	027376.00
Z,\$X1		21.22 00	027376.40
SV,\$X0,I22DMP		31324.01 30	027377.00
SV,\$X0,\$R		11.01 30	027377.40
SV,\$X0,\$X1		21.01 30	027400.00
KV,\$X0,I22DMP		31324.00 90	027400.40
BXE,\$+1.32		27402.72 C2	027401.00
SIC,SEN		1310.00 80	027401.40
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027402.00
-			
KV,\$X0,\$R		11.00 90	027402.40
BXE,\$+1.32		27404.72 C2	027403.00
SIC,SEN		1310.00 80	027403.40
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027404.00
-			
KV,\$X0,\$X1		21.00 90	027404.40
BXE,\$+1.32		27406.72 C2	027405.00
SIC,SEN		1310.00 80	027405.40
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027406.00
-			
B,\$+1.0		27407.50 00	027406.40
BD,I226		27352.04 00	027407.00
SIC,SEN0+.32		1311.40 80	027407.40
B,SSW	-TO SSIP.	1301.10 00	027410.00
BD,\$+.32		27411.04 00	027410.40
-			
LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027411.00
V+,\$X13,BIT5		33336.32 B0	027411.40
SX,\$X13,IC222		31251.33 10	027412.00

1227	LX,\$X0,BIT18	-CHECK SV BIT 18 TO 3 MEMORIES.	33353.00 10	027412.40
	Z,I22DMP		31324.22 00	027413.00
	Z,\$R		11.22 00	027413.40
	Z,\$X1		21.22 00	027414.00
	SV,\$X0,I22DMP		31324.01 30	027414.40
	SV,\$X0,\$R		11.01 30	027415.00
	SV,\$X0,\$X1		21.01 30	027415.40
	KV,\$X0,I22DMP		31324.00 90	027416.00
	BXE,\$+1.32		27420.32 C2	027416.40
	SIC,SEN		1310.00 80	027417.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027417.40
	KV,\$X0,\$R		11.00 90	027420.00
	BXE,\$+1.32		27422.32 C2	027420.40
	SIC,SEN		1310.00 80	027421.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027421.40
	KV,\$X0,\$X1		21.00 90	027422.00
	BXE,\$+1.32		27424.32 C2	027422.40
	SIC,SEN		1310.00 80	027423.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027423.40
	LX,\$X0,BIT19	-CHECK SV BIT 19 TO 3 MEMORIES.	33354.00 10	027424.00
	Z,I22DMP		31324.22 00	027424.40
	Z,\$R		11.22 00	027425.00
	Z,\$X1		21.22 00	027425.40
	SV,\$X0,I22DMP		31324.01 30	027426.00
	SV,\$X0,\$R		11.01 30	027426.40
	SV,\$X0,\$X1		21.01 30	027427.00
	KV,\$X0,I22DMP		31324.00 90	027427.40
	BXE,\$+1.32		27431.72 C2	027430.00
	SIC,SEN		1310.00 80	027430.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027431.00
	KV,\$X0,\$R		11.00 90	027431.40
	BXE,\$+1.32		27433.72 C2	027432.00
	SIC,SEN		1310.00 80	027432.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027433.00
	KV,\$X0,\$X1		21.00 90	027433.40
	BXE,\$+1.32		27435.72 C2	027434.00
	SIC,SEN		1310.00 80	027434.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027435.00

	LX,\$X0,BIT20	-CHECK SV BIT 20 TO 3 MEMORIES.	33355.00 10	027435.40
	Z,I22DMP		31324.22 00	027436.00
	Z,\$R		11.22 00	027436.40
	Z,\$X1		21.22 00	027437.00
	SV,\$X0,I22DMP		31324.01 30	027437.40
	SV,\$X0,\$R		11.01 30	027440.00
	SV,\$X0,\$X1		21.01 30	027440.40
	KV,\$X0,I22DMP		31324.00 90	027441.00
	BXE,\$+1.32		27443.32 C2	027441.40
	SIC,SEN		1310.00 80	027442.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027442.40
			-	
	KV,\$X0,\$R		11.00 90	027443.00
	BXE,\$+1.32		27445.32 C2	027443.40
	SIC,SEN		1310.00 80	027444.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027444.40
			-	
	KV,\$X0,\$X1		21.00 90	027445.00
	BXE,\$+1.32		27447.32 C2	027445.40
	SIC,SEN		1310.00 80	027446.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027446.40
			-	
	B,\$+1.0		27450.10 00	027447.00
	BD,I227		27412.44 00	027447.40
	SIC,SEN0+.32		1311.40 80	027450.00
	B,SSW	-TO SSIP.	1301.10 00	027450.40
	BD,\$+.32		27451.44 00	027451.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027451.40
	V+,\$X13,BIT6		33337.32 B0	027452.00
	SX,\$X13,IC222		31251.33 10	027452.40
			-	
1228	LX,\$X0,BIT21	-CHECK SV BIT 21 TO 3 MEMORIES.	33356.00 10	027453.00
	Z,I22DMP		31324.22 00	027453.40
	Z,\$R		11.22 00	027454.00
	Z,\$X1		21.22 00	027454.40
	SV,\$X0,I22DMP		31324.01 30	027455.00
	SV,\$X0,\$R		11.01 30	027455.40
	SV,\$X0,\$X1		21.01 30	027456.00
	KV,\$X0,I22DMP		31324.00 90	027456.40
	BXE,\$+1.32		27460.72 C2	027457.00
	SIC,SEN		1310.00 80	027457.40
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027460.00
			-	
	KV,\$X0,\$R		11.00 90	027460.40
	BXE,\$+1.32		27462.72 C2	027461.00
	SIC,SEN		1310.00 80	027461.40
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027462.00
			-	
	KV,\$X0,\$X1		21.00 90	027462.40
	BXE,\$+1.32		27464.72 C2	027463.00
	SIC,SEN		1310.00 80	027463.40
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027464.00
			-	

LX,\$X0,BIT22	-CHECK SV BIT 22 TO 3 MEMORIES.	33357.00 10	027464.40
Z,I22DMP		31324.22 00	027465.00
Z,\$R		11.22 00	027465.40
Z,\$X1		21.22 00	027466.00
SV,\$X0,I22DMP		31324.01 30	027466.40
SV,\$X0,\$R		11.01 30	027467.00
SV,\$X0,\$X1		21.01 30	027467.40
KV,\$X0,I22DMP		31324.00 90	027470.00
BXE,\$+1.32		27472.32 C2	027470.40
SIC,SEN		1310.00 80	027471.00
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027471.40
-			
KV,\$X0,\$R		11.00 90	027472.00
BXE,\$+1.32		27474.32 C2	027472.40
SIC,SEN		1310.00 80	027473.00
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027473.40
-			
KV,\$X0,\$X1		21.00 90	027474.00
BXE,\$+1.32		27476.32 C2	027474.40
SIC,SEN		1310.00 80	027475.00
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027475.40
-			
LX,\$X0,BIT23	-CHECK SV BIT 23 TO 3 MEMORIES.	33360.00 10	027476.00
Z,I22DMP		31324.22 00	027476.40
Z,\$R		11.22 00	027477.00
Z,\$X1		21.22 00	027477.40
SV,\$X0,I22DMP		31324.01 30	027500.00
SV,\$X0,\$R		11.01 30	027500.40
SV,\$X0,\$X1		21.01 30	027501.00
KV,\$X0,I22DMP		31324.00 90	027501.40
BXE,\$+1.32		27503.72 C2	027502.00
SIC,SEN		1310.00 80	027502.40
B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027503.00
-			
KV,\$X0,\$R		11.00 90	027503.40
BXE,\$+1.32		27505.72 C2	027504.00
SIC,SEN		1310.00 80	027504.40
B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027505.00
-			
KV,\$X0,\$X1		21.00 90	027505.40
BXE,\$+1.32		27507.72 C2	027506.00
SIC,SEN		1310.00 80	027506.40
B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027507.00
-			
B,\$+1.0		27510.50 00	027507.40
BD,I228		27453.04 00	027510.00
SIC,SEN0+.32		1311.40 80	027510.40
B,SSW	-TO SSIP.	1301.10 00	027511.00
BD,\$+.32		27512.04 00	027511.40
-			
LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027512.00
V+,\$X13,BIT7		33340.32 B0	027512.40
SX,\$X13,IC222		31251.33 10	027513.00

1229	LX,\$X0,IBIT23	-CHECK SV BIT 24 TO 3 MEMORIES.	31302.00 10	027513.40
	Z,I22DMP		31324.22 00	027514.00
	Z,\$R		11.22 00	027514.40
	Z,\$X1		21.22 00	027515.00
	SV,\$X0,I22DMP		31324.01 30	027515.40
	SV,\$X0,\$R		11.01 30	027516.00
	SV,\$X0,\$X1		21.01 30	027516.40
	KV,\$X0,I22DMP		31324.00 90	027517.00
	BXE,\$+1.32		27521.32 C2	027517.40
	SIC,SEN		1310.00 80	027520.00
	B,SERS	-SV ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027520.40
	KV,\$X0,\$R		11.00 90	027521.00
	BXE,\$+1.32		27523.32 C2	027521.40
	SIC,SEN		1310.00 80	027522.00
	B,SERS	-SV ABOVE BIT TO INT MEM FAILED.	1304.10 00	027522.40
	KV,\$X0,\$X1		21.00 90	027523.00
	BXE,\$+1.32		27525.32 C2	027523.40
	SIC,SEN		1310.00 80	027524.00
	B,SERS	-SV ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027524.40
	B,\$+1.0		27526.10 00	027525.00
	BD,I229		27513.44 00	027525.40
	BD,\$+.32		27526.44 00	027526.00
	SIC,SEN0+.32		1311.40 80	027526.40
	B,SSW	-TO SSIP.	1301.10 00	027527.00
	BD,\$+.32		27530.04 00	027527.40
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027530.00
	V+,\$X13,BIT8		33341.32 B0	027530.40
	SX,\$X13,IC222		31251.33 10	027531.00
12210	LX,\$X0,IBIT0	-CHECK SVA TO 3 MEMS, DATA BIT 0.	31253.00 10	027531.40
	LX,\$X1,I22K1		31303.02 10	027532.00
	SX,\$X1,I22WRK		31322.03 10	027532.40
	SVA,\$X0,I22WRK		31322.01 D0	027533.00
	SX,\$X1,\$R		11.03 10	027533.40
	SVA,\$X0,\$R		11.01 D0	027534.00
	SVA,\$X0,\$X1		21.01 D0	027534.40
	NOP		0.30 00	027535.00
	NOP		0.30 00	027535.40
	NOP		0.30 00	027536.00
	KV,\$X0,I22WRK		31322.00 90	027536.40
	BXE,\$+1.32		27540.72 C2	027537.00
	SIC,SEN		1310.00 80	027537.40
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027540.00
	KV,\$X0,\$R		11.00 90	027540.40
	BXE,\$+1.32		27542.72 C2	027541.00
	SIC,SEN		1310.00 80	027541.40
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027542.00
	KV,\$X0,\$X1		21.00 90	027542.40
	BXE,\$+1.32		27544.72 C2	027543.00
	SIC,SEN		1310.00 80	027543.40
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027544.00

	LX,\$X0,IBIT1	-CHECK SVA TO 3 MEMS, DATA BIT 1.	31254.00 10	027544.40
	SX,\$X1,I22WRK		31322.03 10	027545.00
	SVA,\$X0,I22WRK		31322.01 D0	027545.40
	SX,\$X1,\$R		11.03 10	027546.00
	SVA,\$X0,\$R		11.01 D0	027546.40
	SVA,\$X0,\$X1		21.01 D0	027547.00
	NOP		0.30 00	027547.40
	NOP		0.30 00	027550.00
	NOP		0.30 00	027550.40
	KV,\$X0,I22WRK		31322.00 90	027551.00
	BXE,\$+1.32		27553.32 C2	027551.40
	SIC,SEN		1310.00 80	027552.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027552.40
	KV,\$X0,\$R		11.00 90	027553.00
	BXE,\$+1.32		27555.32 C2	027553.40
	SIC,SEN		1310.00 80	027554.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027554.40
	KV,\$X0,\$X1		21.00 90	027555.00
	BXE,\$+1.32		27557.32 C2	027555.40
	SIC,SEN		1310.00 80	027556.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027556.40
	B,\$+1.0		27560.10 00	027557.00
	BD,I2210		27531.44 00	027557.40
	SIC,SEN0+.32		1311.40 80	027560.00
	B,SSW	-TO SSIP.	1301.10 00	027560.40
	BD,\$+.32		27561.44 00	027561.00
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027561.40
	V+,\$X13,BIT9		33342.32 B0	027562.00
	SX,\$X13,IC222		31251.33 10	027562.40
I2211	LX,\$X0,IBIT2	-CHECK SVA TO 3 MEMS, DATA BIT 2.	31255.00 10	027563.00
	LX,\$X1,I22K1		31303.02 10	027563.40
	SX,\$X1,I22WRK		31322.03 10	027564.00
	SVA,\$X0,I22WRK		31322.01 D0	027564.40
	SX,\$X1,\$R		11.03 10	027565.00
	SVA,\$X0,\$R		11.01 D0	027565.40
	SVA,\$X0,\$X1		21.01 D0	027566.00
	NOP		0.30 00	027566.40
	NOP		0.30 00	027567.00
	NOP		0.30 00	027567.40
	KV,\$X0,I22WRK		31322.00 90	027570.00
	BXE,\$+1.32		27572.32 C2	027570.40
	SIC,SEN		1310.00 80	027571.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027571.40
	KV,\$X0,\$R		11.00 90	027572.00
	BXE,\$+1.32		27574.32 C2	027572.40
	SIC,SEN		1310.00 80	027573.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027573.40
	KV,\$X0,\$X1		21.00 90	027574.00
	BXE,\$+1.32		27576.32 C2	027574.40
	SIC,SEN		1310.00 80	027575.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027575.40

	LX,\$X0,IBIT3	-CHECK SVA TO 3 MEMS, DATA BIT 3.	31256.00 10	027576.00
	LX,\$X1,I22K1		31303.02 10	027576.40
	SX,\$X1,I22WRK		31322.03 10	027577.00
	SVA,\$X0,I22WRK		31322.01 D0	027577.40
	SX,\$X1,\$R		11.03 10	027600.00
	SVA,\$X0,\$R		11.01 D0	027600.40
	SVA,\$X0,\$X1		21.01 D0	027601.00
	NOP		0.30 00	027601.40
	NOP		0.30 00	027602.00
	NOP		0.30 00	027602.40
	KV,\$X0,I22WRK		31322.00 90	027603.00
	BXE,\$+1.32		27605.32 C2	027603.40
	SIC,SEN		1310.00 80	027604.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027604.40
			-	
	KV,\$X0,\$R		11.00 90	027605.00
	BXE,\$+1.32		27607.32 C2	027605.40
	SIC,SEN		1310.00 80	027606.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027606.40
	KV,\$X0,\$X1		21.00 90	027607.00
	BXE,\$+1.32		27611.32 C2	027607.40
	SIC,SEN		1310.00 80	027610.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027610.40
			-	
	B,\$+1.0		27612.10 00	027611.00
	BD,I2211		27563.04 00	027611.40
	SIC,SEN0+.32		1311.40 80	027612.00
	B,\$SW	-TO SSIP.	1301.10 00	027612.40
	BD,\$+.32		27613.44 00	027613.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027613.40
	V+,\$X13,BIT10		33343.32 B0	027614.00
	SX,\$X13,IC222		31251.33 10	027614.40
			-	
12212	LX,\$X0,IBIT4	-CHECK SVA TO 3 MEMS, DATA BIT 4.	31257.00 10	027615.00
	LX,\$X1,I22K1		31303.02 10	027615.40
	SX,\$X1,I22WRK		31322.03 10	027616.00
	SVA,\$X0,I22WRK		31322.01 D0	027616.40
	SX,\$X1,\$R		11.03 10	027617.00
	SVA,\$X0,\$R		11.01 D0	027617.40
	SVA,\$X0,\$X1		21.01 D0	027620.00
	NOP		0.30 00	027620.40
	NOP		0.30 00	027621.00
	NOP		0.30 00	027621.40
	KV,\$X0,I22WRK		31322.00 90	027622.00
	BXE,\$+1.32		27624.32 C2	027622.40
	SIC,SEN		1310.00 80	027623.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027623.40
			-	
	KV,\$X0,\$R		11.00 90	027624.00
	BXE,\$+1.32		27626.32 C2	027624.40
	SIC,SEN		1310.00 80	027625.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027625.40
			-	
	KV,\$X0,\$X1		21.00 90	027626.00
	BXE,\$+1.32		27630.32 C2	027626.40
	SIC,SEN		1310.00 80	027627.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027627.40

	LX,\$X0,IBIT5	-CHECK SVA TO 3 MEMS, DATA BIT 5.	31260.00 10	027630.00
	LX,\$X1,I22K1		31303.02 10	027630.40
	SX,\$X1,I22WRK		31322.03 10	027631.00
	SVA,\$X0,I22WRK		31322.01 D0	027631.40
	SX,\$X1,\$R		11.03 10	027632.00
	SVA,\$X0,\$R		11.01 D0	027632.40
	SVA,\$X0,\$X1		21.01 D0	027633.00
	NOP		0.30 00	027633.40
	NOP		0.30 00	027634.00
	NOP		0.30 00	027634.40
	KV,\$X0,I22WRK		31322.00 90	027635.00
	BXE,\$+1.32		27637.32 C2	027635.40
	SIC,SEN		1310.00 80	027636.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027636.40
	KV,\$X0,\$R		11.00 90	027637.00
	BXE,\$+1.32		27641.32 C2	027637.40
	SIC,SEN		1310.00 80	027640.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027640.40
	KV,\$X0,\$X1		21.00 90	027641.00
	BXE,\$+1.32		27643.32 C2	027641.40
	SIC,SEN		1310.00 80	027642.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027642.40
	B,\$+1.0		27644.10 00	027643.00
	BD,I2212		27615.04 00	027643.40
	SIC,SEN0+.32		1311.40 80	027644.00
	B,SSW	-TO SSIP.	1301.10 00	027644.40
	BD,\$+.32		27645.44 00	027645.00
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027645.40
	V+,\$X13,BIT11		33344.32 B0	027646.00
	SX,\$X13,IC222		31251.33 10	027646.40
I2213	LX,\$X0,IBIT6	-CHECK SVA TO 3 MEMS, DATA BIT 6.	31261.00 10	027647.00
	LX,\$X1,I22K1		31303.02 10	027647.40
	SX,\$X1,I22WRK		31322.03 10	027650.00
	SVA,\$X0,I22WRK		31322.01 D0	027650.40
	SX,\$X1,\$R		11.03 10	027651.00
	SVA,\$X0,\$R		11.01 D0	027651.40
	SVA,\$X0,\$X1		21.01 D0	027652.00
	NOP		0.30 00	027652.40
	NOP		0.30 00	027653.00
	NOP		0.30 00	027653.40
	KV,\$X0,I22WRK		31322.00 90	027654.00
	BXE,\$+1.32		27656.32 C2	027654.40
	SIC,SEN		1310.00 80	027655.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027655.40
	KV,\$X0,\$R		11.00 90	027656.00
	BXE,\$+1.32		27660.32 C2	027656.40
	SIC,SEN		1310.00 80	027657.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027657.40
	KV,\$X0,\$X1		21.00 90	027660.00
	BXE,\$+1.32		27662.32 C2	027660.40
	SIC,SEN		1310.00 80	027661.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027661.40

	LX,\$X0,IBIT7	-CHECK SVA TO 3 MEMS, DATA BIT 7.	31262.00 10	027662.00
	LX,\$X1,I22K1		31303.02 10	027662.40
	SX,\$X1,I22WRK		31322.03 10	027663.00
	SVA,\$X0,I22WRK		31322.01 D0	027663.40
	SX,\$X1,\$R		11.03 10	027664.00
	SVA,\$X0,\$R		11.01 D0	027664.40
	SVA,\$X0,\$X1		21.01 D0	027665.00
	NOP		0.30 00	027665.40
	NOP		0.30 00	027666.00
	NOP		0.30 00	027666.40
	KV,\$X0,I22WRK		31322.00 90	027667.00
	BXE,\$+1.32		27671.32 C2	027667.40
	SIC,SEN		1310.00 80	027670.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027670.40
	KV,\$X0,\$R		11.00 90	027671.00
	BXE,\$+1.32		27673.32 C2	027671.40
	SIC,SEN		1310.00 80	027672.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027672.40
	KV,\$X0,\$X1		21.00 90	027673.00
	BXE,\$+1.32		27675.32 C2	027673.40
	SIC,SEN		1310.00 80	027674.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027674.40
	B,\$+1.0		27676.10 00	027675.00
	BD,I2213		27647.04 00	027675.40
	SIC,SEN0+.32		1311.40 80	027676.00
	B,SSW	-TO SSIP.	1301.10 00	027676.40
	BD,\$+.32		27677.44 00	027677.00
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027677.40
	V+,\$X13,BIT12		33345.32 B0	027700.00
	SX,\$X13,IC222		31251.33 10	027700.40
12214	LX,\$X0,IBIT8	-CHECK SVA TO 3 MEMS, DATA BIT 8.	31263.00 10	027701.00
	LX,\$X1,I22K1		31303.02 10	027701.40
	SX,\$X1,I22WRK		31322.03 10	027702.00
	SVA,\$X0,I22WRK		31322.01 D0	027702.40
	SX,\$X1,\$R		11.03 10	027703.00
	SVA,\$X0,\$R		11.01 D0	027703.40
	SVA,\$X0,\$X1		21.01 D0	027704.00
	NOP		0.30 00	027704.40
	NOP		0.30 00	027705.00
	NOP		0.30 00	027705.40
	KV,\$X0,I22WRK		31322.00 90	027706.00
	BXE,\$+1.32		27710.32 C2	027706.40
	SIC,SEN		1310.00 80	027707.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027707.40
	KV,\$X0,\$R		11.00 90	027710.00
	BXE,\$+1.32		27712.32 C2	027710.40
	SIC,SEN		1310.00 80	027711.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027711.40
	KV,\$X0,\$X1		21.00 90	027712.00
	BXE,\$+1.32		27714.32 C2	027712.40
	SIC,SEN		1310.00 80	027713.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027713.40

	LX,\$X0,IBIT9	-CHECK SVA TO 3 MEMS, DATA BIT 9.	31264.00 10	027714.00
	LX,\$X1,I22K1		31303.02 10	027714.40
	SX,\$X1,I22WRK		31322.03 10	027715.00
	SVA,\$X0,I22WRK		31322.01 D0	027715.40
	SX,\$X1,\$R		11.03 10	027716.00
	SVA,\$X0,\$R		11.01 D0	027716.40
	SVA,\$X0,\$X1		21.01 D0	027717.00
	NOP		0.30 00	027717.40
	NOP		0.30 00	027720.00
	NOP		0.30 00	027720.40
	KV,\$X0,I22WRK		31322.00 90	027721.00
	BXE,\$+1.32		27723.32 C2	027721.40
	SIC,SEN		1310.00 80	027722.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027722.40
			-	
	KV,\$X0,\$R		11.00 90	027723.00
	BXE,\$+1.32		27725.32 C2	027723.40
	SIC,SEN		1310.00 80	027724.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027724.40
			-	
	KV,\$X0,\$X1		21.00 90	027725.00
	BXE,\$+1.32		27727.32 C2	027725.40
	SIC,SEN		1310.00 80	027726.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027726.40
			-	
	B,\$+1.0		27730.10 00	027727.00
	BD,I2214		27701.04 00	027727.40
	SIC,SEN0+.32		1311.40 80	027730.00
	B,SSW	-TO SSIP.	1301.10 00	027730.40
	BD,\$+.32		27731.44 00	027731.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027731.40
	V+,\$X13,BIT13		33346.32 B0	027732.00
	SX,\$X13,IC222		31251.33 10	027732.40
			-	
I2215	LX,\$X0,IBIT10	-CHECK SVA TO 3 MEMS, DATA BIT 10.	31265.00 10	027733.00
	LX,\$X1,I22K1		31303.02 10	027733.40
	SX,\$X1,I22WRK		31322.03 10	027734.00
	SVA,\$X0,I22WRK		31322.01 D0	027734.40
	SX,\$X1,\$R		11.03 10	027735.00
	SVA,\$X0,\$R		11.01 D0	027735.40
	SVA,\$X0,\$X1		21.01 D0	027736.00
	NOP		0.30 00	027736.40
	NOP		0.30 00	027737.00
	NOP		0.30 00	027737.40
	KV,\$X0,I22WRK		31322.00 90	027740.00
	BXE,\$+1.32		27742.32 C2	027740.40
	SIC,SEN		1310.00 80	027741.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027741.40
			-	
	KV,\$X0,\$R		11.00 90	027742.00
	BXE,\$+1.32		27744.32 C2	027742.40
	SIC,SEN		1310.00 80	027743.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027743.40
			-	
	KV,\$X0,\$X1		21.00 90	027744.00
	BXE,\$+1.32		27746.32 C2	027744.40
	SIC,SEN		1310.00 80	027745.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027745.40
			-	

	LX,\$X0,IBIT11	-CHECK SVA TO 3 MEMS, DATA BIT 11.	31266.00 10	027746.00
	LX,\$X1,I22K1		31303.02 10	027746.40
	SX,\$X1,I22WRK		31322.03 10	027747.00
	SVA,\$X0,I22WRK		31322.01 D0	027747.40
	SX,\$X1,\$R		11.03 10	027750.00
	SVA,\$X0,\$R		11.01 D0	027750.40
	SVA,\$X0,\$X1		21.01 D0	027751.00
	NOP		0.30 00	027751.40
	NOP		0.30 00	027752.00
	NOP		0.30 00	027752.40
	KV,\$X0,I22WRK		31322.00 90	027753.00
	BXE,\$+1.32		27755.32 C2	027753.40
	SIC,SEN		1310.00 80	027754.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027754.40
			-	
	KV,\$X0,\$R		11.00 90	027755.00
	BXE,\$+1.32		27757.32 C2	027755.40
	SIC,SEN		1310.00 80	027756.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027756.40
			-	
	KV,\$X0,\$X1		21.00 90	027757.00
	BXE,\$+1.32		27761.32 C2	027757.40
	SIC,SEN		1310.00 80	027760.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027760.40
			-	
	B,\$+1.0		27762.10 00	027761.00
	BD,I2215		27733.04 00	027761.40
	SIC,SEN0+.32		1311.40 80	027762.00
	B,SSW	-TO SSIP.	1301.10 00	027762.40
	BD,\$+.32		27763.44 00	027763.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	027763.40
	V+,\$X13,BIT14		33347.32 B0	027764.00
	SX,\$X13,IC222		31251.33 10	027764.40
			-	
12216	LX,\$X0,IBIT12	-CHECK SVA TO 3 MEMS, DATA BIT 12.	31267.00 10	027765.00
	LX,\$X1,I22K1		31303.02 10	027765.40
	SX,\$X1,I22WRK		31322.03 10	027766.00
	SVA,\$X0,I22WRK		31322.01 D0	027766.40
	SX,\$X1,\$R		11.03 10	027767.00
	SVA,\$X0,\$R		11.01 D0	027767.40
	SVA,\$X0,\$X1		21.01 D0	027770.00
	NOP		0.30 00	027770.40
	NOP		0.30 00	027771.00
	NOP		0.30 00	027771.40
	KV,\$X0,I22WRK		31322.00 90	027772.00
	BXE,\$+1.32		27774.32 C2	027772.40
	SIC,SEN		1310.00 80	027773.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	027773.40
			-	
	KV,\$X0,\$R		11.00 90	027774.00
	BXE,\$+1.32		27776.32 C2	027774.40
	SIC,SEN		1310.00 80	027775.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	027775.40
			-	
	KV,\$X0,\$X1		21.00 90	027776.00
	BXE,\$+1.32		30000.32 C2	027776.40
	SIC,SEN		1310.00 80	027777.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	027777.40

	LX,\$X0,IBIT13	-CHECK SVA TO 3 MEMS, DATA BIT 13.	31270.00 10	030000.00
	LX,\$X1,I22K1		31303.02 10	030000.40
	SX,\$X1,I22WRK		31322.03 10	030001.00
	SVA,\$X0,I22WRK		31322.01 D0	030001.40
	SX,\$X1,\$R		11.03 10	030002.00
	SVA,\$X0,\$R		11.01 D0	030002.40
	SVA,\$X0,\$X1		21.01 D0	030003.00
	NOP		0.30 00	030003.40
	NOP		0.30 00	030004.00
	NOP		0.30 00	030004.40
	KV,\$X0,I22WRK		31322.00 90	030005.00
	BXE,\$+1.32		30007.32 C2	030005.40
	SIC,SEN		1310.00 80	030006.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030006.40
			-	
	KV,\$X0,\$R		11.00 90	030007.00
	BXE,\$+1.32		30011.32 C2	030007.40
	SIC,SEN		1310.00 80	030010.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030010.40
			-	
	KV,\$X0,\$X1		21.00 90	030011.00
	BXE,\$+1.32		30013.32 C2	030011.40
	SIC,SEN		1310.00 80	030012.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030012.40
			-	
	B,\$+1.0		30014.10 00	030013.00
	BD,I2216		27765.04 00	030013.40
	SIC,SEN0+.32		1311.40 80	030014.00
	B,SSW	-TO SSIP.	1301.10 00	030014.40
	BD,\$+.32		30015.44 00	030015.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030015.40
	V+,\$X13,BIT15		33350.32 B0	030016.00
	SX,\$X13,IC222		31251.33 10	030016.40
			-	
12217	LX,\$X0,IBIT14	-CHECK SVA TO 3 MEMS, DATA BIT 14.	31271.00 10	030017.00
	LX,\$X1,I22K1		31303.02 10	030017.40
	SX,\$X1,I22WRK		31322.03 10	030020.00
	SVA,\$X0,I22WRK		31322.01 D0	030020.40
	SX,\$X1,\$R		11.03 10	030021.00
	SVA,\$X0,\$R		11.01 D0	030021.40
	SVA,\$X0,\$X1		21.01 D0	030022.00
	NOP		0.30 00	030022.40
	NOP		0.30 00	030023.00
	NOP		0.30 00	030023.40
	KV,\$X0,I22WRK		31322.00 90	030024.00
	BXE,\$+1.32		30026.32 C2	030024.40
	SIC,SEN		1310.00 80	030025.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030025.40
			-	
	KV,\$X0,\$R		11.00 90	030026.00
	BXE,\$+1.32		30030.32 C2	030026.40
	SIC,SEN		1310.00 80	030027.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030027.40
			-	
	KV,\$X0,\$X1		21.00 90	030030.00
	BXE,\$+1.32		30032.32 C2	030030.40
	SIC,SEN		1310.00 80	030031.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030031.40
			-	

	LX,\$X0,IBIT15	-CHECK SVA TO 3 MEMS, DATA BIT 15.	31272.00 10	030032.00
	LX,\$X1,I22K1		31303.02 10	030032.40
	SX,\$X1,I22WRK		31322.03 10	030033.00
	SVA,\$X0,I22WRK		31322.01 D0	030033.40
	SX,\$X1,\$R		11.03 10	030034.00
	SVA,\$X0,\$R		11.01 D0	030034.40
	SVA,\$X0,\$X1		21.01 D0	030035.00
	NOP		0.30 00	030035.40
	NOP		0.30 00	030036.00
	NOP		0.30 00	030036.40
	KV,\$X0,I22WRK		31322.00 90	030037.00
	BXE,\$+1.32		30041.32 C2	030037.40
	SIC,SEN		1310.00 80	030040.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030040.40
			-	
	KV,\$X0,\$R		11.00 90	030041.00
	BXE,\$+1.32		30043.32 C2	030041.40
	SIC,SEN		1310.00 80	030042.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030042.40
			-	
	KV,\$X0,\$X1		21.00 90	030043.00
	BXE,\$+1.32		30045.32 C2	030043.40
	SIC,SEN		1310.00 80	030044.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030044.40
			-	
	B,\$+1.0		30046.10 00	030045.00
	BD,I2217		30017.04 00	030045.40
	SIC,SEN0+.32		1311.40 80	030046.00
	B,SSW	-TO SSIP.	1301.10 00	030046.40
	BD,\$+.32		30047.44 00	030047.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030047.40
	V+,\$X13,BIT16		33351.32 B0	030050.00
	SX,\$X13,IC222		31251.33 10	030050.40
			-	
I2218	LX,\$X0,IBIT16	-CHECK SVA TO 3 MEMS, DATA BIT 16.	31273.00 10	030051.00
	LX,\$X1,I22K1		31303.02 10	030051.40
	SX,\$X1,I22WRK		31322.03 10	030052.00
	SVA,\$X0,I22WRK		31322.01 D0	030052.40
	SX,\$X1,\$R		11.03 10	030053.00
	SVA,\$X0,\$R		11.01 D0	030053.40
	SVA,\$X0,\$X1		21.01 D0	030054.00
	NOP		0.30 00	030054.40
	NOP		0.30 00	030055.00
	NOP		0.30 00	030055.40
	KV,\$X0,I22WRK		31322.00 90	030056.00
	BXE,\$+1.32		30060.32 C2	030056.40
	SIC,SEN		1310.00 80	030057.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030057.40
			-	
	KV,\$X0,\$R		11.00 90	030060.00
	BXE,\$+1.32		30062.32 C2	030060.40
	SIC,SEN		1310.00 80	030061.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030061.40
			-	
	KV,\$X0,\$X1		21.00 90	030062.00
	BXE,\$+1.32		30064.32 C2	030062.40
	SIC,SEN		1310.00 80	030063.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030063.40

	LX,\$X0,IBIT17	-CHECK SVA TO 3 MEMS, DATA BIT 17.	31274.00 10	030064.00
	LX,\$X1,I22K1		31303.02 10	030064.40
	SX,\$X1,I22WRK		31322.03 10	030065.00
	SVA,\$X0,I22WRK		31322.01 D0	030065.40
	SX,\$X1,\$R		11.03 10	030066.00
	SVA,\$X0,\$R		11.01 D0	030066.40
	SVA,\$X0,\$X1		21.01 D0	030067.00
	NOP		0.30 00	030067.40
	NOP		0.30 00	030070.00
	NOP		0.30 00	030070.40
	KV,\$X0,I22WRK		31322.00 90	030071.00
	BXE,\$+1.32		30073.32 C2	030071.40
	SIC,SEN		1310.00 80	030072.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030072.40
			-	
	KV,\$X0,\$R		11.00 90	030073.00
	BXE,\$+1.32		30075.32 C2	030073.40
	SIC,SEN		1310.00 80	030074.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030074.40
			-	
	KV,\$X0,\$X1		21.00 90	030075.00
	BXE,\$+1.32		30077.32 C2	030075.40
	SIC,SEN		1310.00 80	030076.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030076.40
			-	
	B,\$+1.0		30100.10 00	030077.00
	BD,I2218		30051.04 00	030077.40
	SIC,SEN0+.32		1311.40 80	030100.00
	B,SSW	-TO SSIP.	1301.10 00	030100.40
	BD,\$+.32		30101.44 00	030101.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030101.40
	V+,\$X13,BIT17		33352.32 B0	030102.00
	SX,\$X13,IC222		31251.33 10	030102.40
			-	
12219	LX,\$X0,IBIT18	-CHECK SVA TO 3 MEMS, DATA BIT 18.	31275.00 10	030103.00
	LX,\$X1,I22K1		31303.02 10	030103.40
	SX,\$X1,I22WRK		31322.03 10	030104.00
	SVA,\$X0,I22WRK		31322.01 D0	030104.40
	SX,\$X1,\$R		11.03 10	030105.00
	SVA,\$X0,\$R		11.01 D0	030105.40
	SVA,\$X0,\$X1		21.01 D0	030106.00
	NOP		0.30 00	030106.40
	NOP		0.30 00	030107.00
	NOP		0.30 00	030107.40
	KV,\$X0,I22WRK		31322.00 90	030110.00
	BXE,\$+1.32		30112.32 C2	030110.40
	SIC,SEN		1310.00 80	030111.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030111.40
			-	
	KV,\$X0,\$R		11.00 90	030112.00
	BXE,\$+1.32		30114.32 C2	030112.40
	SIC,SEN		1310.00 80	030113.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030113.40
			-	
	KV,\$X0,\$X1		21.00 90	030114.00
	BXE,\$+1.32		30116.32 C2	030114.40
	SIC,SEN		1310.00 80	030115.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030115.40
			-	

	LX,\$X0,IBIT19	-CHECK SVA TO 3 MEMS, DATA BIT 19.	31276.00 10	030116.00
	LX,\$X1,I22K1		31303.02 10	030116.40
	SX,\$X1,I22WRK		31322.03 10	030117.00
	SVA,\$X0,I22WRK		31322.01 D0	030117.40
	SX,\$X1,\$R		11.03 10	030120.00
	SVA,\$X0,\$R		11.01 D0	030120.40
	SVA,\$X0,\$X1		21.01 D0	030121.00
	NOP		0.30 00	030121.40
	NOP		0.30 00	030122.00
	NOP		0.30 00	030122.40
	KV,\$X0,I22WRK		31322.00 90	030123.00
	BXE,\$+1.32		30125.32 C2	030123.40
	SIC,SEN		1310.00 80	030124.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030124.40
			-	
	KV,\$X0,\$R		11.00 90	030125.00
	BXE,\$+1.32		30127.32 C2	030125.40
	SIC,SEN		1310.00 80	030126.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030126.40
			-	
	KV,\$X0,\$X1		21.00 90	030127.00
	BXE,\$+1.32		30131.32 C2	030127.40
	SIC,SEN		1310.00 80	030130.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030130.40
			-	
	B,\$+1.0		30132.10 00	030131.00
	BD,I2219		30103.04 00	030131.40
	SIC,SEN0+.32		1311.40 80	030132.00
	B,SSW	-TO SSIP.	1301.10 00	030132.40
	BD,\$+.32		30133.44 00	030133.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030133.40
	V+,\$X13,BIT18		33353.32 B0	030134.00
	SX,\$X13,IC222		31251.33 10	030134.40
			-	
12220	LX,\$X0,IBIT20	-CHECK SVA TO 3 MEMS, DATA BIT 20.	31277.00 10	030135.00
	LX,\$X1,I22K1		31303.02 10	030135.40
	SX,\$X1,I22WRK		31322.03 10	030136.00
	SVA,\$X0,I22WRK		31322.01 D0	030136.40
	SX,\$X1,\$R		11.03 10	030137.00
	SVA,\$X0,\$R		11.01 D0	030137.40
	SVA,\$X0,\$X1		21.01 D0	030140.00
	NOP		0.30 00	030140.40
	NOP		0.30 00	030141.00
	NOP		0.30 00	030141.40
	KV,\$X0,I22WRK		31322.00 90	030142.00
	BXE,\$+1.32		30144.32 C2	030142.40
	SIC,SEN		1310.00 80	030143.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030143.40
			-	
	KV,\$X0,\$R		11.00 90	030144.00
	BXE,\$+1.32		30146.32 C2	030144.40
	SIC,SEN		1310.00 80	030145.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030145.40
			-	
	KV,\$X0,\$X1		21.00 90	030146.00
	BXE,\$+1.32		30150.32 C2	030146.40
	SIC,SEN		1310.00 80	030147.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030147.40
			-	

	LX,\$X0,IBIT21	-CHECK SVA TO 3 MEMS, DATA BIT 21.	31300.00 10	030150.00
	LX,\$X1,I22K1		31303.02 10	030150.40
	SX,\$X1,I22WRK		31322.03 10	030151.00
	SVA,\$X0,I22WRK		31322.01 D0	030151.40
	SX,\$X1,\$R		11.03 10	030152.00
	SVA,\$X0,\$R		11.01 D0	030152.40
	SVA,\$X0,\$X1		21.01 D0	030153.00
	NOP		0.30 00	030153.40
	NOP		0.30 00	030154.00
	NOP		0.30 00	030154.40
	KV,\$X0,I22WRK		31322.00 90	030155.00
	BXE,\$+1.32		30157.32 C2	030155.40
	SIC,SEN		1310.00 80	030156.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030156.40
			-	
	KV,\$X0,\$R		11.00 90	030157.00
	BXE,\$+1.32		30161.32 C2	030157.40
	SIC,SEN		1310.00 80	030160.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030160.40
			-	
	KV,\$X0,\$X1		21.00 90	030161.00
	BXE,\$+1.32		30163.32 C2	030161.40
	SIC,SEN		1310.00 80	030162.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030162.40
			-	
	B,\$+1.0		30164.10 00	030163.00
	BD,I2220		30135.04 00	030163.40
	SIC,SEN0+.32		1311.40 80	030164.00
	B,SSW	-TO SSIP.	1301.10 00	030164.40
	BD,\$+.32		30165.44 00	030165.00
			-	
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030165.40
	V+,\$X13,BIT19		33354.32 B0	030166.00
	SX,\$X13,IC222		31251.33 10	030166.40
			-	
12221	LX,\$X0,IBIT22	-CHECK SVA TO 3 MEMS, DATA BIT 22.	31301.00 10	030167.00
	LX,\$X1,I22K1		31303.02 10	030167.40
	SX,\$X1,I22WRK		31322.03 10	030170.00
	SVA,\$X0,I22WRK		31322.01 D0	030170.40
	SX,\$X1,\$R		11.03 10	030171.00
	SVA,\$X0,\$R		11.01 D0	030171.40
	SVA,\$X0,\$X1		21.01 D0	030172.00
	NOP		0.30 00	030172.40
	NOP		0.30 00	030173.00
	NOP		0.30 00	030173.40
	KV,\$X0,I22WRK		31322.00 90	030174.00
	BXE,\$+1.32		30176.32 C2	030174.40
	SIC,SEN		1310.00 80	030175.00
	B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030175.40
			-	
	KV,\$X0,\$R		11.00 90	030176.00
	BXE,\$+1.32		30200.32 C2	030176.40
	SIC,SEN		1310.00 80	030177.00
	B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030177.40
	KV,\$X0,\$X1		21.00 90	030200.00
	BXE,\$+1.32		30202.32 C2	030200.40
	SIC,SEN		1310.00 80	030201.00
	B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030201.40

LX,\$X0,IBIT23	-CHECK SVA TO 3 MEMS, DATA BIT 23.	31302.00 10	030202.00
LX,\$X1,I22K1		31303.02 10	030202.40
SX,\$X1,I22WRK		31322.03 10	030203.00
SVA,\$X0,I22WRK		31322.01 D0	030203.40
SX,\$X1,\$R		11.03 10	030204.00
SVA,\$X0,\$R		11.01 D0	030204.40
SVA,\$X0,\$X1		21.01 D0	030205.00
NOP		0.30 00	030205.40
NOP		0.30 00	030206.00
NOP		0.30 00	030206.40
KV,\$X0,I22WRK		31322.00 90	030207.00
BXE,\$+1.32		30211.32 C2	030207.40
SIC,SEN		1310.00 80	030210.00
B,SERS	-SVA ABOVE BIT TO EXT MEM FAILED.	1304.10 00	030210.40
-			
KV,\$X0,\$R		11.00 90	030211.00
BXE,\$+1.32		30213.32 C2	030211.40
SIC,SEN		1310.00 80	030212.00
B,SERS	-SVA ABOVE BIT TO INT MEM FAILED.	1304.10 00	030212.40
-			
KV,\$X0,\$X1		21.00 90	030213.00
BXE,\$+1.32		30215.32 C2	030213.40
SIC,SEN		1310.00 80	030214.00
B,SERS	-SVA ABOVE BIT TO IX CORE STG FAILED.	1304.10 00	030214.40
-			
B,\$+1.0		30216.10 00	030215.00
BD,I2221		30167.04 00	030215.40
SIC,SEN0+.32		1311.40 80	030216.00
B,SSW	-TO SSIP.	1301.10 00	030216.40
BD,\$+.32		30217.44 00	030217.00
-			
LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	030217.40
V+,\$X13,BIT20		33355.32 B0	030220.00
SX,\$X13,IC222		31251.33 10	030220.40

-THIS SECTION OF THE TEST CHECKS THAT THE CORRECT
 -NUMBER OF BITS ARE STORED. DETERMINATION IS
 -DONE AS FOLLOWS.

-1. IF BIT 27 IS 1, INSTRUCTION IS
 -DIRECT INDEX, STORE 19 BITS.

-2. IF BITS 26 AND 27 ARE 10, INST-
 -RUCTIO IS FP, STORE 18 BITS.

-3. IF BITS 25-27 ARE 100, INSTRUCT IS
 -CB OR BIND, STORE 19 BITS.

-4. IF BITS 24-27 ARE 1000, INSTRUCT
 -IS FULL WD, STORE 24 BITS.

-5. IF BITS 23-27 ARE 10000, INSTRUCT
 -IS IMMED IX, STORE 19 BITS.

-6. IF BITS 23-27 ARE 00000, INSTRUCT
 -IS MISC, STORE 19 BITS.

12222	LX,\$X0,122K3A	-CHECK STORE OF 19 BITS INTO DIR	31305.00 10	030221.00
	LX,\$X1,122K4	-IX INST, BIT 27 OF WHICH IS 1.	31314.02 10	030221.40
	SX,\$X1,122WRK		31322.03 10	030222.00
	SVA,\$X0,122WRK		31322.01 D0	030222.40
	LX,\$X2,122WRK		31322.04 10	030223.00
	KV,\$X2,122K3B		31306.04 90	030223.40
	BXH,\$+2.0		30226.33 42	030224.00
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM FAILS	1310.00 80	030224.40
	B,SERS	-TO STORE ANY BITS IN 0-17.	1304.10 00	030225.00
	B,12223		30230.50 00	030225.40
	KV,\$X2,122K3A		31305.04 90	030226.00
	BXL,\$+1.0		30227.72 42	030226.40
	B,\$+1.32		30230.50 00	030227.00
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM FAILS	1310.00 80	030227.40
	B,SERS	-TO STORE ALL BITS IN 0-17.	1304.10 00	030230.00
12223	SX,\$X1,122WRK		31322.03 10	030230.40
	LX,\$X0,122K3C		31307.00 10	030231.00
	SVA,\$X0,122WRK		31322.01 D0	030231.40
	LX,\$X2,122WRK		31322.04 10	030232.00
	KV,\$X2,122K3B		31306.04 90	030232.40
	BXH,\$+1.0		30234.33 42	030233.00
	B,\$+2.0		30235.50 00	030233.40
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM	1310.00 80	030234.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030234.40
	B,12224		30237.50 00	030235.00
	KV,\$X2,122K3D		31310.04 90	030235.40
	BXH,\$+1.32		30237.73 42	030236.00
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM FAILS	1310.00 80	030236.40
	B,SERS	-TO STORE BIT 18.	1304.10 00	030237.00
12224	SX,\$X1,122WRK		31322.03 10	030237.40
	LX,\$X0,122K3D		31310.00 10	030240.00
	SVA,\$X0,122WRK		31322.01 D0	030240.40
	LX,\$X2,122WRK		31322.04 10	030241.00
	KV,\$X2,122K3C		31307.04 90	030241.40
	BXL,\$+2.0		30244.32 42	030242.00
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM	1310.00 80	030242.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	030243.00
	B,12225		30246.10 00	030243.40
		KV,\$X2,122K3E		31311.04 90
BXL,\$+1.32			30246.32 42	030244.40
SIC,SEN		-SVA INTO DIR IX INST IN EXT MEM	1310.00 80	030245.00
B,SERS		-STORES SOME BITS IN 19-23.	1304.10 00	030245.40
12225	LX,\$X0,122K3A		31305.00 10	030246.00
	LX,\$X1,122K4		31314.02 10	030246.40
	SX,\$X1,\$R		11.03 10	030247.00
	SVA,\$X0,\$R		11.01 D0	030247.40
	LX,\$X2,\$R		11.04 10	030250.00
	KV,\$X2,122K3B		31306.04 90	030250.40
	BXH,\$+2.0		30253.33 42	030251.00
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030251.40
	B,SERS	-FAILS TO STORE ANY BITS IN 0-17.	1304.10 00	030252.00
	B,12226		30255.50 00	030252.40

	KV,\$X2,I22K3A		31305.04 90	030253.00
	BXL,\$+1.0		30254.72 42	030253.40
	B,\$+1.32		30255.50 00	030254.00
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030254.40
	B,SERS	-FAILS TO STORE ALL BITS IN 0-17.	1304.10 00	030255.00
12226	SX,\$X1,\$R		11.03 10	030255.40
	LX,\$X0,I22K3C		31307.00 10	030256.00
	SVA,\$X0,\$R		11.01 D0	030256.40
	LX,\$X2,\$R		11.04 10	030257.00
	KV,\$X2,I22K3B		31306.04 90	030257.40
	BXH,\$+1.0		30261.33 42	030260.00
	B,\$+2.0		30262.50 00	030260.40
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030261.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030261.40
	B,I2227		30264.50 00	030262.00
	KV,\$X2,I22K3D		31310.04 90	030262.40
	BXH,\$+1.32		30264.73 42	030263.00
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030263.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	030264.00
12227	SX,\$X1,\$R		11.03 10	030264.40
	LX,\$X0,I22K3D		31310.00 10	030265.00
	SVA,\$X0,\$R		11.01 D0	030265.40
	LX,\$X2,\$R		11.04 10	030266.00
	KV,\$X2,I22K3C		31307.04 90	030266.40
	BXL,\$+2.0		30271.32 42	030267.00
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030267.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	030270.00
	B,I2228		30273.10 00	030270.40
	KV,\$X2,I22K3E		31311.04 90	030271.00
	BXL,\$+1.32		30273.32 42	030271.40
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	030272.00
	B,SERS	-STORE SOME BITS IN 19-23.	1304.10 00	030272.40

12228	LX,\$X0,122K3A		31305.00 10	030273.00
	LX,\$X1,122K4		31314.02 10	030273.40
	SX,\$X1,\$X3		23.03 10	030274.00
	SVA,\$X0,\$X3		23.01 D0	030274.40
	LX,\$X2,\$X3		23.04 10	030275.00
	KV,\$X2,122K3B		31306.04 90	030275.40
	BXH,\$+2.0		30300.33 42	030276.00
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030276.40
	B,SERS	-FAILS TO STORE ANY BITS IN 0-17.	1304.10 00	030277.00
	B,12229		30302.50 00	030277.40
	KV,\$X2,122K3A		31305.04 90	030300.00
	BXL,\$+1.0		30301.72 42	030300.40
	B,\$+1.32		30302.50 00	030301.00
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030301.40
	B,SERS	-FAILS TO STORE ALL BITS IN 0-17.	1304.10 00	030302.00
12229	SX,\$X1,\$X3		23.03 10	030302.40
	LX,\$X0,122K3C		31307.00 10	030303.00
	SVA,\$X0,\$X3		23.01 D0	030303.40
	LX,\$X2,\$X3		23.04 10	030304.00
	KV,\$X2,122K3B		31306.04 90	030304.40
	BXH,\$+1.0		30306.33 42	030305.00
	B,\$+2.0		30307.50 00	030305.40
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030306.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030306.40
	B,12230		30311.50 00	030307.00
	KV,\$X2,122K3D		31310.04 90	030307.40
	BXH,\$+1.32		30311.73 42	030310.00
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030310.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	030311.00
12230	SX,\$X1,\$X3		23.03 10	030311.40
	LX,\$X0,122K3D		31310.00 10	030312.00
	SVA,\$X0,\$X3		23.01 D0	030312.40
	LX,\$X2,\$X3		23.04 10	030313.00
	KV,\$X2,122K3C		31307.04 90	030313.40
	BXL,\$+2.0		30316.32 42	030314.00
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030314.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	030315.00
	B,12230A		30320.10 00	030315.40
	KV,\$X2,122K3E		31311.04 90	030316.00
	BXL,\$+1.32		30320.32 42	030316.40
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	030317.00
	B,SERS	-STORE SOME BITS IN 19-23.	1304.10 00	030317.40
12230A	B,\$+1.0		30321.10 00	030320.00
	BD,12222		30221.04 00	030320.40
	SIC,SENO+.32		1311.40 80	030321.00
	B,SSW	-TO SSIP	1301.10 00	030321.40
	BD,\$+.32		30322.44 00	030322.00
	LX,\$X13,1C222	-UPDATE CONTINUITY CHECK.	31251.32 10	030322.40
	V+,\$X13,BIT21		33356.32 B0	030323.00
	SX,\$X13,1C222		31251.33 10	030323.40

12231	LX,\$X0,I22K3A LX,\$X1,I22K2 SX,\$X1,I22WRK SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2232	-CHECK STORE OF 18 BITS ONLY INTO -A FP INSTRUCT BITS 26 AND 27 -OF WHICH ARE 10. -SVA INTO FP INST IN EXT MEM FAILS -TO STORE ANY BITS IN 0-17.	31305.00 10 31304.02 10 31322.03 10 31322.01 D0 31322.04 10 31306.04 90 30331.33 42 1310.00 80 1304.10 00 30333.50 00	030324.00 030324.40 030325.00 030325.40 030326.00 030326.40 030327.00 030327.40 030330.00 030330.40
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN EXT MEM FAILS -TO STORE ALL BITS IN 0-17.	31305.04 90 30332.72 42 30333.50 00 1310.00 80 1304.10 00	030331.00 030331.40 030332.00 030332.40 030333.00
12232	SX,\$X1,I22WRK LX,\$X0,I22K3C SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2233	-SVA INTO FP INST IN EXT MEM PICKS -UP SOME BITS IN 0-17.	31322.03 10 31307.00 10 31322.01 D0 31322.04 10 31306.04 90 30337.33 42 30340.50 00 1310.00 80 1304.10 00 30342.50 00	030333.40 030334.00 030334.40 030335.00 030335.40 030336.00 030336.40 030337.00 030337.40 030340.00
	KV,\$X2,I22K3D BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN EXT MEM STORES -BIT 18.	31310.04 90 30342.72 42 1310.00 80 1304.10 00	030340.40 030341.00 030341.40 030342.00
12233	SX,\$X1,I22WRK LX,\$X0,I22K3D SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3D BXL,\$+2.0 SIC,SEN B,SERS B,I2234	-SVA INTO FP INST IN EXT MEM PICKS -UP SOME BITS IN 0-18.	31322.03 10 31310.00 10 31322.01 D0 31322.04 10 31310.04 90 30347.32 42 1310.00 80 1304.10 00 30351.10 00	030342.40 030343.00 030343.40 030344.00 030344.40 030345.00 030345.40 030346.00 030346.40
	KV,\$X2,I22K3E BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN EXT MEM STORES -SOME BITS IN 19-23.	31311.04 90 30351.32 42 1310.00 80 1304.10 00	030347.00 030347.40 030350.00 030350.40

12234	LX,\$X0,I22K3A SX,\$X1,\$R SVA,\$X0,\$R LX,\$X2,\$R KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2235	-SVA INTO FP INST IN INT MEM FAILS -TO STORE ANY BITS IN 0-17.	31305.00 10 11.03 10 11.01 D0 11.04 10 31306.04 90 30355.73 42 1310.00 80 1304.10 00 30360.10 00	030351.00 030351.40 030352.00 030352.40 030353.00 030353.40 030354.00 030354.40 030355.00
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN INT MEM FAILS -TO STORE ALL BITS IN 0-17.	31305.04 90 30357.32 42 30360.10 00 1310.00 80 1304.10 00	030355.40 030356.00 030356.40 030357.00 030357.40
12235	SX,\$X1,\$R LX,\$X0,I22K3C SVA,\$X0,\$R LV,\$X2,\$R KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2236	-SVA INTO FP INST IN INT MEM PICKS -UP SOME BITS IN 0-17.	11.03 10 31307.00 10 11.01 D0 11.04 30 31306.04 90 30363.73 42 30365.10 00 1310.00 80 1304.10 00 30367.10 00	030360.00 030360.40 030361.00 030361.40 030362.00 030362.40 030363.00 030363.40 030364.00 030364.40
	KV,\$X2,I22K3D BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN INT MEM STORES -BIT 18.	31310.04 90 30367.32 42 1310.00 80 1304.10 00	030365.00 030365.40 030366.00 030366.40
12236	SX,\$X1,\$R LX,\$X0,I22K3D SVA,\$X0,\$R LX,\$X2,\$R KV,\$X2,I22K3D BXL,\$+2.0 SIC,SEN B,SERS B,I2237	-SVA INTO FP INST IN INT MEM PICKS -UP SOME BITS IN 0-18.	11.03 10 31310.00 10 11.01 D0 11.04 10 31310.04 90 30373.72 42 1310.00 80 1304.10 00 30375.50 00	030367.00 030367.40 030370.00 030370.40 030371.00 030371.40 030372.00 030372.40 030373.00
	KV,\$X2,I22K3E BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN INT MEM STORES -COME BITS IN 19-23.	31311.04 90 30375.72 42 1310.00 80 1304.10 00	030373.40 030374.00 030374.40 030375.00

12237	LX,\$X0,122K3A LX,\$X1,122K2 SX,\$X1,\$X3 SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,122K3B BXH,\$+2.0 SIC,SEN B,SERS B,12238	-SVA INTO FP INST IN IX CORE STG -FAILS TO STORE ANY BITS IN 0-17.	31305.00 10 31304.02 10 23.03 10 23.01 D0 23.04 10 31306.04 90 30402.73 42 1310.00 80 1304.10 00 30405.10 00	030375.40 030376.00 030376.40 030377.00 030377.40 030400.00 030400.40 030401.00 030401.40 030402.00
	KV,\$X2,122K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN IX CORE STG -FAILS TO STORE ALL BITS IN 0-17.	31305.04 90 30404.32 42 30405.10 00 1310.00 80 1304.10 00	030402.40 030403.00 030403.40 030404.00 030404.40
12238	SX,\$X1,\$X3 LX,\$X0,122K3C SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,122K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,12239	-SVA INTO FP INST IN IX CORE STG -PICKS UP SOME BITS IN 0-17.	23.03 10 31307.00 10 23.01 D0 23.04 10 31306.04 90 30410.73 42 30412.10 00 1310.00 80 1304.10 00 30414.10 00	030405.00 030405.40 030406.00 030406.40 030407.00 030407.40 030410.00 030410.40 030411.00 030411.40
	KV,\$X2,122K3D BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN IX CORE STG -STORES BIT 18.	31310.04 90 30414.32 42 1310.00 80 1304.10 00	030412.00 030412.40 030413.00 030413.40
12239	SX,\$X1,\$X3 LX,\$X0,122K3D SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,122K3D BXL,\$+2.0 SIC,SEN B,SERS B,12240	-SVA INTO FP INST IN IX CORE STG -PICKS UP SOME BITS IN 0-18.	23.03 10 31310.00 10 23.01 D0 23.04 10 31310.04 90 30420.72 42 1310.00 80 1304.10 00 30422.50 00	030414.00 030414.40 030415.00 030415.40 030416.00 030416.40 030417.00 030417.40 030420.00
	KV,\$X2,122K3E BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FP INST IN IX CORE STG -STORES SOME BITS IN 19-23.	31311.04 90 30422.72 42 1310.00 80 1304.10 00	030420.40 030421.00 030421.40 030422.00
12240	B,\$+1.0 BD,12231 SIC,SENO+.32 B,SSW BD,\$+.32	-TO SSIP.	30423.50 00 30324.04 00 1311.40 80 1301.10 00 30425.04 00	030422.40 030423.00 030423.40 030424.00 030424.40
	LX,\$X13,1C222 V+,\$X13,BIT22 SX,\$X13,1C222	-UPDATE CONTINUITY CHECK.	31251.32 10 33357.32 B0 31251.33 10	030425.00 030425.40 030426.00

12241	LX,\$X0,I22K3A LX,\$X1,I22K4A SX,\$X1,I22WRK SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2242	-CHECK STORE OF 19 BITS ONLY INTO -A BMK INST, BITS 25-27 OF -WHICH ARE 100. -SVA INTO BMK INST IN EXT MEM -FAILS TO STORE ANY BITS IN 0-17.	31305.00 10 31315.02 10 31322.03 10 31322.01 D0 31322.04 10 31306.04 90 30433.73 42 1310.00 80 1304.10 00 30436.10 00	030426.40 030427.00 030427.40 030430.00 030430.40 030431.00 030431.40 030432.00 030432.40 030433.00
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN EXT MEM -FAILS TO STORE ALL BITS IN 0-17.	31305.04 90 30435.32 42 30436.10 00 1310.00 80 1304.10 00	030433.40 030434.00 030434.40 030435.00 030435.40
12242	SX,\$X1,I22WRK LX,\$X0,I22K3C SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2243	-SVA INTO BMK INST IN EXT MEM -PICKS UP SOME BITS IN 0-17.	31322.03 10 31307.00 10 31322.01 D0 31322.04 10 31306.04 90 30441.73 42 30443.10 00 1310.00 80 1304.10 00 30445.10 00	030436.00 030436.40 030437.00 030437.40 030440.00 030440.40 030441.00 030441.40 030442.00 030442.40
	KV,\$X2,I22K3D BXH,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN EXT MEM -FAILS TO STORE BIT 18.	31310.04 90 30445.33 42 1310.00 80 1304.10 00	030443.00 030443.40 030444.00 030444.40
12243	SX,\$X1,I22WRK LX,\$X0,I22K3D SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3C BXL,\$+2.0 SIC,SEN B,SERS B,I2244	-SVA INTO BMK INST IN EXT MEM -PICKS UP SOME BITS IN 0-18.	31322.03 10 31310.00 10 31322.01 D0 31322.04 10 31307.04 90 30451.72 42 1310.00 80 1304.10 00 30453.50 00	030445.00 030445.40 030446.00 030446.40 030447.00 030447.40 030450.00 030450.40 030451.00
	KV,\$X2,I22K3E BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN EXT MEM -STORES SOME BITS IN 19-23.	31311.04 90 30453.72 42 1310.00 80 1304.10 00	030451.40 030452.00 030452.40 030453.00

12244	LX,\$X0,I22K3A		31305.00 10	030453.40
	LX,\$X1,I22K4A		31315.02 10	030454.00
	SX,\$X1,\$R		11.03 10	030454.40
	SVA,\$X0,\$R		11.01 D0	030455.00
	LX,\$X2,\$R		11.04 10	030455.40
	KV,\$X2,I22K3B		31306.04 90	030456.00
	BXH,\$+2.0		30460.73 42	030456.40
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030457.00
	B,SERS	-FAILS TO STORE ANY BITS IN 0-17.	1304.10 00	030457.40
	B,I2245		30463.10 00	030460.00
			-	
	KV,\$X2,I22K3A		31305.04 90	030460.40
	BXL,\$+1.0		30462.32 42	030461.00
	B,\$+1.32		30463.10 00	030461.40
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030462.00
	B,SERS	-FAILS TO STORE ALL BITS IN 0-17.	1304.10 00	030462.40
			-	
12245	SX,\$X1,\$R		11.03 10	030463.00
	LX,\$X0,I22K3C		31307.00 10	030463.40
	SVA,\$X0,\$R		11.01 D0	030464.00
	LX,\$X2,\$R		11.04 10	030464.40
	KV,\$X2,I22K3B		31306.04 90	030465.00
	BXH,\$+1.0		30466.73 42	030465.40
	B,\$+2.0		30470.10 00	030466.00
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030466.40
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030467.00
			-	
	B,I2246		30472.10 00	030467.40
	KV,\$X2,I22K3D		31310.04 90	030470.00
	BXH,\$+1.32		30472.33 42	030470.40
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030471.00
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	030471.40
			-	
12246	SX,\$X1,\$R		11.03 10	030472.00
	LX,\$X0,I22K3D		31310.00 10	030472.40
	SVA,\$X0,\$R		11.01 D0	030473.00
	LX,\$X2,\$R		11.04 10	030473.40
	KV,\$X2,I22K3C		31307.04 90	030474.00
	BXL,\$+2.0		30476.72 42	030474.40
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030475.00
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	030475.40
	B,I2247		30500.50 00	030476.00
			-	
	KV,\$X2,I22K3E		31311.04 90	030476.40
	BXL,\$+1.32		30500.72 42	030477.00
	SIC,SEN	-SVA INTO BMK INST IN INT MEM	1310.00 80	030477.40
	B,SERS	-STORES SOME BITS IN 19-23.	1304.10 00	030500.00

12247	LX,\$X0,I22K3A LX,\$X1,I22K4A SX,\$X1,\$X3 SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2248	-SVA INTO BMK INST IN IX CORE STG -FAILS TO STORE ANY BITS IN 0-17.	31305.00 10 31315.02 10 23.03 10 23.01 D0 23.04 10 31306.04 90 30505.73 42 1310.00 80 1304.10 00 30510.10 00	030500.40 030501.00 030501.40 030502.00 030502.40 030503.00 030503.40 030504.00 030504.40 030505.00
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN IX CORE STG -FAILS TO STORE ALL BITS IN 0-17.	31305.04 90 30507.32 42 30510.10 00 1310.00 80 1304.10 00	030505.40 030506.00 030506.40 030507.00 030507.40
12248	SX,\$X1,\$X3 LX,\$X0,I22K3C SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2249	-SVA INTO BMK INST IN IX CORE STG -PICKS UP SOME BITS IN 0-17.	23.03 10 31307.00 10 23.01 D0 23.04 10 31306.04 90 30513.73 42 30515.10 00 1310.00 80 1304.10 00 30517.10 00	030510.00 030510.40 030511.00 030511.40 030512.00 030512.40 030513.00 030513.40 030514.00 030514.40
	KV,\$X2,I22K3D BXH,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN IX CORE STG -FAILS TO STORE BIT 18.	31310.04 90 30517.33 42 1310.00 80 1304.10 00	030515.00 030515.40 030516.00 030516.40
12249	SX,\$X1,\$X3 LX,\$X0,I22K3D SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3C BXL,\$+2.0 SIC,SEN B,SERS B,I2250	-SVA INTO BMK INST IN IX CORE STG -PICKS UP SOME BITS IN 0-18.	23.03 10 31310.00 10 23.01 D0 23.04 10 31307.04 90 30523.72 42 1310.00 80 1304.10 00 30525.50 00	030517.00 030517.40 030520.00 030520.40 030521.00 030521.40 030522.00 030522.40 030523.00
	KV,\$X2,I22K3E BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO BMK INST IN IX CORE STG -STOES SOME BITS IN 19-23.	31311.04 90 30525.72 42 1310.00 80 1304.10 00	030523.40 030524.00 030524.40 030525.00
12250	B,\$+1.0 BD,I2241 SIC,SENO+.32 B,SSW BD,\$+.32	-TO SSIP.	30526.50 00 30426.44 00 1311.40 80 1301.10 00 30530.04 00	030525.40 030526.00 030526.40 030527.00 030527.40
	LX,\$X13,IC222 V+,\$X13,BIT23 SX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10 33360.32 B0 31251.33 10	030530.00 030530.40 030531.00

12251	LX,\$X0,I22K3A LX,\$X1,I22K1 SX,\$X1,I22WRK SVA,\$X0,I22WRK LC,\$X2,I22WRK SC,\$X2,\$X2 KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2252	-CHECK STORE OF 24 BITS INTO A -FULL WORD INST, BITS 24-27 OF -WHICH ARE 1000. -SVA INTO FULL WD INST IN EXT MEM -FAILED TO STORE ANY BITS IN 0-17.	31305.00 10 31303.02 10 31322.03 10 31322.01 D0 31322.04 50 22.05 50 31306.04 90 30537.33 42 1310.00 80 1304.10 00 30541.50 00	030531.40 030532.00 030532.40 030533.00 030533.40 030534.00 030534.40 030535.00 030535.40 030536.00 030536.40
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO FULL WD INST IN EXT MEM -FAILED TO STORE ALL BITS IN 0-17.	31305.04 90 30540.72 42 30541.50 00 1310.00 80 1304.10 00	030537.00 030537.40 030540.00 030540.40 030541.00
12252	SX,\$X1,I22WRK LX,\$X0,I22K3B SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3F BZXL,\$+2.0 SIC,SEN B,SERS B,I2253	-SVA INTO FULL WD INST IN EXT MEM -PICKS UP SOME BITS IN 0-17.	31322.03 10 31306.00 10 31322.01 D0 31322.04 10 31312.04 90 30546.32 40 1310.00 80 1304.10 00 30550.10 00	030541.40 030542.00 030542.40 030543.00 030543.40 030544.00 030544.40 030545.00 030545.40
	KV,\$X2,I22K3G BXL,\$+1.32 SIC,SEN B,SERS	-SVA INTO FULL WD INST IN EXT MEM -FAILS TO STORE BIT 18.	31313.04 90 30550.32 42 1310.00 80 1304.10 00	030546.00 030546.40 030547.00 030547.40
12253	SX,\$X1,I22WRK LX,\$X0,I22K3D SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3G BZXL,\$+2.0 SIC,SEN B,SERS B,I2254	-SVA INTO FULL WD INST IN EXT MEM -PICKS UP SOME BITS 0-18.	31322.03 10 31310.00 10 31322.01 D0 31322.04 10 31313.04 90 30554.72 40 1310.00 80 1304.10 00 30556.10 00	030550.00 030550.40 030551.00 030551.40 030552.00 030552.40 030553.00 030553.40 030554.00
	BXE,\$+1.32 SIC,SEN B,SERS	-SVA INTO FULL WD INST IN EXT MEM -FAILS TO STORE SOME OR ALL BITS 19-23.	30556.32 C2 1310.00 80 1304.10 00	030554.40 030555.00 030555.40
12254	SX,\$X1,I22WRK LX,\$X0,I00Z SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I00Z BXE,\$+1.32 SIC,SEN B,SERS	-SVA INTO FULL WD INST IN EXT MEM -PICK UP SOME BITS 0-23.	31322.03 10 33311.00 10 31322.01 D0 31322.04 10 33311.04 90 30562.32 C2 1310.00 80 1304.10 00	030556.00 030556.40 030557.00 030557.40 030560.00 030560.40 030561.00 030561.40

12255	LX,\$X0,122K3A		31305.00 10	030562.00
	LX,\$X1,122K1		31303.02 10	030562.40
	SX,\$X1,\$R		11.03 10	030563.00
	SVA,\$X0,\$R		11.01 D0	030563.40
	LC,\$X2,\$R		11.04 50	030564.00
	SC,\$X2,\$X2		22.05 50	030564.40
	KV,\$X2,122K3B		31306.04 90	030565.00
	BXH,\$+2.0		30567.73 42	030565.40
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030566.00
	B,SERS	-FAILED TO STORE ANY BITS IN 0-17.	1304.10 00	030566.40
	B,12256		30572.10 00	030567.00
	KV,\$X2,122K3A		31305.04 90	030567.40
	BXL,\$+1.0		30571.32 42	030570.00
	B,\$+1.32		30572.10 00	030570.40
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030571.00
	B,SERS	-FAILED TO STORE ALL BITS IN 0-17.	1304.10 00	030571.40
12256	SX,\$X1,\$R		11.03 10	030572.00
	LX,\$X0,122K3B		31306.00 10	030572.40
	SVA,\$X0,\$R		11.01 D0	030573.00
	LX,\$X2,\$R		11.04 10	030573.40
	KV,\$X2,122K3F		31312.04 90	030574.00
	BZXL,\$+2.0		30576.72 40	030574.40
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030575.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030575.40
	B,12257		30600.50 00	030576.00
	KV,\$X2,122K3G		31313.04 90	030576.40
	BXL,\$+1.32		30600.72 42	030577.00
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030577.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	030600.00
12257	SX,\$X1,\$R		11.03 10	030600.40
	LX,\$X0,122K3D		31310.00 10	030601.00
	SVA,\$X0,\$R		11.01 D0	030601.40
	LX,\$X2,\$R		11.04 10	030602.00
	KV,\$X2,122K3G		31313.04 90	030602.40
	BZXL,\$+2.0		30605.32 40	030603.00
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030603.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	030604.00
	B,12258		30606.50 00	030604.40
	BXE,\$+1.32		30606.72 C2	030605.00
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030605.40
	B,SERS	-FAILS TO STORE SOME OR ALL BITS 19-23.	1304.10 00	030606.00
12258	SX,\$X1,\$R		11.03 10	030606.40
	LX,\$X0,100Z		33311.00 10	030607.00
	SVA,\$X0,\$R		11.01 D0	030607.40
	LX,\$X2,\$R		11.04 10	030610.00
	KV,\$X2,100Z		33311.04 90	030610.40
	BXE,\$+1.32		30612.72 C2	030611.00
	SIC,SEN	-SVA INTO FULL WD INST IN INT MEM	1310.00 80	030611.40
	B,SERS	-PICKS UP SOME BITS 0-23.	1304.10 00	030612.00

12259	LX,\$X0,I22K3A		31305.00 10	030612.40
	LX,\$X1,I22K1		31303.02 10	030613.00
	SX,\$X1,\$X3		23.03 10	030613.40
	SVA,\$X0,\$X3		23.01 D0	030614.00
	LC,\$X2,\$X3		23.04 50	030614.40
	SC,\$X2,\$X2		22.05 50	030615.00
	KV,\$X2,I22K3B		31306.04 90	030615.40
	BXH,\$+2.0		30620.33 42	030616.00
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030616.40
	B,SERS	-FAILED TO STORE ANY BITS IN 0-17.	1304.10 00	030617.00
	B,I2260		30622.50 00	030617.40
	KV,\$X2,I22K3A		31305.04 90	030620.00
	BXL,\$+1.0		30621.72 42	030620.40
	B,\$+1.32		30622.50 00	030621.00
	SIC,SEN	-SVA INTO FULL WD INST IN IX CROE STG	1310.00 80	030621.40
	B,SERS	-FAILED TO STORE ALL BITS IN 0-17	1304.10 00	030622.00
12260	SX,\$X1,\$X3		23.03 10	030622.40
	LX,\$X0,I22K3B		31306.00 10	030623.00
	SVA,\$X0,\$X3		23.01 D0	030623.40
	LX,\$X2,\$X3		23.04 10	030624.00
	KV,\$X2,I22K3F		31312.04 90	030624.40
	BZXL,\$+2.0		30627.32 40	030625.00
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030625.40
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10 00	030626.00
	B,I2261		30631.10 00	030626.40
	KV,\$X2,I22K3G		31313.04 90	030627.00
	BXL,\$+1.32		30631.32 42	030627.40
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030630.00
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	030630.40
12261	SX,\$X1,\$X3		23.03 10	030631.00
	LX,\$X0,I22K3D		31310.00 10	030631.40
	SVA,\$X0,\$X3		23.01 D0	030632.00
	LX,\$X2,\$X3		23.04 10	030632.40
	KV,\$X2,I22K3G		31313.04 90	030633.00
	BZXL,\$+2.0		30635.72 40	030633.40
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030634.00
	B,SERS	-PICKS UP SOME BITS 0-18.	1304.10 00	030634.40
	B,I2262		30637.10 00	030635.00
	BXE,\$+1.32		30637.32 C2	030635.40
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030636.00
	B,SERS	-FAILS TO STORE SOME OR ALL BITS 19-23.	1304.10 00	030636.40
12262	SX,\$X1,\$X3		23.03 10	030637.00
	LX,\$X0,I00Z		33311.00 10	030637.40
	SVA,\$X0,\$X3		23.01 D0	030640.00
	LX,\$X2,\$X3		23.04 10	030640.40
	KV,\$X2,I00Z		33311.04 90	030641.00
	BXE,\$+1.32		30643.32 C2	030641.40
	SIC,SEN	-SVA INTO FULL WD INST IN IX CORE STG	1310.00 80	030642.00
	B,SERS	-PICKS UP SOME BITS 0-23.	1304.10 00	030642.40
	B,\$+1.0		30644.10 00	030643.00
	BD,I2251		30531.44 00	030643.40
	SIC,SEN0+.32		1311.40 80	030644.00
	B,SSW	-TO SSIP.	1301.10 00	030644.40
	BD,\$+.32		30645.44 00	030645.00

LX,\$X13,IC222
SC,\$X13,\$X12
V+,\$X12,BIT0
LC,\$X13,\$X12
SX,\$X13,IC222

-UPDATE CONTINUITY CHECK.

31251.32 10
34.33 50
33331.30 B0
34.32 50
31251.33 10

030645.40
030646.00
030646.40
030647.00
030647.40

12263	LX,\$X0,I22K3A LX,\$X1,I22K5 SX,\$X1,I22WRK SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2264	-CHECK STORE OF 19 BITS ONLY INTO -IM IX INST BITS 23-27 OF WHICH -ARE 10000. -SVA INTO IM IX INST IN EXT MEM -FAILS TO STORE ANY BITS IN 0-17.	31305.00 10 31316.02 10 31322.03 10 31322.01 D0 31322.04 10 31306.04 90 30655.33 42 1310.00 80 1304.10 00 30657.50 00	030650.00 030650.40 030651.00 030651.40 030652.00 030652.40 030653.00 030653.40 030654.00 030654.40
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN EXT MEM -FAILS TO STORE ALL BITS IN 0-17.	31305.04 90 30656.72 42 30657.50 00 1310.00 80 1304.10 00	030655.00 030655.40 030656.00 030656.40 030657.00
12264	SX,\$X1,I22WRK LX,\$X0,I22K3C SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2265	-SVA INTO IM IX INST IN EXT MEM -PICKS UP SOME IBTS IN 0-17.	31322.03 10 31307.00 10 31322.01 D0 31322.04 10 31306.04 90 30663.33 42 30664.50 00 1310.00 80 1304.10 00 30666.50 00	030657.40 030660.00 030660.40 030661.00 030661.40 030662.00 030662.40 030663.00 030663.40 030664.00
	KV,\$X2,I22K3D BXH,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN EXT MEM -FAILS TO STORE BIT 18.	31310.04 90 30666.73 42 1310.00 80 1304.10 00	030664.40 030665.00 030665.40 030666.00
12265	SX,\$X1,I22WRK LX,\$X0,I22K3D SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3C BXL,\$+2.0 SIC,SEN B,SERS B,I2266	-SVA INTO IM IX INST IN EXT MEM -PICKS UP SOME BITS IN 0-18.	31322.03 10 31310.00 10 31322.01 D0 31322.04 10 31307.04 90 30673.32 42 1310.00 80 1304.10 00 30675.10 00	030666.40 030667.00 030667.40 030670.00 030670.40 030671.00 030671.40 030672.00 030672.40
	KV,\$X2,I22K3E BXE,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN EXT MEM -STORES SOME BITS IN 19-23.	31311.04 90 30675.32 C2 1310.00 80 1304.10 00	030673.00 030673.40 030674.00 030674.40

12266	LX,\$X0,I22K3A		31305.00	10	030675.00
	LX,\$X1,I22K5		31316.02	10	030675.40
	SX,\$X1,\$R		11.03	10	030676.00
	SVA,\$X0,\$R		11.01	D0	030676.40
	LX,\$X2,\$R		11.04	10	030677.00
	KV,\$X2,I22K3B		31306.04	90	030677.40
	BXH,\$+2.0		30702.33	42	030700.00
	SIC,SEN	-SVA INTO IM IX INST IN INT MEM	1310.00	80	030700.40
	B,SERS	-FAILS TO STORE ANY BITS IN 0-17.	1304.10	00	030701.00
	B,I2267		30704.50	00	030701.40
	KV,\$X2,I22K3A		31305.04	90	030702.00
	BXL,\$+1.0		30703.72	42	030702.40
	B,\$+1.32		30704.50	00	030703.00
	SIC,SEN	-SVA INTO IM IX INST IN INT MEM	1310.00	80	030703.40
	B,SERS	-FAILS TO STORE ALL BITS IN 0-17.	1304.10	00	030704.00
12267	SX,\$X1,\$R		11.03	10	030704.40
	LX,\$X0,I22K3C		31307.00	10	030705.00
	SVA,\$X0,\$R		11.01	D0	030705.40
	LX,\$X2,\$R		11.04	10	030706.00
	KV,\$X2,I22K3B		31306.04	90	030706.40
	BXH,\$+1.0		30710.33	42	030707.00
	B,\$+2.0		30711.50	00	030707.40
	SIC,SEN	-SVA INTO IM IX INST IN INT MEM	1310.00	80	030710.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10	00	030710.40
	B,I2268		30713.50	00	030711.00
	KV,\$X2,I22K3D		31310.04	90	030711.40
	BXH,\$+1.32		30713.73	42	030712.00
	SIC,SEN	-SVA INTO IM IX INST IN INT MEM	1310.00	80	030712.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10	00	030713.00
12268	SX,\$X1,\$R		11.03	10	030713.40
	LX,\$X0,I22K3D		31310.00	10	030714.00
	SVA,\$X0,\$R		11.01	D0	030714.40
	LX,\$X2,\$R		11.04	10	030715.00
	KV,\$X2,I22K3C		31307.04	90	030715.40
	BXL,\$+2.0		30720.32	42	030716.00
	SIC,SEN	-SVA INTO IM IX INST IN INT MEM	1310.00	80	030716.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10	00	030717.00
	B,I2269		30722.10	00	030717.40
		KV,\$X2,I22K3E		31311.04	90
BXE,\$+1.32			30722.32	C2	030720.40
SIC,SEN		-SVA INTO IM IX INST IN INT MEM	1310.00	80	030721.00
B,SERS		-STORES SOME BITS IN 19-23.	1304.10	00	030721.40

12269	LX,\$X0,I22K3A LX,\$X1,I22K5 SX,\$X1,\$X3 SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2270	-SVA INTO IM IX INST IN IX CORE STG -FAILS TO STORE ANY BITS IN 0-17.	31305.00 10 31316.02 10 23.03 10 23.01 D0 23.04 10 31306.04 90 30727.33 42 1310.00 80 1304.10 00 30731.50 00	030722.00 030722.40 030723.00 030723.40 030724.00 030724.40 030725.00 030725.40 030726.00 030726.40
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN IX CORE STG -FAILS TO STORE ALL BITS IN 0-17.	31305.04 90 30730.72 42 30731.50 00 1310.00 80 1304.10 00	030727.00 030727.40 030730.00 030730.40 030731.00
12270	SX,\$X1,\$X3 LX,\$X0,I22K3C SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2271	-SVA INTO IM IX INST IN IX CORE STG -PICKS UP SOME BITS IN 0-17.	23.03 10 31307.00 10 23.01 D0 23.04 10 31306.04 90 30735.33 42 30736.50 00 1310.00 80 1304.10 00 30740.50 00	030731.40 030732.00 030732.40 030733.00 030733.40 030734.00 030734.40 030735.00 030735.40 030736.00
	KV,\$X2,I22K3D BXH,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN IX CORE STG -FAILS TO STORE BIT 18.	31310.04 90 30740.73 42 1310.00 80 1304.10 00	030736.40 030737.00 030737.40 030740.00
12271	SX,\$X1,\$X3 LX,\$X0,I22K3D SVA,\$X0,\$X3 LX,\$X2,\$X3 KV,\$X2,I22K3C BXL,\$+2.0 SIC,SEN B,SERS B,I2272	-SVA INTO IM IX INST IN IX CORE STG -PICKS UP SOME BITS IN 0-18.	23.03 10 31310.00 10 23.01 D0 23.04 10 31307.04 90 30745.32 42 1310.00 80 1304.10 00 30747.10 00	030740.40 030741.00 030741.40 030742.00 030742.40 030743.00 030743.40 030744.00 030744.40
	KV,\$X2,I22K3E BXE,\$+1.32 SIC,SEN B,SERS	-SVA INTO IM IX INST IN IX CORE STG -STORES SOME BITS IN 19-23.	31311.04 90 30747.32 C2 1310.00 80 1304.10 00	030745.00 030745.40 030746.00 030746.40
12272	B,\$+1.0 BD,I2263 SIC,SEN0+.32 B,SSW BD,\$+.32	-TO SSIP.	30750.10 00 30650.04 00 1311.40 80 1301.10 00 30751.44 00	030747.00 030747.40 030750.00 030750.40 030751.00
	LX,\$X13,IC222 SC,\$X13,\$X12 V+,\$X12,BIT1 LC,\$X13,\$X12 SX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10 34.33 50 33332.30 B0 34.32 50 31251.33 10	030751.40 030752.00 030752.40 030753.00 030753.40

12273	LX,\$X0,I22K3A LX,\$X1,I22K6 SX,\$X1,I22WRK SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+2.0 SIC,SEN B,SERS B,I2274	-CHECK STORE OF 19 BITS ONLY INTO -MISC INST, BITS 23-27 OF WHICH -ARE 00000.	31305.00 10 31317.02 10 31322.03 10 31322.01 D0 31322.04 10 31306.04 90 30761.33 42 1310.00 80 1304.10 00 30763.50 00	030754.00 030754.40 030755.00 030755.40 030756.00 030756.40 030757.00 030757.40 030760.00 030760.40
	KV,\$X2,I22K3A BXL,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-SVA INTO MISC INST IN EXT MEM FAILS -TO STORE ALL BITS IN 0-17.	31305.04 90 30762.72 42 30763.50 00 1310.00 80 1304.10 00	030761.00 030761.40 030762.00 030762.40 030763.00
12274	SX,\$X1,I22WRK LX,\$X0,I22K3C SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3B BXH,\$+1.0 B,\$+2.0 SIC,SEN B,SERS B,I2275	-SVA INTO MISC INST IN EXT MEM -PICKS UP SOME BITS IN 0-17.	31322.03 10 31307.00 10 31322.01 D0 31322.04 10 31306.04 90 30767.33 42 30770.50 00 1310.00 80 1304.10 00 30772.50 00	030763.40 030764.00 030764.40 030765.00 030765.40 030766.00 030766.40 030767.00 030767.40 030770.00
	KV,\$X2,I22K3D BXH,\$+1.32 SIC,SEN B,SERS	-SVA INTO MISC INST IN EXT MEM -FAILS TO STORE BIT 18.	31310.04 90 30772.73 42 1310.00 80 1304.10 00	030770.40 030771.00 030771.40 030772.00
12275	SX,\$X1,I22WRK LX,\$X0,I22K3D SVA,\$X0,I22WRK LX,\$X2,I22WRK KV,\$X2,I22K3C BXL,\$+2.0 SIC,SEN B,SERS B,I2276	-SVA INTO MISC INST IN EXT MEM -PICKS UP SOME BITS IN 0-18.	31322.03 10 31310.00 10 31322.01 D0 31322.04 10 31307.04 90 30777.32 42 1310.00 80 1304.10 00 31001.10 00	030772.40 030773.00 030773.40 030774.00 030774.40 030775.00 030775.40 030776.00 030776.40
	KV,\$X2,I22K6 BXE,\$+1.32 SIC,SEN B,SERS	-SVA INTO MISC INST IN EXT MEM -STORES SOME BITS IN 19-23.	31317.04 90 31001.32 C2 1310.00 80 1304.10 00	030777.00 030777.40 031000.00 031000.40

12276	LX,\$X0,I22K3A		31305.00	10	031001.00
	LX,\$X1,I22K6		31317.02	10	031001.40
	SX,\$X1,\$R		11.03	10	031002.00
	SVA,\$X0,\$R		11.01	D0	031002.40
	LX,\$X2,\$R		11.04	10	031003.00
	KV,\$X2,I22K3B		31306.04	90	031003.40
	BXH,\$+2.0		31006.33	42	031004.00
	SIC,SEN	-SVA INTO MISC INST IN INT MEM FAILS	1310.00	80	031004.40
	B,SERS	-TO STORE ANY BITS IN 0-17.	1304.10	00	031005.00
	B,I2277		31010.50	00	031005.40
	KV,\$X2,I22K3A		31305.04	90	031006.00
	BXL,\$+1.0		31007.72	42	031006.40
	B,\$+1.32		31010.50	00	031007.00
	SIC,SEN	-SVA INTO MISC INST IN INT MEM FAILS	1310.00	80	031007.40
	B,SERS	-TO STORE ALL BITS IN 0-17.	1304.10	00	031010.00
12277	SX,\$X1,\$R		11.03	10	031010.40
	LX,\$X0,I22K3C		31307.00	10	031011.00
	SVA,\$X0,\$R		11.01	D0	031011.40
	LX,\$X2,\$R		11.04	10	031012.00
	KV,\$X2,I22K3B		31306.04	90	031012.40
	BXH,\$+1.0		31014.33	42	031013.00
	B,\$+2.0		31015.50	00	031013.40
	SIC,SEN	-SVA INTO MISC INST IN INT MEM	1310.00	80	031014.00
	B,SERS	-PICKS UP SOME BITS IN 0-17.	1304.10	00	031014.40
	B,I2278		31017.50	00	031015.00
	KV,\$X2,I22K3D		31310.04	90	031015.40
	BXH,\$+1.32		31017.73	42	031016.00
	SIC,SEN	-SVA INTO MISC INST IN INT MEM	1310.00	80	031016.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10	00	031017.00
12278	SX,\$X1,\$R		11.03	10	031017.40
	LX,\$X0,I22K3D		31310.00	10	031020.00
	SVA,\$X0,\$R		11.01	D0	031020.40
	LX,\$X2,\$R		11.04	10	031021.00
	KV,\$X2,I22K3C		31307.04	90	031021.40
	BXL,\$+2.0		31024.32	42	031022.00
	SIC,SEN	-SVA INTO MISC INST IN INT MEM	1310.00	80	031022.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10	00	031023.00
	B,I2279		31026.10	00	031023.40
	KV,\$X2,I22K6		31317.04	90	031024.00
	BXE,\$+1.32		31026.32	C2	031024.40
	SIC,SEN	-SVA INTO MISC INST IN INT MEM	1310.00	80	031025.00
	B,SERS	-STORES SOME BITS IN 19-23.	1304.10	00	031025.40

12279	LX,\$X0,I22K3A		31305.00 10	031026.00
	LX,\$X1,I22K6		31317.02 10	031026.40
	SX,\$X1,\$X3		23.03 10	031027.00
	SVA,\$X0,\$X3		23.01 D0	031027.40
	LX,\$X2,\$X3		23.04 10	031030.00
	KV,\$X2,I22K3B		31306.04 90	031030.40
	BXH,\$+2.0		31033.33 42	031031.00
	SIC,SEN	-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031031.40
	B,SERS	-FAILS TO STORE ANY BITS IN 0-17.	1304.10 00	031032.00
	B,I2280		31035.50 00	031032.40
	KV,\$X2,I22K3A		31305.04 90	031033.00
	BXL,\$+1.0		31034.72 42	031033.40
	B,\$+1.32		31035.50 00	031034.00
	SIC,SEN	-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031034.40
	B,SERS	-FAILS TO STORE ALL BITS IN 0-17.	1304.10 00	031035.00
12280	SX,\$X1,\$X3		23.03 10	031035.40
	LX,\$X0,I22K3C		31307.00 10	031036.00
	SVA,\$X0,\$X3		23.01 D0	031036.40
	LX,\$X2,\$X3		23.04 10	031037.00
	KV,\$X2,I22K3B		31306.04 90	031037.40
	BXH,\$+1.0		31041.33 42	031040.00
	B,\$+2.0		31042.50 00	031040.40
	SIC,SEN	-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031041.00
	B,SERS	-PICKS UP SOME BITS IN 0-17	1304.10 00	031041.40
	B,I2281		13774.50 00	031042.00
	KV,\$X2,I22K3D		31310.04 90	031042.40
	BXH,\$+1.32		31044.73 42	031043.00
	SIC,SEN	-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031043.40
	B,SERS	-FAILS TO STORE BIT 18.	1304.10 00	031044.00
12281	SX,\$X1,\$X3		23.03 10	031044.40
	LX,\$X0,I22K3D		31310.00 10	031045.00
	SVA,\$X0,\$X3		23.01 D0	031045.40
	LX,\$X2,\$X3		23.04 10	031046.00
	KV,\$X2,I22K3C		31307.04 90	031046.40
	BXL,\$+2.0		31051.32 42	031047.00
	SIC,SEN	-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031047.40
	B,SERS	-PICKS UP SOME BITS IN 0-18.	1304.10 00	031050.00
	B,I2282		31053.10 00	031050.40
		KV,\$X2,I22K6		31317.04 90
BXE,\$+1.32			31053.32 C2	031051.40
SIC,SEN		-SVA INTO MISC INST IN IX CORE STG	1310.00 80	031052.00
B,SERS		-STORE SOME BITS IN 19-23.	1304.10 00	031052.40
12282	B,\$+1.0		31054.10 00	031053.00
	BD,I2273		30754.04 00	031053.40
	SIC,SENO+.32		1311.40 80	031054.00
	B,SSW	-TO SSIP.	1301.10 00	031054.40
	BD,\$+.32		31055.44 00	031055.00
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	031055.40
	SC,\$X13,\$X12		34.33 50	031056.00
	V+,\$X12,BIT2		33333.30 B0	031056.40
	LC,\$X13,\$X12		34.32 50	031057.00
	SX,\$X13,IC222		31251.33 10	031057.40

12283	LX,\$X0,100Z	-CHECK THAT AN 18 BIT STORE DOES	33311.00 10	031060.00
	LX,\$X1,122K7	-NOT CORRUPT BITS 18-23.	31320.02 10	031060.40
	SX,\$X1,122WRK		31322.03 10	031061.00
	SVA,\$X0,122WRK		31322.01 D0	031061.40
	LX,\$X2,122WRK		31322.04 10	031062.00
	KV,\$X2,122K7		31320.04 90	031062.40
	BXE,\$+1.32		31064.72 C2	031063.00
	SIC,SEN	-SVA INTO FP INST IN EXT MEM	1310.00 80	031063.40
	B,SERS	-CORRUPTS BITS 18-23.	1304.10 00	031064.00
	L%BU□,122K7		31320.00 80 000000.20 50	031064.40
	SVA,\$X0,\$R		11.01 D0	031065.40
	LX,\$X2,\$R		11.04 10	031066.00
	KV,\$X2,122K7		31320.04 90	031066.40
	BXE,\$+1.32		31070.72 C2	031067.00
	SIC,SEN	-SVA INTO FP INST IN INT MEM	1310.00 80	031067.40
	B,SERS	-CORRUPTS BITS 18-23.	1304.10 00	031070.00
	LX,\$X1,122K7		31320.02 10	031070.40
	SVA,\$X0,\$X1		21.01 D0	031071.00
	KV,\$X1,122K7		31320.02 90	031071.40
	BXE,\$+1.32		31073.72 C2	031072.00
	SIC,SEN	-SVA INTO FP INST IN IX CORE STG	1310.00 80	031072.40
	B,SERS	-CORRUPTS BITS 18-23.	1304.10 00	031073.00
	LX,\$X0,100Z	-CHECK THAT A 19 BIT STORE DOES	33311.00 10	031073.40
	LX,\$X1,122K8	-NOT CORRUPT BITS 19-23	31321.02 10	031074.00
	SX,\$X1,122WRK		31322.03 10	031074.40
	SVA,\$X0,122WRK		31322.01 D0	031075.00
	LX,\$X2,122WRK		31322.04 10	031075.40
	KV,\$X2,122K8		31321.04 90	031076.00
	BXE,\$+1.32		31100.32 C2	031076.40
	SIC,SEN	-SVA INTO DIR IX INST IN EXT MEM	1310.00 80	031077.00
	B,SERS	-CORRUPTS BITS 19-23.	1304.10 00	031077.40
	L%BU□,122K8		31321.00 80 000000.20 50	031100.00
	SVA,\$X0,\$R		11.01 D0	031101.00
	LX,\$X2,\$R		11.04 10	031101.40
	KV,\$X2,122K8		31321.04 90	031102.00
	BXE,\$+1.32		31104.32 C2	031102.40
	SIC,SEN	-SVA INTO DIR IX INST IN INT MEM	1310.00 80	031103.00
	B,SERS	-CORRUPTS BITS 19-23.	1304.10 00	031103.40
	SVA,\$X0,\$X1		21.01 D0	031104.00
	KV,\$X1,122K8		31321.02 90	031104.40
	BXE,\$+1.32		31106.72 C2	031105.00
	SIC,SEN	-SVA INTO DIR IX INST IN IX CORE STG	1310.00 80	031105.40
	B,SERS	-CORRUPTS BITS 19-23.	1304.10 00	031106.00
	B,\$+1.0		31107.50 00	031106.40
	BD,12283		31060.04 00	031107.00
	SIC,SENO+.32		1311.40 80	031107.40
	B,SSW	-TO SSIP.	1301.10 00	031110.00
	BD,\$+.32		31111.04 00	031110.40
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	031111.00
	SC,\$X13,\$X12		34.33 50	031111.40
	V+,\$X12,BIT3		33334.30 80	031112.00
	LC,\$X13,\$X12		34.32 50	031112.40
	SX,\$X13,IC222		31251.33 10	031113.00

12284	LX,\$X0,1000	-CHECK SV AND SVA INTO 1.0	33312.00 10	031113.40
	SV,\$X0,1.0		1.01 30	031114.00
	NOP		0.30 00	031114.40
	NOP		0.30 00	031115.00
	NOP		0.30 00	031115.40
	NOP		0.30 00	031116.00
	NOP		0.30 00	031116.40
	LX,\$X1,1.0		1.02 10	031117.00
	KV,\$X1,1000		33312.02 90	031117.40
	BXE,12285		31126.32 C2	031120.00
	SV,\$X0,1.0	-NOT OK, TRY AGAIN SINCE CLOCK	1.01 30	031120.40
	NOP	-MAY HAVE STEPPED.	0.30 00	031121.00
	NOP		0.30 00	031121.40
	NOP		0.30 00	031122.00
	NOP		0.30 00	031122.40
	NOP		0.30 00	031123.00
	LX,\$X1,1.0		1.02 10	031123.40
	KV,\$X1,1000		33312.02 90	031124.00
	BXE,\$+1.32		31126.32 C2	031124.40
	SIC,SEN		1310.00 80	031125.00
	B,SERS	-SV OF ALL ONES TO 1.0 FAILS.	1304.10 00	031125.40
12285	LX,\$X0,100Z		33311.00 10	031126.00
	LX,\$X2,1000		33312.04 10	031126.40
	SV,\$X2,1.0		1.05 30	031127.00
	SV,\$X0,1.0		1.01 30	031127.40
	NOP		0.30 00	031130.00
	NOP		0.30 00	031130.40
	NOP		0.30 00	031131.00
	NOP		0.30 00	031131.40
	NOP		0.30 00	031132.00
	LX,\$X1,1.0		1.02 10	031132.40
	KV,\$X1,100Z		33311.02 90	031133.00
	BXE,12286		31142.72 C2	031133.40
	LX,\$X2,1000		33312.04 10	031134.00
	SV,\$X2,1.0		1.05 30	031134.40
	SV,\$X0,1.0	-NOT OK, TRY AGAIN SINCE CLOCK	1.01 30	031135.00
	NOP	-MAY HAVE STEPPED.	0.30 00	031135.40
	NOP		0.30 00	031136.00
	NOP		0.30 00	031136.40
	NOP		0.30 00	031137.00
	NOP		0.30 00	031137.40
	LX,\$X1,1.0		1.02 10	031140.00
	KV,\$X1,100Z		33311.02 90	031140.40
	BXE,\$+1.32		31142.72 C2	031141.00
	SIC,SEN		1310.00 80	031141.40
	B,SERS	-SV OF ALL ZEROES TO 1.0 FAILS.	1304.10 00	031142.00

12286	LX,\$X0,100Z	-CHECK SV WILL NOT GO TO 1.32.	33311.00 10	031142.40
	LX,\$X1,1000		33312.02 10	031143.00
	LX,\$X2,0.0		0.04 10	031143.40
12286A	SV,\$X0,1.32		1.41 30	031144.00
	LX,\$X3,1.0		1.06 10	031144.40
	SV,\$X1,1.32		1.43 30	031145.00
	LX,\$X4,1.0		1.10 10	031145.40
	SR,\$X3,\$X3	-IF REFILLS OF X3 AND X4 ARE EQUAL,	23.07 70	031146.00
	SR,\$X4,\$X4	-THEN SV TO 1.32 OK. IF NOT SAME	24.11 70	031146.40
	KV,\$X3,\$X4	-THEN REPEAT TEST ONCE MORE.	24.06 90	031147.00
	BXE,12287		31153.72 C2	031147.40
	LX,\$X2,\$X2		22.04 10	031150.00
	BXCZ,\$+1.0		31151.70 42	031150.40
	B,12286B		31152.50 00	031151.00
	LX,\$X2,BIT30		33367.04 10	031151.40
	B,12286A		31144.10 00	031152.00
12286B	SIC,SEN	-SV TO 1.32 PERMITS DATA TO ENTER,	1310.00 80	031152.40
	B,SERS	-SV ERROR.	1304.10 00	031153.00
12287	B,\$+1.0		31154.50 00	031153.40
	BD,12284		31113.44 00	031154.00
	SIC,SEN0+.32		1311.40 80	031154.40
	B,SSW	-TO SSIP.	1301.10 00	031155.00
	BD,\$+.32		31156.04 00	031155.40
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	031156.00
	SC,\$X13,\$X12		34.33 50	031156.40
	V+,\$X12,BIT4		33335.30 B0	031157.00
	LC,\$X13,\$X12		34.32 50	031157.40
	SX,\$X13,IC222		31251.33 10	031160.00
12288	LX,\$X0,1000		33312.00 10	031160.40
	LX,\$X2,100Z		33311.04 10	031161.00
	SV,\$X2,1.0		1.05 30	031161.40
	SVA,\$X0,1.0		1.01 D0	031162.00
	NOP		0.30 00	031162.40
	NOP		0.30 00	031163.00
	NOP		0.30 00	031163.40
	NOP		0.30 00	031164.00
	NOP		0.30 00	031164.40
	LX,\$X1,1.0		1.02 10	031165.00
	KVI,\$X1,%8#1777777		777777.43 04	031165.40
	BXE,12289		31174.32 C2	031166.00
	SV,\$X2,1.0	-NOT OK, TRY AGAIN SINCE CLOCK	1.05 30	031166.40
	SVA,\$X0,1.0	-MAY HAVE STEPPED.	1.01 D0	031167.00
	NOP		0.30 00	031167.40
	NOP		0.30 00	031170.00
	NOP		0.30 00	031170.40
	NOP		0.30 00	031171.00
	LX,\$X1,1.0		1.02 10	031171.40
	KVI,\$X1,%8#1777777		777777.43 04	031172.00
	BXE,\$+1.32		31174.32 C2	031172.40
	SIC,SEN		1310.00 80	031173.00
	B,SERS	-SVA,19 BITS,OF ALL ONES TO 1.0 FAILS.	1304.10 00	031173.40

12289	LX,\$X0,100Z		33311.00 10	031174.00
	SVA,\$X0,1.0		1.01 D0	031174.40
	NOP		0.30 00	031175.00
	NOP		0.30 00	031175.40
	NOP		0.30 00	031176.00
	NOP		0.30 00	031176.40
	NOP		0.30 00	031177.00
	LX,\$X1,1.0		1.02 10	031177.40
	KV,\$X1,100Z	20000	33311.02 90	031200.00
	BXE,12290		31207.32 C2	031200.40
	LX,\$X1,1000.		33312.02 10	031201.00
	SV,\$X1,1.0	-NOT OK, TRY AGAIN SINCE CLOCK	1.03 30	031201.40
	SVA,\$X0,1.0	-MAY HAVE STEPPED.	1.01 D0	031202.00
	NOP		0.30 00	031202.40
	NOP		0.30 00	031203.00
	NOP		0.30 00	031203.40
	NOP		0.30 00	031204.00
	LX,\$X1,1.0		1.02 10	031204.40
	KV,\$X1,100Z		33311.02 90	031205.00
	BXE,\$+1.32		31207.32 C2	031205.40
	SIC,SEN		1310.00 80	031206.00
	B,SERS	-SVA, 19 BITS, ALL ZEROS TO 1.0 FAILS.	1304.10 00	031206.40
12290	LX,\$X0,100Z		33311.00 10	031207.00
	LX,\$X1,122K3D		31310.02 10	031207.40
	SV,\$X0,1.0		1.01 30	031210.00
	SVA,\$X1,1.0		1.03 D0	031210.40
	LX,\$X2,1.0		1.04 10	031211.00
	KV,\$X2,122K3E		31311.04 90	031211.40
	BXL,12291		31216.32 42	031212.00
	SV,\$X0,1.0	-SVA INTO 1.0 ALTERS 19-23 WHEN 1.24	1.01 30	031212.40
	SVA,\$X1,1.0	-IS 0, TRY AGAIN.	1.03 D0	031213.00
	LX,\$X2,1.0		1.04 10	031213.40
	KV,\$X2,122K3E		31311.04 90	031214.00
	BXL,\$+1.32		31216.32 42	031214.40
	SIC,SEN	-SVA INTO 1.0 ALTERS 19-23 WHEN 1.24	1310.00 80	031215.00
	B,SERS	-IS 0.	1304.10 00	031215.40
12291	LX,\$X0,BIT24		33361.00 10	031216.00
	LX,\$X1,122K3D	BIT24	31310.02 10	031216.40
	SV,\$X0,1.0		1.01 30	031217.00
	SVA,\$X1,1.0		1.03 D0	031217.40
	LX,\$X2,1.0		1.04 10	031220.00
	KV,\$X2,100Z		33311.04 90	031220.40
	BXL,12292-.32		31225.32 42	031221.00
	SV,\$X0,1.0	-TRY AGAIN.	1.01 30	031221.40
	SVA,\$X1,1.0		1.03 D0	031222.00
	LX,\$X2,1.0		1.04 10	031222.40
	KV,\$X2,100Z		33311.04 90	031223.00
	BXL,\$+1.32		31225.32 42	031223.40
	SIC,SEN	-SVA INTO 1.0 FAILS TO ALTER 19-23	1310.00 80	031224.00
	B,SERS	-WHEN 1.24 IS 1.	1304.10 00	031224.40

(CLR THE T.C.)

	Z,\$X2		22.22 00	031225.00
12292	LX,\$X0,100Z	-CHK SVA DOES NOT GO TO 1.32.	33311.00 10	031225.40
	LX,\$X1,1000		33312.02 10	031226.00
	SVA,\$X0,1.32		1.41 D0	031226.40
	LX,\$X3,1.0		1.06 10	031227.00
	SVA,\$X1,1.32		1.43 D0	031227.40
	LX,\$X4,1.0		1.10 10	031230.00
	SR,\$X3,\$X3	-IF REFILLS OF X3 AND X4 ARE EQUAL,	23.07 70	031230.40
	SR,\$X4,\$X4	-THEN SVA TO 1.32 OK. IF NOT SAME	24.11 70	031231.00
	KV,\$X3,\$X4	-THEN REPEAT TEST ONCE MORE.	24.06 90	031231.40
	BXE,12293		31236.32 C2	031232.00
	LX,\$X2,\$X2		22.04 10	031232.40
	BXCZ,\$+1.0		31234.30 42	031233.00
	B,12292A		31235.10 00	031233.40
	LX,\$X2,BIT30		33367.04 10	031234.00
	B,12292		31225.50 00	031234.40
12292A	SIC,SEN	-SVA TO 1.32 PERMITS DATA TO ENTER,	1310.00 80	031235.00
	B,SERS	-SVA ERROR,	1304.10 00	031235.40
12293	B,\$+1.0		31237.10 00	031236.00
	BD,12288		31160.44 00	031236.40
	SIC,SEN0+.32		1311.40 80	031237.00
	B,SSW	-TO SSIP.	1301.10 00	031237.40
	BD,\$+.32		31240.44 00	031240.00
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	031240.40
	SC,\$X13,\$X12		34.33 50	031241.00
	V+,\$X12,BIT5		33336.30 B0	031241.40
	LC,\$X13,\$X12		34.32 50	031242.00
	SX,\$X13,IC222		31251.33 10	031242.40
	LX,\$X13,IC222	-UPDATE CONTINUITY CHECK.	31251.32 10	031243.00
	KV,\$X13,ICK222		31252.32 90	031243.40
	SIC,SEN		1310.00 80	031244.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	031244.40
	SC,\$X13,\$X13		35.33 50	031245.00
	LX,\$X12,ICK222		31252.30 10	031245.40
	SC,\$X12,\$X12		34.31 50	031246.00
	KV,\$X13,\$X12		34.32 90	031246.40
	SIC,SEN		1310.00 80	031247.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	031247.40
	B,124		31325.10 00	031250.00
	CNOP		0.30 00	031250.40
IC222	XW,0,0,0	-CONTINUITY REG 1222.	0.00 00 000000.00 00	031251.00
ICK222	XW,%8□777777.77,%8□770000,0		777777.77 OF 600000.00 00	031252.00

CNOP		-CONSTANTS FOR I222						
IBIT0	XW,%8□-400000.0,0,0			400000.00	80	000000.00	00	031253.00
IBIT1	XW,%8□-200000.0,0,0			200000.00	80	000000.00	00	031254.00
IBIT2	XW,%8□-100000.0,0,0			100000.00	80	000000.00	00	031255.00
IBIT3	XW,%8□-40000.0,0,0			40000.00	80	000000.00	00	031256.00
IBIT4	XW,%8□-20000.0,0,0			20000.00	80	000000.00	00	031257.00
IBIT5	XW,%8□-10000.0,0,0			10000.00	80	000000.00	00	031260.00
IBIT6	XW,%8□-4000.00,0,0			4000.00	80	000000.00	00	031261.00
IBIT7	XW,%8□-2000.0,0,0			2000.00	80	000000.00	00	031262.00
IBIT8	XW,%8□-1000.0,0,0			1000.00	80	000000.00	00	031263.00
IBIT9	XW,%8□-400.0,0,0			400.00	80	000000.00	00	031264.00
IBIT10	XW,%8□-200.0,0,0			200.00	80	000000.00	00	031265.00
IBIT11	XW,%8□-100.0,0,0			100.00	80	000000.00	00	031266.00
IBIT12	XW,%8□-40.0,0,0			40.00	80	000000.00	00	031267.00
IBIT13	XW,%8□-20.0,0,0			20.00	80	000000.00	00	031270.00
IBIT14	XW,%8□-10.0,0,0			10.00	80	000000.00	00	031271.00
IBIT15	XW,%8□-4.0,0,0			4.00	80	000000.00	00	031272.00
IBIT16	XW,%8□-2.0,0,0			2.00	80	000000.00	00	031273.00
IBIT17	XW,%8□-1.0,0,0			1.00	80	000000.00	00	031274.00
IBIT18	XW,%8□-0.40,0,0			0.40	80	000000.00	00	031275.00
IBIT19	XW,%8□-0.20,0,0			0.20	80	000000.00	00	031276.00
IBIT20	XW,%8□-0.10,0,0			0.10	80	000000.00	00	031277.00
IBIT21	XW,%8□-0.04,0,0			0.04	80	000000.00	00	031300.00
IBIT22	XW,%8□-0.02,0,0			0.02	80	000000.00	00	031301.00
IBIT23	XW,%8□-0.01,0,0			0.01	80	000000.00	00	031302.00
I22K1	\$L%BU□,0			0.00	80	000000.20	50	031303.00
I22K2	L%N□,0	-FP LOAD.		0.00	60			031304.00
	NOP			0.30	00			031304.40
I22K3A	XW,%8□777777.0,0,0	-TEST WD.		777777.00	00	000000.00	00	031305.00
I22K3B	XW,%8□0.77,0,0	-TEST WD.		0.77	00	000000.00	00	031306.00
I22K3C	XW,0.32,0,0	-TEST WD.		0.40	00	000000.00	00	031307.00
I22K3D	XW,0.31,0,0	-TEST WD.		0.37	00	000000.00	00	031310.00
I22K3E	XW,0.01,0,0	-TEST WD.		0.01	00	000000.00	00	031311.00
I22K3F	XW,-0.63,0,0	-TEST WORD.		0.77	80	000000.00	00	031312.00
I22K3G	XW,-0.31,0,0	-TEST WORD.		0.37	80	000000.00	00	031313.00
I22K4	LV,\$X0,0			0.00	30			031314.00
	NOP			0.30	00			031314.40
I22K4A	\$BMK,0			0.00	42			031315.00
	NOP			0.30	00			031315.40
I22K5	LVI,\$X0,0			0.01	01			031316.00
	NOP			0.30	00			031316.40
I22K6	R,0			0.02	00			031317.00
	NOP			0.30	00			031317.40
I22K7	%8□DD%BU,32,8□,37540					00000037540		031320.00
	NOP			0.30	00			031320.40
I22K8	\$RNX,\$X15,0			0.37	F0			031321.00
	NOP			0.30	00			031321.40
I22WRK	DR%BU,64,8□,1			1.00				031322.00
	XW,0,0,0			0.00	00	000000.00	00	031323.00
I22DMP	XW,0,0,0			0.00	00	000000.00	00	031324.00

----1224---TEST STORE COUNT.

-THIS ROUTINE CHECKS STORE COUNT DATA,BIT BY
-BIT, AND CONTROL.

		-THIS TEST CHECKS THE STORING -OF ALL BIT POSITIONS OF THE -COUNT FIELD ALSO THE RESET -OF BITS 18 TO 24		
124	LX,\$X0,I24NAM SX,\$X0,DPET13 SIC,RET B,IDF1 Z,IC224	-PLACE INDENTIFICATION INTO -SSIP -PRINT ID.	31631.00 10 1437.01 10 1306.40 80 1443.10 00 31627.22 00	031325.00 031325.40 031326.00 031326.40 031327.00
	LX,\$X1,I000 SX,\$X1,I24DUP	-PLACE ONES IN STORE LOCATION	33312.02 10 31632.03 10	031327.40 031330.00
1241	LX,\$X0,BIT45 SC,\$X0,I24DUP KC,\$X0,I24DUP SIC,SEN BZXE,SERS SC,\$X0,\$X2 KC,\$X0,\$X2 SIC,SEN BZXE,SERS SC,\$X0,\$R KC,\$X0,\$R SIC,SEN BZXE,SERS	-TEST BIT 45 -WORD WITH 1 AT BIT 45 -EXT MEM -FAILED TO PROPERLY STORE -INTO BIT 17 OF MEM LOC -INDEX STORAGE -FAILED TO PROPERLY STORE -INTO BIT 17 OF INDEX ST -INTERNAL REGISTER -FAILED TO PROPERLY STORE -INTO BIT 17 OF INT REG	33406.00 10 31632.01 50 31632.01 90 1310.00 80 1304.32 C0 22.01 50 22.01 90 1310.00 80 1304.32 C0 11.01 50 11.01 90 1310.00 80 1304.32 C0	031330.40 031331.00 031331.40 031332.00 031332.40 031333.00 031333.40 031334.00 031334.40 031335.00 031335.40 031336.00 031336.40
1242	LX,\$X0,BIT44 SC,\$X0,I24DUP KC,\$X0,I24DUP SIC,SEN BZXE,SERS SC,\$X0,\$X2 KC,\$X0,\$X2 SIC,SEN BZXE,SERS SC,\$X0,\$R KC,\$X0,\$R SIC,SEN BZXE,SERS B,\$+1.0 BD,I24 SIC,SEN0+.32 B,SSW BD,\$+.32	-TEST BIT 44 -WORD WITH 1 AT BIT 44 -EXT MEM -FAILED TO PROPERLY STORE -INTO BIT 16 OF MEM LOC -INDEX STORAGE -FAILED TO PROPERLY STORE -INTO BIT 16 OF INDEX ST -INTERNAL REGISTER -FAILED TO PROPERLY STORE -INTO BIT 16 OF INT REG	33405.00 10 31632.01 50 31632.01 90 1310.00 80 1304.32 C0 22.01 50 22.01 90 1310.00 80 1304.32 C0 11.01 50 11.01 90 1310.00 80 1304.32 C0 31346.50 00 31325.04 00 1311.40 80 1301.10 00 31350.04 00	031337.00 031337.40 031340.00 031340.40 031341.00 031341.40 031342.00 031342.40 031343.00 031343.40 031344.00 031344.40 031345.00 031345.40 031346.00 031346.40 031347.00 031347.40
	LX,\$X13,IC224 V+,\$X13,BIT0 SX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10 33331.32 B0 31627.33 10	031350.00 031350.40 031351.00
1243	LX,\$X0,BIT43 SC,\$X0,I24DUP KC,\$X0,I24DUP SIC,SEN BZXE,SERS	-TEST BIT 43 -WORD WITH 1 AT BIT 43 -EXT MEM -FAILED TO PROPERLY STORE -INTO BIT 15 OF MEM LOC	33404.00 10 31632.01 50 31632.01 90 1310.00 80 1304.32 C0	031351.40 031352.00 031352.40 031353.00 031353.40

	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031354.00
	KC,\$X0,\$X2		22.01 90	031354.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031355.00
	BZXE,SERS	-INTO BIT 15 OF INDEX ST	1304.32 C0	031355.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031356.00
	KC,\$X0,\$R		11.01 90	031356.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031357.00
	BZXE,SERS	-INTO BIT 15 OF INT REG	1304.32 C0	031357.40
		-TEST BIT 42		
1244	LX,\$X0,BIT42	-WORD WITH 1 AT BIT 42	33403.00 10	031360.00
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031360.40
	KC,\$X0,I24DUP		31632.01 90	031361.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031361.40
	BZXE,SERS	-INTO BIT 14 OF MEM LOC	1304.32 C0	031362.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031362.40
	KC,\$X0,\$X2		22.01 90	031363.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031363.40
	BZXE,SERS	-INTO BIT 14 OF INDEX ST	1304.32 C0	031364.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031364.40
	KC,\$X0,\$R		11.01 90	031365.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031365.40
	BZXE,SERS	-INTO BIT 14 OF INT REG	1304.32 C0	031366.00
		-TEST BIT 41		
1245	LX,\$X0,BIT41	-WORD WITH 1 AT BIT 41	33402.00 10	031366.40
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031367.00
	KC,\$X0,I24DUP		31632.01 90	031367.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031370.00
	BZXE,SERS	-INTO BIT 13 OF MEM LOC	1304.32 C0	031370.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031371.00
	KC,\$X0,\$X2		22.01 90	031371.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031372.00
	BZXE,SERS	-INTO BIT 13 OF INDEX ST	1304.32 C0	031372.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031373.00
	KC,\$X0,\$R		11.01 90	031373.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031374.00
	BZXE,SERS	-INTO BIT 13 OF INT REG	1304.32 C0	031374.40
	B,\$+1.0		31376.10 00	031375.00
	BD,I243		31351.44 00	031375.40
	SIC,SEN0+.32		1311.40 80	031376.00
	B,SSW		1301.10 00	031376.40
	BD,\$+.32		31377.44 00	031377.00
	LX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10	031377.40
	V+,\$X13,BIT1		33332.32 B0	031400.00
	SX,\$X13,IC224		31627.33 10	031400.40
		-TEST BIT 40		
1246	LX,\$X0,BIT40	-WORD WITH 1 AT BIT 40	33401.00 10	031401.00
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031401.40
	KC,\$X0,I24DUP		31632.01 90	031402.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031402.40
	BZXE,SERS	-INTO BIT 12 OF MEM LOC	1304.32 C0	031403.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031403.40

	KC,\$X0,\$X2		22.01 90	031404.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031404.40
	BZXE,SERS	-INTO BIT 12 OF INDEX ST	1304.32 C0	031405.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031405.40
	KC,\$X0,\$R		11.01 90	031406.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031406.40
	BZXE,SERS	-INTO BIT 12 OF INT REG	1304.32 C0	031407.00
		-TEST BIT 39		
1247	LX,\$X0,BIT39	-WORD WITH 1 AT BIT 39	33400.00 10	031407.40
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031410.00
	KC,\$X0,I24DUP		31632.01 90	031410.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031411.00
	BZXE,SERS	-INTO BIT 11 OF MEM LOC	1304.32 C0	031411.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031412.00
	KC,\$X0,\$X2		22.01 90	031412.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031413.00
	BZXE,SERS	-INTO BIT 11 OF INDEX ST	1304.32 C0	031413.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031414.00
	KC,\$X0,\$R		11.01 90	031414.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031415.00
	BZXE,SERS	-INTO BIT 11 OF INT REG	1304.32 C0	031415.40
		-TEST BIT 38		
1248	LX,\$X0,BIT38	-WORD WITH 1 AT BIT 38	33377.00 10	031416.00
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031416.40
	KC,\$X0,I24DUP		31632.01 90	031417.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031417.40
	BZXE,SERS	-INTO BIT 10 OF MEM LOC	1304.32 C0	031420.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031420.40
	KC,\$X0,\$X2		22.01 90	031421.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031421.40
	BZXE,SERS	-INTO BIT 10 OF INDEX ST	1304.32 C0	031422.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031422.40
	KC,\$X0,\$R		11.01 90	031423.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031423.40
	BZXE,SERS	-INTO BIT 10 OF INT REG	1304.32 C0	031424.00
	B,\$+1.0		31425.50 00	031424.40
	BD,I246		31401.04 00	031425.00
	SIC,SENO+.32		1311.40 80	031425.40
	B,\$SW		1301.10 00	031426.00
	BD,\$+.32		31427.04 00	031426.40
	LX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10	031427.00
	V+,\$X13,BIT2		33333.32 B0	031427.40
	SX,\$X13,IC224		31627.33 10	031430.00
		-TEST BIT 37		
1249	LX,\$X0,BIT37	-WORD WITH 1 AT BIT 37	33376.00 10	031430.40

	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031431.00
	KC,\$X0,I24DUP		31632.01 90	031431.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031432.00
	BZXE,SERS	-INTO BIT 9 OF MEM LOC	1304.32 C0	031432.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031433.00
	KC,\$X0,\$X2		22.01 90	031433.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031434.00
	BZXE,SERS	-INTO BIT 9 OF INDEX ST	1304.32 C0	031434.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031435.00
	KC,\$X0,\$R		11.01 90	031435.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031436.00
	BZXE,SERS	-INTO BIT 9 OF INT REG	1304.32 C0	031436.40
		-TEST BIT 36		
12410	LX,\$X0,BIT36	-WORD WITH 1 AT BIT 36	33375.00 10	031437.00
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031437.40
	KC,\$X0,I24DUP		31632.01 90	031440.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031440.40
	BZXE,SERS	-INTO BIT 8 OF MEM LOC	1304.32 C0	031441.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031441.40
	KC,\$X0,\$X2		22.01 90	031442.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031442.40
	BZXE,SERS	-INTO BIT 8 OF INDEX ST	1304.32 C0	031443.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031443.40
	KC,\$X0,\$R		11.01 90	031444.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031444.40
	BZXE,SERS	-INTO BIT 8 OF INT REG	1304.32 C0	031445.00
		-TEST BIT 35		
12411	LX,\$X0,BIT35	-WORD WITH 1 AT BIT 35	33374.00 10	031445.40
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031446.00
	KC,\$X0,I24DUP		31632.01 90	031446.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031447.00
	BZXE,SERS	-INTO BIT 7 OF MEM LOC	1304.32 C0	031447.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031450.00
	KC,\$X0,\$X2		22.01 90	031450.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031451.00
	BZXE,SERS	-INTO BIT 7 OF INDEX ST	1304.32 C0	031451.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031452.00
	KC,\$X0,\$R		11.01 90	031452.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031453.00
	BZXE,SERS	-INTO BIT 7 OF INT REG	1304.32 C0	031453.40

	B,\$+1.0		31455.10 00	031454.00
	BD,I249		31430.44 00	031454.40
	SIC,SENO+.32		1311.40 80	031455.00
	B,SSW		1301.10 00	031455.40
	BD,\$+.32		31456.44 00	031456.00
	LX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10	031456.40
	V+,\$X13,BIT3		33334.32 B0	031457.00
	SX,\$X13,IC224		31627.33 10	031457.40
12412	LX,\$X0,BIT34	-TEST BIT 34	33373.00 10	031460.00
	SC,\$X0,I24DUP	-WORD WITH 1 AT BIT 34	31632.01 50	031460.40
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031461.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031461.40
	BZXE,SERS	-INTO BIT 6 OF MEM LOC	1304.32 C0	031462.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031462.40
	KC,\$X0,\$X2		22.01 90	031463.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031463.40
	BZXE,SERS	-INTO BIT 6 OF INDEX ST	1304.32 C0	031464.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031464.40
	KC,\$X0,\$R		11.01 90	031465.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031465.40
	BZXE,SERS	-INTO BIT 6 OF INT REG	1304.32 C0	031466.00
12413	LX,\$X0,BIT33	-TEST BIT 33	33372.00 10	031466.40
	SC,\$X0,I24DUP	-WORD WITH 1 AT BIT 33	31632.01 50	031467.00
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031467.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031470.00
	BZXE,SERS	-INTO BIT 5 OF MEM LOC	1304.32 C0	031470.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031471.00
	KC,\$X0,\$X2		22.01 90	031471.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031472.00
	BZXE,SERS	-INTO BIT 5 OF INDEX ST	1304.32 C0	031472.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031473.00
	KC,\$X0,\$R		11.01 90	031473.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031474.00
	BZXE,SERS	-INTO BIT 5 OF INT REG	1304.32 C0	031474.40
12414	LX,\$X0,BIT32	-TEST BIT 32	33371.00 10	031475.00
	SC,\$X0,I24DUP	-WORD WITH 1 AT BIT 32	31632.01 50	031475.40
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031476.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031476.40
	BZXE,SERS	-INTO BIT 4 OF MEM LOC	1304.32 C0	031477.00

	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031477.40
	KC,\$X0,\$X2		22.01 90	031500.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031500.40
	BZXE,SERS	-INTO BIT 4 OF INDEX ST	1304.32 C0	031501.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031501.40
	KC,\$X0,\$R		11.01 90	031502.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031502.40
	BZXE,SERS	-INTO BIT 4 OF INT REG	1304.32 C0	031503.00
		-TEST BIT 31		
12415	LX,\$X0,BIT31	-WORD WITH 1 AT BIT 31	33370.00 10	031503.40
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031504.00
	KC,\$X0,I24DUP		31632.01 90	031504.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031505.00
	BZXE,SERS	-INTO BIT 3 OF MEM LOC	1304.32 C0	031505.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031506.00
	KC,\$X0,\$X2		22.01 90	031506.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031507.00
	BZXE,SERS	-INTO BIT 3 OF INDEX ST	1304.32 C0	031507.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031510.00
	KC,\$X0,\$R		11.01 90	031510.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031511.00
	BZXE,SERS	-INTO BIT 3 OF INT REG	1304.32 C0	031511.40
	B,\$+1.0		31513.10 00	031512.00
	BD,I2412		31460.04 00	031512.40
	SIC,SEN0+.32		1311.40 80	031513.00
	B,SSW		1301.10 00	031513.40
	BD,\$+.32		31514.44 00	031514.00
	LX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10	031514.40
	V+,\$X13,BIT4		33335.32 80	031515.00
	SX,\$X13,IC224		31627.33 10	031515.40
		-TEST BIT 30		
12416	LX,\$X0,BIT30	-WORD WITH 1 AT BIT 30	33367.00 10	031516.00
	SC,\$X0,I24DUP	-EXT MEM	31632.01 50	031516.40
	KC,\$X0,I24DUP		31632.01 90	031517.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031517.40
	BZXE,SERS	-INTO BIT 2 OF MEM LOC	1304.32 C0	031520.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031520.40
	KC,\$X0,\$X2		22.01 90	031521.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031521.40
	BZXE,SERS	-INTO BIT 2 OF INDEX ST	1304.32 C0	031522.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031522.40
	KC,\$X0,\$R		11.01 90	031523.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031523.40
	BZXE,SERS	-INTO BIT 2 OF INT REG	1304.32 C0	031524.00

12417	LX,\$X0,BIT29	-TEST BIT 29	33366.00 10	031524.40
	SC,\$X0,I24DUP	-WORD WITH 1 A BIT	31632.01 50	031525.00
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031525.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031526.00
	BZXE,SERS	-INTO BIT 1 OF MEM LOC	1304.32 C0	031526.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031527.00
	KC,\$X0,\$X2		22.01 90	031527.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031530.00
	BZXE,SERS	-INTO BIT 1 OF INDEX ST	1304.32 C0	031530.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031531.00
	KC,\$X0,\$R		11.01 90	031531.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031532.00
	BZXE,SERS	-INTO BIT 1 OF INT REG	1304.32 C0	031532.40
12418	LX,\$X0,BIT28	-TEST BIT 28	33365.00 10	031533.00
	SC,\$X0,I24DUP	-WORD WITH 1 A BIT	31632.01 50	031533.40
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031534.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031534.40
	BZXE,SERS	-INTO BIT 0 OF MEM LOC	1304.32 C0	031535.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031535.40
	KC,\$X0,\$X2		22.01 90	031536.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031536.40
	BZXE,SERS	-INTO BIT 0 OF INDEX ST	1304.32 C0	031537.00
	B,\$+1.0		31540.50 00	031537.40
	BD,I2416		31516.04 00	031540.00
	SIC,SEN0+.32		1311.40 80	031540.40
	B,SSW		1301.10 00	031541.00
	BD,\$+.32		31542.04 00	031541.40
	LX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10	031542.00
	V+,\$X13,BIT5		33336.32 80	031542.40
	SX,\$X13,IC224		31627.33 10	031543.00
12419	LX,\$X0,I000	-TEST ALL ONES	33312.00 10	031543.40
	SC,\$X0,I24DUP	-WORD WITH 1 BITS	31632.01 50	031544.00
	KC,\$X0,I24DUP	-EXT MEM	31632.01 90	031544.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031545.00
	BZXE,SERS	-ALL ONES	1304.32 C0	031545.40
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031546.00
	KC,\$X0,\$X2		22.01 90	031546.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031547.00
	BZXE,SERS	-ALL ONES	1304.32 C0	031547.40
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031550.00

	KC,\$X0,\$R		11.01 90	031550.40
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031551.00
	BZXE,SERS	-ALL ONES	1304.32 C0	031551.40
		-TEST ALL ZEROS		
12420	LX,\$X0,100Z	-WORD WITH BITS	33311.00 10	031552.00
	SC,\$X0,124DUP	-EXT MEM	31632.01 50	031552.40
	KC,\$X0,124DUP		31632.01 90	031553.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031553.40
	BZXE,SERS	-ALL ZEROS	1304.32 C0	031554.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031554.40
	KC,\$X0,\$X2		22.01 90	031555.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031555.40
	BZXE,SERS	-ALL ZEROS	1304.32 C0	031556.00
	SC,\$X0,\$R	-INTERNAL REGISTER	11.01 50	031556.40
	KC,\$X0,\$R		11.01 90	031557.00
	SIC,SEN	-FAILED TO PROPERLY STORE	1310.00 80	031557.40
	BZXE,SERS	-ALL ZEROS	1304.32 C0	031560.00
		-TEST THE RESET OF BIT 18-24		
12421	LX,\$X0,1000	-ALL ONES WORD	33312.00 10	031560.40
	SR,\$X0,\$X1	-SET COMAPRE FIELD	21.01 70	031561.00
	SC,\$X0,124DUP	-EXT MEM	31632.01 50	031561.40
	KV,\$X1,124DUP		31632.02 90	031562.00
	SIC,SEN	-BITS 18 TO 24 ARE NOT ZEROS	1310.00 80	031562.40
	BZXE,SERS	-AFTER SC TO EXT MEM	1304.32 C0	031563.00
	SC,\$X0,\$X2	-INDEX STORAGE	22.01 50	031563.40
	KV,\$X1,\$X2		22.02 90	031564.00
	SIC,SEN	-BITS 18 TO 24 ARE NOT ZEROS	1310.00 80	031564.40
	BZXE,SERS	-AFTER SC TO INDEX ST	1304.32 C0	031565.00
	SC,\$X0,\$R	-INTERNAL REG	11.01 50	031565.40
	KV,\$X1,\$R		11.02 90	031566.00
	SIC,SEN	-BITS 18 TO 24 ARE NOT ZEROS	1310.00 80	031566.40
	BZXE,SERS	-AFTER SC TO INT REG	1304.32 C0	031567.00
	B,\$+1.0		31570.50 00	031567.40
	BD,12419		31543.44 00	031570.00
	SIC,SEN0+.32		1311.40 80	031570.40
	B,SSW		1301.10 00	031571.00
	BD,\$+.32		31572.04 00	031571.40
	LX,\$X13,1C224	-UPDATE CONTINUITY CHECK.	31627.32 10	031572.00
	V+,\$X13,BIT6		33337.32 B0	031572.40
	SX,\$X13,1C224		31627.33 10	031573.00

124100	LX,\$X0,100Z SC,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=0.40 BXH,\$+1.0 B,124101	-CHECK THAT SC WILL STORE 18 -BITS INTO 1.0. -OK	33311.00 10 1.01 50 1.02 10 0.43 04 31576.73 42 31602.10 00	031573.40 031574.00 031574.40 031575.00 031575.40 031576.00
	SC,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=0.40 BXH,\$+1.0 B,\$+1.32 SIC,SEN B,SERS	-S OF ALL ZEROES -TO 1.0 FAILS.	1.01 50 1.02 10 0.43 04 31601.33 42 31602.10 00 1310.00 80 1304.10 00	031576.40 031577.00 031577.40 031600.00 031600.40 031601.00 031601.40
124101	LX,\$X0,1000 SC,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=777777.0 BXE,124102	-OK	33312.00 10 1.01 50 1.02 10 777777.03 04 31607.72 C2	031602.00 031602.40 031603.00 031603.40 031604.00
	SC,\$X0,1.0 LX,\$X1,1.0 KVI,\$X1,%8=777777.0 BXE,\$+1.32 SIC,SEN B,SERS	-S OF ALL ONES -TO 1.0 FAILS.	1.01 50 1.02 10 777777.03 04 31607.72 C2 1310.00 80 1304.10 00	031604.40 031605.00 031605.40 031606.00 031606.40 031607.00
124102	LX,\$X0,100Z LX,\$X1,1000 Z,\$X2	-CHECK NO STORE TO 1.32.	33311.00 10 33312.02 10 22.22 00	031607.40 031610.00 031610.40
124103	SC,\$X0,1.32 LX,\$X3,1.0 SC,\$X1,1.32 LX,\$X4,1.0 SC,\$X3,\$X3 SC,\$X4,\$X4 KV,\$X3,\$X4 BXE,124104 LX,\$X2,\$X2 BXCZ,\$+1.0 B,\$+1.32 LX,\$X2,BIT30 B,124103 SIC,SEN B,SERS	-IF COUNT FLDS OF X3 AND X4 -ARE EQUAL, THEN NO STORE TOOK -PLACE. IF NOT EQUAL, THEN DO -TEST ONCE MORE.	1.41 50 1.06 10 1.43 50 1.10 10 23.07 50 24.11 50 24.06 90 31620.72 C2 22.04 10 31616.70 42 31617.50 00 33367.04 10 31611.10 00 1310.00 80 1304.10 00	031611.00 031611.40 031612.00 031612.40 031613.00 031613.40 031614.00 031614.40 031615.00 031615.40 031616.00 031616.40 031617.00 031617.40 031620.00
124104	B,\$+1.0 BD,124100 SIC,SEN0+.32 B,SSW BD,\$+.32	-TO SSIP.	31621.50 00 31573.44 00 1311.40 80 1301.10 00 31623.04 00	031620.40 031621.00 031621.40 031622.00 031622.40
	LX,\$X13,IC224 V+,\$X13,BIT7 SX,\$X13,IC224	-UPDATE CONTINUITY CHECK.	31627.32 10 33340.32 B0 31627.33 10	031623.00 031623.40 031624.00
	LX,\$X13,IC224 KV,\$X13,ICK224 SIC,SEN BZXE,SERS B,126	-UPDATE CONTINUITY CHECK. -CONTINUITY ERROR.	31627.32 10 31630.32 90 1310.00 80 1304.32 C0 31633.10 00	031624.40 031625.00 031625.40 031626.00 031626.40

CNOP

IC224 XW,0,0,0 -CONTINUITY REG I224.
ICK224 XW,%8□776000.00,0,0
I24NAM %IQSX□DD%BU,64,8□, I224 X
I24DUP DR%BU,64,8□,%1□

0.00 00 000000.00 00 031627.00
776000.00 00 000000.00 00 031630.00
1.00 031631.00
031632.00

-----1226---TEST LOAD VALUE.

		-TEST THE LOADING OF ALL BITS		
		-INTO BITS 0 TO 24 OF INDEX		
		-STORAGE LOADING IS CHECKED		
		-FROM THE THREE TYPE MEMORIES		
126	LX,\$X0,126NAM	-UPDATE IDENTIFICATION IN SSIP	32260.00 10	031633.00
	SX,\$X0,DPET13		1437.01 10	031633.40
	SIC,RET		1306.40 80	031634.00
	B,IDF1	-PRINT ID.	1443.10 00	031634.40
	Z,IC226		32256.22 00	031635.00
1261	LX,\$X0,BIT24	-TEST LV BIT 24	33361.00 10	031635.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 24	21.01 10	031636.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01 10	031636.40
	LV,\$X0,BIT24	-PLACE IN INT REG	33361.00 30	031637.00
	KV,\$X0,BIT24	-FROM EXT MEM	33361.00 90	031637.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031640.00
	BZXE,SERS	-BIT 24 FROM EXT MEM	1304.32 C0	031640.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04 30	031641.00
	KV,\$X2,BIT24		33361.04 90	031641.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031642.00
	BZXE,SERS	-BIT 24 FROM INDEX STORAGE	1304.32 C0	031642.40
	LV,\$X3,\$R	-FROM INT REG	11.06 30	031643.00
	KV,\$X3,BIT24		33361.06 90	031643.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031644.00
	BZXE,SERS	-BIT 24 FROM INT REG	1304.32 C0	031644.40
1262	LX,\$X0,BIT23	-TEST LV BIT 23	33360.00 10	031645.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 23	21.01 10	031645.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01 10	031646.00
	LV,\$X0,BIT23	-PLACE IN INT REG	33360.00 30	031646.40
	KV,\$X0,BIT23	-FROM EXT MEM	33360.00 90	031647.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031647.40
	BZXE,SERS	-BIT 23 FROM EXT MEM	1304.32 C0	031650.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04 30	031650.40
	KV,\$X2,BIT23		33360.04 90	031651.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031651.40
	BZXE,SERS	-BIT 23 FROM INDEX STORAGE	1304.32 C0	031652.00
	LV,\$X3,\$R	-FROM INT REG	11.06 30	031652.40
	KV,\$X3,BIT23		33360.06 90	031653.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031653.40
	BZXE,SERS	-BIT 23 FROM INT REG	1304.32 C0	031654.00
1263	LX,\$X0,BIT22	-TEST LV BIT 22	33357.00 10	031654.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 22	21.01 10	031655.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01 10	031655.40
	LV,\$X0,BIT22	-PLACE IN INT REG	33357.00 30	031656.00
	KV,\$X0,BIT22	-FROM EXT MEM	33357.00 90	031656.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031657.00
	BZXE,SERS	-BIT 22 FROM EXT MEM	1304.32 C0	031657.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04 30	031660.00
	KV,\$X2,BIT22		33357.04 90	031660.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031661.00
	BZXE,SERS	-BIT 22 FROM INDEX STORAGE	1304.32 C0	031661.40
	LV,\$X3,\$R	-FROM INT REG	11.06 30	031662.00
	KV,\$X3,BIT22		33357.06 90	031662.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031663.00
	BZXE,SERS	-BIT 22 FROM INT REG	1304.32 C0	031663.40
1264	LX,\$X0,BIT21	-TEST LV BIT 21	33356.00 10	031664.00
		-WORD WITH 1 IN BIT 21		

SX,\$X0,\$X1	-PLACE IN INDEX STG.	21.01 10	031664.40
SX,\$X0,\$R	-PLACE IN INT REG	11.01 10	031665.00
LV,\$X0,BIT21	-FROM EXT MEM	33356.00 30	031665.40
KV,\$X0,BIT21		33356.00 90	031666.00
SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031666.40
BZXE,SERS	-BIT 21 FROM EXT MEM	1304.32 C0	031667.00
LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04 30	031667.40
KV,\$X2,BIT21		33356.04 90	031670.00
SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031670.40
BZXE,SERS	-BIT 21 FROM INDEX STORAGE	1304.32 C0	031671.00
LV,\$X3,\$R	-FROM INT REG	11.06 30	031671.40
KV,\$X3,BIT21		33356.06 90	031672.00
SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00 80	031672.40
BZXE,SERS	-BIT 21 FROM INT REG	1304.32 C0	031673.00

1265	LX,\$X0,BIT20	-TEST LV BIT 20	33355.00	10	031673.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 20	21.01	10	031674.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031674.40
	LV,\$X0,BIT20	-PLACE IN INT REG	33355.00	30	031675.00
	KV,\$X0,BIT20	-FROM EXT MEM	33355.00	90	031675.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031676.00
	BZXE,SERS	-BIT 20 FROM EXT MEM	1304.32	C0	031676.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031677.00
	KV,\$X2,BIT20		33355.04	90	031677.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031700.00
	BZXE,SERS	-BIT 20 FROM INDEX STORAGE	1304.32	C0	031700.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031701.00
1266	KV,\$X3,BIT20		33355.06	90	031701.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031702.00
	BZXE,SERS	-BIT 20 FROM INT REG	1304.32	C0	031702.40
	LX,\$X0,BIT19	-TEST LV BIT 19	33354.00	10	031703.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 19	21.01	10	031703.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031704.00
	LV,\$X0,BIT19	-PLACE IN INT REG	33354.00	30	031704.40
	KV,\$X0,BIT19	-FROM EXT MEM	33354.00	90	031705.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031705.40
	BZXE,SERS	-BIT 19 FROM EXT MEM	1304.32	C0	031706.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031706.40
	1267	KV,\$X2,BIT19		33354.04	90
SIC,SEN		-FAILED TO PROPERLY LOAD	1310.00	80	031707.40
BZXE,SERS		-BIT 19 FROM INDEX STORAGE	1304.32	C0	031710.00
LV,\$X3,\$R		-FROM INT REG	11.06	30	031710.40
KV,\$X3,BIT19			33354.06	90	031711.00
SIC,SEN		-FAILED TO PROPERLY LOAD	1310.00	80	031711.40
BZXE,SERS		-BIT 19 FROM INT REG	1304.32	C0	031712.00
LX,\$X0,BIT18		-TEST LV BIT 18	33353.00	10	031712.40
SX,\$X0,\$X1		-WORD WITH 1 IN BIT 18	21.01	10	031713.00
SX,\$X0,\$R		-PLACE IN INDEX STG.	11.01	10	031713.40
LV,\$X0,BIT18		-PLACE IN INT REG	33353.00	30	031714.00
KV,\$X0,BIT18		-FROM EXT MEM	33353.00	90	031714.40
1268	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031715.00
	BZXE,SERS	-BIT 18 FROM EXT MEM	1304.32	C0	031715.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031716.00
	KV,\$X2,BIT18		33353.04	90	031716.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031717.00
	BZXE,SERS	-BIT 18 FROM INDEX STORAGE	1304.32	C0	031717.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031720.00
	KV,\$X3,BIT18		33353.06	90	031720.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031721.00
	BZXE,SERS	-BIT 18 FROM INT REG	1304.32	C0	031721.40
	LX,\$X0,BIT17	-TEST LV BIT 17	33352.00	10	031722.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 17	21.01	10	031722.40
SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031723.00	
LV,\$X0,BIT17	-PLACE IN INT REG	33352.00	30	031723.40	
KV,\$X0,BIT17	-FROM EXT MEM	33352.00	90	031724.00	
1269	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031724.40
	BZXE,SERS	-BIT 17 FROM EXT MEM	1304.32	C0	031725.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031725.40
	KV,\$X2,BIT17		33352.04	90	031726.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031726.40
	BZXE,SERS	-BIT 17 FROM INDEX STORAGE	1304.32	C0	031727.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031727.40
	KV,\$X3,BIT17		33352.06	90	031730.00

SIC,SEN
BZXE,SERS

-FAILED TO PROPERLY LOAD
-BIT 17 FROM INT REG

1310.00 80
1304.32 C0

031730.40
031731.00

B,\$+1.0
BD,I261
SIC,SENO+0.32
B,SSW
BD,\$+.32

-TO SSIP.

31732.50 00
31635.44 00
1311.40 80
1301.10 00
31734.04 00

031731.40
031732.00
031732.40
031733.00
031733.40

LX,\$X13,IC226
V+,\$X13,BIT0
SX,\$X13,IC226

-UPDATE CONTINUITY CHECK.

32256.32 10
33331.32 80
32256.33 10

031734.00
031734.40
031735.00

1269	LX,\$X0,BIT16	-TEST LV BIT 16	33351.00	10	031735.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 16	21.01	10	031736.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031736.40
	LV,\$X0,BIT16	-PLACE IN INT REG	33351.00	30	031737.00
	KV,\$X0,BIT16	-FROM EXT MEM	33351.00	90	031737.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031740.00
	BZXE,SERS	-BIT 16 FROM EXT MEM	1304.32	C0	031740.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031741.00
	KV,\$X2,BIT16		33351.04	90	031741.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031742.00
	BZXE,SERS	-BIT 16 FROM INDEX STORAGE	1304.32	C0	031742.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031743.00
	KV,\$X3,BIT16		33351.06	90	031743.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031744.00
BZXE,SERS	-BIT 16 FROM INT REG	1304.32	C0	031744.40	
12610	LX,\$X0,BIT15	-TEST LV BIT 15	33350.00	10	031745.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 15	21.01	10	031745.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031746.00
	LV,\$X0,BIT15	-PLACE IN INT REG	33350.00	30	031746.40
	KV,\$X0,BIT15	-FROM EXT MEM	33350.00	90	031747.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031747.40
	BZXE,SERS	-BIT 15 FROM EXT MEM	1304.32	C0	031750.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031750.40
	KV,\$X2,BIT15		33350.04	90	031751.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031751.40
	BZXE,SERS	-BIT 15 FROM INDEX STORAGE	1304.32	C0	031752.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031752.40
	KV,\$X3,BIT15		33350.06	90	031753.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031753.40
BZXE,SERS	-BIT 15 FROM INT REG	1304.32	C0	031754.00	
12611	LX,\$X0,BIT14	-TEST LV BIT 14	33347.00	10	031754.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 14	21.01	10	031755.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031755.40
	LV,\$X0,BIT14	-PLACE IN INT REG	33347.00	30	031756.00
	KV,\$X0,BIT14	-FROM EXT MEM	33347.00	90	031756.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031757.00
	BZXE,SERS	-BIT 14 FROM EXT MEM	1304.32	C0	031757.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031760.00
	KV,\$X2,BIT14		33347.04	90	031760.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031761.00
	BZXE,SERS	-BIT 14 FROM INDEX STORAGE	1304.32	C0	031761.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031762.00
	KV,\$X3,BIT14		33347.06	90	031762.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031763.00
BZXE,SERS	-BIT 14 FROM INT REG	1304.32	C0	031763.40	
12612	LX,\$X0,BIT13	-TEST LV BIT 13	33346.00	10	031764.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 13	21.01	10	031764.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031765.00
	LV,\$X0,BIT13	-PLACE IN INT REG	33346.00	30	031765.40
	KV,\$X0,BIT13	-FROM EXT MEM	33346.00	90	031766.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031766.40
	BZXE,SERS	-BIT 13 FROM EXT MEM	1304.32	C0	031767.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031767.40
	KV,\$X2,BIT13		33346.04	90	031770.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031770.40
	BZXE,SERS	-BIT 13 FROM INDEX STORAGE	1304.32	C0	031771.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	031771.40
	KV,\$X3,BIT13		33346.06	90	031772.00

	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031772.40
	BZXE,SERS	-BIT 13 FROM INT REG	1304.32	CO	031773.00
12613	LX,\$X0,BIT12	-TEST LV BIT 12	33345.00	10	031773.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT	21.01	10	031774.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	031774.40
	LV,\$X0,BIT12	-PLACE IN INT REG	33345.00	30	031775.00
	KV,\$X0,BIT12	-FROM EXT MEM	33345.00	90	031775.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	031776.00
	BZXE,SERS	-BIT 12 FROM EXT MEM	1304.32	CO	031776.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	031777.00
	KV,\$X2,BIT12		33345.04	90	031777.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032000.00
	BZXE,SERS	-BIT 12 FROM INDEX STORAGE	1304.32	CO	032000.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032001.00
	KV,\$X3,BIT12		33345.06	90	032001.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032002.00
	BZXE,SERS	-BIT 12 FROM INT REG	1304.32	CO	032002.40
12614	LX,\$X0,BIT11	-TEST LV BIT 11	33344.00	10	032003.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT	21.01	10	032003.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032004.00
	LV,\$X0,BIT11	-PLACE IN INT REG	33344.00	30	032004.40
	KV,\$X0,BIT11	-FROM EXT MEM	33344.00	90	032005.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032005.40
	BZXE,SERS	-BIT 11 FROM EXT MEM	1304.32	CO	032006.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032006.40
	KV,\$X2,BIT11		33344.04	90	032007.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032007.40
	BZXE,SERS	-BIT 11 FROM INDEX STORAGE	1304.32	CO	032010.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032010.40
	KV,\$X3,BIT11		33344.06	90	032011.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032011.40
	BZXE,SERS	-BIT 11 FROM INT REG	1304.32	CO	032012.00
12615	LX,\$X0,BIT10	-TEST LV BIT 10	33343.00	10	032012.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 10	21.01	10	032013.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032013.40
	LV,\$X0,BIT10	-PLACE IN INT REG	33343.00	30	032014.00
	KV,\$X0,BIT10	-FROM EXT MEM	33343.00	90	032014.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032015.00
	BZXE,SERS	-BIT 10 FROM EXT MEM	1304.32	CO	032015.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032016.00
	KV,\$X2,BIT10		33343.04	90	032016.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032017.00
	BZXE,SERS	-BIT 10 FROM INDEX STORAGE	1304.32	CO	032017.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032020.00
	KV,\$X3,BIT10		33343.06	90	032020.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032021.00
	BZXE,SERS	-BIT 10 FROM INT REG	1304.32	CO	032021.40
12616	LX,\$X0,BIT9	-TEST LV BIT	33342.00	10	032022.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 9	21.01	10	032022.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032023.00
	LV,\$X0,BIT9	-PLACE IN INT REG	33342.00	30	032023.40
	KV,\$X0,BIT9	-FROM EXT MEM	33342.00	90	032024.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032024.40
	BZXE,SERS	-BIT 9 FROM EXT MEM	1304.32	CO	032025.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032025.40
	KV,\$X2,BIT9		33342.04	90	032026.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032026.40
	BZXE,SERS	-BIT 9 FROM INDEX STORAGE	1304.32	CO	032027.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032027.40
	KV,\$X3,BIT9		33342.06	90	032030.00

SIC,SEN
BZXE,SERS

-FAILED TO PROPERLY LOAD
-BIT 9 FROM INT REG

1310.00 80
1304.32 C0

032030.40
032031.00

B,\$+1.0
BD,I269
SIC,SENO+0.32
B,SSW
BD,\$+.32

-TO SSIP.

32032.50 00
31735.44 00
1311.40 80
1301.10 00
32034.04 00

032031.40
032032.00
032032.40
032033.00
032033.40

LX,\$X13,IC226
V+,\$X13,BIT1
SX,\$X13,IC226

-UPDATE CONTINUITY CHECK.

32256.32 10
33332.32 B0
32256.33 10

032034.00
032034.40
032035.00

12617	LX,\$X0,BIT8	-TEST LV BIT 8	33341.00	10	032035.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 8	21.01	10	032036.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032036.40
	LV,\$X0,BIT8	-PLACE IN INT REG	33341.00	30	032037.00
	KV,\$X0,BIT8	-FROM EXT MEM	33341.00	90	032037.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032040.00
	BZXE,SERS	-BIT 8 FROM EXT MEM	1304.32	C0	032040.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032041.00
	KV,\$X2,BIT8		33341.04	90	032041.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032042.00
	BZXE,SERS	-BIT 8 FROM INDEX STORAGE	1304.32	C0	032042.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032043.00
	KV,\$X3,BIT8		33341.06	90	032043.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032044.00
BZXE,SERS	-BIT 8 FROM INT REG	1304.32	C0	032044.40	
12618	LX,\$X0,BIT7	-TEST LV BIT 7	33340.00	10	032045.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT	21.01	10	032045.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032046.00
	LV,\$X0,BIT7	-PLACE IN INT REG	33340.00	30	032046.40
	KV,\$X0,BIT7	-FROM EXT MEM	33340.00	90	032047.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032047.40
	BZXE,SERS	-BIT 7 FROM EXT MEM	1304.32	C0	032050.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032050.40
	KV,\$X2,BIT7		33340.04	90	032051.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032051.40
	BZXE,SERS	-BIT 7 FROM INDEX STORAGE	1304.32	C0	032052.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032052.40
	KV,\$X3,BIT7		33340.06	90	032053.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032053.40
BZXE,SERS	-BIT 7 FROM INT REG	1304.32	C0	032054.00	
12619	LX,\$X0,BIT6	-TEST LV BIT 6	33337.00	10	032054.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 6	21.01	10	032055.00
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032055.40
	LV,\$X0,BIT6	-PLACE IN INT REG	33337.00	30	032056.00
	KV,\$X0,BIT6	-FROM EXT MEM	33337.00	90	032056.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032057.00
	BZXE,SERS	-BIT 6 FROM EXT MEM	1304.32	C0	032057.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032060.00
	KV,\$X2,BIT6		33337.04	90	032060.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032061.00
	BZXE,SERS	-BIT 6 FROM INDEX STORAGE	1304.32	C0	032061.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032062.00
	KV,\$X3,BIT6		33337.06	90	032062.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032063.00
BZXE,SERS	-BIT 6 FROM INT REG	1304.32	C0	032063.40	
12620	LX,\$X0,BIT5	-TEST LV BIT 5	33336.00	10	032064.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 5	21.01	10	032064.40
	SX,\$X0,\$R	-PLACE IN INDEX STG.	11.01	10	032065.00
	LV,\$X0,BIT5	-PLACE IN INT REG	33336.00	30	032065.40
	KV,\$X0,BIT5	-FROM EXT MEM	33336.00	90	032066.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032066.40
	BZXE,SERS	-BIT 5 FROM EXT MEM	1304.32	C0	032067.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032067.40
	KV,\$X2,BIT5		33336.04	90	032070.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032070.40
	BZXE,SERS	-BIT 5 FROM INDEX STORAGE	1304.32	C0	032071.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032071.40
	KV,\$X3,BIT5		33336.06	90	032072.00

	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032072.40
	BZXE,SERS	-BIT 5 FROM INT REG	1304.32	C0	032073.00
12621	LX,\$X0,BIT4	-TEST LV BIT 4	33335.00	10	032073.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 4	21.01	10	032074.00
	SX,\$X0,\$R	-PLACE IN INDEX STG	11.01	10	032074.40
	LV,\$X0,BIT4	-PLACE IN INT REG	33335.00	30	032075.00
	KV,\$X0,BIT4	-FROM EXT MEM	33335.00	90	032075.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032076.00
	BZXE,SERS	-BIT 4 FROM EXT MEM	1304.32	C0	032076.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032077.00
	KV,\$X2,BIT4		33335.04	90	032077.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032100.00
	BZXE,SERS	-BIT 4 FROM INDEX STORAGE	1304.32	C0	032100.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032101.00
	KV,\$X3,BIT4		33335.06	90	032101.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032102.00
	BZXE,SERS	-BIT 4 FROM INT REG	1304.32	C0	032102.40
12622	LX,\$X0,BIT3	-TEST LV BIT 3	33334.00	10	032103.00
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 3	21.01	10	032103.40
	SX,\$X0,\$R	-PLACE IN INDEX STG	11.01	10	032104.00
	LV,\$X0,BIT3	-PLACE IN INT REG	33334.00	30	032104.40
	KV,\$X0,BIT3	-FROM EXT MEM	33334.00	90	032105.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032105.40
	BZXE,SERS	-BIT 3 FROM EXT MEM	1304.32	C0	032106.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032106.40
	KV,\$X2,BIT3		33334.04	90	032107.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032107.40
	BZXE,SERS	-BIT 3 FROM INDEX STORAGE	1304.32	C0	032110.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032110.40
	KV,\$X3,BIT3		33334.06	90	032111.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032111.40
	BZXE,SERS	-BIT 3 FROM INT REG	1304.32	C0	032112.00
12623	LX,\$X0,BIT2	-TEST LV BIT 2	33333.00	10	032112.40
	SX,\$X0,\$X1	-WORD WITH 1 IN BIT 2	21.01	10	032113.00
	SX,\$X0,\$R	-PLACE IN INDEX STG	11.01	10	032113.40
	LV,\$X0,BIT2	-PLACE IN INT REG	33333.00	30	032114.00
	KV,\$X0,BIT2	-FROM EXT MEM	33333.00	90	032114.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032115.00
	BZXE,SERS	-BIT 2 FROM EXT MEM	1304.32	C0	032115.40
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032116.00
	KV,\$X2,BIT2		33333.04	90	032116.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032117.00
	BZXE,SERS	-BIT 2 FROM INDEX STORAGE	1304.32	C0	032117.40
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032120.00
	KV,\$X3,BIT2		33333.06	90	032120.40
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032121.00
	BZXE,SERS	-BIT 2 FROM INT REG	1304.32	C0	032121.40
12624	LX,\$X0,BIT1	-TEST LV BIT 1	33332.00	10	032122.00
	SX,\$X0,\$R	-WORD WITH 1 IN BIT 1	11.01	10	032122.40
	SX,\$X0,\$X1	-PLACE IN INT REG	21.01	10	032123.00
	LV,\$X0,BIT1	-PLACE IN INDEX STG	33332.00	30	032123.40
	KV,\$X0,BIT1	-FROM EXT MEM	33332.00	90	032124.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032124.40
	BZXE,SERS	-BIT 1 FROM EXT MEM	1304.32	C0	032125.00
	LV,\$X2,\$X1	-FROM INDEX STORAGE	21.04	30	032125.40
	KV,\$X2,BIT1		33332.04	90	032126.00
	SIC,SEN	-FAILED TO PROPERLY LOAD	1310.00	80	032126.40
	BZXE,SERS	-BIT 1 FROM INDEX STORAGE	1304.32	C0	032127.00
	LV,\$X3,\$R	-FROM INT REG	11.06	30	032127.40
	KV,\$X3,BIT1		33332.06	90	032130.00

SIC,SEN -FAILED TO PROPERLY LOAD
BZXE,SERS -BIT 1 FROM INT REG

1310.00 80
1304.32 C0

032130.40
032131.00

B,\$+1.0
BD,I2617
SIC,SENO+0.32
B,SSW
BD,\$+.32

-TO SSIP.

32132.50 00
32035.44 00
1311.40 80
1301.10 00
32134.04 00

032131.40
032132.00
032132.40
032133.00
032133.40

LX,\$X13,IC226
V+,\$X13,BIT2
SX,\$X13,IC226

-UPDATE CONTINUITY CHECK.

32256.32 10
33333.32 B0
32256.33 10

032134.00
032134.40
032135.00

12625	LX,\$X0,BIT0 SX,\$X0,\$R SX,\$X0,\$X1 LV,\$X0,BIT0 KV,\$X0,BIT0 SIC,SEN BZXE,SERS LV,\$X2,\$X1 KV,\$X2,BIT0 SIC,SEN BZXE,SERS LV,\$X3,\$R KV,\$X3,BIT0 SIC,SEN BZXE,SERS	-TEST LV BIT 0 -WORD WITH 1 IN BIT 0 -PLACE IN INT REG -PLACE IN INDEX STG -FROM EXT MEM -FAILED TO PROPERLY LOAD -BIT 0 FROM EXT MEM -FROM INDEX STORAGE -FAILED TO PROPERLY LOAD -BIT 0 FROM INDEX STORAGE -FROM INT REG -FAILED TO PROPERLY LOAD -BIT 0 FROM INT REG -TEST LV ALL ONES	33331.00 10 11.01 10 21.01 10 33331.00 30 33331.00 90 1310.00 80 1304.32 C0 21.04 30 33331.04 90 1310.00 80 1304.32 C0 11.06 30 33331.06 90 1310.00 80 1304.32 C0	032135.40 032136.00 032136.40 032137.00 032137.40 032140.00 032140.40 032141.00 032141.40 032142.00 032142.40 032143.00 032143.40 032144.00 032144.40
12626	LX,\$X0,1000 SX,\$X0,\$X1 SX,\$X0,\$R LV,\$X0,1000 KV,\$X0,1000 SIC,SEN BZXE,SERS LV,\$X2,\$X1 KV,\$X2,1000 SIC,SEN BZXE,SERS LV,\$X3,\$R KV,\$X3,1000 SIC,SEN BZXE,SERS	-WORD WITH 1 IN BIT 0-63 -PLACE IN INDEX STORAGE -PLACE IN INT REG -FROM EXT MEM -FAILED TO PROPERLY LOAD ALL -BITS FROM EXT MEM -FROM INDEX STORAGE -FAILED TO PROPERLY LOAD ALL -BITS FROM INDEX STORAGE -FROM INT REG -FAILED TO PROPERLY LOAD ALL -BITS FROM INT REG	33312.00 10 21.01 10 11.01 10 33312.00 30 33312.00 90 1310.00 80 1304.32 C0 21.04 30 33312.04 90 1310.00 80 1304.32 C0 11.06 30 33312.06 90 1310.00 80 1304.32 C0	032145.00 032145.40 032146.00 032146.40 032147.00 032147.40 032150.00 032150.40 032151.00 032151.40 032152.00 032152.40 032153.00 032153.40 032154.00
12627	LX,\$X0,100Z SX,\$X0,\$X1 SX,\$X0,\$R LV,\$X0,100Z KV,\$X0,100Z SIC,SEN BZXE,SERS LV,\$X2,\$X1 KV,\$X2,100Z SIC,SEN BZXE,SERS LV,\$X3,\$R KV,\$X3,100Z SIC,SEN BZXE,SERS B,\$+1.0 BD,12625 SIC,SEN0+0.32 B,SSW BD,\$+.32 LX,\$X13,IC226 V+,\$X13,BIT3 SX,\$X13,IC226 B,12628	-TEST LV ALL ZEROS -WORD WITH 0 BITS -PLACE IN INDEX STORAGE -PLACE IN INT REG -FROM EXT MEM -FAILED TO PROPERLY LOAD ALL -ZEROS FROM EXT MEM -FROM INDEX STORAGE -FAILED TO PROPERLY LOAD ALL -ZERO FROM INDEX STORAGE -FROM INT REG -FAILED TO PROPERLY ALL -ZEROS FROM INT REG -UPDATE CONTINUITY CHECK.	33311.00 10 21.01 10 11.01 10 33311.00 30 33311.00 90 1310.00 80 1304.32 C0 21.04 30 33311.04 90 1310.00 80 1304.32 C0 11.06 30 33311.06 90 1310.00 80 1304.32 C0 32165.10 00 32135.44 00 1311.40 80 1301.10 00 32166.44 00 32256.32 10 33334.32 B0 32256.33 10 32170.50 00	032154.40 032155.00 032155.40 032156.00 032156.40 032157.00 032157.40 032160.00 032160.40 032161.00 032161.40 032162.00 032162.40 032163.00 032163.40 032164.00 032164.40 032165.00 032165.40 032166.00 032166.40 032167.00 032167.40 032170.00

12628	LX,\$X0,1000	-TEST LVI BITS 0-24 RESET	33312.00	10	032170.40
	LVI,\$X0,%8#000000.0	-ALL ONES	0.01	01	032171.00
	KV,\$X0,100Z	-0 BITS IN POSITIONS 0 TO 24	33311.00	90	032171.40
	SIC,SEN	-WORD WITH 0 BITS	1310.00	80	032172.00
	BZXE,SERS	-BIT 0-24 DID NOT LOAD PROPERLY	1304.32	C0	032172.40
		-TEST LVI BIT 18			
	LVI,\$X0,%8#0.40		0.41	01	032173.00
	KV,\$X0,BIT18	-WORD WITH BIT IN 18	33353.00	90	032173.40
	SIC,SEN		1310.00	80	032174.00
	BZXE,SERS	-BIT 18 DID NOT LOAD PROPERLY	1304.32	C0	032174.40
		-TEST LVI BIT 17			
	LVI,\$X0,%8#000001.	-BIT IN POSITION 17	1.01	01	032175.00
	KV,\$X0,BIT17	-WORD WITH BIT IN 17	33352.00	90	032175.40
	SIC,SEN		1310.00	80	032176.00
	BZXE,SERS	-BIT 17 DID NOT LOAD PROPERLY	1304.32	C0	032176.40
		-TEST LVI BIT 16			
12629	LVI,\$X0,%8#000002.	-BIT IN POSITION 16	2.01	01	032177.00
	KV,\$X0,BIT16	-WORD WITH BIT IN 16	33351.00	90	032177.40
	SIC,SEN		1310.00	80	032200.00
	BZXE,SERS	-BIT 16 DID NOT LOAD PROPERLY	1304.32	C0	032200.40
		-TEST LVI BIT 15			
	LVI,\$X0,%8#000004.	-BIT IN POSITION 15	4.01	01	032201.00
	KV,\$X0,BIT15	-WORD WITH BIT IN 15	33350.00	90	032201.40
	SIC,SEN		1310.00	80	032202.00
	BZXE,SERS	-BIT 15 DID NOT LOAD PROPERLY	1304.32	C0	032202.40
		-TEST LVI BIT 14			
	LVI,\$X0,%8#000010.	-BIT IN POSITION 14	10.01	01	032203.00
	KV,\$X0,BIT14	-WORD WITH BIT IN 14	33347.00	90	032203.40
	SIC,SEN		1310.00	80	032204.00
	BZXE,SERS	-BIT 14 DID NOT LOAD PROPERLY	1304.32	C0	032204.40
		-TEST LVI BIT 13			
12630	LVI,\$X0,%8#000020.	-BIT IN POSITION 13	20.01	01	032205.00
	KV,\$X0,BIT13	-WORD WITH BIT IN 13	33346.00	90	032205.40
	SIC,SEN		1310.00	80	032206.00
	BZXE,SERS	-BIT 13 DID NOT LOAD PROPERLY	1304.32	C0	032206.40
		-TEST LVI BIT 12			
	LVI,\$X0,%8#000040.	-BIT IN POSITION 12	40.01	01	032207.00
	KV,\$X0,BIT12	-WORD WITH BIT IN 12	33345.00	90	032207.40
	SIC,SEN		1310.00	80	032210.00
	BZXE,SERS	-BIT 13 DID NOT LOAD PROPERLY	1304.32	C0	032210.40
		-TEST LVI BIT 11			
	LVI,\$X0,%8#000100.	-BIT IN POSITION 11	100.01	01	032211.00
	KV,\$X0,BIT11	-WORD WITH BIT IN 11	33344.00	90	032211.40
	SIC,SEN		1310.00	80	032212.00
	BZXE,SERS	-BIT 11 DID NOT LOAD PROPERLY	1304.32	C0	032212.40

12631	LVI,\$X0,%8=000200.	-TEST LVI BIT 10	200.01 01	032213.00
	KV,\$X0,BIT10	-BIT IN POSITION 10	33343.00 90	032213.40
	SIC,SEN	-WORD WITH BIT IN 10	1310.00 80	032214.00
	BZXE,SERS	-BIT 10 DID NOT LOAD PROPERLY	1304.32 C0	032214.40
	LVI,\$X0,%8=000400.	-TEST LVI BIT 9	400.01 01	032215.00
	KV,\$X0,BIT9	-BIT IN POSITION 9	33342.00 90	032215.40
	SIC,SEN	-WORD WITH BIT IN 9	1310.00 80	032216.00
	BZXE,SERS	-BIT 9 DID NOT LOAD PROPERLY	1304.32 C0	032216.40
	LVI,\$X0,%8=001000.	-TEST LVI BIT 8	1000.01 01	032217.00
	KV,\$X0,BIT8	-BIT ON POSITION 8	33341.00 90	032217.40
	SIC,SEN	-WORD WITH BIT IN 8	1310.00 80	032220.00
	BZXE,SERS	-BIT 8 DID NOT LOAD PROPERLY	1304.32 C0	032220.40
12632	LVI,\$X0,%8=002000.	-TEST LVI BIT 7	2000.01 01	032221.00
	KV,\$X0,BIT7	-BIT IN POSITION 7	33340.00 90	032221.40
	SIC,SEN	-WORD WITH BIT IN 7	1310.00 80	032222.00
	BZXE,SERS	-BIT 7 DID NOT LOAD PROPERLY	1304.32 C0	032222.40
	LVI,\$X0,%8=004000.	-TEST LVI BIT 6	4000.01 01	032223.00
	KV,\$X0,BIT6	-BIT IN POSITION 6	33337.00 90	032223.40
	SIC,SEN	-WORD WITH BIT IN 6	1310.00 80	032224.00
	BZXE,SERS	-BIT 6 DID NOT LOAD PROPERLY	1304.32 C0	032224.40
	LVI,\$X0,%8=010000.	-TEST LVI BIT 5	10000.01 01	032225.00
	KV,\$X0,BIT5	-BIT IN POSITION 5	33336.00 90	032225.40
	SIC,SEN	-WORD WITH BIT IN 5	1310.00 80	032226.00
	BZXE,SERS	-BIT 5 DID NOT LOAD PROPERLY	1304.32 C0	032226.40
12633	LVI,\$X0,%8=020000.	-TEST LVI BIT 4	20000.01 01	032227.00
	KV,\$X0,BIT4	-BIT IN POSITION 4	33335.00 90	032227.40
	SIC,SEN	-WORD WITH BIT IN 4	1310.00 80	032230.00
	BZXE,SERS	-BIT 4 DID NOT LOAD PROPERLY	1304.32 C0	032230.40
	LVI,\$X0,%8=040000.	-TEST LVI BIT 3	40000.01 01	032231.00
	KV,\$X0,BIT3	-BIT IN POSITION 3	33334.00 90	032231.40
	SIC,SEN	-WORD WITH BIT IN 3	1310.00 80	032232.00
	BZXE,SERS	-BIT 3 DID NOT LOAD PROPERLY	1304.32 C0	032232.40

	LVI,\$X0,%8=100000.	-TEST LVI BIT 2	100000.01	01	032233.00
	KV,\$X0,BIT2	-BIT IN POSITION 2	33333.00	90	032233.40
	SIC,SEN	-WORD WITH BIT IN 2	1310.00	80	032234.00
	BZXE,SERS	-BIT 2 DID NOT LOAD PROPERLY	1304.32	CO	032234.40
12634	LVI,\$X0,%8=200000.	-TEST LVI BIT 1	200000.01	01	032235.00
	KV,\$X0,BIT1	-BIT IN POSITION 1	33332.00	90	032235.40
	SIC,SEN	-WORD WITH BIT IN 1	1310.00	80	032236.00
	BZXE,SERS	-BIT 1 DID NOT LOAD PROPERLY	1304.32	CO	032236.40
	LVI,\$X0,%8=400000.	-TEST LVI BIT 0	400000.01	01	032237.00
	KV,\$X0,BIT0	-BIT IN POSITION 0	33331.00	90	032237.40
	SIC,SEN	-WORD WITH BIT IN 0	1310.00	80	032240.00
	BZXE,SERS	-BIT 0 DID NOT LOAD PROPERLY	1304.32	CO	032240.40
	LVI,\$X0,%8=777777.40	-TEST LVI ALL ONES	777777.41	01	032241.00
	KV,\$X0,I26K	-WORD WITH BITS IN 0 THRU 18.	32261.00	90	032241.40
	SIC,SEN		1310.00	80	032242.00
	BZXE,SERS	-ALL ONES DID NOT LOAD PROPERLY	1304.32	CO	032242.40
	B,\$+1.0		32244.10	00	032243.00
	BD,I2628		32170.44	00	032243.40
	SIC,SEN0+0.32		1311.40	80	032244.00
	B,SSW		1301.10	00	032244.40
12635	LVNI,\$X0,1.0	-TEST LVNI	1.01	09	032245.00
	KV,\$X0,BIT17		33352.00	90	032245.40
	SIC,SEN		1310.00	80	032246.00
	BZXL,SERS	-LVNI NOT SETTING BIT 24.	1304.32	40	032246.40
	B,\$+1.0		32250.10	00	032247.00
	BD,I2635		32245.04	00	032247.40
	SIC,SEN0+0.32		1311.40	80	032250.00
	B,SSW		1301.10	00	032250.40
	BD,\$+.32		32251.44	00	032251.00
	LX,\$X13,IC226	-UPDATE CONTINUITY CHECK.	32256.32	10	032251.40
	V+,\$X13,BIT4		33335.32	80	032252.00
	SX,\$X13,IC226		32256.33	10	032252.40
	LX,\$X13,IC226	-UPDATE CONTINUITY CHECK.	32256.32	10	032253.00
	KV,\$X13,ICK226		32257.32	90	032253.40
	SIC,SEN		1310.00	80	032254.00
	BZXE,SERS	-CONTINUITY ERROR.	1304.32	CO	032254.40
	B,I28		32261.50	00	032255.00
	CNOP		0.30	00	032255.40
	CNOP				
IC226	XW,0,0,0	-CONTINUITY REG I226.	0.00	00 000000.00 00	032256.00
ICK226	XW,%8=760000.00,0,0		760000.00	00 000000.00 00	032257.00
126NAM	%IQSZ=DD%BU,64,8=,I226	Z			032260.00
126K	VF,%8=777777.40		777777.40+		032261.00

-----1228---TEST LOAD COUNT AND LOAD COUNT IMMED.

ID	Instruction	Description	Count	Time	Address
		-TEST CORRECT LOADING OF EVERY -POSITION OF COUNT FIELD			
128	LX,\$X0,I28ID	-UPDATE IDENTIFICATION	32633.00	10	032261.40
	SX,\$X0,DPET13		1437.01	10	032262.00
	SIC,RET		1306.40	80	032262.40
	B,IDF1		1443.10	00	032263.00
	Z,IC228		32631.22	00	032263.40
1281	LX,\$X0,BIT17	-TEST BIT 45	33352.00	10	032264.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032264.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032265.00
	LC,\$X0,BIT17	-PLACE IN INTERNAL REG	33352.00	50	032265.40
	KC,\$X0,BIT17		33352.01	90	032266.00
	SIC,SEN	-FAILED TO LOAD BIT 45	1310.00	80	032266.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032267.00
	LC,\$X0,\$X1		21.00	50	032267.40
	KC,\$X0,BIT17		33352.01	90	032270.00
	SIC,SEN	-FAILED TO LOAD BIT 45	1310.00	80	032270.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032271.00
	LC,\$X0,\$R		11.00	50	032271.40
	KC,\$X0,BIT17		33352.01	90	032272.00
	SIC,SEN	-FAILED TO LOAD BIT 45	1310.00	80	032272.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032273.00
1282	LX,\$X0,BIT16	-TEST BIT 44	33351.00	10	032273.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032274.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032274.40
	LC,\$X0,BIT16	-PLACE IN INTERNAL REG	33351.00	50	032275.00
	KC,\$X0,BIT16		33351.01	90	032275.40
	SIC,SEN	-FAILED TO LOAD BIT 44	1310.00	80	032276.00
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032276.40
	LC,\$X0,\$X1		21.00	50	032277.00
	KC,\$X0,BIT16		33351.01	90	032277.40
	SIC,SEN	-FAILED TO LOAD BIT 44	1310.00	80	032300.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032300.40
	LC,\$X0,\$R		11.00	50	032301.00
	KC,\$X0,BIT16		33351.01	90	032301.40
	SIC,SEN	-FAILED TO LOAD BIT 44	1310.00	80	032302.00
	BZXE,SERS	-FROM INDEX	1304.32	C0	032302.40
1283	LX,\$X0,BIT15	-TEST BIT 43	33350.00	10	032303.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032303.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032304.00
	LC,\$X0,BIT15	-PLACE IN INTERNAL REG	33350.00	50	032304.40
	KC,\$X0,BIT15		33350.01	90	032305.00
	SIC,SEN	-FAILED TO LOAD BIT 43	1310.00	80	032305.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032306.00
	LC,\$X0,\$X1		21.00	50	032306.40
	KC,\$X0,BIT15		33350.01	90	032307.00
	SIC,SEN	-FAILED TO LOAD BIT 43	1310.00	80	032307.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032310.00
	LC,\$X0,\$R		11.00	50	032310.40
	KC,\$X0,BIT15		33350.01	90	032311.00
	SIC,SEN	-FAILED TO LOAD BIT 43	1310.00	80	032311.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032312.00
	B,\$+1.0		32313.50	00	032312.40
	BD,I281		32264.04	00	032313.00
	SIC,SEN0+0.32		1311.40	80	032313.40

B,SSW
BD,\$+.32

LX,\$X13,IC228
V+,\$X13,BIT0
SX,\$X13,IC228
B,I284

-UPDATE CONTINUITY CHECK.

1301.10 00
32315.04 00
-
32631.32 10
33331.32 B0
32631.33 10
32317.10 00

032314.00
032314.40
032315.00
032315.40
032316.00
032316.40

1284	LX,\$X0,BIT14	-TEST BIT 42	33347.00	10	032317.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032317.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032320.00
	LC,\$X0,BIT14	-PLACE IN INTERNAL REG	33347.00	50	032320.40
	KC,\$X0,BIT14		33347.01	90	032321.00
	SIC,SEN	-FAILED TO LOAD BIT 42	1310.00	80	032321.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032322.00
	LC,\$X0,\$X1		21.00	50	032322.40
	KC,\$X0,BIT14		33347.01	90	032323.00
	SIC,SEN	-FAILED TO LOAD BIT 42	1310.00	80	032323.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032324.00
	LC,\$X0,\$R		11.00	50	032324.40
1285	KC,\$X0,BIT14		33347.01	90	032325.00
	SIC,SEN	-FAILED TO LOAD BIT 42	1310.00	80	032325.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032326.00
	LX,\$X0,BIT13	-TEST BIT 41	33346.00	10	032326.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032327.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032327.40
	LC,\$X0,BIT13	-PLACE IN INTERNAL REG	33346.00	50	032330.00
	KC,\$X0,BIT13		33346.01	90	032330.40
	SIC,SEN	-FAILED TO LOAD BIT 41	1310.00	80	032331.00
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032331.40
	LC,\$X0,\$X1		21.00	50	032332.00
	KC,\$X0,BIT13		33346.01	90	032332.40
SIC,SEN	-FAILED TO LOAD BIT 41	1310.00	80	032333.00	
BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032333.40	
LC,\$X0,\$R		11.00	50	032334.00	
1286	KC,\$X0,BIT13		33346.01	90	032334.40
	SIC,SEN	-FAILED TO LOAD BIT 41	1310.00	80	032335.00
	BZXE,SERS	-FROM INDEX	1304.32	C0	032335.40
	LX,\$X0,BIT12	-TEST BIT 40	33345.00	10	032336.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032336.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032337.00
	LC,\$X0,BIT12	-PLACE IN INTERNAL REG	33345.00	50	032337.40
	KC,\$X0,BIT12		33345.01	90	032340.00
	SIC,SEN	-FAILED TO LOAD BIT 40	1310.00	80	032340.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032341.00
	LC,\$X0,\$X1		21.00	50	032341.40
	KC,\$X0,BIT12		33345.01	90	032342.00
SIC,SEN	-FAILED TO LOAD BIT 40	1310.00	80	032342.40	
BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032343.00	
LC,\$X0,\$R		11.00	50	032343.40	
1287	KC,\$X0,BIT12		33345.01	90	032344.00
	SIC,SEN	-FAILED TO LOAD BIT 40	1310.00	80	032344.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032345.00
	B,\$+1.0		32346.50	00	032345.40
	BD,I284		32317.04	00	032346.00
	SIC,SEN0+0.32		1311.40	80	032346.40
	B,\$SSW		1301.10	00	032347.00
	BD,\$+.32		32350.04	00	032347.40
	LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32	10	032350.00
	V+,\$X13,BIT1		33332.32	B0	032350.40
	SX,\$X13,IC228		32631.33	10	032351.00
	B,I287		32352.10	00	032351.40

1287	LX,\$X0,BIT11	-TEST BIT 39		
	SX,\$X0,\$X1	-OPERAND USED IN LC	33344.00 10	032352.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	21.01 10	032352.40
	LC,\$X0,BIT11	-PLACE IN INTERNAL REG	11.01 10	032353.00
	KC,\$X0,BIT11		33344.00 50	032353.40
	SIC,SEN	-FAILED TO LOAD BIT 39	33344.01 90	032354.00
	BZXE,SERS	-FROM EXT MEM	1310.00 80	032354.40
	LC,\$X0,\$X1		1304.32 C0	032355.00
	KC,\$X0,BIT11		21.00 50	032355.40
	SIC,SEN	-FAILED TO LOAD BIT 39	33344.01 90	032356.00
	BZXE,SERS	-FROM INDEX STORAGE	1310.00 80	032356.40
	LC,\$X0,\$R		1304.32 C0	032357.00
1288	KC,\$X0,BIT11		11.00 50	032357.40
	SIC,SEN	-FAILED TO LOAD BIT 39	33344.01 90	032360.00
	BZXE,SERS	-FROM INDEX	1310.00 80	032360.40
	LX,\$X0,BIT10	-TEST BIT 38	1304.32 C0	032361.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	33343.00 10	032361.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	21.01 10	032362.00
	LC,\$X0,BIT10	-PLACE IN INTERNAL REG	11.01 10	032362.40
	KC,\$X0,BIT10		33343.00 50	032363.00
	SIC,SEN	-FAILED TO LOAD BIT 38	33343.01 90	032363.40
	BZXE,SERS	-FROM EXT MEM	1310.00 80	032364.00
	LC,\$X0,\$X1		1304.32 C0	032364.40
	1289	KC,\$X0,BIT10		21.00 50
SIC,SEN		-FAILED TO LOAD BIT 38	33343.01 90	032365.40
BZXE,SERS		-FROM INDEX STORAGE	1310.00 80	032366.00
LC,\$X0,\$R			1304.32 C0	032366.40
KC,\$X0,BIT10			11.00 50	032367.00
SIC,SEN		-FAILED TO LOAD BIT 38	33343.01 90	032367.40
BZXE,SERS		-FROM INDEX	1310.00 80	032370.00
LC,\$X0,\$R		-TEST BIT 37	1304.32 C0	032370.40
KC,\$X0,BIT9		-OPERAND USED IN LC	33342.00 10	032371.00
SIC,SEN		-PLACE IN INDEX STORAGE	21.01 10	032371.40
BZXE,SERS		-PLACE IN INTERNAL REG	11.01 10	032372.00
LC,\$X0,\$X1			33342.00 50	032372.40
KC,\$X0,BIT9		33342.01 90	032373.00	
SIC,SEN	-FAILED TO LOAD BIT 37	1310.00 80	032373.40	
BZXE,SERS	-FROM EXT MEM	1304.32 C0	032374.00	
LC,\$X0,\$X1		21.00 50	032374.40	
KC,\$X0,BIT9		33342.01 90	032375.00	
SIC,SEN	-FAILED TO LOAD BIT 37	1310.00 80	032375.40	
BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032376.00	
LC,\$X0,\$R		11.00 50	032376.40	
KC,\$X0,BIT9		33342.01 90	032377.00	
SIC,SEN	-FAILED TO LOAD BIT 37	1310.00 80	032377.40	
BZXE,SERS	-FROM INDEX	1304.32 C0	032400.00	
B,\$+1.0		32401.50 00	032400.40	
BD,I287		32352.04 00	032401.00	
SIC,SEN0+0.32		1311.40 80	032401.40	
B,\$SW		1301.10 00	032402.00	
BD,\$+.32		32403.04 00	032402.40	
LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32 10	032403.00	
V+,\$X13,BIT2		33333.32 B0	032403.40	
SX,\$X13,IC228		32631.33 10	032404.00	
B,I2810		32405.10 00	032404.40	

12810	LX,\$X0,BIT8	-TEST BIT 36	33341.00	10	032405.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032405.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032406.00
	LC,\$X0,BIT8	-PLACE IN INTERNAL REG	33341.00	50	032406.40
	KC,\$X0,BIT8		33341.01	90	032407.00
	SIC,SEN	-FAILED TO LOAD BIT 36	1310.00	80	032407.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032410.00
	LC,\$X0,\$X1		21.00	50	032410.40
	KC,\$X0,BIT8		33341.01	90	032411.00
	SIC,SEN	-FAILED TO LOAD BIT 36	1310.00	80	032411.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032412.00
	LC,\$X0,\$R		11.00	50	032412.40
12811	KC,\$X0,BIT8		33341.01	90	032413.00
	SIC,SEN	-FAILED TO LOAD BIT 36	1310.00	80	032413.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032414.00
	LX,\$X0,BIT7	-TEST BIT 35	33340.00	10	032414.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032415.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032415.40
	LC,\$X0,BIT7	-PLACE IN INTERNAL REG	33340.00	50	032416.00
	KC,\$X0,BIT7		33340.01	90	032416.40
	SIC,SEN	-FAILED TO LOAD BIT 35	1310.00	80	032417.00
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032417.40
	LC,\$X0,\$X1		21.00	50	032420.00
	12812	KC,\$X0,BIT7		33340.01	90
SIC,SEN		-FAILED TO LOAD BIT 35	1310.00	80	032421.00
BZXE,SERS		-FROM INDEX STORAGE	1304.32	C0	032421.40
LC,\$X0,\$R			11.00	50	032422.00
KC,\$X0,BIT7			33340.01	90	032422.40
SIC,SEN		-FAILED TO LOAD BIT 35	1310.00	80	032423.00
BZXE,SERS		-FROM INDEX	1304.32	C0	032423.40
LX,\$X0,BIT6		-TEST BIT 34	33337.00	10	032424.00
SX,\$X0,\$X1		-OPERAND USED IN LC	21.01	10	032424.40
SX,\$X0,\$R		-PLACE IN INDEX STORAGE	11.01	10	032425.00
LC,\$X0,BIT6		-PLACE IN INTERNAL REG	33337.00	50	032425.40
KC,\$X0,BIT6			33337.01	90	032426.00
SIC,SEN	-FAILED TO LOAD BIT 34	1310.00	80	032426.40	
BZXE,SERS	-FROM EXT MEM	1304.32	C0	032427.00	
LC,\$X0,\$X1		21.00	50	032427.40	
KC,\$X0,BIT6		33337.01	90	032430.00	
SIC,SEN	-FAILED TO LOAD BIT 34	1310.00	80	032430.40	
BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032431.00	
LC,\$X0,\$R		11.00	50	032431.40	
KC,\$X0,BIT6		33337.01	90	032432.00	
SIC,SEN	-FAILED TO LOAD BIT 34	1310.00	80	032432.40	
BZXE,SERS	-FROM INDEX	1304.32	C0	032433.00	
B,\$+1.0		32434.50	00	032433.40	
BD,12810		32405.04	00	032434.00	
SIC,SEN0+0.32		1311.40	80	032434.40	
B,SSW		1301.10	00	032435.00	
BD,\$+.32		32436.04	00	032435.40	
LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32	10	032436.00	
V+,\$X13,BIT3		33334.32	B0	032436.40	
SX,\$X13,IC228		32631.33	10	032437.00	
B,12813		32440.10	00	032437.40	

12813	LX,\$X0,BIT5	-TEST BIT 33	33336.00	10	032440.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032440.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032441.00
	LC,\$X0,BIT5	-PLACE IN INTERNAL REG	33336.00	50	032441.40
	KC,\$X0,BIT5		33336.01	90	032442.00
	SIC,SEN	-FAILED TO LOAD BIT 33	1310.00	80	032442.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032443.00
	LC,\$X0,\$X1		21.00	50	032443.40
	KC,\$X0,BIT5		33336.01	90	032444.00
	SIC,SEN	-FAILED TO LOAD BIT 33	1310.00	80	032444.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032445.00
	LC,\$X0,\$R		11.00	50	032445.40
	KC,\$X0,BIT5		33336.01	90	032446.00
	SIC,SEN	-FAILED TO LOAD BIT 33	1310.00	80	032446.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032447.00
12814	LX,\$X0,BIT4	-TEST BIT 32	33335.00	10	032447.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032450.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032450.40
	LC,\$X0,BIT4	-PLACE IN INTERNAL REG	33335.00	50	032451.00
	KC,\$X0,BIT4		33335.01	90	032451.40
	SIC,SEN	-FAILED TO LOAD BIT 32	1310.00	80	032452.00
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032452.40
	LC,\$X0,\$X1		21.00	50	032453.00
	KC,\$X0,BIT4		33335.01	90	032453.40
	SIC,SEN	-FAILED TO LOAD BIT 32	1310.00	80	032454.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032454.40
	LC,\$X0,\$R		11.00	50	032455.00
	KC,\$X0,BIT4		33335.01	90	032455.40
	SIC,SEN	-FAILED TO LOAD BIT 32	1310.00	80	032456.00
	BZXE,SERS	-FROM INDEX	1304.32	C0	032456.40
12815	LX,\$X0,BIT3	-TEST BIT 31	33334.00	10	032457.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032457.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032460.00
	LC,\$X0,BIT3	-PLACE IN INTERNAL REG	33334.00	50	032460.40
	KC,\$X0,BIT3		33334.01	90	032461.00
	SIC,SEN	-FAILED TO LOAD BIT 31	1310.00	80	032461.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032462.00
	LC,\$X0,\$X1		21.00	50	032462.40
	KC,\$X0,BIT3		33334.01	90	032463.00
	SIC,SEN	-FAILED TO LOAD BIT 31	1310.00	80	032463.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032464.00
	LC,\$X0,\$R		11.00	50	032464.40
	KC,\$X0,BIT3		33334.01	90	032465.00
	SIC,SEN	-FAILED TO LOAD BIT 31	1310.00	80	032465.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032466.00
	B,\$+1.0		32467.50	00	032466.40
	BD,12813		32440.04	00	032467.00
	SIC,SEN0+0.32		1311.40	80	032467.40
	B,SSW		1301.10	00	032470.00
	BD,\$+.32		32471.04	00	032470.40
	LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32	10	032471.00
	V+,\$X13,BIT4		33335.32	B0	032471.40
	SX,\$X13,IC228		32631.33	10	032472.00
	B,12816		32473.10	00	032472.40

12816	LX,\$X0,BIT2	-TEST BIT 30	33333.00	10	032473.00
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032473.40
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032474.00
	LC,\$X0,BIT2	-PLACE IN INTERNAL REG	33333.00	50	032474.40
	KC,\$X0,BIT2		33333.01	90	032475.00
	SIC,SEN	-FAILED TO LOAD BIT 30	1310.00	80	032475.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032476.00
	LC,\$X0,\$X1		21.00	50	032476.40
	KC,\$X0,BIT2		33333.01	90	032477.00
	SIC,SEN	-FAILED TO LOAD BIT 30	1310.00	80	032477.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032500.00
	LC,\$X0,\$R		11.00	50	032500.40
12817	KC,\$X0,BIT2		33333.01	90	032501.00
	SIC,SEN	-FAILED TO LOAD BIT 30	1310.00	80	032501.40
	BZXE,SERS	-FROM INDEX	1304.32	C0	032502.00
	LX,\$X0,BIT1	-TEST BIT 29	33332.00	10	032502.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01	10	032503.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01	10	032503.40
	LC,\$X0,BIT1	-PLACE IN INTERNAL REG	33332.00	50	032504.00
	KC,\$X0,BIT1		33332.01	90	032504.40
	SIC,SEN	-FAILED TO LOAD BIT 29	1310.00	80	032505.00
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032505.40
	LC,\$X0,\$X1		21.00	50	032506.00
	12818	KC,\$X0,BIT1		33332.01	90
SIC,SEN		-FAILED TO LOAD BIT 29	1310.00	80	032507.00
BZXE,SERS		-FROM INDEX STORAGE	1304.32	C0	032507.40
LC,\$X0,\$R			11.00	50	032510.00
KC,\$X0,BIT1			33332.01	90	032510.40
SIC,SEN		-FAILED TO LOAD BIT 29	1310.00	80	032511.00
BZXE,SERS		-FROM INDEX	1304.32	C0	032511.40
LX,\$X0,BIT0		-TEST BIT 28	33331.00	10	032512.00
SX,\$X0,\$X1		-OPERAND USED IN LC	21.01	10	032512.40
SX,\$X0,\$R		-PLACE IN INDEX STORAGE	11.01	10	032513.00
LC,\$X0,BIT0		-PLACE IN INTERNAL REG	33331.00	50	032513.40
KC,\$X0,BIT0			33331.01	90	032514.00
SIC,SEN	-FAILED TO LOAD BIT 28	1310.00	80	032514.40	
BZXE,SERS	-FROM EXT MEM	1304.32	C0	032515.00	
LC,\$X0,\$X1		21.00	50	032515.40	
KC,\$X0,BIT0		33331.01	90	032516.00	
SIC,SEN	-FAILED TO LOAD BIT 28	1310.00	80	032516.40	
BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032517.00	
LC,\$X0,\$R		11.00	50	032517.40	
KC,\$X0,BIT0		33331.01	90	032520.00	
SIC,SEN	-FAILED TO LOAD BIT 28	1310.00	80	032520.40	
BZXE,SERS	-FROM INDEX	1304.32	C0	032521.00	
B,\$+1.0		32522.50	00	032521.40	
BD,12816		32473.04	00	032522.00	
SIC,SEN0+0.32		1311.40	80	032522.40	
B,SSW		1301.10	00	032523.00	
BD,\$+.32		32524.04	00	032523.40	
LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32	10	032524.00	
V+,\$X13,BIT5		33336.32	B0	032524.40	
SX,\$X13,IC228		32631.33	10	032525.00	
B,12819		32526.50	00	032525.40	
BD,\$+.32		32526.44	00	032526.00	

12819	LX,\$X0,100Z	-TEST ALL ZEROS	33311.00 10	032526.40
	SX,\$X0,\$X1	-OPERAND USED IN LC	21.01 10	032527.00
	SX,\$X0,\$R	-PLACE IN INDEX STORAGE	11.01 10	032527.40
	LC,\$X0,100Z	-PLACE IN INTERNAL REG	33311.00 50	032530.00
	KC,\$X0,100Z		33311.01 90	032530.40
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	032531.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032531.40
	LC,\$X0,\$X1		21.00 50	032532.00
	KC,\$X0,100Z		33311.01 90	032532.40
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	032533.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032533.40
	LC,\$X0,\$R		11.00 50	032534.00
	KC,\$X0,100Z		33311.01 90	032534.40
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	032535.00
	BZXE,SERS	-FROM INDEX	1304.32 C0	032535.40
		-TEST ALL ONES		
12820	LX,\$X0,1000	-OPERAND USED IN LC	33312.00 10	032536.00
	SX,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 10	032536.40
	SX,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 10	032537.00
	LC,\$X0,1000		33312.00 50	032537.40
	KC,\$X0,1000		33312.01 90	032540.00
	SIC,SEN	-FAILED TO LOAD ALL ONES	1310.00 80	032540.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032541.00
	LC,\$X0,\$X1		21.00 50	032541.40
	KC,\$X0,1000		33312.01 90	032542.00
	SIC,SEN	-FAILED TO LOAD ALL ONES	1310.00 80	032542.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032543.00
	LC,\$X0,\$R		11.00 50	032543.40
	KC,\$X0,1000		33312.01 90	032544.00
	SIC,SEN	-FAILED TO LOAD ALL ONES	1310.00 80	032544.40
	BZXE,SERS	-FROM INDEX	1304.32 C0	032545.00
	B,\$+1.0		32546.50 00	032545.40
	BD,12819		32526.44 00	032546.00
	SIC,SEN0+0.32		1311.40 80	032546.40
	B,SSW		1301.10 00	032547.00
	BD,\$+.32		32550.04 00	032547.40
	LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32 10	032550.00
	V+,\$X13,BIT6		33337.32 B0	032550.40
	SX,\$X13,IC228		32631.33 10	032551.00
	B,12821		32552.50 00	032551.40
	BD,\$+.32		32552.44 00	032552.00

12821	LCI,\$X0,%8#000001	-TEST BIT 45	1.01 02	032552.40
	KC,\$X0,BIT17		33352.01 90	032553.00
	SIC,SEN	-FAILED TO LOAD BIT 45	1310.00 80	032553.40
	BZXE,SERS		1304.32 C0	032554.00
	LCI,\$X0,%8#000002	-TEST BIT 44	2.01 02	032554.40
	KC,\$X0,BIT16		33351.01 90	032555.00
	SIC,SEN		1310.00 80	032555.40
	BZXE,SERS	-FAILED TO LOAD BIT 44	1304.32 C0	032556.00
	LCI,\$X0,%8#000004	-TEST BIT 43	4.01 02	032556.40
	KC,\$X0,BIT15		33350.01 90	032557.00
	SIC,SEN		1310.00 80	032557.40
	BZXE,SERS	-FAILED TO LOAD BIT 43	1304.32 C0	032560.00
	LCI,\$X0,%8#000010	-TEST BIT 42	10.01 02	032560.40
	KC,\$X0,BIT14		33347.01 90	032561.00
	SIC,SEN		1310.00 80	032561.40
	BZXE,SERS	-FAILED TO LOAD BIT 42	1304.32 C0	032562.00
12822	LCI,\$X0,%8#000020	-TEST BIT 41	20.01 02	032562.40
	KC,\$X0,BIT13		33346.01 90	032563.00
	SIC,SEN	-FAILED TO LOAD BIT 41	1310.00 80	032563.40
	BZXE,SERS		1304.32 C0	032564.00
	LCI,\$X0,%8#000040	-TEST BIT 40	40.01 02	032564.40
	KC,\$X0,BIT12		33345.01 90	032565.00
	SIC,SEN		1310.00 80	032565.40
	BZXE,SERS	-FAILED TO LOAD BIT 40	1304.32 C0	032566.00
	LCI,\$X0,%8#000100	-TEST BIT 39	100.01 02	032566.40
	KC,\$X0,BIT11		33344.01 90	032567.00
	SIC,SEN		1310.00 80	032567.40
	BZXE,SERS	-FAILED TO LOAD BIT 39	1304.32 C0	032570.00
	LCI,\$X0,%8#000200	-TEST BIT 38	200.01 02	032570.40
	KC,\$X0,BIT10		33343.01 90	032571.00
	SIC,SEN		1310.00 80	032571.40
	BZXE,SERS	-FAILED TO LOAD BIT 38	1304.32 C0	032572.00
12823	LCI,\$X0,%8#000400	-TEST BIT 37	400.01 02	032572.40
	KC,\$X0,BIT9		33342.01 90	032573.00
	SIC,SEN	-FAILED TO LOAD BIT 37	1310.00 80	032573.40
	BZXE,SERS		1304.32 C0	032574.00
	LCI,\$X0,%8#001000	-TEST BIT 36	1000.01 02	032574.40
	KC,\$X0,BIT8		33341.01 90	032575.00
	SIC,SEN		1310.00 80	032575.40
	BZXE,SERS	-FAILED TO LOAD BIT 36	1304.32 C0	032576.00

	LCI,\$X0,%8=002000	-TEST BIT 35	2000.01 02	032576.40
	KC,\$X0,BIT7		33340.01 90	032577.00
	SIC,SEN		1310.00 80	032577.40
	BZXE,SERS	-FAILED TO LOAD BIT 35	1304.32 C0	032600.00
	LCI,\$X0,%8=004000	-TEST BIT 34	4000.01 02	032600.40
	KC,\$X0,BIT6		33337.01 90	032601.00
	SIC,SEN		1310.00 80	032601.40
	BZXE,SERS	-FAILED TO LOAD BIT 34	1304.32 C0	032602.00
12824	LCI,\$X0,%8=010000	-TEST BIT 33	10000.01 02	032602.40
	KC,\$X0,BIT5		33336.01 90	032603.00
	SIC,SEN	-FAILED TO LOAD BIT 33	1310.00 80	032603.40
	BZXE,SERS		1304.32 C0	032604.00
	LCI,\$X0,%8=020000	-TEST BIT 32	20000.01 02	032604.40
	KC,\$X0,BIT4		33335.01 90	032605.00
	SIC,SEN		1310.00 80	032605.40
	BZXE,SERS	-FAILED TO LOAD BIT 32	1304.32 C0	032606.00
	LCI,\$X0,%8=040000	-TEST BIT 31	40000.01 02	032606.40
	KC,\$X0,BIT3		33334.01 90	032607.00
	SIC,SEN		1310.00 80	032607.40
	BZXE,SERS	-FAILED TO LOAD BIT 31	1304.32 C0	032610.00
	LCI,\$X0,%8=100000	-TEST BIT 30	100000.01 02	032610.40
	KC,\$X0,BIT2		33333.01 90	032611.00
	SIC,SEN		1310.00 80	032611.40
	BZXE,SERS	-FAILED TO LOAD BIT 30	1304.32 C0	032612.00
12825	LCI,\$X0,%8=200000	-TEST BIT 29	200000.01 02	032612.40
	KC,\$X0,BIT1		33332.01 90	032613.00
	SIC,SEN	-FAILED TO LOAD BIT 29	1310.00 80	032613.40
	BZXE,SERS		1304.32 C0	032614.00
	LCI,\$X0,%8=400000	-TEST BIT 28	400000.01 02	032614.40
	KC,\$X0,BIT0		33331.01 90	032615.00
	SIC,SEN		1310.00 80	032615.40
	BZXE,SERS	-FAILED TO LOAD BIT 28	1304.32 C0	032616.00
	LCI,\$X0,%8=777777	-TEST ALL ONES	777777.01 02	032616.40
	KC,\$X0,1000		33312.01 90	032617.00
	SIC,SEN		1310.00 80	032617.40
	BZXE,SERS	-FAILED TO LOAD ALL ONES	1304.32 C0	032620.00
	LCI,\$X0,%8=000000	-TEST ALL ZEROS	0.01 02	032620.40
	KC,\$X0,100Z		33311.01 90	032621.00
	SIC,SEN		1310.00 80	032621.40
	BZXE,SERS	-FAILED TO LOAD ALL ZEROS	1304.32 C0	032622.00
	B,\$+1.0		32623.50 00	032622.40
	BD,12821		32552.44 00	032623.00
	SIC,SEN0+0.32		1311.40 80	032623.40
	B,SSW		1301.10 00	032624.00
	BD,\$+.32		32625.04 00	032624.40
	LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32 10	032625.00
	V+,\$X13,BIT7		33340.32 B0	032625.40
	SX,\$X13,IC228		32631.33 10	032626.00
	LX,\$X13,IC228	-UPDATE CONTINUITY CHECK.	32631.32 10	032626.40
	KV,\$X13,ICK228		32632.32 90	032627.00
	SIC,SEN		1310.00 80	032627.40
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	032630.00
	B,130		32634.10 00	032630.40
	CNOP			
IC228	XW,0,0,0	-CONTINUITY REG 1228.	0.00 00 000000.00 00	032631.00
ICK228	XW,%8=776000.00,0,0		776000.00 00 000000.00 00	032632.00
1281D	%IQSZ=DD%BU,64,8=,1228	Z		032633.00

-----I230---TEST LOAD REFILL AND LOAD REFILL IMMED.

		-TEST CORRECT LOADING OF -EVERY POSITION OF REFILL -FIELD		
130	LX,\$X0,I30ID SX,\$X0,DPET13 SIC,RET B,IDF1 Z,IC230	-UPDATE IDENTIFICATION -PRINT ID.	33302.00 10 1437.01 10 1306.40 80 1443.10 00 33277.22 00	032634.00 032634.40 032635.00 032635.40 032636.00
		-TEST BIT 63		
1301	LX,\$X0,BIT63 BD,\$+.32 SR,\$X0,\$X1 SR,\$X0,\$R SR,\$X0,\$X2 LR,\$X0,BIT17 SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS LR,\$X0,\$X1 SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS LR,\$X0,\$R SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS	-OPERAND USED IN LR -PLACE IN INDEX STORAGE -PLACE IN INTERNAL REG -SET UP X2 FOR KV -FAILED TO LOAD BIT 63 -FROM EXT MEM -FAILED TO LOAD BIT 63 -FROM INDEX STORAGE -FAILED TO LOAD BIT 63 -FROM INTERNAL REG	33430.00 10 32637.44 00 21.01 70 11.01 70 22.01 70 33352.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0 21.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0 11.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0	032636.40 032637.00 032637.40 032640.00 032640.40 032641.00 032641.40 032642.00 032642.40 032643.00 032643.40 032644.00 032644.40 032645.00 032645.40 032646.00 032646.40 032647.00 032647.40 032650.00
		-TEST BIT 62		
1302	LX,\$X0,BIT62 SR,\$X0,\$X1 SR,\$X0,\$R SR,\$X0,\$X2 LR,\$X0,BIT16 SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS LR,\$X0,\$X1 SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS LR,\$X0,\$R SR,\$X0,I30ST KV,\$X2,I30ST SIC,SEN BZXE,SERS	-OPERAND USED IN LR -PLACE IN INDEX STORAGE -PLACE IN INTERNAL REG -SET UP X2 FOR KV -FAILED TO LOAD BIT 62 -FROM EXT MEM -FAILED TO LOAD BIT 62 -FROM INDEX STORAGE -FAILED TO LOAD BIT 62 -FROM INTERNAL REG	33427.00 10 21.01 70 11.01 70 22.01 70 33351.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0 21.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0 11.00 70 33301.01 70 33301.04 90 1310.00 80 1304.32 C0	032650.40 032651.00 032651.40 032652.00 032652.40 032653.00 032653.40 032654.00 032654.40 032655.00 032655.40 032656.00 032656.40 032657.00 032657.40 032660.00 032660.40 032661.00 032661.40

		-TEST BIT 61		
1303	LX,\$X0,BIT61	-OPERAND USED IN LR	33426.00 10	032662.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032662.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032663.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032663.40
	LR,\$X0,BIT15		33350.00 70	032664.00
	SR,\$X0,I30ST		33301.01 70	032664.40
	KV,\$X2,I30ST		33301.04 90	032665.00
	SIC,SEN	-FAILED TO LOAD BIT 61	1310.00 80	032665.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032666.00
	LR,\$X0,\$X1		21.00 70	032666.40
	SR,\$X0,I30ST		33301.01 70	032667.00
	KV,\$X2,I30ST		33301.04 90	032667.40
	SIC,SEN	-FAILED TO LOAD BIT 61	1310.00 80	032670.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032670.40
	LR,\$X0,\$R		11.00 70	032671.00
	SR,\$X0,I30ST		33301.01 70	032671.40
	KV,\$X2,I30ST		33301.04 90	032672.00
	SIC,SEN	-FAILED TO LOAD BIT 61	1310.00 80	032672.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032673.00
	B,\$+1.0		32674.50 00	032673.40
	BD,I301		32636.44 00	032674.00
	SIC,SEN0+0.32		1311.40 80	032674.40
	B,\$SSW		1301.10 00	032675.00
	BD,\$+.32		32676.04 00	032675.40
	LX,\$X13,I3230	-UPDATE CONTINUITY CHECK.	33277.32 10	032676.00
	V+,\$X13,BIT0		33331.32 B0	032676.40
	SX,\$X13,I3230		33277.33 10	032677.00
		-TEST BIT 60		
1304	LX,\$X0,BIT60	-OPERAND USED IN LR	33425.00 10	032677.40
	BD,\$+.32		32700.44 00	032700.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032700.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032701.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032701.40
	LR,\$X0,BIT14		33347.00 70	032702.00
	SR,\$X0,I30ST		33301.01 70	032702.40
	KV,\$X2,I30ST		33301.04 90	032703.00
	SIC,SEN	-FAILED TO LOAD BIT 60	1310.00 80	032703.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032704.00
	LR,\$X0,\$X1		21.00 70	032704.40
	SR,\$X0,I30ST		33301.01 70	032705.00
	KV,\$X2,I30ST		33301.04 90	032705.40
	SIC,SEN	-FAILED TO LOAD BIT 60	1310.00 80	032706.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032706.40
	LR,\$X0,\$R		11.00 70	032707.00
	SR,\$X0,I30ST		33301.01 70	032707.40
	KV,\$X2,I30ST		33301.04 90	032710.00
	SIC,SEN	-FAILED TO LOAD BIT 60	1310.00 80	032710.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032711.00

		-TEST BIT 59		
1305	LX,\$X0,BIT59	-OPERAND USED IN LR	33424.00 10	032711.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032712.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032712.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032713.00
	LR,\$X0,BIT13		33346.00 70	032713.40
	SR,\$X0,I30ST		33301.01 70	032714.00
	KV,\$X2,I30ST		33301.04 90	032714.40
	SIC,SEN	-FAILED TO LOAD BIT 59	1310.00 80	032715.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032715.40
	LR,\$X0,\$X1		21.00 70	032716.00
	SR,\$X0,I30ST		33301.01 70	032716.40
	KV,\$X2,I30ST		33301.04 90	032717.00
	SIC,SEN	-FAILED TO LOAD BIT 59	1310.00 80	032717.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032720.00
	LR,\$X0,\$R		11.00 70	032720.40
	SR,\$X0,I30ST		33301.01 70	032721.00
	KV,\$X2,I30ST		33301.04 90	032721.40
	SIC,SEN	-FAILED TO LOAD BIT 59	1310.00 80	032722.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032722.40
		-TEST BIT 58		
1306	LX,\$X0,BIT58	-OPERAND USED IN LR	33423.00 10	032723.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032723.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032724.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032724.40
	LR,\$X0,BIT12		33345.00 70	032725.00
	SR,\$X0,I30ST		33301.01 70	032725.40
	KV,\$X2,I30ST		33301.04 90	032726.00
	SIC,SEN	-FAILED TO LOAD BIT 58	1310.00 80	032726.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032727.00
	LR,\$X0,\$X1		21.00 70	032727.40
	SR,\$X0,I30ST		33301.01 70	032730.00
	KV,\$X2,I30ST		33301.04 90	032730.40
	SIC,SEN	-FAILED TO LOAD BIT 58	1310.00 80	032731.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032731.40
	LR,\$X0,\$R		11.00 70	032732.00
	SR,\$X0,I30ST		33301.01 70	032732.40
	KV,\$X2,I30ST		33301.04 90	032733.00
	SIC,SEN	-FAILED TO LOAD BIT 58	1310.00 80	032733.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032734.00
	B,\$+1.0		32735.50 00	032734.40
	BD,I304		32677.44 00	032735.00
	SIC,SEN0+0.32		1311.40 80	032735.40
	B,SSW		1301.10 00	032736.00
	BD,\$+.32		32737.04 00	032736.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	032737.00
	V+,\$X13,BIT1		33332.32 B0	032737.40
	SX,\$X13,IC230		33277.33 10	032740.00

		-TEST BIT 57		
1307	LX,\$X0,BIT57	-OPERAND USED IN LR	33422.00 10	032740.40
	BD,\$+,32		32741.44 00	032741.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032741.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032742.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032742.40
	LR,\$X0,BIT11		33344.00 70	032743.00
	SR,\$X0,I30ST		33301.01 70	032743.40
	KV,\$X2,I30ST		33301.04 90	032744.00
	SIC,SEN	-FAILED TO LOAD BIT 57	1310.00 80	032744.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032745.00
	LR,\$X0,\$X1		21.00 70	032745.40
	SR,\$X0,I30ST		33301.01 70	032746.00
	KV,\$X2,I30ST		33301.04 90	032746.40
	SIC,SEN	-FAILED TO LOAD BIT 57	1310.00 80	032747.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032747.40
	LR,\$X0,\$R		11.00 70	032750.00
	SR,\$X0,I30ST		33301.01 70	032750.40
	KV,\$X2,I30ST		33301.04 90	032751.00
	SIC,SEN	-FAILED TO LOAD BIT 57	1310.00 80	032751.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032752.00
		-TEST BIT 56		
1308	LX,\$X0,BIT56	-OPERAND USED IN LR	33421.00 10	032752.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	032753.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	032753.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	032754.00
	LR,\$X0,BIT10		33343.00 70	032754.40
	SR,\$X0,I30ST		33301.01 70	032755.00
	KV,\$X2,I30ST		33301.04 90	032755.40
	SIC,SEN	-FAILED TO LOAD BIT 56	1310.00 80	032756.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	032756.40
	LR,\$X0,\$X1		21.00 70	032757.00
	SR,\$X0,I30ST		33301.01 70	032757.40
	KV,\$X2,I30ST		33301.04 90	032760.00
	SIC,SEN	-FAILED TO LOAD BIT 56	1310.00 80	032760.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	032761.00
	LR,\$X0,\$R		11.00 70	032761.40
	SR,\$X0,I30ST		33301.01 70	032762.00
	KV,\$X2,I30ST		33301.04 90	032762.40
	SIC,SEN	-FAILED TO LOAD BIT 56	1310.00 80	032763.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	032763.40

Address	Instruction	Comment	Value	Unit	Address
		-TEST BIT 55			
1309	LX,\$X0,BIT55	-OPERAND USED IN LR	33420.00	10	032764.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01	70	032764.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01	70	032765.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01	70	032765.40
	LR,\$X0,BIT9		33342.00	70	032766.00
	SR,\$X0,I30ST		33301.01	70	032766.40
	KV,\$X2,I30ST		33301.04	90	032767.00
	SIC,SEN	-FAILED TO LOAD BIT 55	1310.00	80	032767.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	032770.00
	LR,\$X0,\$X1		21.00	70	032770.40
	SR,\$X0,I30ST		33301.01	70	032771.00
	KV,\$X2,I30ST		33301.04	90	032771.40
	SIC,SEN	-FAILED TO LOAD BIT 55	1310.00	80	032772.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	032772.40
	LR,\$X0,\$R		11.00	70	032773.00
	SR,\$X0,I30ST		33301.01	70	032773.40
	KV,\$X2,I30ST		33301.04	90	032774.00
	SIC,SEN	-FAILED TO LOAD BIT 55	1310.00	80	032774.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32	C0	032775.00
	B,\$+1.0		32776.50	00	032775.40
	BD,I307		32740.44	00	032776.00
	SIC,SEN0+0.32		1311.40	80	032776.40
	B,SSW		1301.10	00	032777.00
	BD,\$+.32		33000.04	00	032777.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32	10	033000.00
	V+,\$X13,BIT2		33333.32	B0	033000.40
	SX,\$X13,IC230		33277.33	10	033001.00
		-TEST BIT 54			
13010	LX,\$X0,BIT54	-OPERAND USED IN LR	33417.00	10	033001.40
	BD,\$+.32		33002.44	00	033002.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01	70	033002.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01	70	033003.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01	70	033003.40
	LR,\$X0,BIT8		33341.00	70	033004.00
	SR,\$X0,I30ST		33301.01	70	033004.40
	KV,\$X2,I30ST		33301.04	90	033005.00
	SIC,SEN	-FAILED TO LOAD BIT 54	1310.00	80	033005.40
	BZXE,SERS	-FROM EXT MEM	1304.32	C0	033006.00
	LR,\$X0,\$X1		21.00	70	033006.40
	SR,\$X0,I30ST		33301.01	70	033007.00
	KV,\$X2,I30ST		33301.04	90	033007.40
	SIC,SEN	-FAILED TO LOAD BIT 54	1310.00	80	033010.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32	C0	033010.40
	LR,\$X0,\$R		11.00	70	033011.00
	SR,\$X0,I30ST		33301.01	70	033011.40
	KV,\$X2,I30ST		33301.04	90	033012.00
	SIC,SEN	-FAILED TO LOAD BIT 54	1310.00	80	033012.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32	C0	033013.00

		-TEST BIT 53		
13011	LX,\$X0,BIT53	-OPERAND USED IN LR	33416.00 10	033013.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033014.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033014.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033015.00
	LR,\$X0,BIT7		33340.00 70	033015.40
	SR,\$X0,I30ST		33301.01 70	033016.00
	KV,\$X2,I30ST		33301.04 90	033016.40
	SIC,SEN	-FAILED TO LOAD BIT 53	1310.00 80	033017.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033017.40
	LR,\$X0,\$X1		21.00 70	033020.00
	SR,\$X0,I30ST		33301.01 70	033020.40
	KV,\$X2,I30ST		33301.04 90	033021.00
	SIC,SEN	-FAILED TO LOAD BIT 53	1310.00 80	033021.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033022.00
	LR,\$X0,\$R		11.00 70	033022.40
	SR,\$X0,I30ST		33301.01 70	033023.00
	KV,\$X2,I30ST		33301.04 90	033023.40
	SIC,SEN	-FAILED TO LOAD BIT 53	1310.00 80	033024.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033024.40
		-TEST BIT 52		
13012	LX,\$X0,BIT52	-OPERAND USED IN LR	33415.00 10	033025.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033025.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033026.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033026.40
	LR,\$X0,BIT6		33337.00 70	033027.00
	SR,\$X0,I30ST		33301.01 70	033027.40
	KV,\$X2,I30ST		33301.04 90	033030.00
	SIC,SEN	-FAILED TO LOAD BIT 52	1310.00 80	033030.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033031.00
	LR,\$X0,\$X1		21.00 70	033031.40
	SR,\$X0,I30ST		33301.01 70	033032.00
	KV,\$X2,I30ST		33301.04 90	033032.40
	SIC,SEN	-FAILED TO LOAD BIT 52	1310.00 80	033033.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033033.40
	LR,\$X0,\$R		11.00 70	033034.00
	SR,\$X0,I30ST		33301.01 70	033034.40
	KV,\$X2,I30ST		33301.04 90	033035.00
	SIC,SEN	-FAILED TO LOAD BIT 52	1310.00 80	033035.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033036.00
	B,\$+1.0		33037.50 00	033036.40
	BD,I3010		33001.44 00	033037.00
	SIC,SEN0+0.32		1311.40 80	033037.40
	B,SSW		1301.10 00	033040.00
	BD,\$+.32		33041.04 00	033040.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033041.00
	V+,\$X13,BIT3		33334.32 B0	033041.40
	SX,\$X13,IC230		33277.33 10	033042.00

		-TEST BIT 51		
13013	LX,\$X0,BIT51	-OPERAND USED IN LR	33414.00 10	033042.40
	BD,\$+,32		33043.44 00	033043.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033043.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033044.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033044.40
	LR,\$X0,BIT5		33336.00 70	033045.00
	SR,\$X0,I30ST		33301.01 70	033045.40
	KV,\$X2,I30ST		33301.04 90	033046.00
	SIC,SEN	-FAILED TO LOAD BIT 51	1310.00 80	033046.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033047.00
	LR,\$X0,\$X1		21.00 70	033047.40
	SR,\$X0,I30ST		33301.01 70	033050.00
	KV,\$X2,I30ST		33301.04 90	033050.40
	SIC,SEN	-FAILED TO LOAD BIT 51	1310.00 80	033051.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033051.40
	LR,\$X0,\$R		11.00 70	033052.00
	SR,\$X0,I30ST		33301.01 70	033052.40
	KV,\$X2,I30ST		33301.04 90	033053.00
	SIC,SEN	-FAILED TO LOAD BIT 51	1310.00 80	033053.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033054.00
		-TEST BIT 50		
13014	LX,\$X0,BIT50	-OPERAND USED IN LR	33413.00 10	033054.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033055.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033055.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033056.00
	LR,\$X0,BIT4		33335.00 70	033056.40
	SR,\$X0,I30ST		33301.01 70	033057.00
	KV,\$X2,I30ST		33301.04 90	033057.40
	SIC,SEN	-FAILED TO LOAD BIT 50	1310.00 80	033060.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033060.40
	LR,\$X0,\$X1		21.00 70	033061.00
	SR,\$X0,I30ST		33301.01 70	033061.40
	KV,\$X2,I30ST		33301.04 90	033062.00
	SIC,SEN	-FAILED TO LOAD BIT 50	1310.00 80	033062.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033063.00
	LR,\$X0,\$R		11.00 70	033063.40
	SR,\$X0,I30ST		33301.01 70	033064.00
	KV,\$X2,I30ST		33301.04 90	033064.40
	SIC,SEN	-FAILED TO LOAD BIT 50	1310.00 80	033065.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033065.40

		-TEST BIT 49		
13015	LX,\$X0,BIT49	-OPERAND USED IN LR	33412.00 10	033066.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033066.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033067.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033067.40
	LR,\$X0,BIT3		33334.00 70	033070.00
	SR,\$X0,I30ST		33301.01 70	033070.40
	KV,\$X2,I30ST		33301.04 90	033071.00
	SIC,SEN	-FAILED TO LOAD BIT 49	1310.00 80	033071.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033072.00
	LR,\$X0,\$X1		21.00 70	033072.40
	SR,\$X0,I30ST		33301.01 70	033073.00
	KV,\$X2,I30ST		33301.04 90	033073.40
	SIC,SEN	-FAILED TO LOAD BIT 49	1310.00 80	033074.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033074.40
	LR,\$X0,\$R		11.00 70	033075.00
	SR,\$X0,I30ST		33301.01 70	033075.40
	KV,\$X2,I30ST		33301.04 90	033076.00
	SIC,SEN	-FAILED TO LOAD BIT 49	1310.00 80	033076.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033077.00
	B,\$+1.0		33100.50 00	033077.40
	BD,I3013		33042.44 00	033100.00
	SIC,SEN0+0.32		1311.40 80	033100.40
	B,SSW		1301.10 00	033101.00
	BD,\$+.32		33102.04 00	033101.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033102.00
	V+,\$X13,BIT4		33335.32 B0	033102.40
	SX,\$X13,IC230		33277.33 10	033103.00
		-TEST BIT 48		
13016	LX,\$X0,BIT48	-OPERAND USED IN LR	33411.00 10	033103.40
	BD,\$+.32		33104.44 00	033104.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033104.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033105.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033105.40
	LR,\$X0,BIT2		33333.00 70	033106.00
	SR,\$X0,I30ST		33301.01 70	033106.40
	KV,\$X2,I30ST		33301.04 90	033107.00
	SIC,SEN	-FAILED TO LOAD BIT 48	1310.00 80	033107.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033110.00
	LR,\$X0,\$X1		21.00 70	033110.40
	SR,\$X0,I30ST		33301.01 70	033111.00
	KV,\$X2,I30ST		33301.04 90	033111.40
	SIC,SEN	-FAILED TO LOAD BIT 48	1310.00 80	033112.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033112.40
	LR,\$X0,\$R		11.00 70	033113.00
	SR,\$X0,I30ST		33301.01 70	033113.40
	KV,\$X2,I30ST		33301.04 90	033114.00
	SIC,SEN	-FAILED TO LOAD BIT 48	1310.00 80	033114.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033115.00

		-TEST BIT 47		
13017	LX,\$X0,BIT47	-OPERAND USED IN LR	33410.00 10	033115.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033116.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033116.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033117.00
	LR,\$X0,BIT1		33332.00 70	033117.40
	SR,\$X0,I30ST		33301.01 70	033120.00
	KV,\$X2,I30ST		33301.04 90	033120.40
	SIC,SEN	-FAILED TO LOAD BIT 47	1310.00 80	033121.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033121.40
	LR,\$X0,\$X1		21.00 70	033122.00
	SR,\$X0,I30ST		33301.01 70	033122.40
	KV,\$X2,I30ST		33301.04 90	033123.00
	SIC,SEN	-FAILED TO LOAD BIT 47	1310.00 80	033123.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033124.00
	LR,\$X0,\$R		11.00 70	033124.40
	SR,\$X0,I30ST		33301.01 70	033125.00
	KV,\$X2,I30ST		33301.04 90	033125.40
	SIC,SEN	-FAILED TO LOAD BIT 47	1310.00 80	033126.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033126.40
		-TEST BIT 46		
13018	LX,\$X0,BIT46	-OPERAND USED IN LR	33407.00 10	033127.00
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033127.40
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033130.00
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033130.40
	LR,\$X0,BIT0		33331.00 70	033131.00
	SR,\$X0,I30ST		33301.01 70	033131.40
	KV,\$X2,I30ST		33301.04 90	033132.00
	SIC,SEN	-FAILED TO LOAD BIT 46	1310.00 80	033132.40
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033133.00
	LR,\$X0,\$X1		21.00 70	033133.40
	SR,\$X0,I30ST		33301.01 70	033134.00
	KV,\$X2,I30ST		33301.04 90	033134.40
	SIC,SEN	-FAILED TO LOAD BIT 46	1310.00 80	033135.00
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033135.40
	LR,\$X0,\$R		11.00 70	033136.00
	SR,\$X0,I30ST		33301.01 70	033136.40
	KV,\$X2,I30ST		33301.04 90	033137.00
	SIC,SEN	-FAILED TO LOAD BIT 46	1310.00 80	033137.40
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033140.00
	B,\$+1.0		33141.50 00	033140.40
	BD,I3016		33103.44 00	033141.00
	SIC,SEN0+0.32		1311.40 80	033141.40
	B,\$SSW		1301.10 00	033142.00
	BD,\$+.32		33143.04 00	033142.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033143.00
	V+,\$X13,BIT5		33336.32 B0	033143.40
	SX,\$X13,IC230		33277.33 10	033144.00

13019	LX,\$X0,1000	-TEST ALL ONES		
	BD,\$+.32	-OPERAND USED IN LR	33312.00 10	033144.40
	SR,\$X0,\$X1		33145.44 00	033145.00
	SR,\$X0,\$R	-PLACE IN INDEX STORAGE	21.01 70	033145.40
	SR,\$X0,\$X2	-PLACE IN INTERNAL REG	11.01 70	033146.00
	LR,\$X0,1000	-SET UP X2 FOR KV	22.01 70	033146.40
	SR,\$X0,130ST		33312.00 70	033147.00
	KV,\$X2,130ST		33301.01 70	033147.40
	SIC,SEN	-FAILED TO LOAD ALL ONES	33301.04 90	033150.00
	BZXE,SERS	-FROM EXT MEM	1310.00 80	033150.40
	LR,\$X0,\$X1		1304.32 C0	033151.00
	SR,\$X0,130ST		21.00 70	033151.40
	KV,\$X2,130ST		33301.01 70	033152.00
	SIC,SEN	-FAILED TO LOAD ALL ONES	33301.04 90	033152.40
	BZXE,SERS	-FROM INDEX STORAGE	1310.00 80	033153.00
	LR,\$X0,\$R		1304.32 C0	033153.40
	SR,\$X0,130ST		11.00 70	033154.00
	KV,\$X2,130ST		33301.01 70	033154.40
	SIC,SEN	-FAILED TO LOAD ALL ONES	33301.04 90	033155.00
	BZXE,SERS	-FROM INTERNAL REG	1310.00 80	033155.40
			1304.32 C0	033156.00
		-TEST ALL ZEROS		
13020	LX,\$X0,100Z	-OPERAND USED IN LR	33311.00 10	033156.40
	SR,\$X0,\$X1	-PLACE IN INDEX STORAGE	21.01 70	033157.00
	SR,\$X0,\$R	-PLACE IN INTERNAL REG	11.01 70	033157.40
	SR,\$X0,\$X2	-SET UP X2 FOR KV	22.01 70	033160.00
	LR,\$X0,100Z		33311.00 70	033160.40
	SR,\$X0,130ST		33301.01 70	033161.00
	KV,\$X2,130ST		33301.04 90	033161.40
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	033162.00
	BZXE,SERS	-FROM EXT MEM	1304.32 C0	033162.40
	LR,\$X0,\$X1		21.00 70	033163.00
	SR,\$X0,130ST		33301.01 70	033163.40
	KV,\$X2,130ST		33301.04 90	033164.00
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	033164.40
	BZXE,SERS	-FROM INDEX STORAGE	1304.32 C0	033165.00
	LR,\$X0,\$R		11.00 70	033165.40
	SR,\$X0,130ST		33301.01 70	033166.00
	KV,\$X2,130ST		33301.04 90	033166.40
	SIC,SEN	-FAILED TO LOAD ALL ZEROS	1310.00 80	033167.00
	BZXE,SERS	-FROM INTERNAL REG	1304.32 C0	033167.40
	B,\$+1.0		33171.10 00	033170.00
	BD,13019		33144.44 00	033170.40
	SIC,SEN0+0.32		1311.40 80	033171.00
	B,SSW		1301.10 00	033171.40
	BD,\$+.32		33172.44 00	033172.00
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033172.40
	V+,\$X13,BIT6		33337.32 B0	033173.00
	SX,\$X13,IC230		33277.33 10	033173.40

13021	LVI,\$X2,%8#000001.	-TEST BIT 63	1.05 01	033174.00
	BD,\$+.32		33175.04 00	033174.40
	LRI,\$X0,%8#000001		1.01 03	033175.00
	SR,\$X0,I30ST		33301.01 70	033175.40
	KV,\$X2,I30ST		33301.04 90	033176.00
	SIC,SEN	-FAILED TO LOAD BIT 63	1310.00 80	033176.40
	BZXE,SERS		1304.32 C0	033177.00
	LVI,\$X2,%8#000002.	-TEST BIT 62	2.05 01	033177.40
	LRI,\$X0,%8#000002		2.01 03	033200.00
	SR,\$X0,I30ST		33301.01 70	033200.40
	KV,\$X2,I30ST		33301.04 90	033201.00
	SIC,SEN		1310.00 80	033201.40
	BZXE,SERS	-FAILED TO LOAD BIT 62	1304.32 C0	033202.00
	LVI,\$X2,%8#000004.	-TEST BIT 61	4.05 01	033202.40
	LRI,\$X0,%8#000004		4.01 03	033203.00
	SR,\$X0,I30ST		33301.01 70	033203.40
	KV,\$X2,I30ST		33301.04 90	033204.00
	SIC,SEN		1310.00 80	033204.40
	BZXE,SERS	-FAILED TO LOAD BIT 61	1304.32 C0	033205.00
13022	LVI,\$X2,%8#000010.	-TEST BIT 60	10.05 01	033205.40
	LRI,\$X0,%8#000010		10.01 03	033206.00
	SR,\$X0,I30ST		33301.01 70	033206.40
	KV,\$X2,I30ST		33301.04 90	033207.00
	SIC,SEN	-FAILED TO LOAD BIT 60	1310.00 80	033207.40
	BZXE,SERS		1304.32 C0	033210.00
	LVI,\$X2,%8#000020.	-TEST BIT 59	20.05 01	033210.40
	LRI,\$X0,%8#000020		20.01 03	033211.00
	SR,\$X0,I30ST		33301.01 70	033211.40
	KV,\$X2,I30ST		33301.04 90	033212.00
	SIC,SEN		1310.00 80	033212.40
	BZXE,SERS	-FAILED TO LOAD BIT 59	1304.32 C0	033213.00
	LVI,\$X2,%8#000040.	-TEST BIT 58	40.05 01	033213.40
	LRI,\$X0,%8#000040		40.01 03	033214.00
	SR,\$X0,I30ST		33301.01 70	033214.40
	KV,\$X2,I30ST		33301.04 90	033215.00
	SIC,SEN		1310.00 80	033215.40
	BZXE,SERS	-FAILED TO LOAD BIT 58	1304.32 C0	033216.00
13023	LVI,\$X2,%8#000100.	-TEST BIT 57	100.05 01	033216.40
	LRI,\$X0,%8#000100		100.01 03	033217.00
	SR,\$X0,I30ST		33301.01 70	033217.40
	KV,\$X2,I30ST		33301.04 90	033220.00
	SIC,SEN	-FAILED TO LOAD BIT 57	1310.00 80	033220.40
	BZXE,SERS		1304.32 C0	033221.00

	LVI,\$X2,%8#000200.	-TEST BIT 56	200.05 01	033221.40
	LRI,\$X0,%8#000200		200.01 03	033222.00
	SR,\$X0,I30ST		33301.01 70	033222.40
	KV,\$X2,I30ST		33301.04 90	033223.00
	SIC,SEN		1310.00 80	033223.40
	BZXE,SERS	-FAILED TO LOAD BIT 56	1304.32 C0	033224.00
	LVI,\$X2,%8#000400.	-TEST BIT 55	400.05 01	033224.40
	LRI,\$X0,%8#000400		400.01 03	033225.00
	SR,\$X0,I30ST		33301.01 70	033225.40
	KV,\$X2,I30ST		33301.04 90	033226.00
	SIC,SEN		1310.00 80	033226.40
	BZXE,SERS	-FAILED TO LOAD BIT 55	1304.32 C0	033227.00
I3024	LVI,\$X2,%8#001000.	-TEST BIT 54	1000.05 01	033227.40
	LRI,\$X0,%8#001000		1000.01 03	033230.00
	SR,\$X0,I30ST		33301.01 70	033230.40
	KV,\$X2,I30ST		33301.04 90	033231.00
	SIC,SEN		1310.00 80	033231.40
	BZXE,SERS	-FAILED TO LOAD BIT 54	1304.32 C0	033232.00
	LVI,\$X2,%8#002000.	-TEST BIT 53	2000.05 01	033232.40
	LRI,\$X0,%8#002000		2000.01 03	033233.00
	SR,\$X0,I30ST		33301.01 70	033233.40
	KV,\$X2,I30ST		33301.04 90	033234.00
	SIC,SEN		1310.00 80	033234.40
	BZXE,SERS	-FAILED TO LOAD BIT 53	1304.32 C0	033235.00
	LVI,\$X2,%8#004000.	-TEST BIT 52	4000.05 01	033235.40
	LRI,\$X0,%8#004000		4000.01 03	033236.00
	SR,\$X0,I30ST		33301.01 70	033236.40
	KV,\$X2,I30ST		33301.04 90	033237.00
	SIC,SEN		1310.00 80	033237.40
	BZXE,SERS	-FAILED TO LOAD BIT 52	1304.32 C0	033240.00
I3025	LVI,\$X2,%8#010000.	-TEST BIT 51	10000.05 01	033240.40
	LRI,\$X0,%8#010000		10000.01 03	033241.00
	SR,\$X0,I30ST		33301.01 70	033241.40
	KV,\$X2,I30ST		33301.04 90	033242.00
	SIC,SEN		1310.00 80	033242.40
	BZXE,SERS	-FAILED TO LOAD BIT 51	1304.32 C0	033243.00
	LVI,\$X2,%8#020000.	-TEST BIT 50	20000.05 01	033243.40
	LRI,\$X0,%8#020000		20000.01 03	033244.00
	SR,\$X0,I30ST		33301.01 70	033244.40
	KV,\$X2,I30ST		33301.04 90	033245.00
	SIC,SEN		1310.00 80	033245.40
	BZXE,SERS	-FAILED TO LOAD BIT 50	1304.32 C0	033246.00
	LVI,\$X2,%8#040000.	-TEST BIT 49	40000.05 01	033246.40
	LRI,\$X0,%8#040000		40000.01 03	033247.00
	SR,\$X0,I30ST		33301.01 70	033247.40
	KV,\$X2,I30ST		33301.04 90	033250.00
	SIC,SEN		1310.00 80	033250.40
	BZXE,SERS	-FAILED TO LOAD BIT 49	1304.32 C0	033251.00

13026	LVI,\$X2,%8#100000.	-TEST BIT 48	100000.05 01	033251.40
	LRI,\$X0,%8#100000		100000.01 03	033252.00
	SR,\$X0,I30ST		33301.01 70	033252.40
	KV,\$X2,I30ST		33301.04 90	033253.00
	SIC,SEN	-FAILED TO LOAD BIT 48	1310.00 80	033253.40
	BZXE,SERS		1304.32 C0	033254.00
	LVI,\$X2,%8#200000.	-TEST BIT 47	200000.05 01	033254.40
	LRI,\$X0,%8#200000		200000.01 03	033255.00
	SR,\$X0,I30ST		33301.01 70	033255.40
	KV,\$X2,I30ST		33301.04 90	033256.00
	SIC,SEN		1310.00 80	033256.40
	BZXE,SERS	-FAILED TO LOAD BIT 47	1304.32 C0	033257.00
	LVI,\$X2,%8#400000.	-TEST BIT 46	400000.05 01	033257.40
	LRI,\$X0,%8#400000		400000.01 03	033260.00
	SR,\$X0,I30ST		33301.01 70	033260.40
	KV,\$X2,I30ST		33301.04 90	033261.00
	SIC,SEN		1310.00 80	033261.40
	BZXE,SERS	-FAILED TO LOAD BIT 46	1304.32 C0	033262.00
13027	LVI,\$X2,%8#777777.	-TEST ALL ONES	777777.05 01	033262.40
	LRI,\$X0,%8#777777		777777.01 03	033263.00
	SR,\$X0,I30ST		33301.01 70	033263.40
	KV,\$X2,I30ST		33301.04 90	033264.00
	SIC,SEN	-FAILED TO LOAD ALL ONES	1310.00 80	033264.40
	BZXE,SERS		1304.32 C0	033265.00
	LVI,\$X2,%8#000000.	-TEST ALL ZEROS	0.05 01	033265.40
	LRI,\$X0,%8#000000		0.01 03	033266.00
	SR,\$X0,I30ST		33301.01 70	033266.40
	KV,\$X2,I30ST		33301.04 90	033267.00
	SIC,SEN		1310.00 80	033267.40
	BZXE,SERS	-FAILED TO LOAD ALL ZEROS	1304.32 C0	033270.00
	B,\$+1.0		33271.50 00	033270.40
	BD,I3021		33174.04 00	033271.00
	SIC,SEN0+0.32		1311.40 80	033271.40
	B,\$SSW		1301.10 00	033272.00
	BD,\$+.32		33273.04 00	033272.40
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033273.00
	V+,\$X13,BIT7		33340.32 B0	033273.40
	SX,\$X13,IC230		33277.33 10	033274.00
	LX,\$X13,IC230	-UPDATE CONTINUITY CHECK.	33277.32 10	033274.40
	KV,\$X13,ICK230		33300.32 90	033275.00
	SIC,SEN		1310.00 80	033275.40
	BZXE,SERS	-CONTINUITY ERROR.	1304.32 C0	033276.00
	B,I30ID+1.00		33303.10 00	033276.40
IC230	XW,0,0,0	-CONTINUITY REG 1230.	0.00 00 000000.00 00	033277.00
ICK230	XW,%8#776000.00,0,0		776000.00 00 000000.00 00	033300.00
130ST	DR%BU,64,8#,%1#		1.00	033301.00
130ID	%IQSZ#DD%BU,64,8#,I230	Z		033302.00
	LX,\$X1,%8#103.0		103.02 10	033303.00
	KV,\$X1,I00LC		33307.42 90	033303.40
	BZXE,%8#20000.0	-LOOP	20000.32 C0	033304.00
	KC,\$X1,I00LC		33307.43 90	033304.40
	BZXE,%8#20000.0	-LOOP	20000.32 C0	033305.00

SR,\$X1,17.0
KV,\$X1,100LC+.32
BZXE,%8#20000.0
B,%8#34000.0

-LOOP
-CONTINUE
-COMMON CONSTANTS FOR FETCH ONLY

21.03 70
33310.02 90
20000.32 C0
34000.10 00

033305.40
033306.00
033306.40
033307.00

SENO SYN,SENO
100LC %8#DD%BU,64,8#,0 000 011 770 025 700 000 000
100Z XW,0,0,0,0 -ZERO INDX WD.
1000 %8#DD%BU,64,8#,17777777777777777777-INDX WD. OF ONES.
100VO XW,%8#-777777.77,0,0
100CO XW,0,%8#777777,0

1311.00+ +00000000
0000011770025700000000 033307.40
0.00 00 000000.00 00 033311.00
17777777777777777777 033312.00
777777.77 80 000000.00 00 033313.00
0.00 0F 777760.00 00 033314.00

DD%BU,64,8#,0,0,0

00000000000000000000 033315.00

DD%BU,64,8#,0,0,0

00000000000000000000 033316.00

DD%BU,64,8#,0,0,0

00000000000000000000 033317.00

DD%BU,64,8#,0,0,0

00000000000000000000 033320.00

00000000000000000000 033321.00

00000000000000000000 033322.00

00000000000000000000 033323.00

00000000000000000000 033324.00

00000000000000000000 033325.00

00000000000000000000 033326.00

00000000000000000000 033327.00

00000000000000000000 033330.00

BIT0 XW,%8#400000.00,0,0
BIT1 XW,%8#200000.00,0,0
BIT2 XW,%8#100000.00,0,0
BIT3 XW,%8#40000.00,0,0
BIT4 XW,%8#20000.00,0,0
BIT5 XW,%8#10000.00,0,0
BIT6 XW,%8#4000.00,0,0
BIT7 XW,%8#2000.00,0,0
BIT8 XW,%8#1000.00,0,0
BIT9 XW,%8#400.00,0,0
BIT10 XW,%8#200.00,0,0
BIT11 XW,%8#100.00,0,0
BIT12 XW,%8#40.00,0,0
BIT13 XW,%8#20.00,0,0
BIT14 XW,%8#10.00,0,0
BIT15 XW,%8#4.00,0,0
BIT16 XW,%8#2.00,0,0
BIT17 XW,%8#1.00,0,0
BIT18 XW,%8#.40,0,0
BIT19 XW,%8#.20,0,0
BIT20 XW,%8#.10,0,0
BIT21 XW,%8#0.04,0,0
BIT22 XW,%8#0.02,0,0
BIT23 XW,%8#0.01,0,0
BIT24 %8#DD%BU#,0 000 000 010 000 000 000 000
BIT25 XW,0,0,0,4
BIT26 XW,0,0,0,2
BIT27 XW,0,0,0,1
BIT28 XW,0,131072,0
BIT29 XW,0,65536,0
BIT30 XW,0,32768,0
BIT31 XW,0,16384,0
BIT32 XW,0,8192,0
BIT33 XW,0,4096,0
BIT34 XW,0,2048,0
BIT35 XW,0,1024,0

400000.00 00 000000.00 00 033331.00
200000.00 00 000000.00 00 033332.00
100000.00 00 000000.00 00 033333.00
40000.00 00 000000.00 00 033334.00
20000.00 00 000000.00 00 033335.00
10000.00 00 000000.00 00 033336.00
4000.00 00 000000.00 00 033337.00
2000.00 00 000000.00 00 033340.00
1000.00 00 000000.00 00 033341.00
400.00 00 000000.00 00 033342.00
200.00 00 000000.00 00 033343.00
100.00 00 000000.00 00 033344.00
40.00 00 000000.00 00 033345.00
20.00 00 000000.00 00 033346.00
10.00 00 000000.00 00 033347.00
4.00 00 000000.00 00 033350.00
2.00 00 000000.00 00 033351.00
1.00 00 000000.00 00 033352.00
0.40 00 000000.00 00 033353.00
0.20 00 000000.00 00 033354.00
0.10 00 000000.00 00 033355.00
0.04 00 000000.00 00 033356.00
0.02 00 000000.00 00 033357.00
0.01 00 000000.00 00 033360.00
0000000010000000000000 033361.00
0.00 40 000000.00 00 033362.00
0.00 20 000000.00 00 033363.00
0.00 10 000000.00 00 033364.00
0.00 08 000000.00 00 033365.00
0.00 04 000000.00 00 033366.00
0.00 02 000000.00 00 033367.00
0.00 01 000000.00 00 033370.00
0.00 00 400000.00 00 033371.00
0.00 00 200000.00 00 033372.00
0.00 00 100000.00 00 033373.00
0.00 00 040000.00 00 033374.00

BIT36 XW,0,512,0
BIT37 XW,0,256,0
BIT38 XW,0,128,0
BIT39 XW,0,64,0
BIT40 XW,0,32,0
BIT41 XW,0,16,0
BIT42 XW,0,8,0
BIT43 XW,0,4,0
BIT44 XW,0,2,0
BIT45 XW,0,1,0
BIT46 XW,0,0,131072
BIT47 XW,0,0,65536
BIT48 XW,0,0,32768
BIT49 XW,0,0,16384
BIT50 XW,0,0,8192
BIT51 XW,0,0,4096
BIT52 XW,0,0,2048
BIT53 XW,0,0,1024
BIT54 XW,0,0,512
BIT55 XW,0,0,256
BIT56 XW,0,0,128
BIT57 XW,0,0,64
BIT58 XW,0,0,32
BIT59 XW,0,0,16
BIT60 XW,0,0,8
BIT61 XW,0,0,4
BIT62 XW,0,0,2
BIT63 XW,0,0,1

0.00 00 020000.00 00 033375.00
0.00 00 010000.00 00 033376.00
0.00 00 004000.00 00 033377.00
0.00 00 002000.00 00 033400.00
0.00 00 001000.00 00 033401.00
0.00 00 000400.00 00 033402.00
0.00 00 000200.00 00 033403.00
0.00 00 000100.00 00 033404.00
0.00 00 000040.00 00 033405.00
0.00 00 000020.00 00 033406.00
0.00 00 000010.00 00 033407.00
0.00 00 000004.00 00 033410.00
0.00 00 000002.00 00 033411.00
0.00 00 000001.00 00 033412.00
0.00 00 000000.40 00 033413.00
0.00 00 000000.20 00 033414.00
0.00 00 000000.10 00 033415.00
0.00 00 000000.04 00 033416.00
0.00 00 000000.02 00 033417.00
0.00 00 000000.01 00 033420.00
0.00 00 000000.00 80 033421.00
0.00 00 000000.00 40 033422.00
0.00 00 000000.00 20 033423.00
0.00 00 000000.00 10 033424.00
0.00 00 000000.00 08 033425.00
0.00 00 000000.00 04 033426.00
0.00 00 000000.00 02 033427.00
0.00 00 000000.00 01 033430.00

SSW SYN,%8□1301.0
ERS SYN,%8□1302.0
SERS SYN,%8□1304.0
RET SYN,%8□1306.40
RET1 SYN,%8□1307.0
RET2 SYN,%8□1307.40
SEN SYN,%8□1310.0
SENO SYN,%8□1311.0
DPET13 SYN,%8□1437.0
INT SYN,%8□1353.0
IDF1 SYN,%8□1443.0
IDF2 SYN,%8□1444.40
END,%8□20000.0

1301.00+ +00000000
1302.00+ +00000000
1304.00+ +00000000
1306.40+ +00000000
1307.00+ +00000000
1307.40+ +00000000
1310.00+ +00000000
1311.00+ +00000000
1437.00+ +00000000
1353.00+ +00000000
1443.00+ +00000000
1444.40+ +00000000
20000.00

033431.00