



# I.C.T 1900 SERIES

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## COMMUNICATIONS MULTIPLEXOR 1992

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### DESCRIPTION

The 1992 Communications Multiplexor is connected via Standard Interface to permit on-line data input and/or output between the Central Processor and up to 63 remote devices; the data transmission facilities will initially comprise telegraph and telephone links or a combination of these facilities. More than one Multiplexor can be connected to the larger processors.

A single-character buffer terminates each line at the computer end, and the Multiplexor scans the lines in sequence to make single-character transfers where appropriate between the buffers and corresponding message areas in the processor store. Control words established in the store by the Executive program for each line define the message area, address, length, etc., and are utilized by the processor's 'hesitation control' circuits during the transfer of each character.

A program interrupt is made to Executive when a complete message has been received or transmitted, by recognition of an end-of-message or transmission character, whereupon the communications program is initiated for message handling and line re-start procedures.

- Up to 63 lines served by one Multiplexor unit.
- Communication between processor and distant data transmission/reception devices.
- Automatic conversion to and from 1900 code.

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## **Communications facilities**

The Multiplexor can cater for any number of communication lines up to 63. Each line can permit one-way (simplex) data input, one-way data output or either-way non-simultaneous (half-duplex) data input/output. Communication can be made over telegraph lines with remote devices including teleprinters at speeds up to ten characters per second, and over telephone lines with I.C.T high-speed terminal equipment at speeds up to 100 characters per second, or more on private connections. The type of remote device will determine the form of buffer and associated termination equipment specified at the Multiplexor end of the line.

Code converters perform translations between transmission and internal computer character formats, and different converters can be specified in one system. For example, on input, a converter model is available to convert I.S.O. seven data-bit code to 1900 code automatically. Transmission codes of eight or more data bits are split into six-bit characters for computer entry. Parity checking is available on input.

## **Speed**

The limit on transmission speeds will be set by line capacity and/or by central processor characteristics.

*This specification is subject to modification*

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