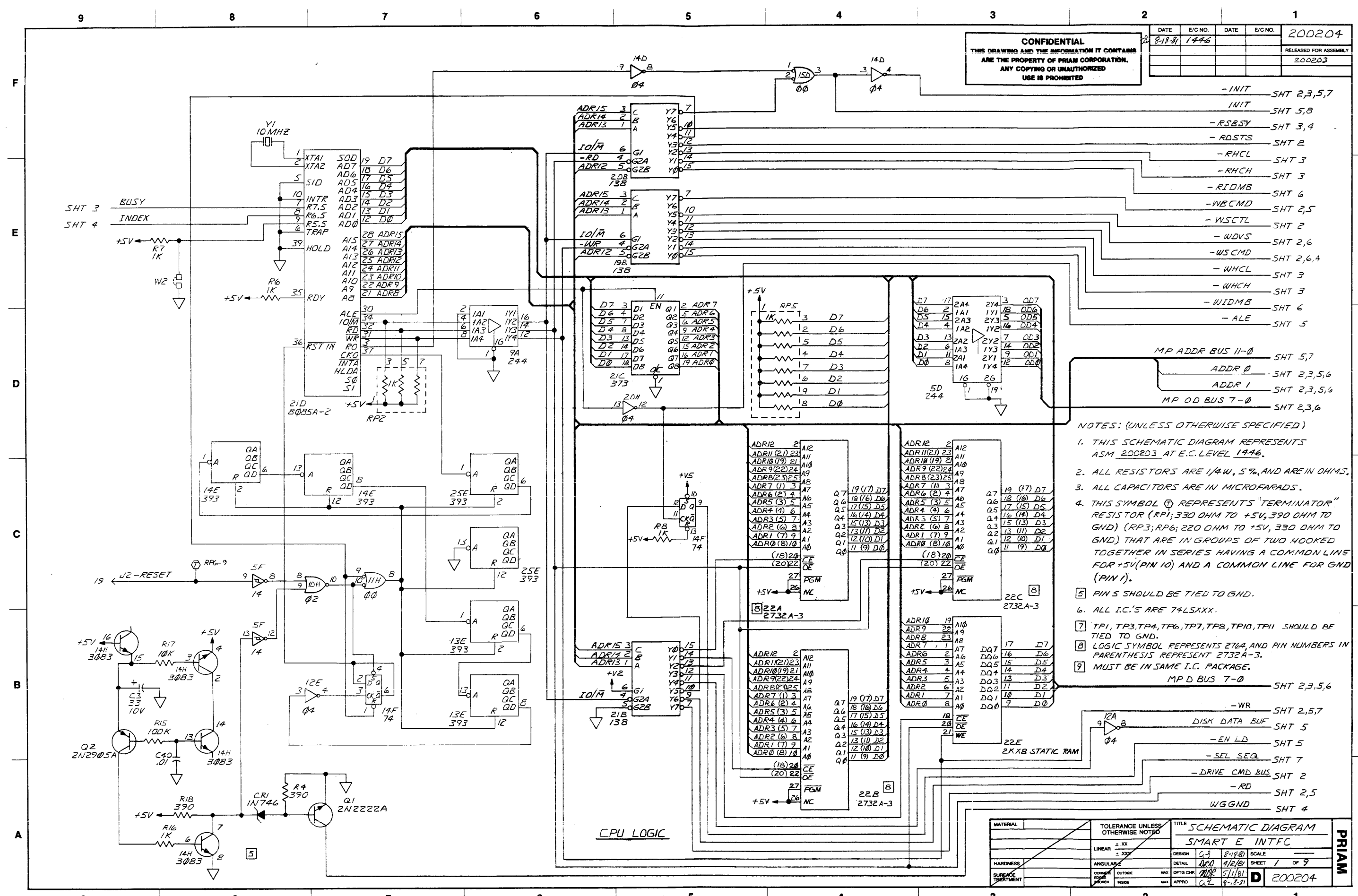


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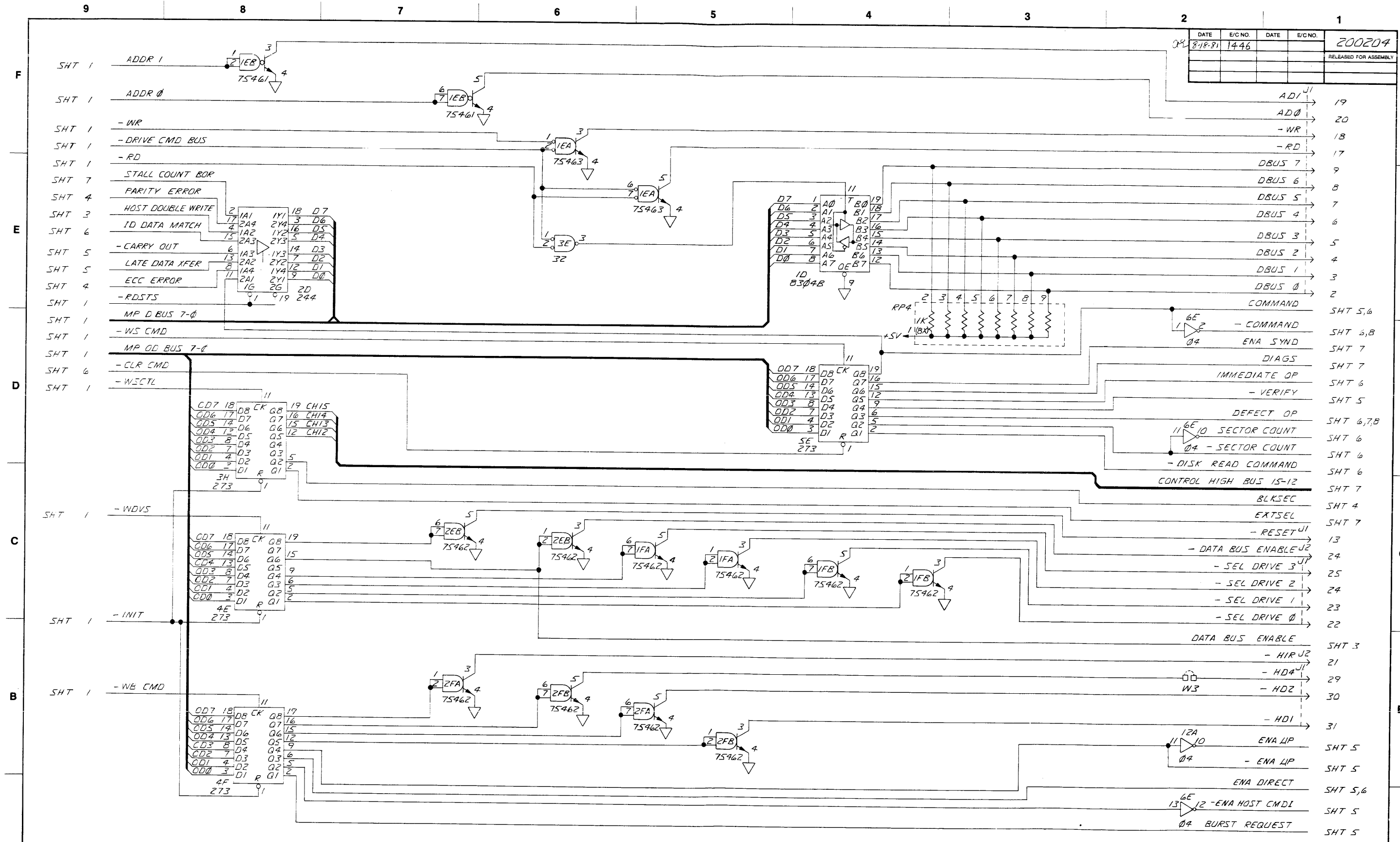
DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1446			
				RELEASED FOR ASSEMBLY
				200203



- NOTES: (UNLESS OTHERWISE SPECIFIED)
1. THIS SCHEMATIC DIAGRAM REPRESENTS ASM 200203 AT E.C. LEVEL 1446.
 2. ALL RESISTORS ARE 1/4W, 5%, AND ARE IN OHMS.
 3. ALL CAPACITORS ARE IN MICROFARADS.
 4. THIS SYMBOL $\text{\textcircled{R}}$ REPRESENTS "TERMINATOR" RESISTOR (RP1; 330 OHM TO +5V, 390 OHM TO GND) (RP3; RP6; 220 OHM TO +5V, 330 OHM TO GND) THAT ARE IN GROUPS OF TWO HOOKED TOGETHER IN SERIES HAVING A COMMON LINE FOR +5V (PIN 10) AND A COMMON LINE FOR GND (PIN 1).
 5. PIN 5 SHOULD BE TIED TO GND.
 6. ALL I.C.'S ARE 74LSXXX.
 7. TP1, TP3, TP4, TP6, TP7, TP8, TP10, TP11 SHOULD BE TIED TO GND.
 8. LOGIC SYMBOL REPRESENTS 2764, AND PIN NUMBERS IN PARENTHESIS REPRESENT 2732A-3.
 9. MUST BE IN SAME I.C. PACKAGE.

MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE		PRAM
		SCHEMATIC DIAGRAM SMART E INTFC		
HARDNESS	LINEAR 2. XX	DESIGN	SCALE	D 200204
SURFACE TREATMENT	ANGULAR 2. XX	DETAIL	SHEET 1 OF 9	
	CONTOUR 2. XX	DATE	8-18-81	
	FINISH 2. XX	APPROV	QZ	

DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1446			RELEASED FOR ASSEMBLY



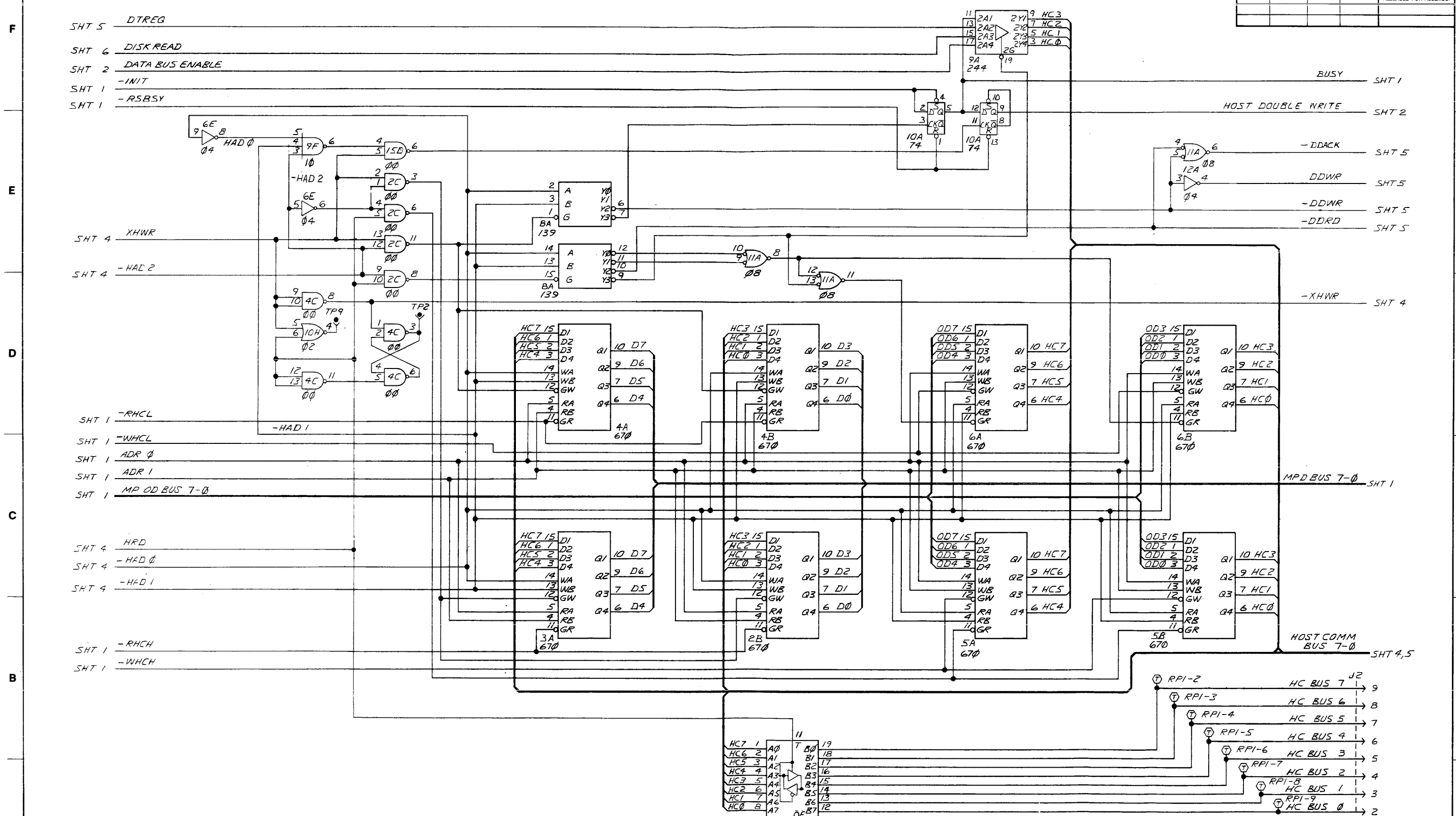
MISC REGISTERS

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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	SCHEMATIC DIAGRAM
	LINEAR ±.XX	DETAIL	SMART E INTFC
HARDNESS	ANGULAR ±.XXX	DESIGN	Q2 2/18/81
SURFACE TREATMENT	CORNER ROUNDED	DETAIL	Q2A 6/17/81
	MAX OUTSIDE	MAX DFTG CHG	705R 6/24/81
	MIN INSIDE	MAX APPRO	Q2 8-18-81
			200204

PRIAM

DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1446			RELEASED FOR ASSEMBLY

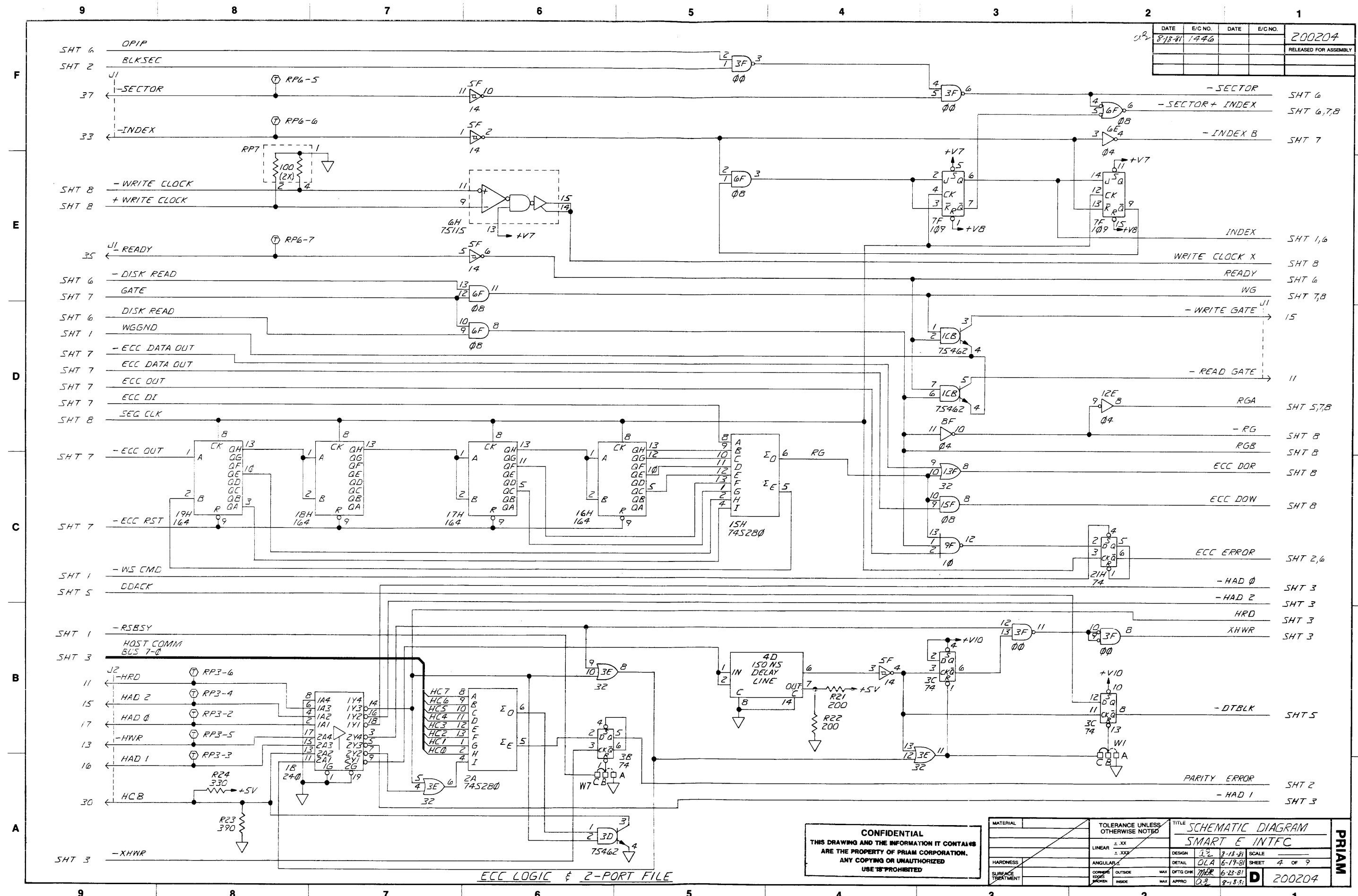


2-PORT FILE

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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	SCHEMATIC DIAGRAM	PRIAM
LINEAR ±.XX		SMART E INTFC		
HARDNESS	ANGULAR ±.XXX	DESIGN	8-18-81	SCALE
SURFACE TREATMENT	CORNER ROUNDED	DETAIL	4-21-81	SHEET 3 OF 9
	OUTSIDE MAX	DATE CHG	11/20/81	
	INSIDE MAX	APPRO	8-18-81	200204

DATE	E/C NO	DATE	E/C NO	200204
8-18-81	1446			RELEASED FOR ASSEMBLY



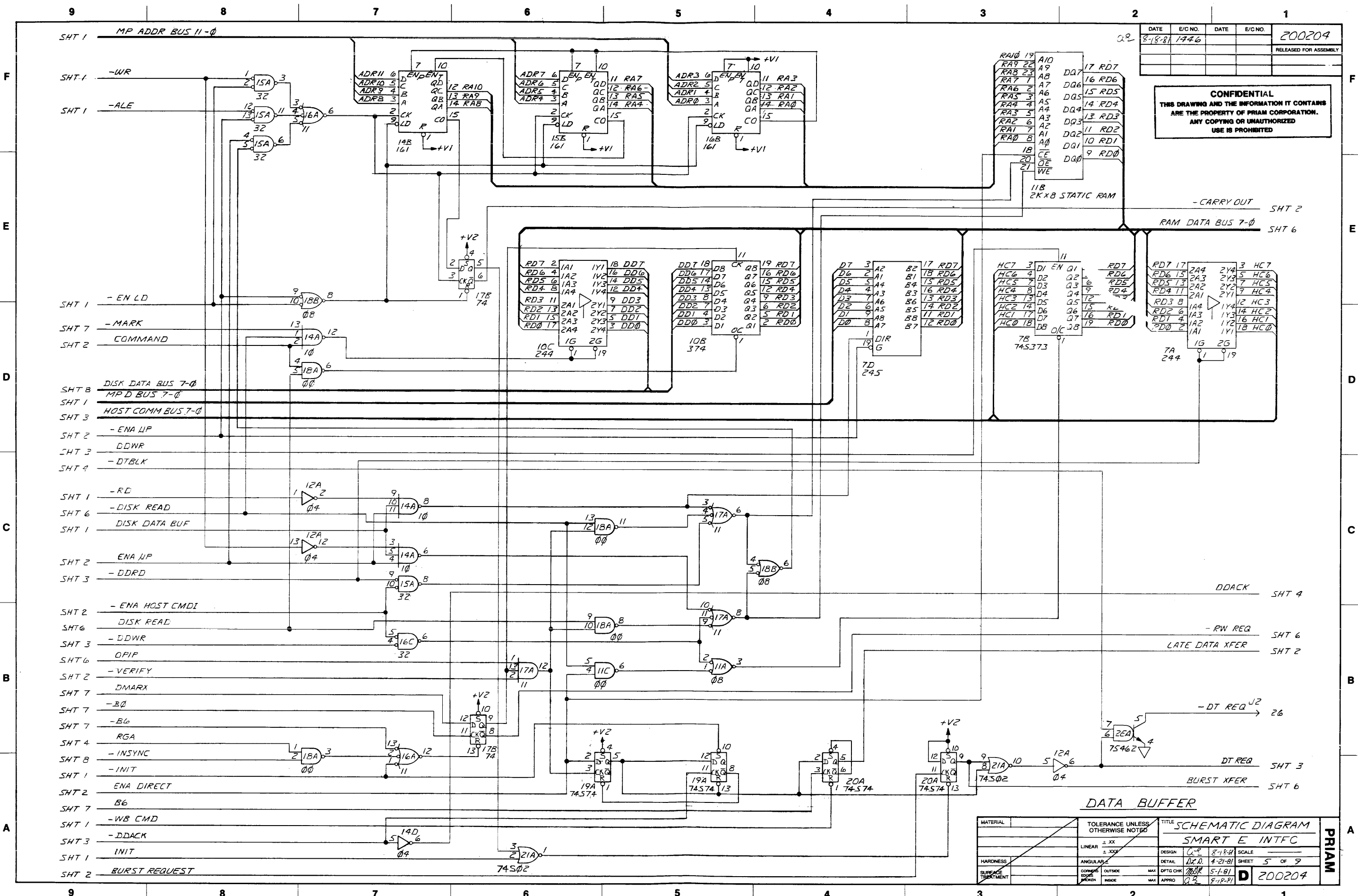
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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	SCHEMATIC DIAGRAM
	LINEAR ± .XX	DETAIL	OLA 6-19-81
HARDNESS	ANGULAR ± .005	SCALE	4 OF 9
SURFACE TREATMENT	CORNER RADIUS	DATE	8-18-81
	OUTSIDE	DESIGN	22
	INSIDE	APPRO	02
		DATE	8-18-81
		SCALE	D 200204

ECC LOGIC & 2-PORT FILE

DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1446			RELEASED FOR ASSEMBLY

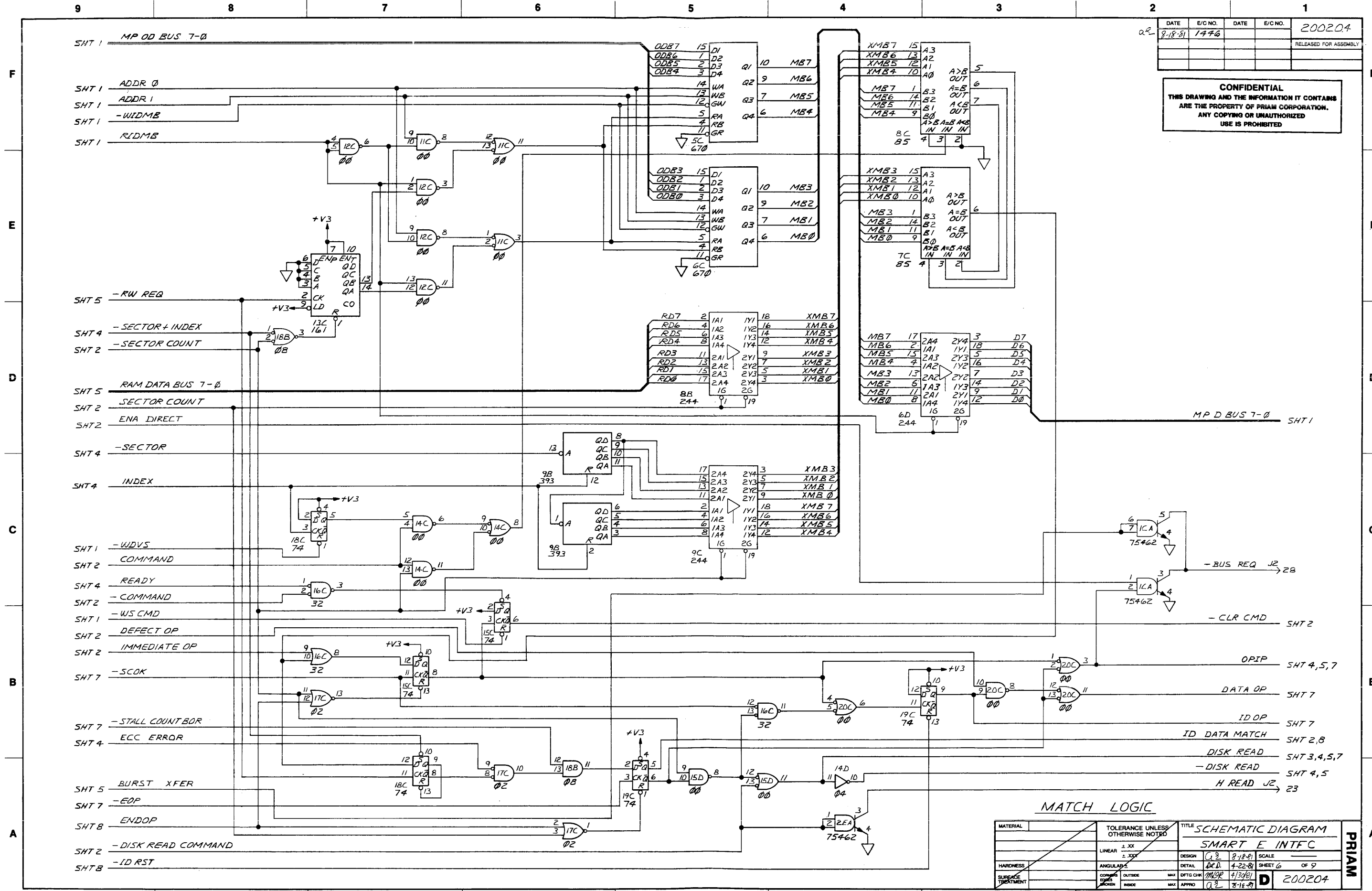
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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE SCHEMATIC DIAGRAM		PRIAM
		SMART E INTFC		
HARDNESS	ANGULAR	DESIGN	SCALE	
SURFACE TREATMENT	CORROSION PROTECTED	DETAIL	5 OF 9	
		MAX DFTG CHK	5-1-81	D
		MAX APPRO	8-1-81	200204

DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1476			
RELEASED FOR ASSEMBLY				

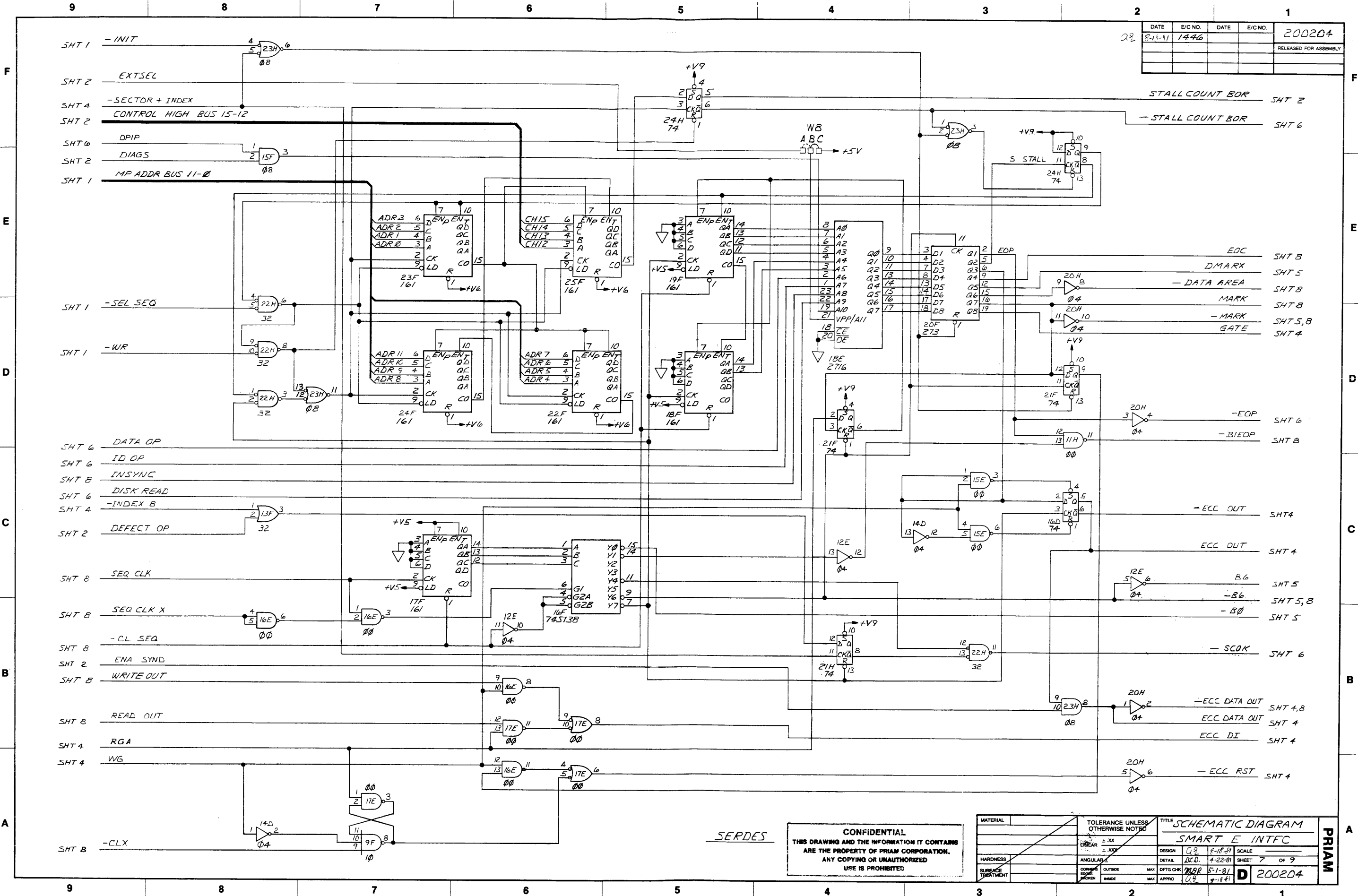
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MATCH LOGIC

MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	PRIMA
	LINEAR ±.XX	SCHMATIC DIAGRAM	
	ANGULAR ±.XX	SMART E INTFC	
HARDNESS	DESIGN	DATE	SCALE
SURFACE TREATMENT	DETAIL	4-22-81	SHEET 6 OF 9
	COMP. DATE	MAX. DATE	APPRO. DATE
	1/30/81	2-18-81	D 200204

DATE	E/C NO.	DATE	E/C NO.	200204
8-11-81	1446			RELEASED FOR ASSEMBLY

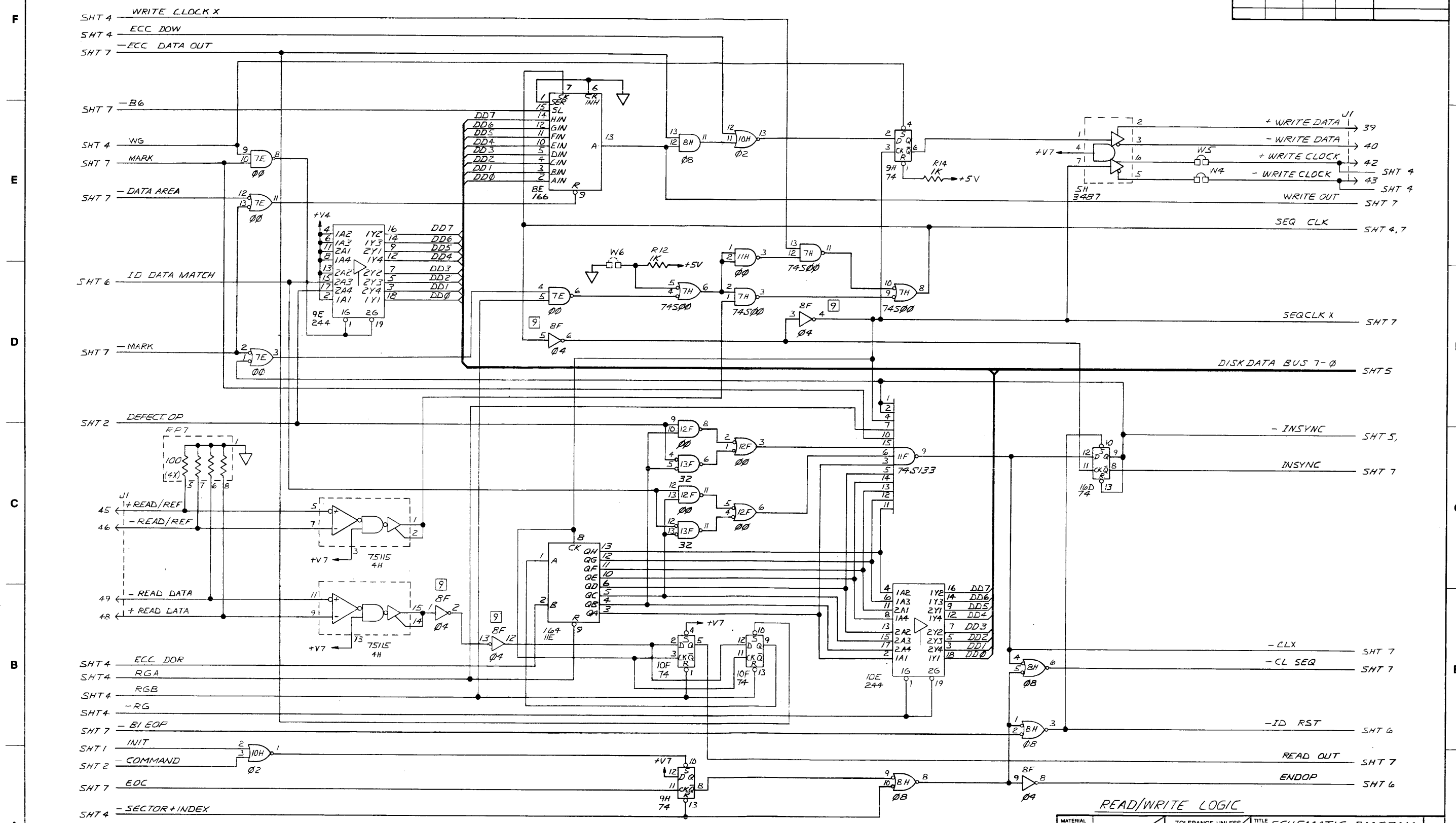


SERDES

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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	PRIAM
		SCHMATIC DIAGRAM	
		SMART E INTFC	
HARDNESS	ANGULAR	DESIGN	SCALE
SURFACE TREATMENT	CORNER R	DETAIL	7 OF 9
	OUTSIDE	DFTG CHG	
	INSIDE	MAX	
	APPRO	MAX	
		DATE	200204

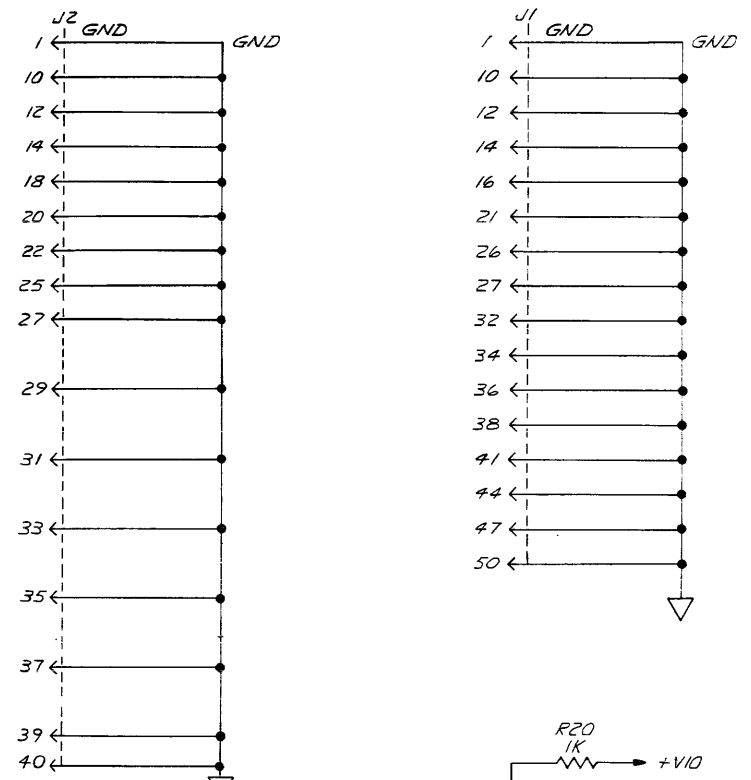
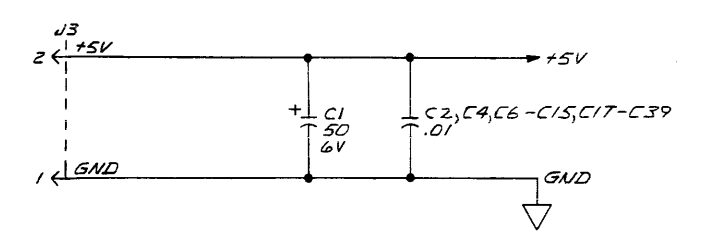
DATE	E/C NO.	DATE	E/C NO.	200204
8-18-81	1446			RELEASED FOR ASSEMBLY



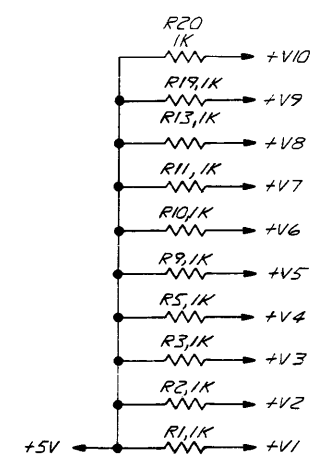
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	LINEAR ± .XX		ANGULAR ± .XX°		SCHEMATIC DIAGRAM		
	HARDNESS		SURFACE TREATMENT		SMART E INTFC		
	CORNER RADIUS		OUTSIDE		DESIGN 12-5-81 SCALE		
INSIDE		MAX		DETAIL 4-22-81 SHEET 8 OF 9		DATE 5/1/81	
		MAX		DFTG CHK		APPROV	
				MAX		APPROV	
						D 200204	

DATE	E/C NO.	DATE	E/C NO.	200204
1446				RELEASED FOR ASSEMBLY

I.C. TYPE	LOCATION	UNUSED ELEMENTS	+5V	GND
74LS00	18A,3F,2C,4C,11C,12C,14C,20C,15D,7E,15E,16E,17E,12F,11H	11H-1,2,15E-3,4,14C-1	14	7
74LS02	17C,10H	17C-2	14	7
74LS04	12A,14D,6E,12E,8F,20H	21A-2,4	14	7
74LS08	11A,18B,6F,15F,8H,23H	20H-3,6E-2,12E-1	14	7
74LS10	14A,9F	15F-2,4	14	7
74LS11	16A,17A	16A-3,17A-1	14	7
74LS14	5F		14	7
74LS240	1B	1B-4	20	10
74LS32	15A,16C,8D,13F,22H,3E	3E-1	14	7
74LS74	3B,3C,10A,17E,15C,17C,16D,10F,19F,21F,9H,21H,24H,18C	3B-2	14	7
74LS85	7C,2C		14	7
74LS109	7F		16	8
74LS133	11F		16	8
74LS138	19B,20B,21B		16	8
74LS139	8A		16	8
74LS161	14B,15B,16B,13C,17F,18F,19F,22F,23F,24F,25F		16	8
74LS164	11E,16H,17H,18H,19H		14	7
74LS166	8E		16	8
74LS244	7A,9A,2B,9C,10C,2D,5D,6D,9E,10E		20	10
74LS245	7D		20	10
74LS273	4E,5E,4F,20F,3H		20	10
74LS373	21F		20	10
74LS373	7B		20	10
74LS374	10E		20	10
74LS393	9B,25E,14E,13E	25E-2	14	7
74LS670	3A,4A,5A,6A,2B,4B,5B,6B,5C,6C		16	8
75461	1EB		8	4
75462	1CA,2EA,2EB,1FA,1FB,2FA,2FB,1CA,3D	3D-2	8	4
75463	1EA		8	4
74S286	15H,2A		7	14
2716	18E		24	12
75462	1CE		8	
34E7	5H	5H-2	16	8
8085	21D		40	20
83046	1A,1D		20	10
75115	4H,6H	6H-1	16	8
2083	14H			5
2732A-5	22A,22E,22L		24	12
2Kx8 STATIC RAM	22E,11E		24	12
74LS138	16F		16	8
DELAY LINE	4D			8,14



REFERENCE DESIGNATION	
LAST USED	NOT USED
C75	C5, C16
Q2	
RP7	
R22	
Y1	
CR1	
WB	
TP11	TP5
J3	



V DESIGNATION	CONNECTED TO
V1	14B-1,15B-1,16B-1,7,10
V2	17B-4,10,20A-10,12,19A-4,21B-6
V3	13C-9,7,10,18C-2,4,15C-2,10,19C-4,12,10
V4	7E-2,4,3,13,6,11
V5	14F-10,18F-9,19F-9,17F-9,7,10
V6	23F-1,24F-1,22F-1,25F-1
V7	6H-13,7F-5,11,4H-13,3,5H-4,10F-4,9H-12
V8	7F-1,15
V9	24H-4,10,12,21F-10,4,21H-10
V10	3C-2,4,10

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MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE	SCALE
		SCHEMATIC DIAGRAM	
		SMART E INTFC	
HARDNESS	ANGULAR	DESIGN	SCALE
		4-22-81	9 OF 9
SURFACE TREATMENT	CORNER RADIUS	DETAIL	APPRO
		WDR	7/4/81
			D 200204

PRAM