

# **DRAWING MANUAL**

- **P 150**
  - **P 300**
    - **P 600**
      - **OPTIONS**

**PART NO. 106597**  
**APRIL 1982**

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## **PRINTRONIX**

17500 CARTWRIGHT, IRVINE, CALIFORNIA 92714  
(714) 549-7700 TWX 910-595-2535



Printronic Printer with Pedestal



Printronic Printer with Pedestal

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# MNEMONIC DICTIONARY

The following list presents and defines, in alphabetical order, the Mnemonics used in logic diagrams contained in the basic printer. Initial letter N denotes negative -true. All references to Mnemonics will be found under the positive -true listing, i.e., with the "N" not appended. Numeric mnemonics are found at the end of the alphabetic list.

ADV	Advance. Active when the Paper Advance flip flop is set or the slew signal is active.
BC	Binary Count (EVFU.....DB-5 True)
CC1, CC2, CC3	Character Column Binary Count
CLK, CLK A, CLK B	Clock (2-MHz System Clock)
COM	Compare (Serial Data to Hammer Bank Shift Register)
DBEL	Bell Character (Decoded Command Not Used)
DB1-DB8	Data Bits (Latched)
DC1-DC4	Dot Column Count Stages (Binary)
DCRR	Carriage Return (Decoded Command)
D8LPI	Enable 8 LPI (Decoded Command)
DEL	Elongated Character (Decoded Command)
DFMFD	Form Feed (Decoded Command)
DLC	Delayed Last Count (EVFU.....RC=3)
DLNFD	Line Feed (Decoded Command)
DR	Data Request
DSTB	Delayed Strobe
DUL	Underline (Decoded Command)
DVT	Vertical Tab (VFU.....Decoded Command)
ECR	End Carriage Return
ECS	Extended Character Set (SOV + IDB8)
EDT	Enable Data Transfer
EFF	End Form Feed (Line 66)
ELB	End Load Buffer (RESYNC . EOL)
ELD	End Load (Decoded Command VFU)
EMOV	Early (Paper) Move (Controller Prom)
ENDPRT	End Print (7th; 9th Dot Rows Under PRT Control)
EOL	End of Load (Buffer Loaded)
EOP	End of Print
EPFP	End Paper Feed Pulse
EPP	Even Paper Pulse
ESR	End Shift Register (Shift Register Justified)
EVR	Enable EVFU Ready
EXC	Extra Characters (Prom Select)
FIR	First (Character Set; Prom Select)
FL	Fault (Paper Motion)
FLL	Forms Last Line (Line 65)
FLT	Fault (Power Supply or Interlock)
FMFD	Forms Feed
GND	Ground (Logic Return

HB21	Hammer Bank (Hammer) 21
HC1-HC44	Hammer Coil Drive
HCK	Hammer Clock (Latch)
HDP	Half Dot Plot (Command)
HMC	Hammer Master Clear (+5V dc up)
HRS	Hammer Reset
HSC	Hammer Shift Clock (Register)
IACK	Acknowledge (Interface)
IBOF	Bottom of Form (Interface)
ICBY	Busy (Interface)
ICPE	Ready for Centronics, Printer Enable
ICSTB, IDSTB	Strobe (Interface....From Controller)
IDB1-IDB8	Data Bits (Interface)
IDR	Data Requests (Interface....."Data Demand")
IONL	On Line (Interface)
IPI	Paper Instruction (Interface)
IRDY	Ready (Interface)
LB	Load Buffer (Printer State Control)
LC	Lower Case (PROM Select)
LCHK	Check Lamp (Control Panel)
LD	Load EVFU
L8LPI	8 Lines Per Inch Lamp Drive
LONL	On Line Lamp (Control Panel)
LVRDY	Top of Form Lamp (EVFU Loaded)
MC	Master Clear (Power-up Start)
MC3	Master Clear to MVFU
MPU	Magnetic Pickup (Amplified Digital Signal from Timing Disk)
NBOF	Bottom Of Form from Mechanical VFU (option)
ND8LPI	Decoded 8 Lines Per Inch Command
NDCRR	Decoded Carriage Return
NDELCL	Decoded Elongated Character
NDFMFD	Decoded Form Feed Command
NDLC	Delayed Last Count (EVFU.....RC=0)
NDLNFD	Decoded Line Feed Command
NDSTB	Data Strobe. Internally generated Data Strobe Initiated by receipt of either ICSTB or IDSTB.
NDUL	Decoded Underline Command
NDVT	Decoded Vertical Tab Command
NECR	End Carriage Return
NECS	Extended Character Set. Activated by Data Bit 8. Selects extended character PROMS.
NEFF	End Form Feed. Resets the Form Feed Flip Flop
NELB	End Load Buffer. Terminates the Load Buffer mode and places printer in Print mode.
NEMV	Enable Mechanical VFU
NELD	End Load Command to EVFU.

NEOL	End of Load. Activated upon receipt of a paper movement command.
NESR	End Shift Register. Active when the Shift Register Memory Counter holds the count of 132.
NEVC	End MVFU Command. When active, indicates MVFU has stopped at designated channel on carriage tape.
NEVR	Electronic VFU Ready. Indicates EVFU memory has been loaded.
NFIR	0-64 Character Enable Paper Motion Fault.
NFL	Fault. Activated by paper motion detection logic.
NFLT	Paper motion fault.
NFMFD	Fault. Indicates platen open, paper out or power supply voltages not in tolerance.
NFMRS	Form Feed. Active when Form Feed flip flop is set.
NHCK	Form Feed Reset. Generated by MVFU at end of form-feed operation.
NHCK	Hammer Clock. Transfers hammer activation data from Hammer Shift Register to Hammer Data Latches and causes hammers to fire.
NHDP	Half Dot Plot.
NHSC	Hammer Shift Register Clock. Clocks hammer serial data (COM) into Hammer Driver Shift Register.
NLC	Lower Case. Addresses lower case PROMS.
NLD	Load. Active when the EVFU is in the load cycle.
NMVE	Mechanical VFU Enabled. Active when carriage tape is installed in the MVFU reader.
NNRESYNC	Output of the Q output of the NRESYNC flip flop.
NOCD	Output Clock Disable(for test).
NONL	On Line
NOPP	Odd Paper Pulse.
NPADV	Paper Advance. Active when the Paper Advance flip flop is set.
NPAL	Print Additional Lines. Active when printing lower-case characters with descenders or when printing underlines.
NPC	Printable Character. Active when the printable character flip flop is set.
NPDE	Paper Drive Enable. Inversion of RDY.
NPIC	Paper Instruction Command. Active (low) only when jumper W5 on Logic A board is installed.
NPM	Plot Mode.
NPRT	Print Mode. Active when Print Mode flip flop is set.
NPSYNC	Print Sync.
NPT	Printable Character. Active when either data bit 6 or data bit 7, or both, is high.
NPTCH	Printable Character. NPT in combination with DSTB.
NRCLK	Row Counter Clock.
NRCR	Row Counter Reset.
NRDY	Ready. Active when there are no faults in the printer.
NREC	Recirculate. Controls whether the Shift Register Memory will be in the load of the recirculate mode.
NRESYNC	Resync. Indicates the hammer bank shuttle is starting a left-to-right cycle.
NRLB	Combination of Load Buffer mode and On Line.

NSD-NSD1	Shuttle Drive. Activates the hammer bank shuttle drive motor and the ribbon drive motors.
NSLW	Slew. Places the paper feed logic in high speed operation.
NSP	Space Code.
NSPFP	Start Paper Feed Pulse.
NSPI	Paper Instruction Strobe. Combination of DSTB and PI.
NSPT	Start Print. Occurs during shuttle turn-around.
NSRCLK	Clocks Shuttle-Up-To-Speed logic.
NST	Shift Register Clocks.
NSTB3	Self Test. Places printer in self-test mode when both Check and Paper Advance switches are activated simultaneously.
NSTCH	Strobe 3. Used in the generation of IACK.
NSTCLR	MVFU runaway.
NSTL	Self Test Clear. Active when the ACK flip flop is reset.
NSWNE SR	Start Load. Command to EVFU logic to start loading EVFU memory.
NTFL	Combination of self-test switch and NESR (End Shift Register).
NTOF	Final Line Count (Top Counter Clock).
NULR	Top of Form. Command used to set the Form Feed flip flop.
NVB5	Underline Recirculate. Controls whether the Underline Shift Register will be in the Load or Recirculate mode.
NVFS	VFU Bit 5
NVSC	Vertical Format Slew. Places Paper Feed logic in high speed operation.
NVTF	Vertical Format Shift Clock. Used to clock the EVFU shift register memory.
NVTOF	Vertical Format Top Of Form. Commands the EVFU to search for Top Of Form.
OE	Vertical Format Top Of Form. Command from optional MVFU or FLS to move paper to top of form.
ONL	Output Enable (Hammer Drive Interlock or Timing)
OPP	On Line
PADV	Odd Paper Pulse
PAL	Paper Advance
PAPER OUT SW	Print Additional Lines
PC	Paper Out Detector Microswitch Below Platen
PDE	Printable Character (Latched)
PFD	Paper Drive Enable
PF1	Paper Feed Drive (Interlock)
PF2	Paper Feed 1 (2 Phase Generator)
PFM01-PFM04	Paper Feed 2 (2 Phase Generator)
PI	Paper Feed Motor Phases 1-4 (Current Drive)
PIC	Paper Instruction (Interface or Decoded)
PM	Paper Instruction Code (Centronics Configured Jumper)
PMD	Plot Mode
	Paper Motion Detect

PPF	Paper Feed (Printer State Control)
PRT	Print (Printer State Control)
PS	Polarity Select (Interface Polarity)
PUA(B,C,D)	Pull-up (x) to +5V
PT	Printable Character (No Strobe)
PTCH	Printable Character (Strobed)
PSYNC	Print (Re)sync
P600E	Printer (Model) 600 Enable
RC1-RC4	Row Clock (Binary) Count
RCLK	Row Clock
RCR	Row Counter Reset
RDY	Ready, Indicates absence of faults within the printer
REC	Recirculate (Buffer Memory)
RESYNC*	Resync Pulse (From MPU, 25 Msec.)
RESYNCR	Resync (Or Hammer Reset)
RLB	Reset Load Buffer (Load Buffer . On Line)
RSPL	Row Sprocket Logic. Sprocket Logic pulse generated internally on the MVFU PCBA
S5	Stage 5 (16- $\mu$ sec CLK Count)
S8LPI	8 LPI Switch (Control Panel)
SCHK	Check Switch (Control Panel)
SD	Shut Down (Hammer Driver Interlock Line)
SDB1-SDB8	Shift Register Data Bits (Print Buffer Output)
SDUL	Shift Underline (Print Buffer Output)
SLW	Slew
SONL	On Line Switch (Control Panel)
SOV	Shift Out (Special Character Set)
SP	Space (Code)
SPADV	Paper Advance Switch
SPFP	Start Paper Feed Pulse
SPI	Strobed Paper Instruction (PI . DSTB)
SRCRST	Shift Register Counter Reset
ST	Self Test (Mode Switch)
STB1, 2, 3	Strobe 1, 2, 3 (Interface Character Timing)
STCLR	Self Test Clear
STOF	Top of Form Switch (Control Panel)
SRCLK	Shift Register Clock
STL	Start Load (Decoded Command VFU)
SUS	Shuttle Up to Speed (Unlabeled)
SWNESR	Line Feed in Self Test (Self Test Switch and Justified Buffer Signal).
SYNC	Sync Pulse from MPU (0.5 $\mu$ sec.)
S45	(Test)
TAI	Turn Around Indication (Controller Prom)
TC3	Top of Form Counter Stage 3 (Count 4)
TFLC	Final Line Count (TOF Counter Clock)
ULR	Underline Recirculate (Buffer Memory)

\* This term also has derivatives NRESYNC and NNRESYNC. They are functions of the Resync F/F and wide MPU Slot detection.

VB1-VB4	VFU Data Bits 1-4
VB5	VFU Bit 5 (Enable Binary Count or Centronics Mode)
VFS	Vertical Format Slew
VRDY	VFU Memory Loaded (Ready for Commands)
VR	1.0V dc Reference Voltage
VREF	Reference Voltage . + 4.7 Volts
VSC	VFU Shift Register Clock
2CPH(3,6)	(X) Characters Per Hammer
5HL	+5V dc Hammer Level (Supply to Hammer Latches)
8PI	8(V) Power Indicator (Lamp Voltage)
+8VDC	+8V dc Supply (Filtered)
+28VH	+28V dc Supply
-30VDC	Filtered -30V dc, Paper Feed
+36VH	+36V dc Supply (Hammers)
-33VDC	Filtered - 33Vdc
+70V	+70V (For Paper Feed)
160US	Time Out (For Hammer Reset) 176 sec

# DATA PRODUCTS CONNECTOR

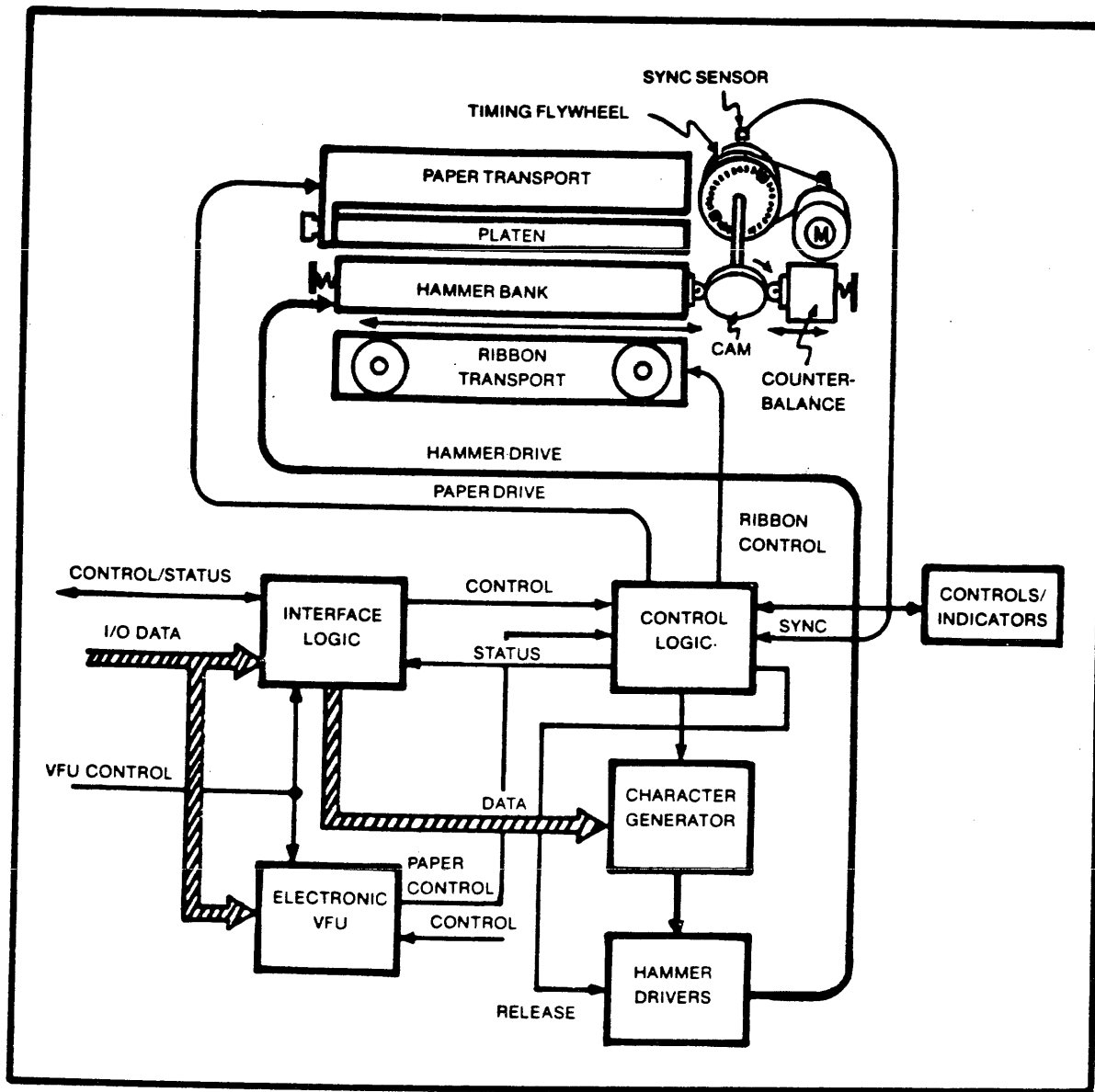
SIGNAL NAME	3M CONN	WINCHESTER	WIRE COLOR
IDB1	1	B	brown
RET	2	D	red
IDB2	3	F	orange
RET	4	J	yellow
IDB3	5	L	green
RET	6	N	blue
IDB4	7	R	violet
RET	8	T	gray
IDB5	9	V	white
RET	10	X	black
IDB6	11	Z	brown
RET	12	b	red
IDB7	13	n	orange
RET	14	k	yellow
IDB8	15	h	green
RET	16	e	blue
IPI	17	p	violet
RET	18	s	gray
IDSTB	19	j	white
RET	20	m	black
IDR	21	E	brown
RET	22	C	red
IRDY	23	CC	orange
RET	24	EE	yellow
IONL	25	Y	green
RET	26	AA	blue
+5 Volts	27	HH	violet
Interface Verify	Jumper	x to v	
Spare	37	P	violet
RET	38	M	gray
Spare	35	d	green
RET	36	f	blue



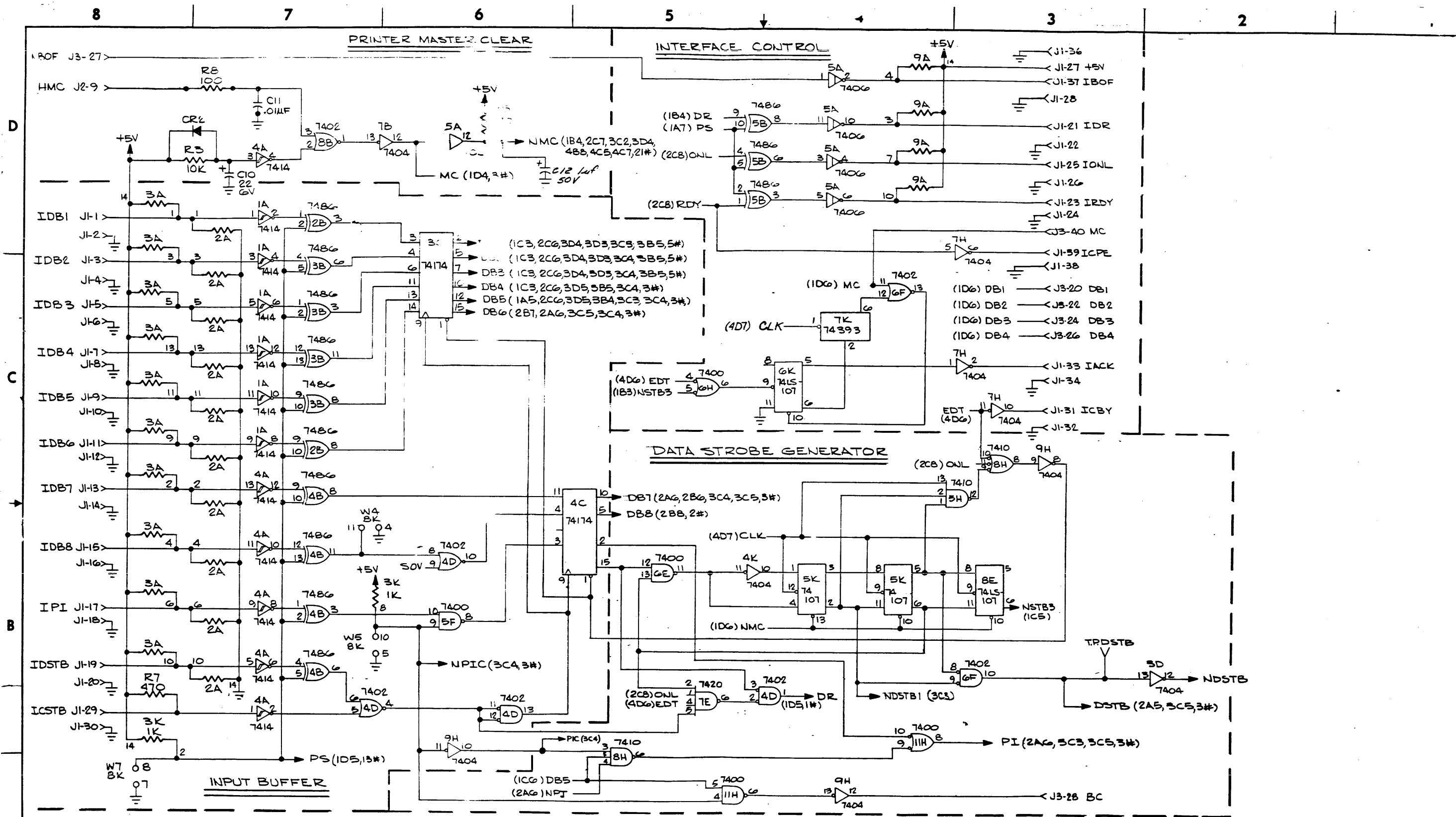
# CENTRONICS CONNECTOR

SIGNAL NAME	3M CONN	AMPHENOL	COLOR
IDB1	1	2	brown
RET	2	20	red
IDB2	3	3	orange
RET	4	21	yellow
IDB3	5	4	green
RET	6	22	blue
IDB4	7	5	violet
RET	8	23	gray
IDB5	9	6	white
RET	10	24	black
IDB6	11	7	brown
RET	12	25	red
IDB7	13	8	orange
RET	14	26	yellow
IDB8	15	9	green
RET	16	27	blue
IPI	17	15	violet
RET	18	14	gray
IDSTB	19	*	white
RET	20	*	black
IONL	25	13	green
+5 Volts	27	18	violet
ICSTB	29	1	white
RET	30	19	black
ICBY	31	11	brown
RET	32	29	red
IACK	33	10	orange
RET	34	28	yellow
ICPE	39	12	white
RED	22	16	red
GND	24	33	yellow
CHASSIS GND	--	17	-----
JUMPER		13 to 32	-----

\* Solder white wire (No. 19) to black wire (No. 20) and cover junction with shrink tubing.



Printronix, General Functional Organization



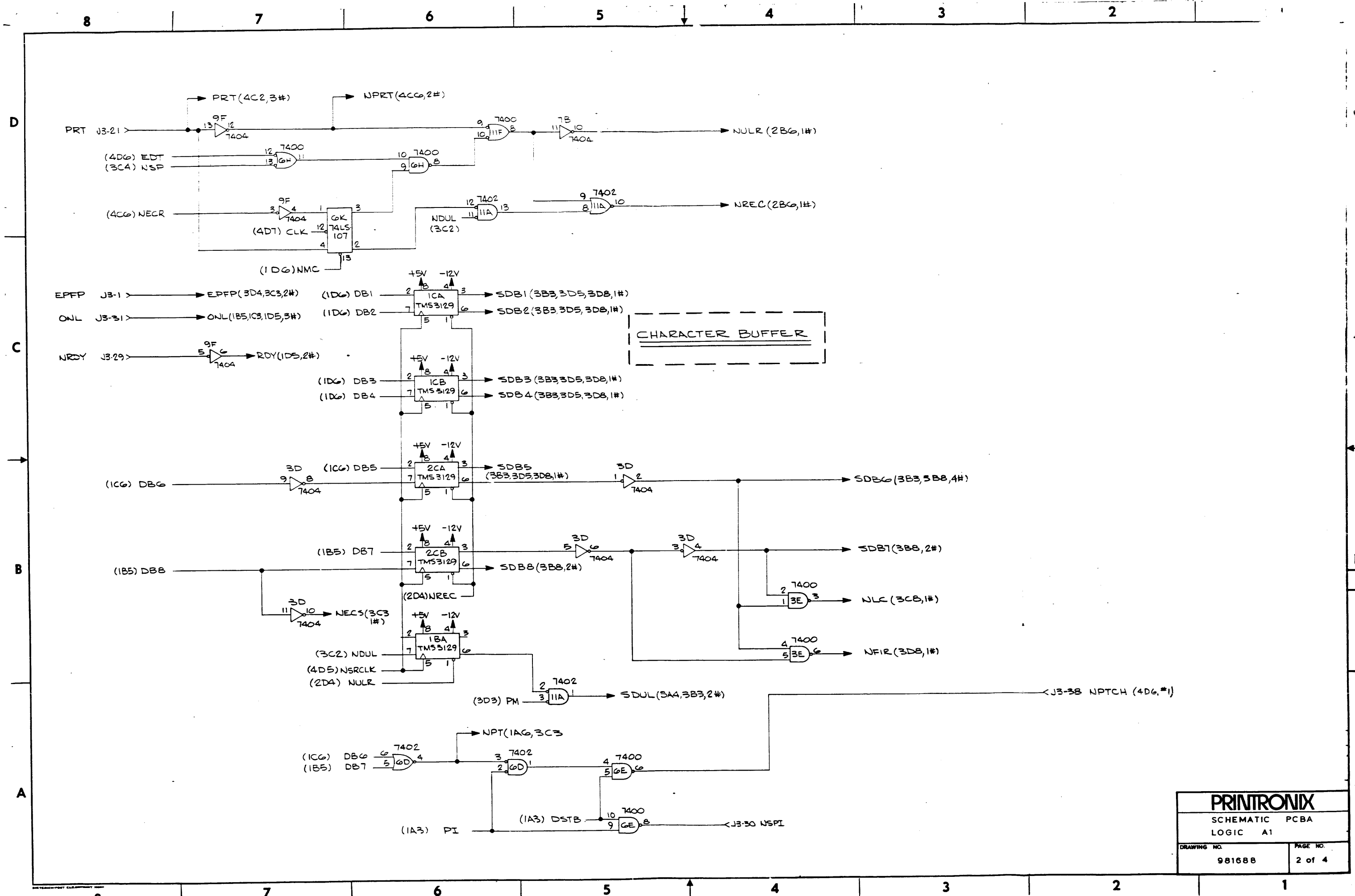
**JUMPER TABLE**

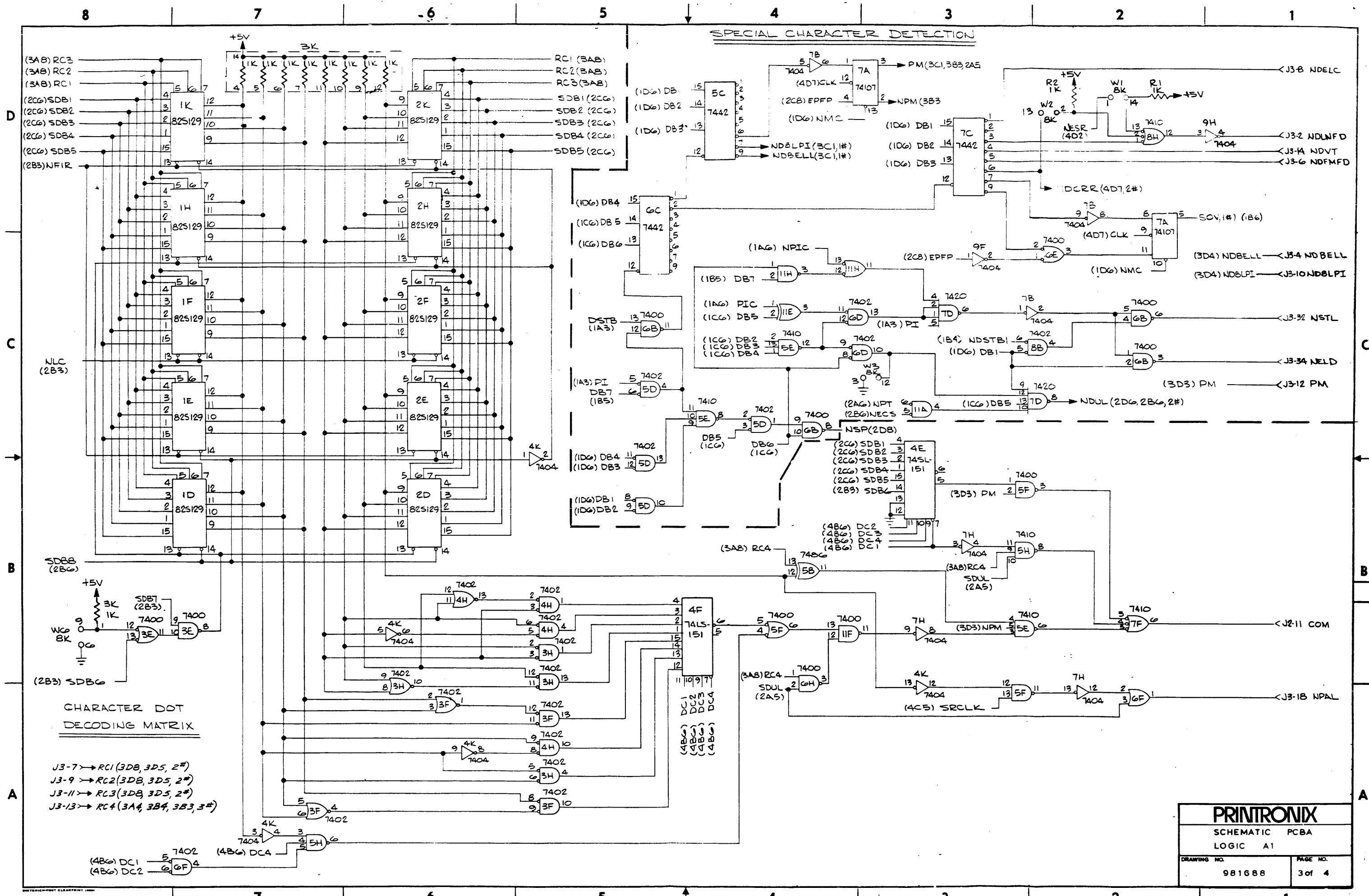
W1	AUTOMATIC LINE FEED
W2	CR=LF
W3	UNDERLINE DISABLE
W4	DATA BITS DISABLE
W5	PI DISABLE
W6	PRINT LOWER CASE AS UPPER CASE
W7	I/O POLARITY

**CONNECTORS**

J1	INPUT
J2	POWER
J3	BOARD TRANSFER

**PRINTRONIX**  
 SCHEMATIC PCBA  
 LOGIC A1  
 DRAWING NO. 981688 PAGE NO. 1 of 4





**CHARACTER DOT  
DECODING MATRIX**

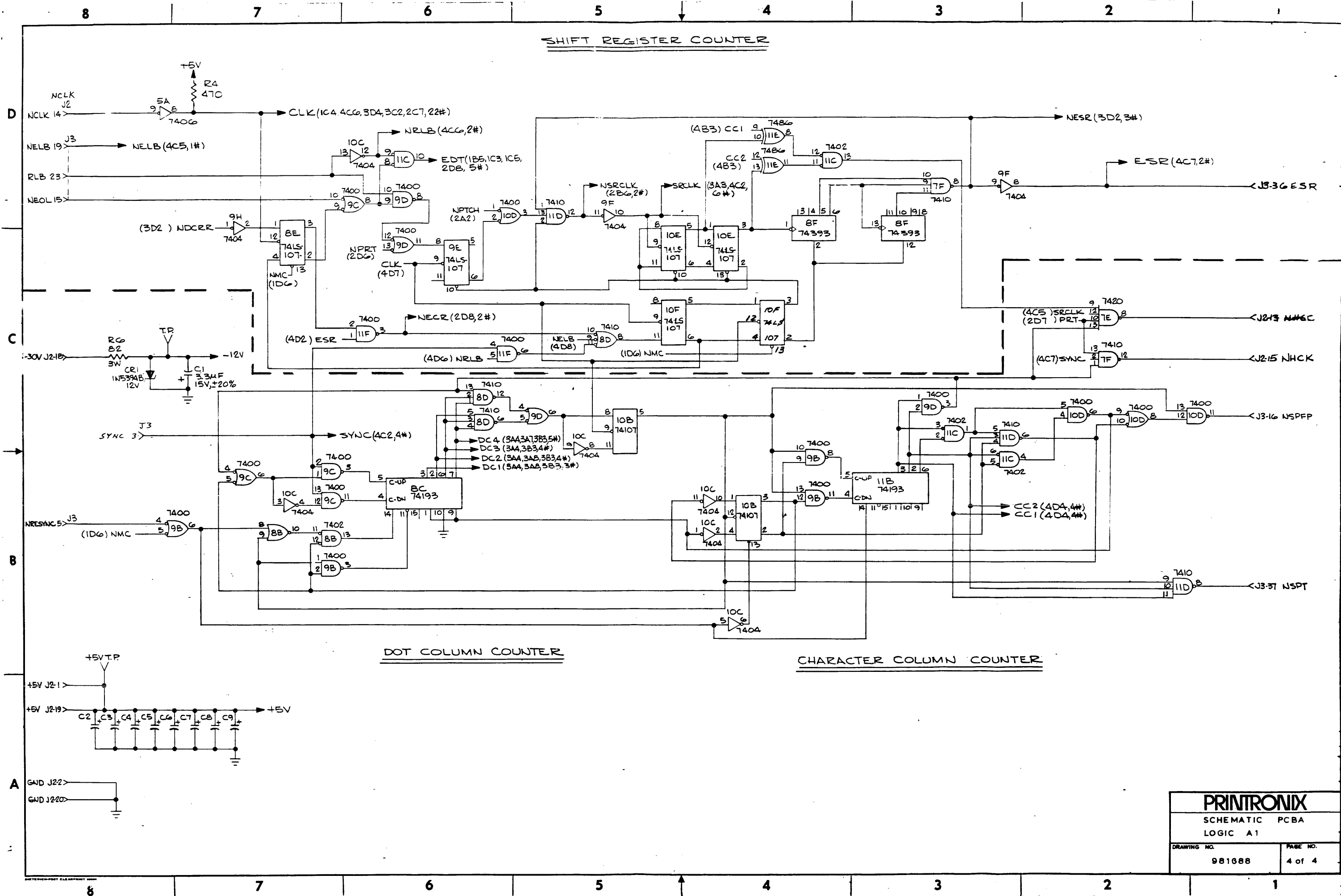
- J3-7 → RC1 (3DB, 3DS, 2#)
- J3-9 → RC2 (3DB, 3DS, 2#)
- J3-11 → RC3 (3DB, 3DS, 2#)
- J3-13 → RC4 (3A4, 3B4, 3B3, 3#)

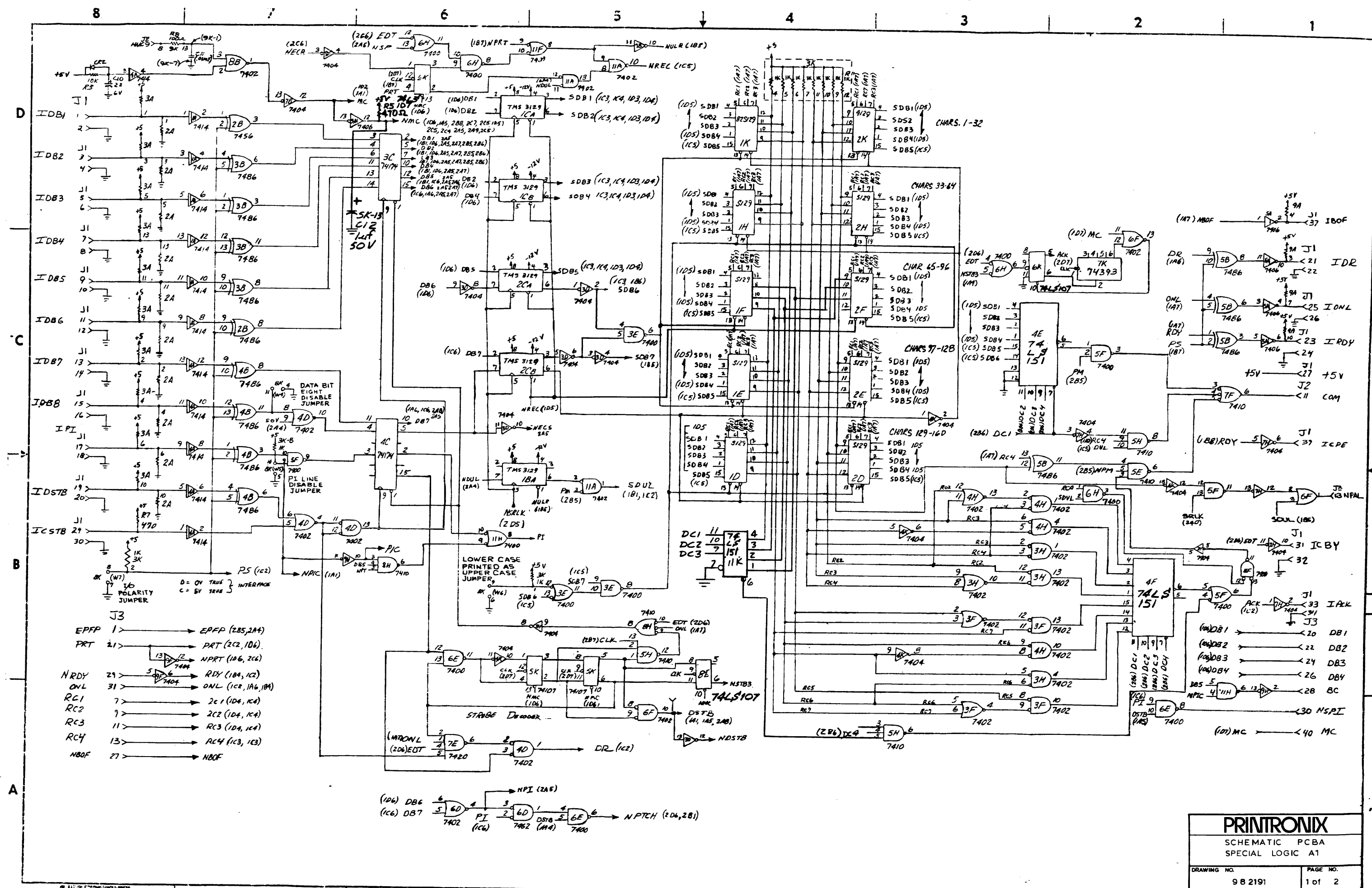
<b>PRINTRONIX</b>	
SCHEMATIC PCBA	
LOGIC A1	
DRAWING NO. 981688	PAGE NO. 3 of 4

SHIFT REGISTER COUNTER

DOT COLUMN COUNTER

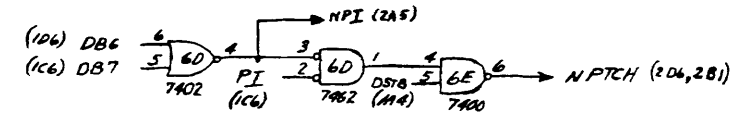
CHARACTER COLUMN COUNTER

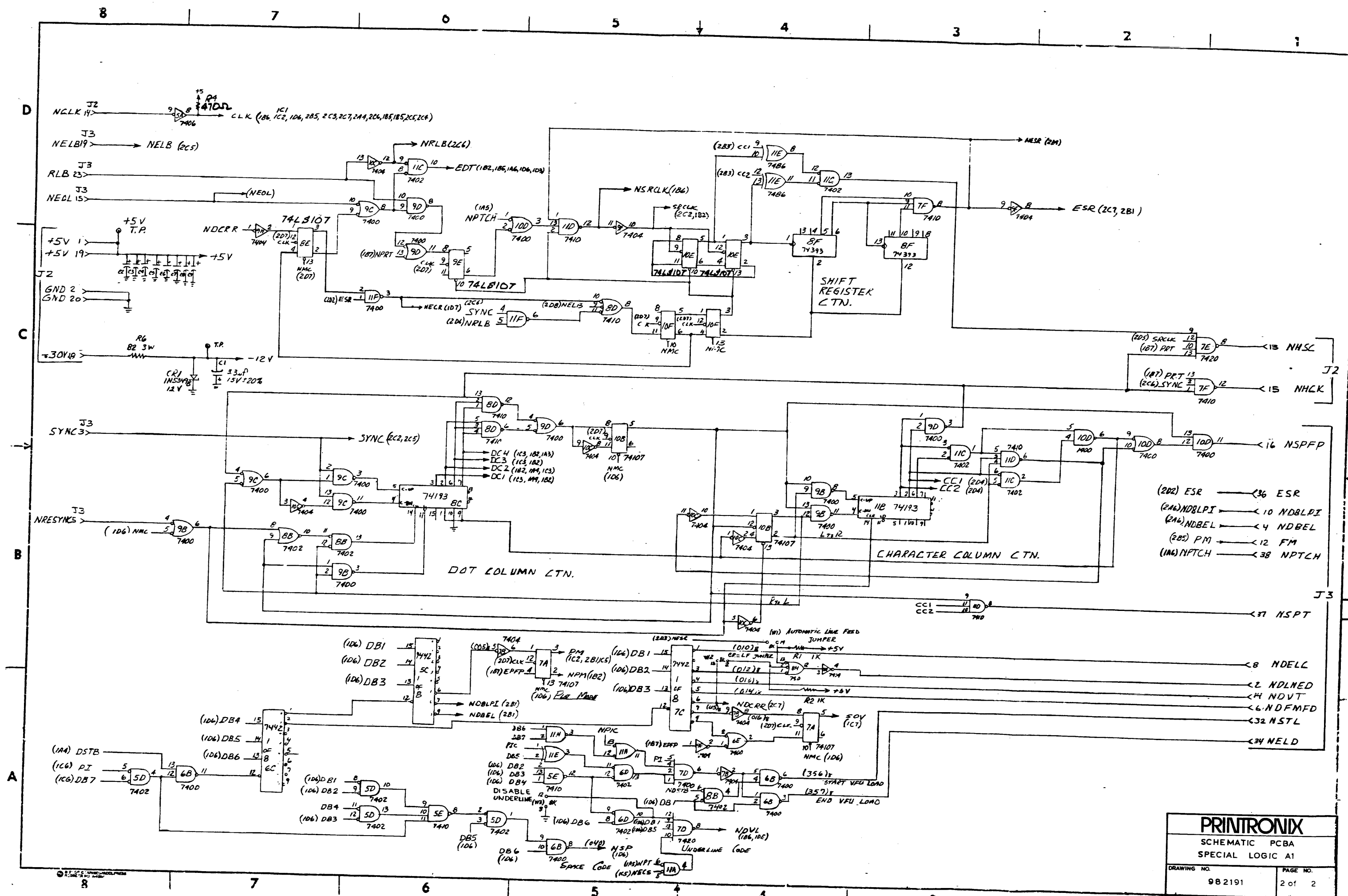




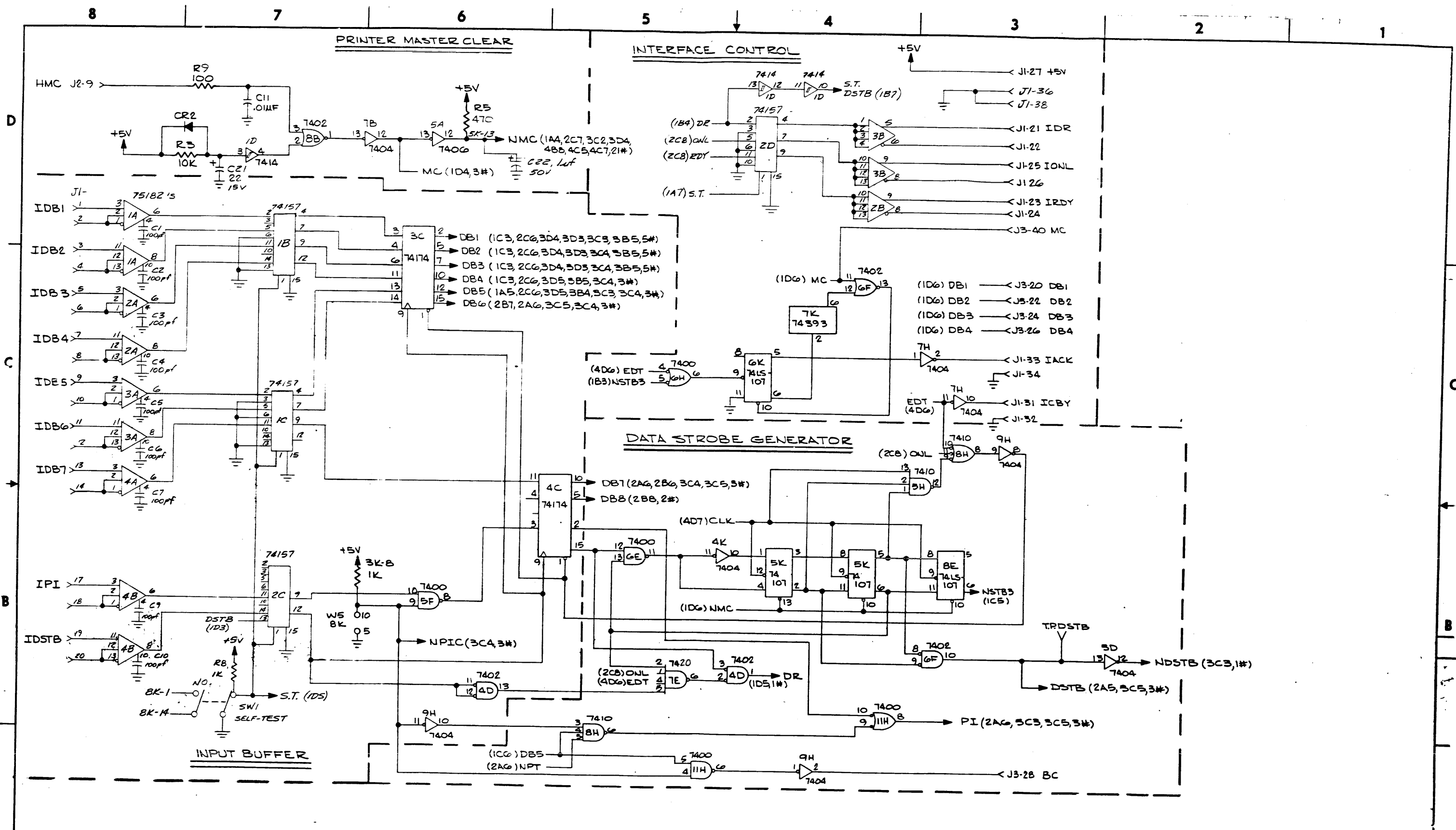
J3

EPFP	1	→	EPFP (285, 2A4)
PRT	21	→	PRT (2C2, 106)
		→	NPRT (106, 2C6)
NRDY	29	→	RDY (104, 1C2)
ONL	31	→	ONL (1C2, 1A6, 104)
RC1	7	→	2C1 (104, 1C4)
RC2	7	→	2C2 (104, 1C4)
RC3	11	→	RC3 (104, 1C4)
RC4	13	→	RC4 (1C3, 1C3)
NBOF	27	→	NBOF









JUMPER TABLE

W1	AUTOMATIC LINE FEED
W2	CR=LF
W3	UNDERLINE DISABLE
W5	PI DISABLE
W7	SLOW PRINT RATE

CONNECTORS	
J1	INPUT
J2	POWER
J3	B BOARD TRANSFER

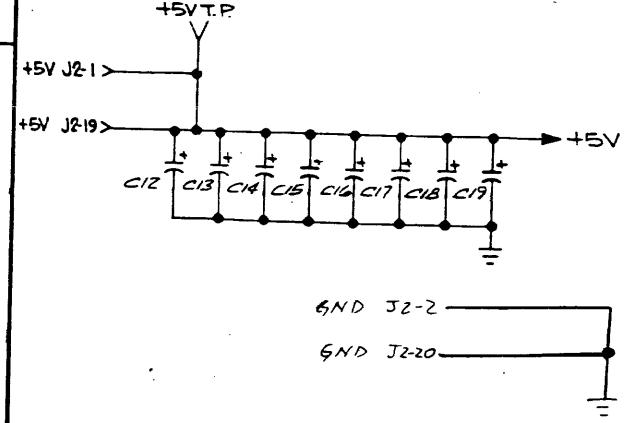
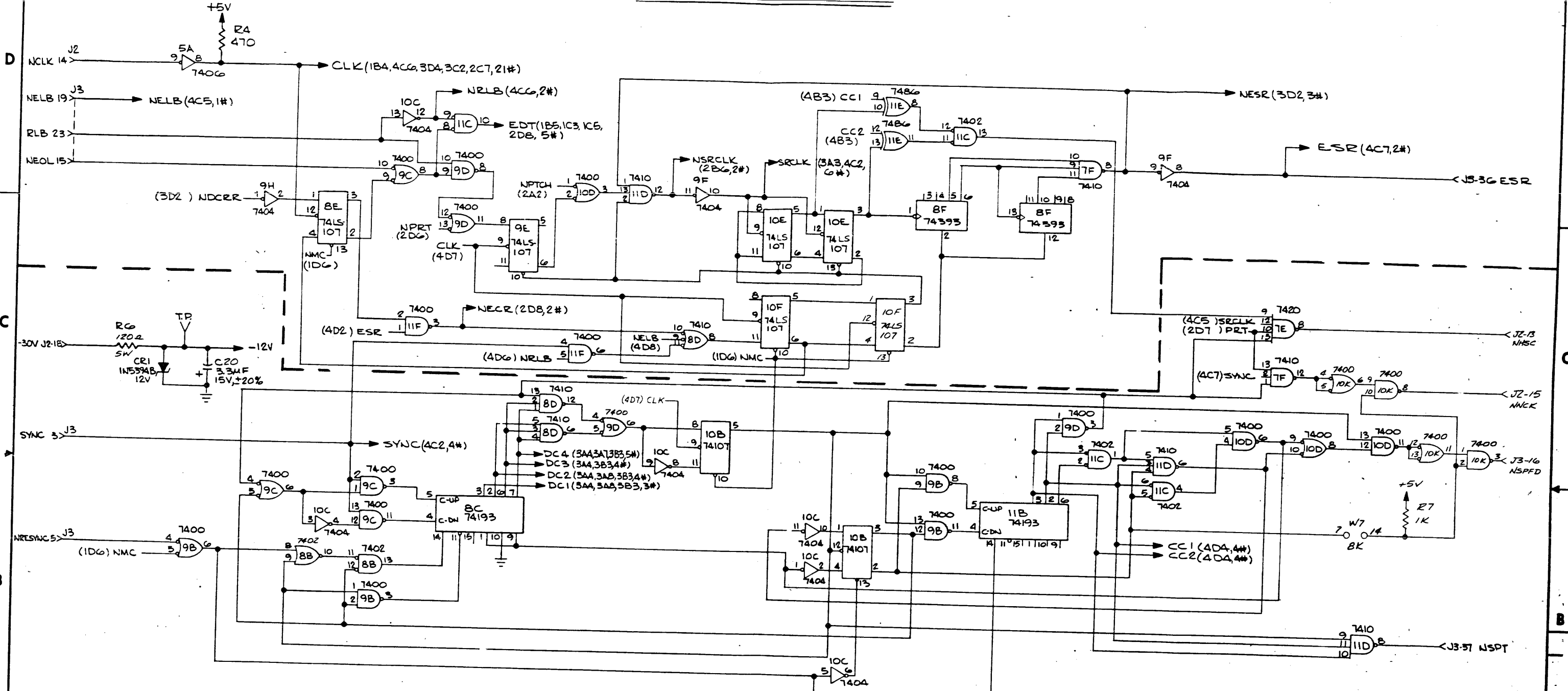




SHIFT REGISTER COUNTER

DOT COLUMN COUNTER

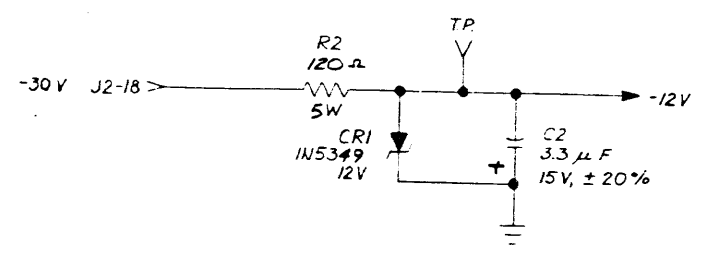
CHARACTER COLUMN COUNTER



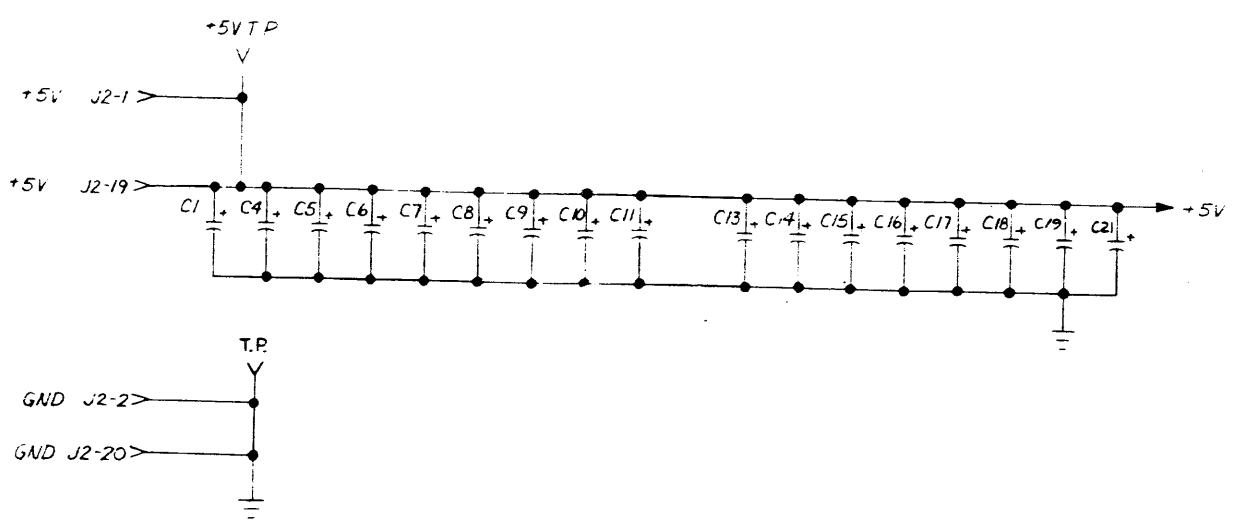
8 7 6 5 4 3 2 1

D

C



B



A

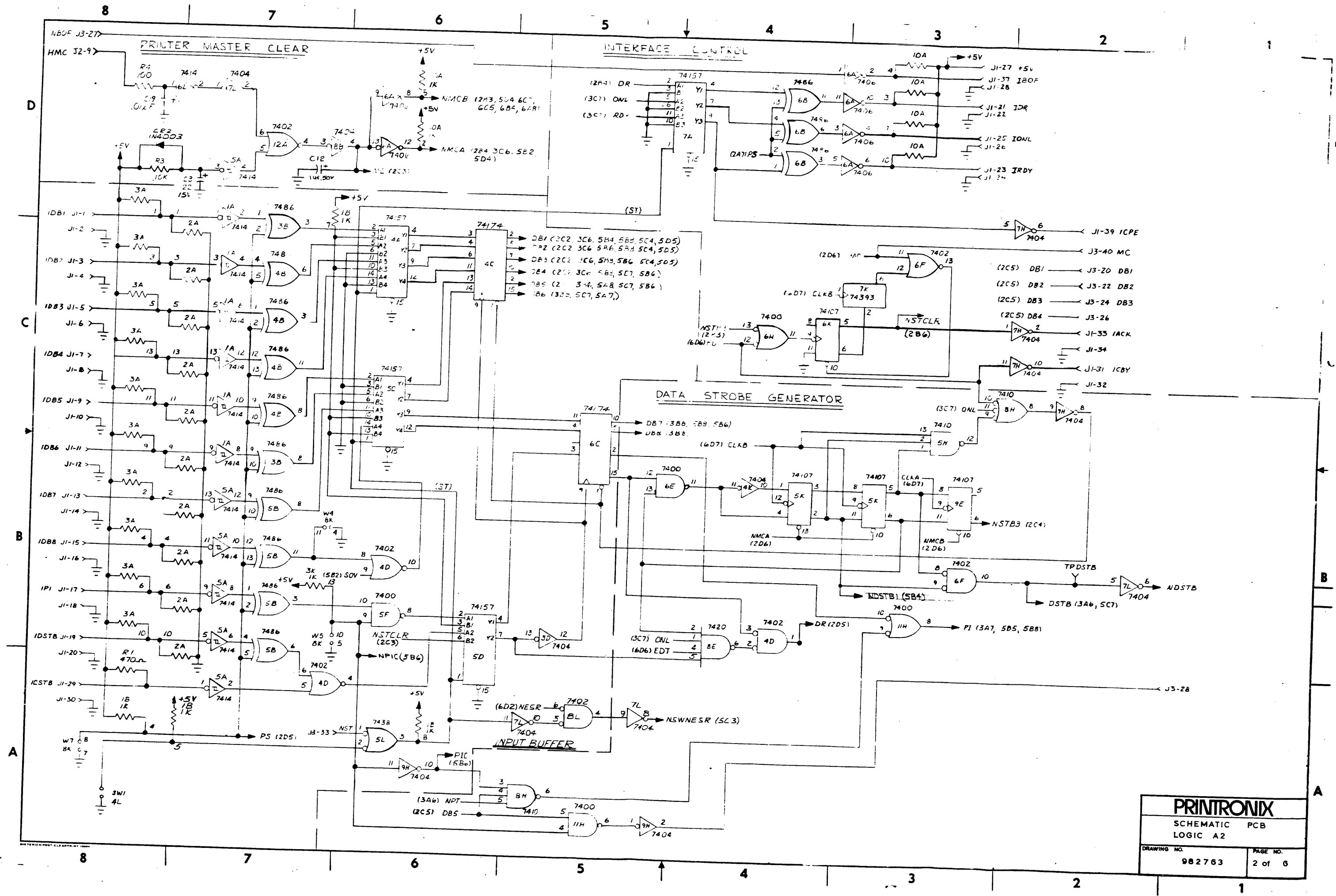
JUMPER TABLE

W1	AUTOMATIC LINE FEED
W2	CR = LF
W3	UNDERLINE DISABLE
W4	DATA BIT AND DISABLE
W5	P. DISABLE
W6	PRINT LOWER CASE AS UPPER CASE
W7	I/O POLARITY
W8-W13	NOT USED
W14	P150

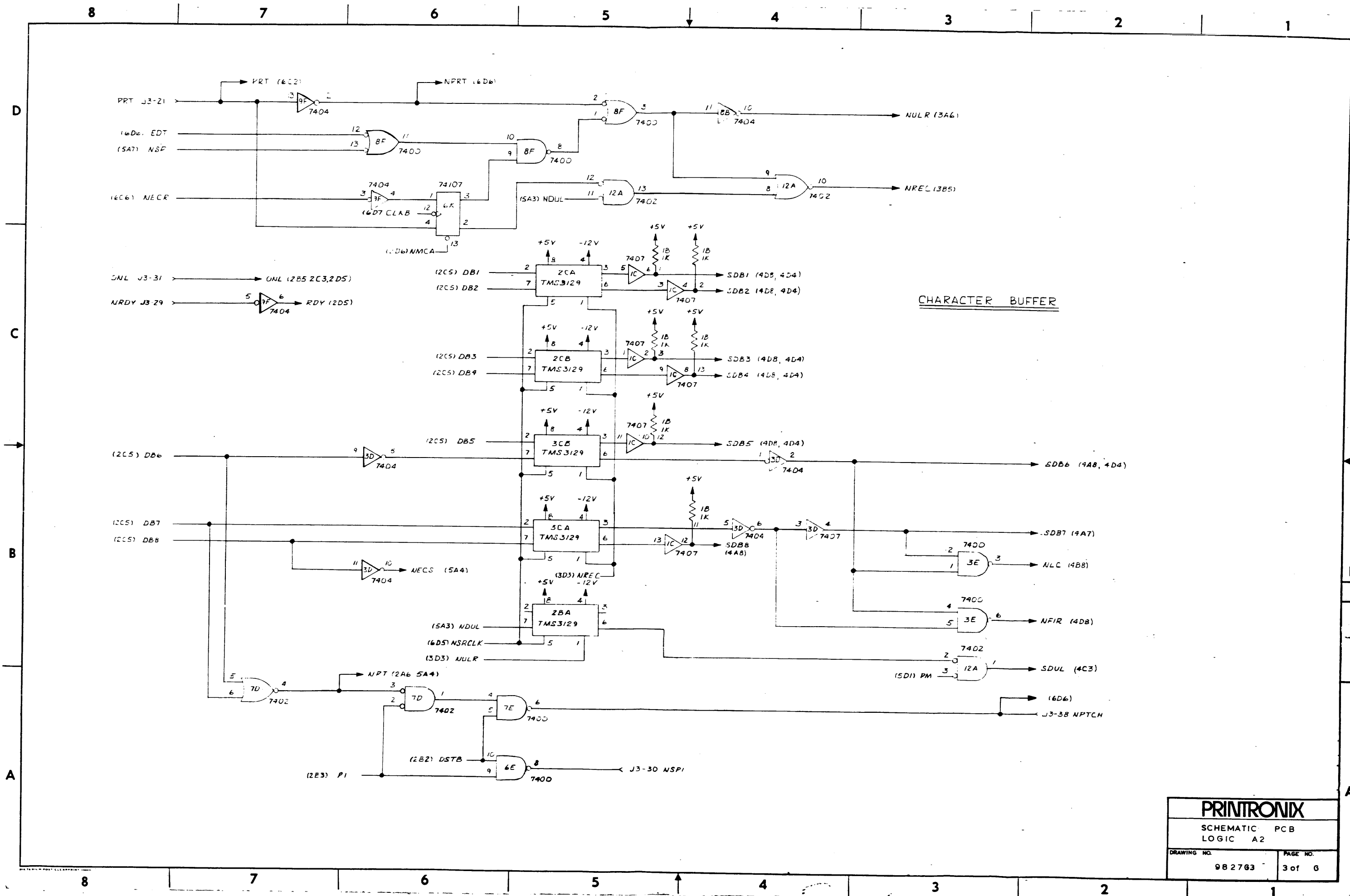
CONNECTORS	
J1	INPUT
J2	POWER
J3	B BOARD TRANSFER

<b>PRINTRONIX</b>	
SCHEMATIC PCB LOGIC A2	
DRAWING NO. 982763	PAGE NO. 1 of 6

8 7 6 5 4 3 2 1



**PRINTRONIX**  
 SCHEMATIC PCB  
 LOGIC A2  
 DRAWING NO. 982763 PAGE NO. 2 of 6

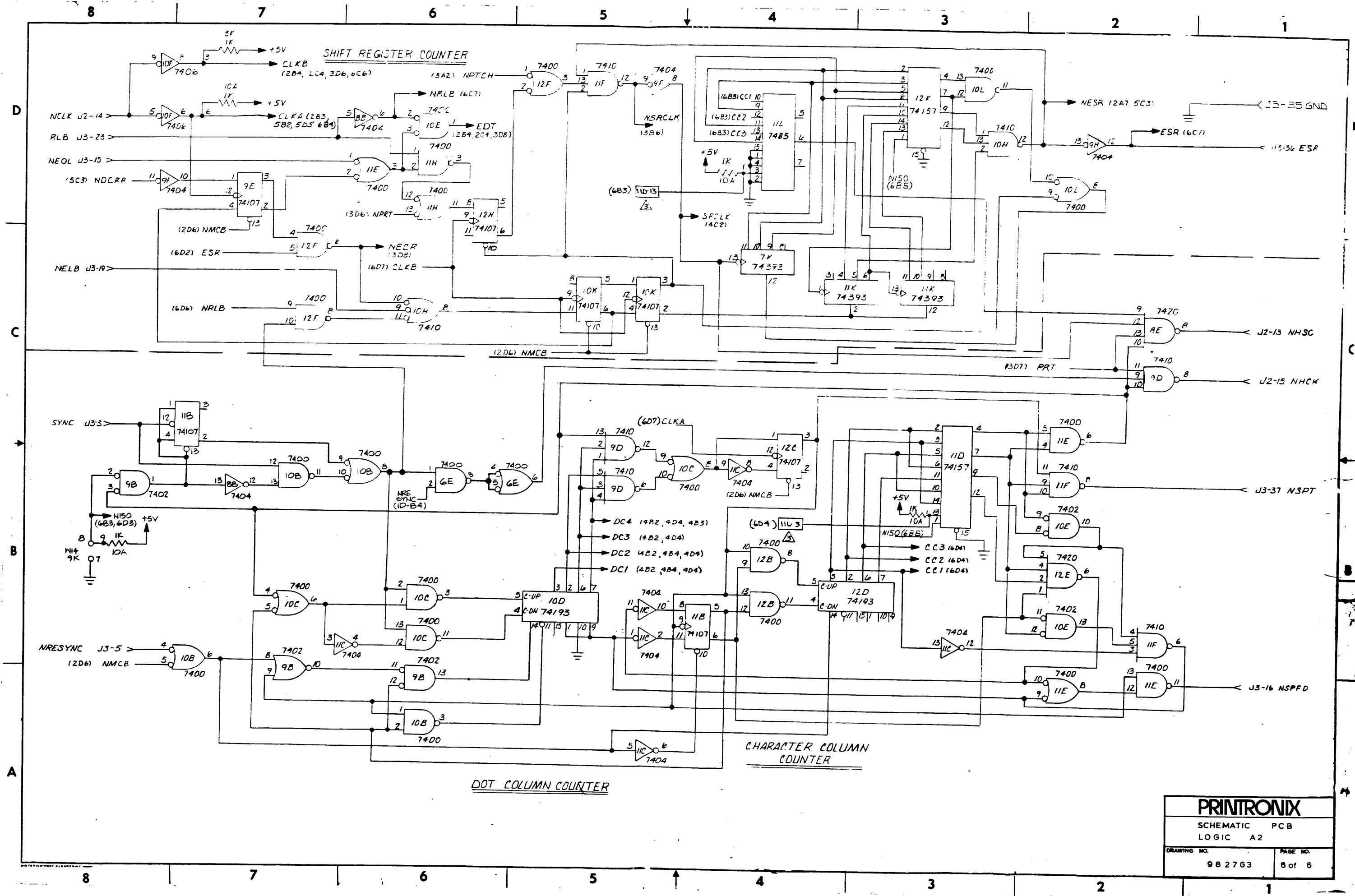


CHARACTER BUFFER









8 7 6 5 4 3 2

JUMPER TABLE	
W1	SPARE
W2	INVERTED DSTB
W3	UNDERLINE DISABLE
W4	DATA BIT B DISABLE
W5	P1 DISABLE
W6	PRINT LOWER CASE AS UPPER CASE
W7	I/O POLARITY
W8	SPARE
W9	HALF DOT PLOT ENABLE
W10	SPARE
W11	P300 ENABLE
W12	AUTOMATIC LINE FEED
W13	CR = LF
W14	P150 ENABLE
E1 - E2	ELONGATED CHARACTER CODE = 010 <sub>8</sub> (NORMAL CODING)
E3 - E4	SHIFT OUT CODE = 016 <sub>8</sub> (NORMAL CODING)
E1 - E4	SHIFT OUT CODE = 010 <sub>8</sub> (CENTRONIX CODING)
E2 - E3	ELONGATED CHARACTER CODE = 016 <sub>8</sub> (CENTRONIX CODING)

CONNECTORS	
J1	INPUT
J2	POWER
J3	B BOARD TRANSFER

D

C

B

A

D

C

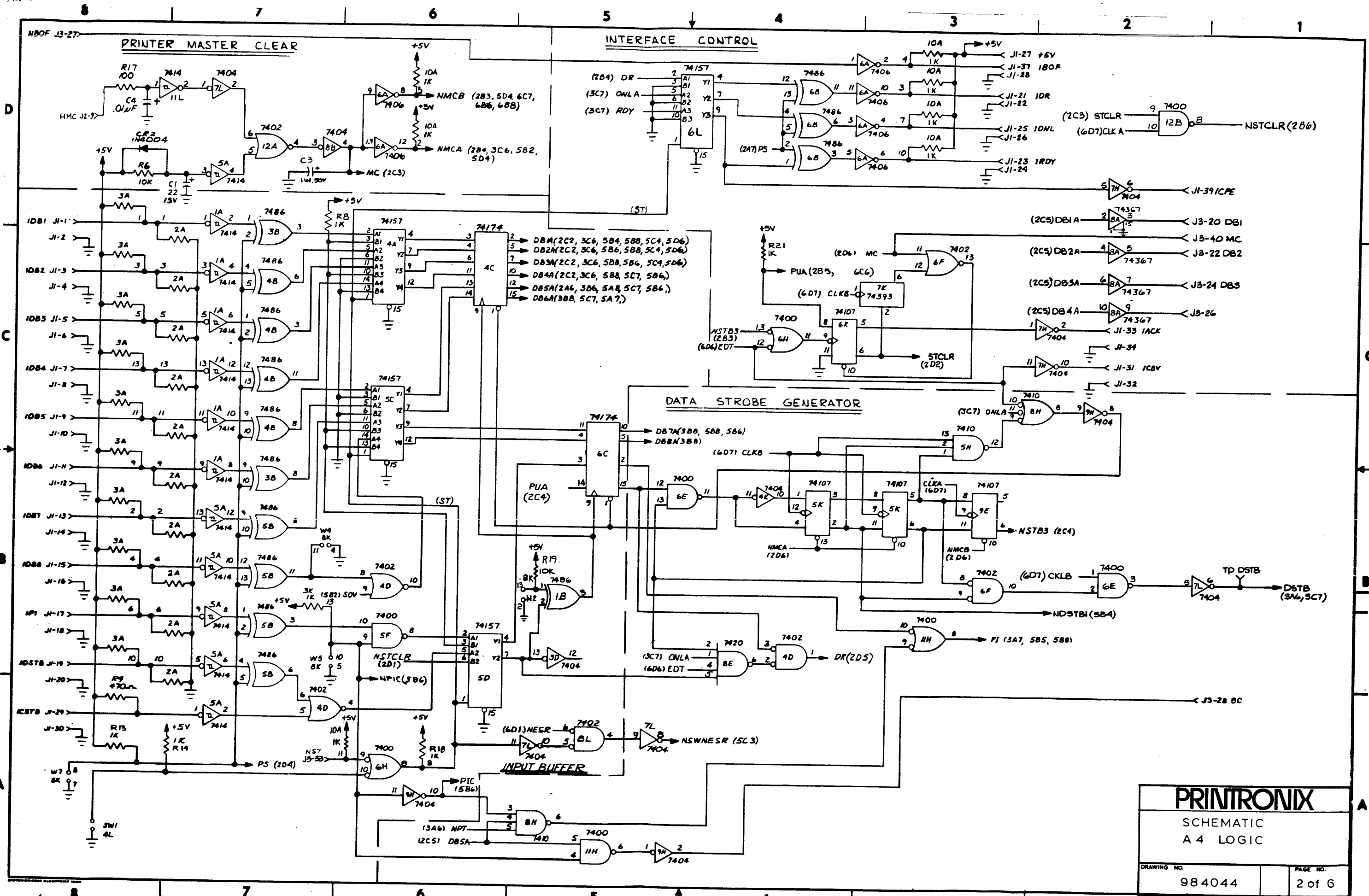
B

A

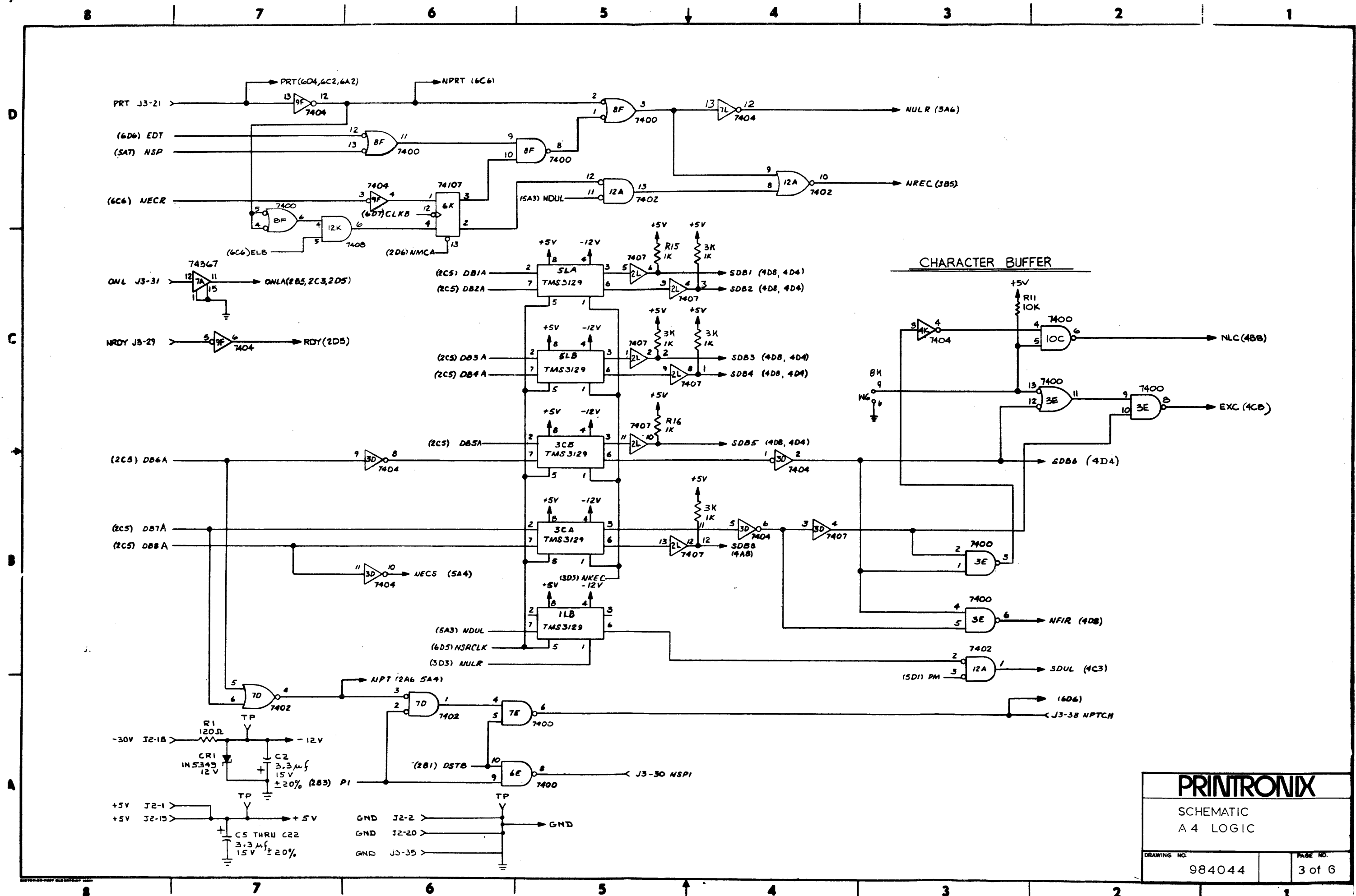
- 3 SEE JUMPER TABLE FOR CONNECTIONS
  - 2 ALL CAPACITOR VALUES ARE GIVEN IN MICROFARADS, ±20%
  - 1 ALL RESISTOR VALUES ARE GIVEN IN OHMS, ±5%, 1/4W
- NOTES : UNLESS OTHERWISE SPECIFIED

<b>PRINTRONIX</b>		
SCHEMATIC A4 LOGIC		
DRAWING NO. 984044	REV. 1	PAGE NO. 1 of 6

8 7 6 5 4 3 2 1

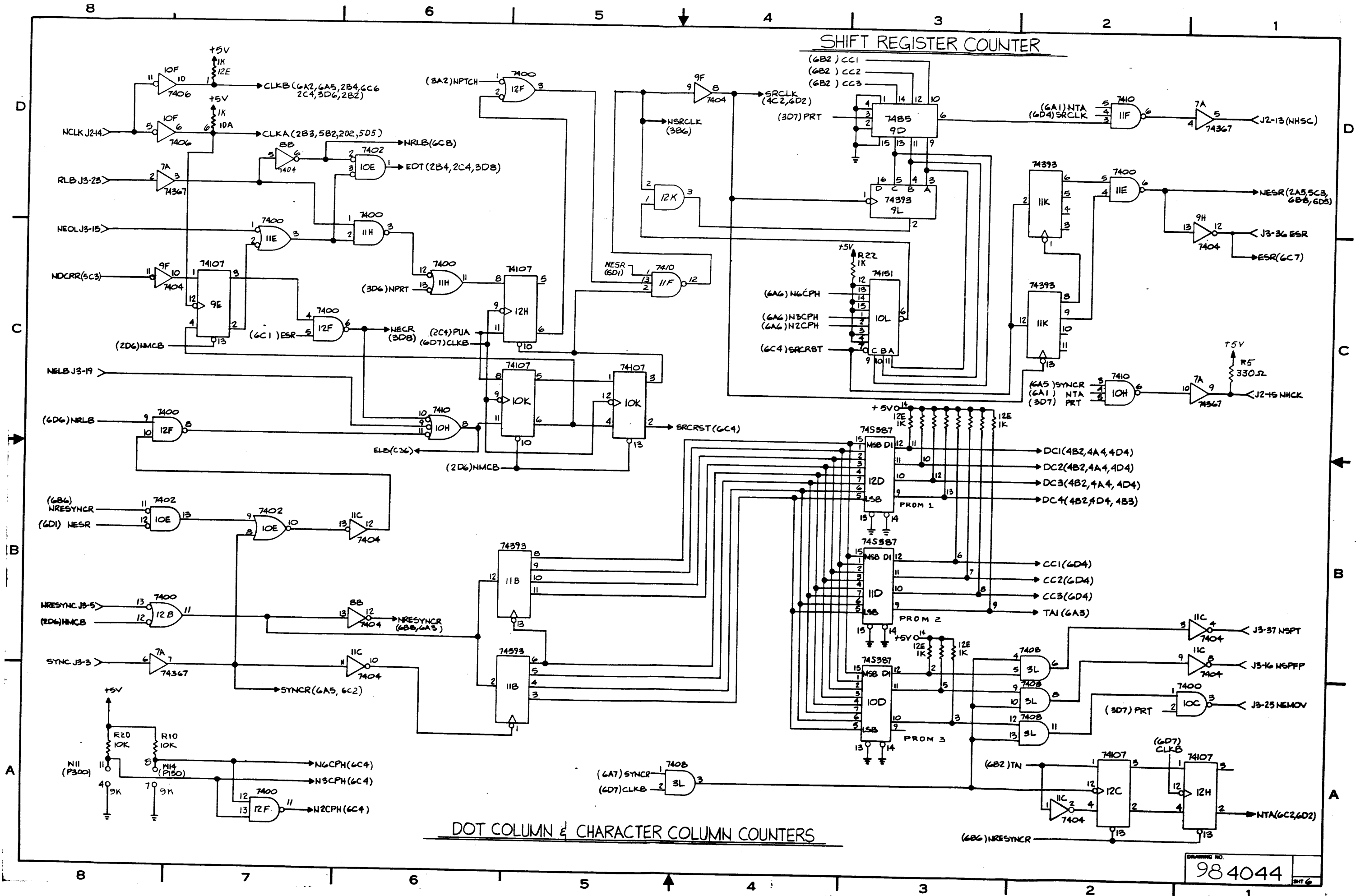


**PRINTRONIX**  
 SCHEMATIC  
 A 4 LOGIC  
 DRAWING NO. 984044 PAGE NO. 2 of 6





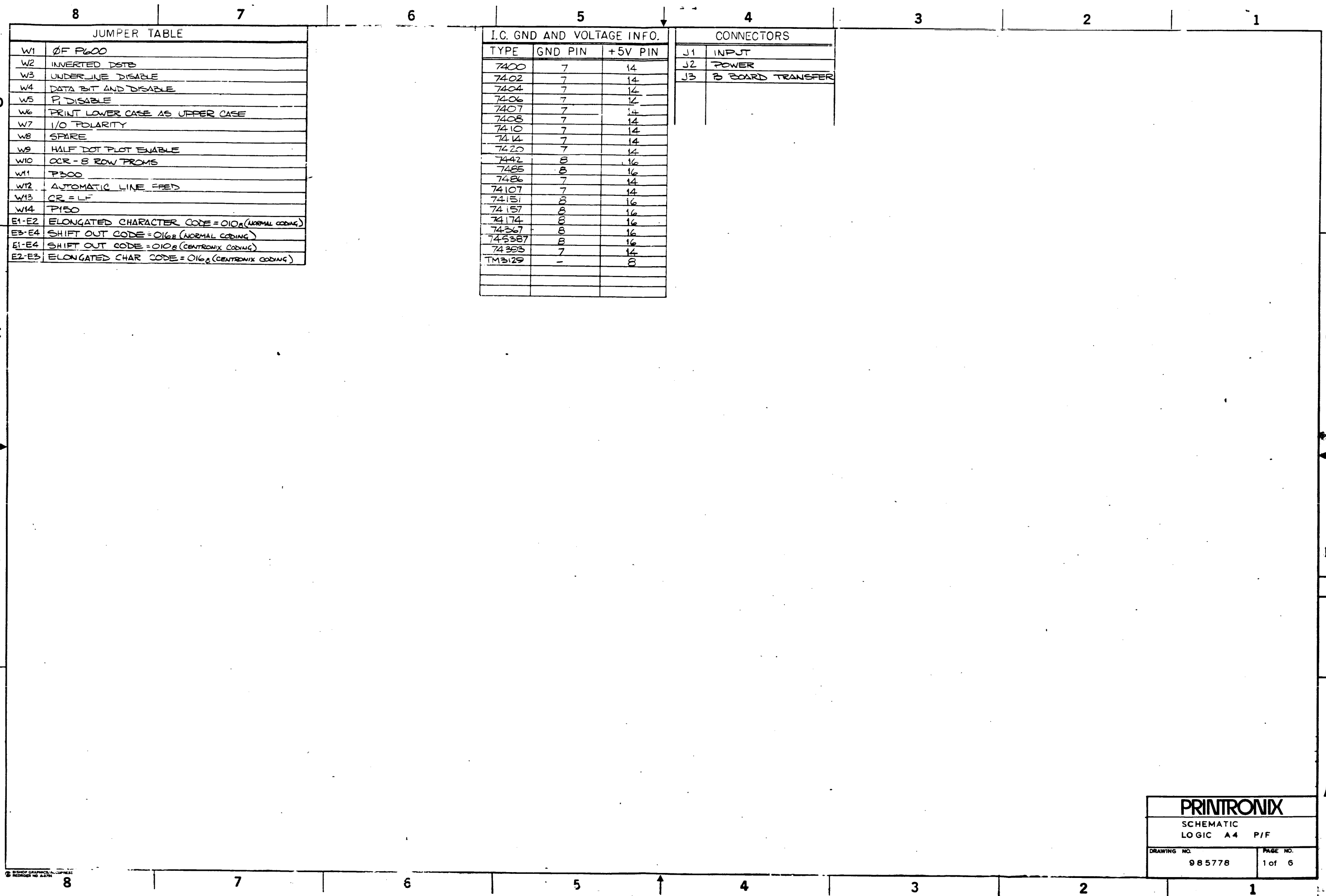




SHIFT REGISTER COUNTER

DOT COLUMN & CHARACTER COLUMN COUNTERS





JUMPER TABLE	
W1	ØF P600
W2	INVERTED DSTB
W3	UNDERLINE DISABLE
W4	DATA BIT AND DISABLE
W5	P1 DISABLE
W6	PRINT LOWER CASE AS UPPER CASE
W7	I/O POLARITY
W8	SPARE
W9	HALF DOT PLOT ENABLE
W10	OCR - 8 ROW PROMS
W11	P300
W12	AUTOMATIC LINE FEED
W13	CR = LF
W14	P150
E1-E2	ELONGATED CHARACTER CODE = 010 <sub>8</sub> (NORMAL CODING)
E3-E4	SHIFT OUT CODE = 016 <sub>8</sub> (NORMAL CODING)
E1-E4	SHIFT OUT CODE = 010 <sub>8</sub> (CENTRONIX CODING)
E2-E3	ELONGATED CHAR CODE = 016 <sub>8</sub> (CENTRONIX CODING)

I.C. GND AND VOLTAGE INFO.		
TYPE	GND PIN	+5V PIN
7400	7	14
7402	7	14
7404	7	14
7406	7	14
7407	7	14
7408	7	14
7410	7	14
7414	7	14
7420	7	14
7442	8	16
7485	8	16
7486	7	14
74107	7	14
74151	8	16
74157	8	16
74174	8	16
74367	8	16
745387	8	16
74393	7	14
TM3129	-	8

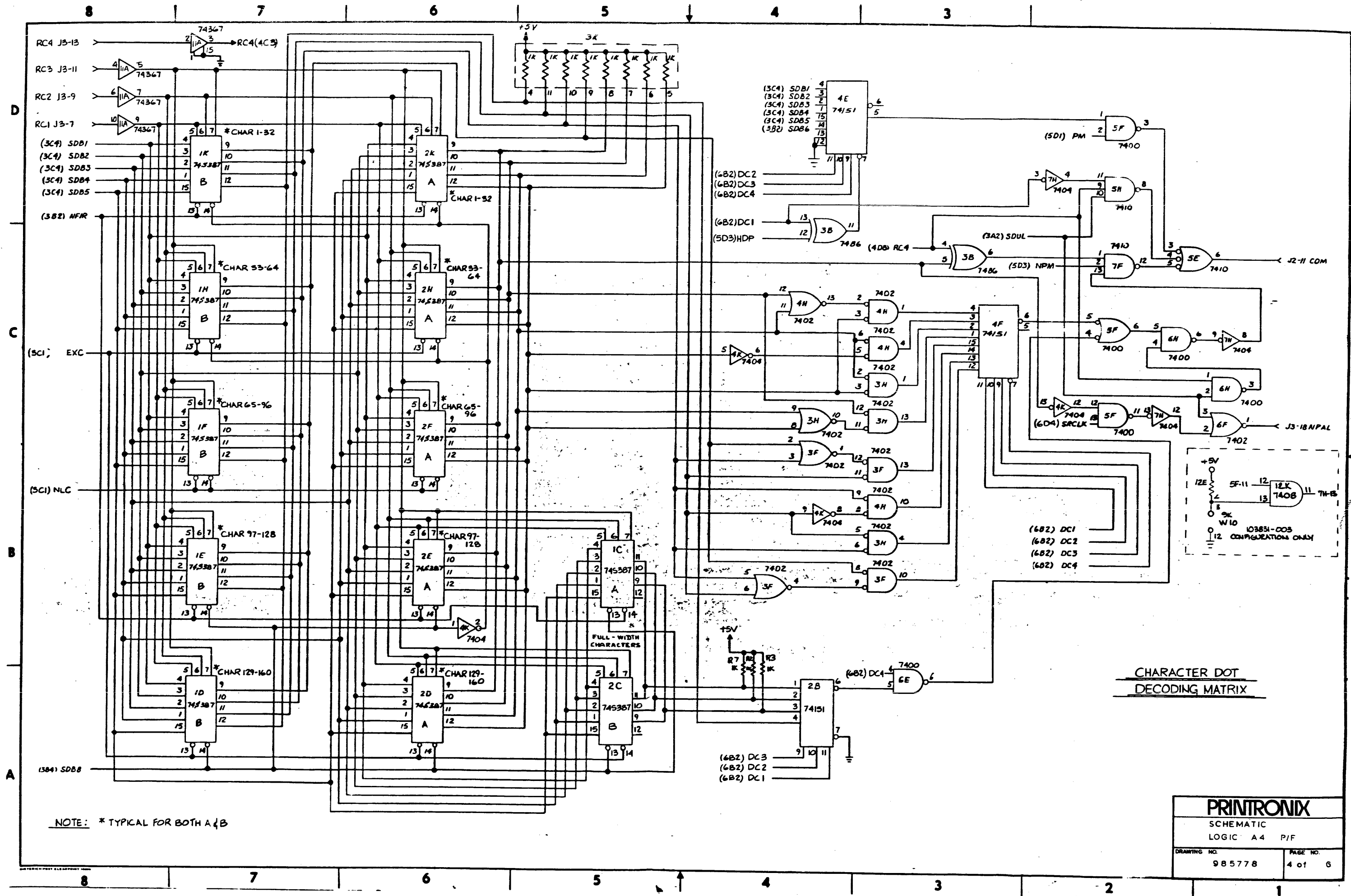
CONNECTORS	
J1	INPUT
J2	POWER
J3	B BOARD TRANSFER

<b>PRINTRONIX</b>	
SCHEMATIC LOGIC A4 P/F	
DRAWING NO.	PAGE NO.
985778	1 of 6

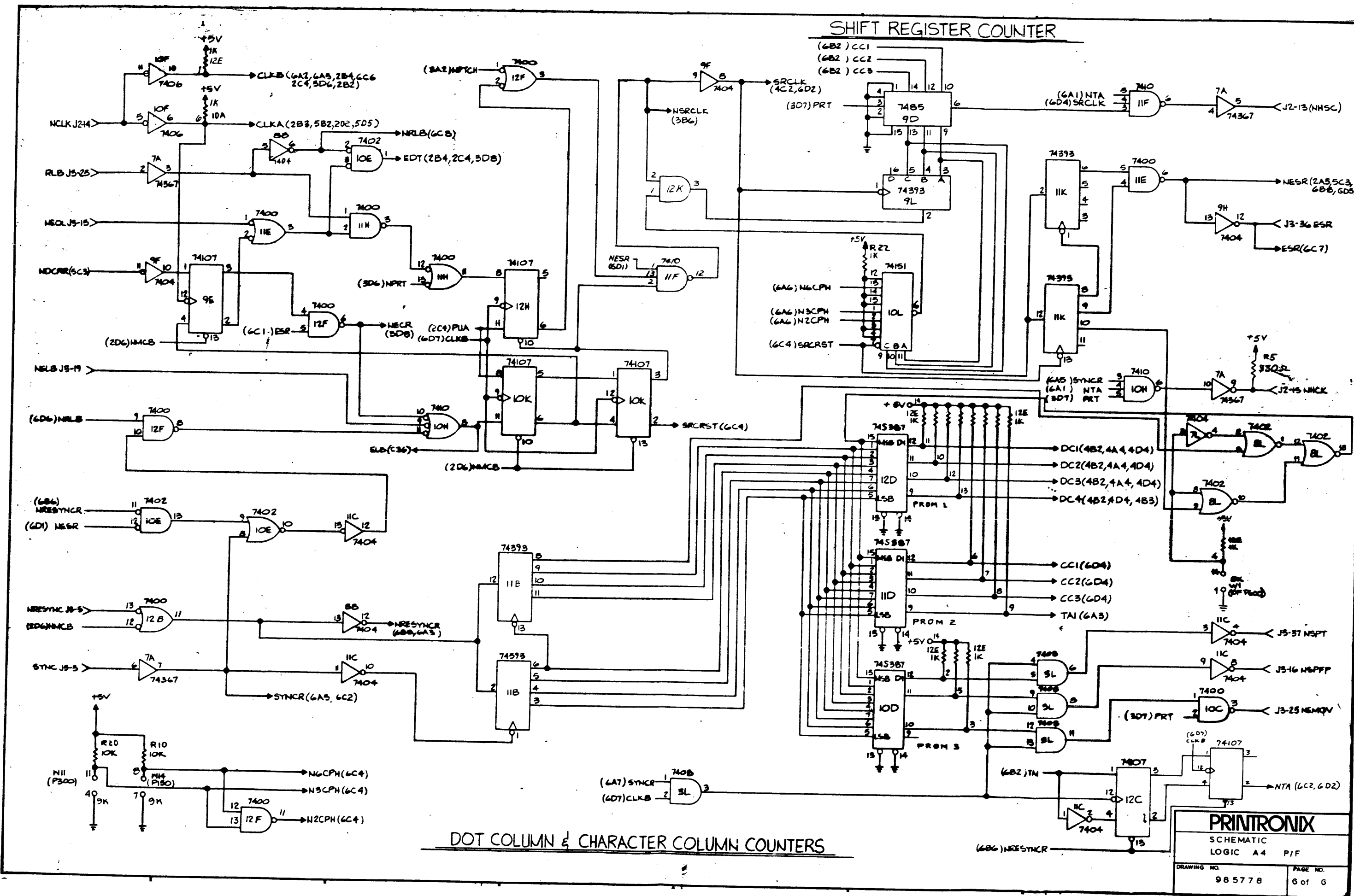
8 BISHOP GRAPHICS COMPANY  
REORDER NO. 8374











SHIFT REGISTER COUNTER

DOT COLUMN & CHARACTER COLUMN COUNTERS

JUMPER TABLE	
W1	PHASE FIRING ENABLE
W2	INVERTED DSTB
W3	UNDERLINE DISABLE
W4	DATA BIT 8 DISABLE
W5	PI DISABLE
W6	PRINT LOWER CASE AS UPPERCASE
W7	I/O POLARITY
W8	SPARE
W9	HALF DOT PLOT ENABLE
W10	OCR - 8 ROW CHARACTERS
W11	P300
W12	AUTOMATIC LINE FEED
W13	CR=LF
W14	P150
E1-E2	ELONGATED CHARACTER CODE = 010 <sub>8</sub> (NORMAL CODING)
E3-E4	SHIFT OUT CODE = 016 <sub>8</sub> (NORMAL CODING)
E1-E4	SHIFT OUT CODE = 010 <sub>8</sub> (CENTRONIX CODING)
E2-E3	ELONGATED CHARACTER CODE = 016 <sub>8</sub> (CENTRONIX CODING)

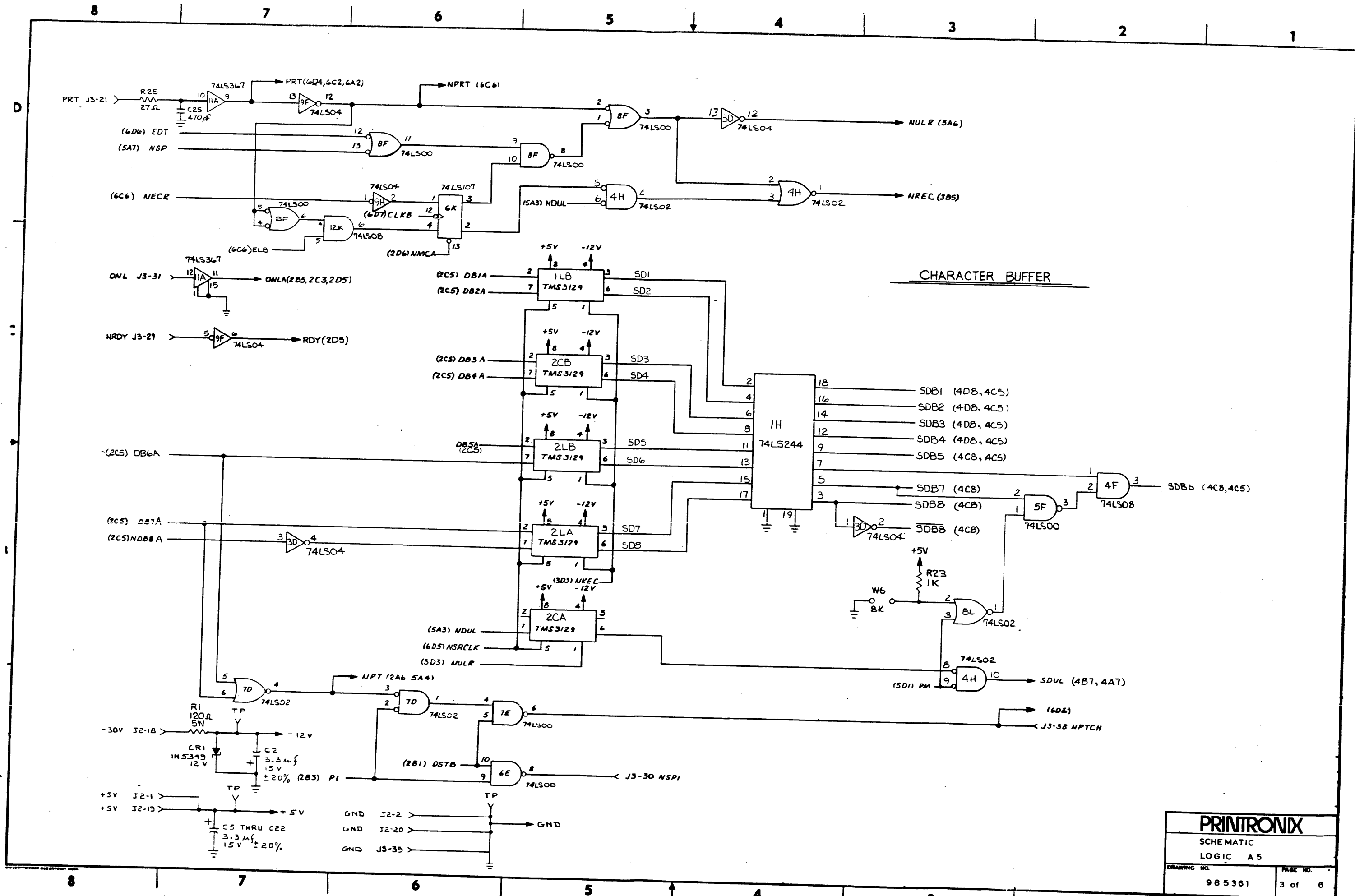
I.C. GND AND VOLTAGE INFO.		
TYPE	GND PIN	+5V PIN
74LS00	7	14
74LS02	7	14
74LS04	7	14
7406	7	14
74LS08	7	14
74LS10	7	14
74LS14	7	14
74LS20	7	14
74LS37	7	14
74LS42	8	16
74LS85	8	16
74LS86	7	14
74LS107	7	14
74LS151	8	16
74LS157	8	16
74LS174	8	16
74LS244	10	20
74LS367	8	16
74LS387	8	16
74LS393	7	14
TMS3129	-	8
2316	12	24
2716	12	24

CONNECTORS	
J1	INPUT
J2	POWER
J3	B BOARD TRANSFER

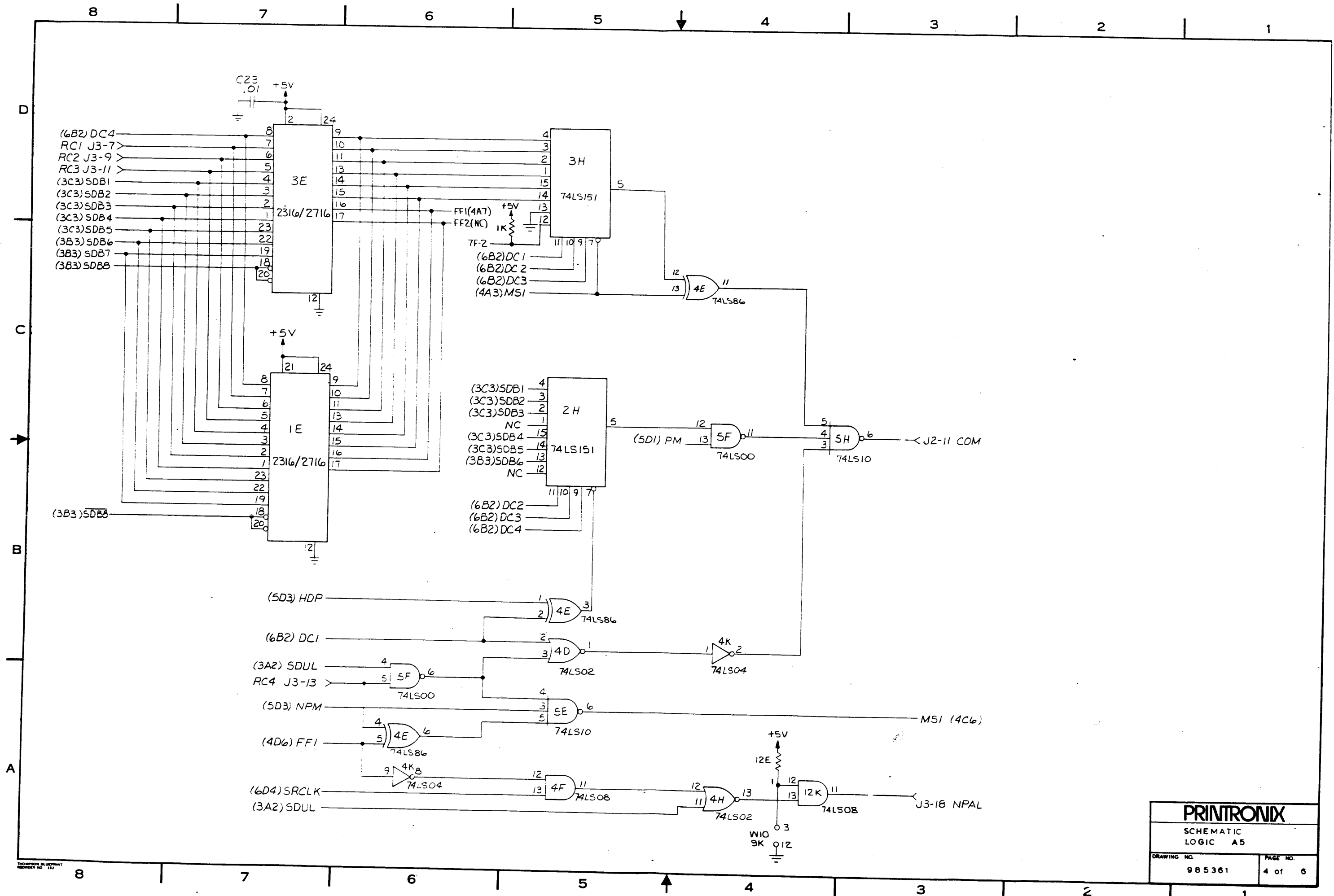
SCHEMATIC LOCATION OF JUMPER		
JUMPER	SHEET	ZONE
W1	6	B6
W2	2	B5
W3	4	A5
W4	2	B6
W5	2	A7
W6	3	B3
W7	2	A8
W8		SPARE
W9	5	D5
W10	4	A4
W11	6	A8
W12	4	C3
W13	4	C3
W14	6	A8
E1-E4	4	C3

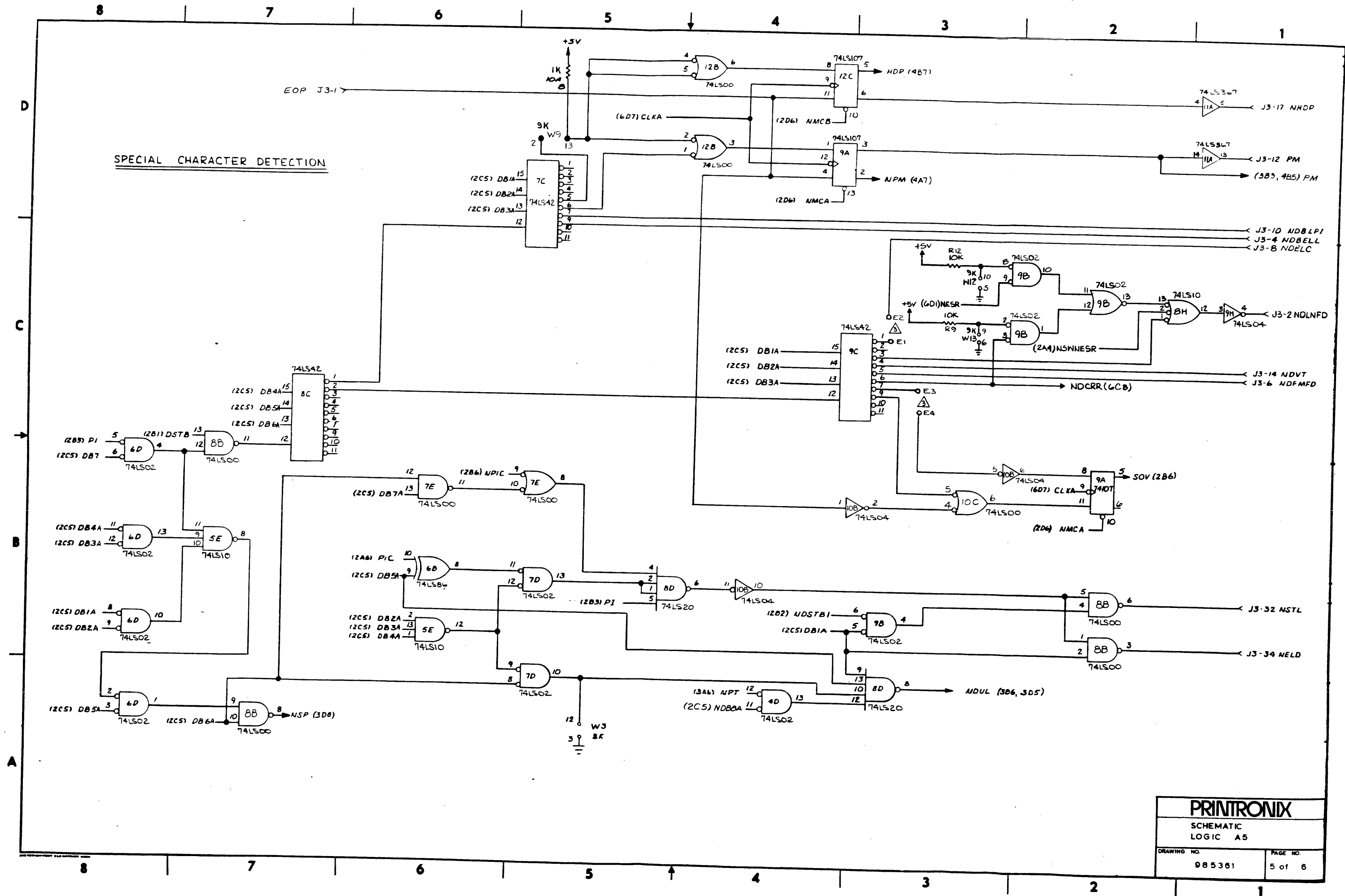


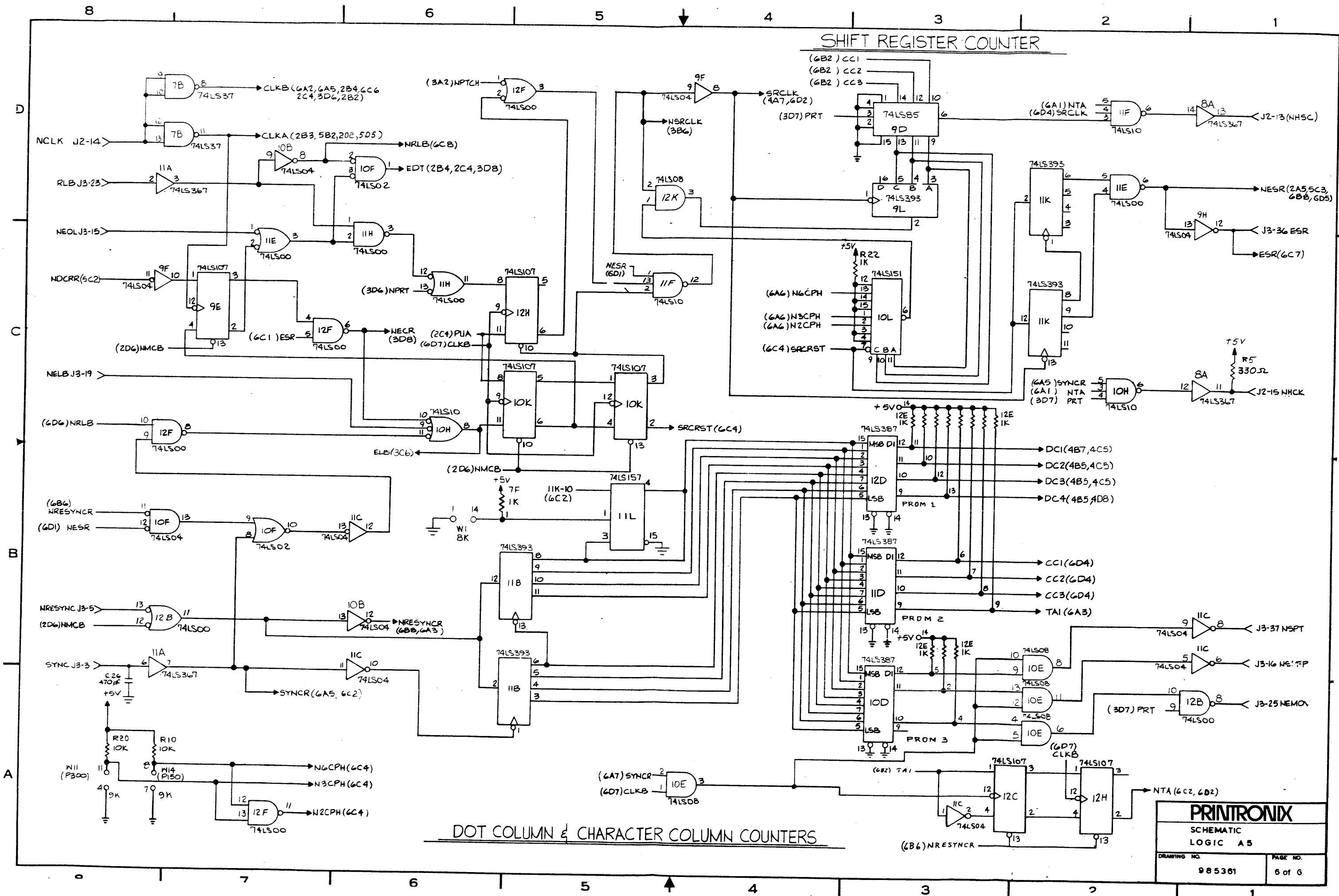




**CHARACTER BUFFER**



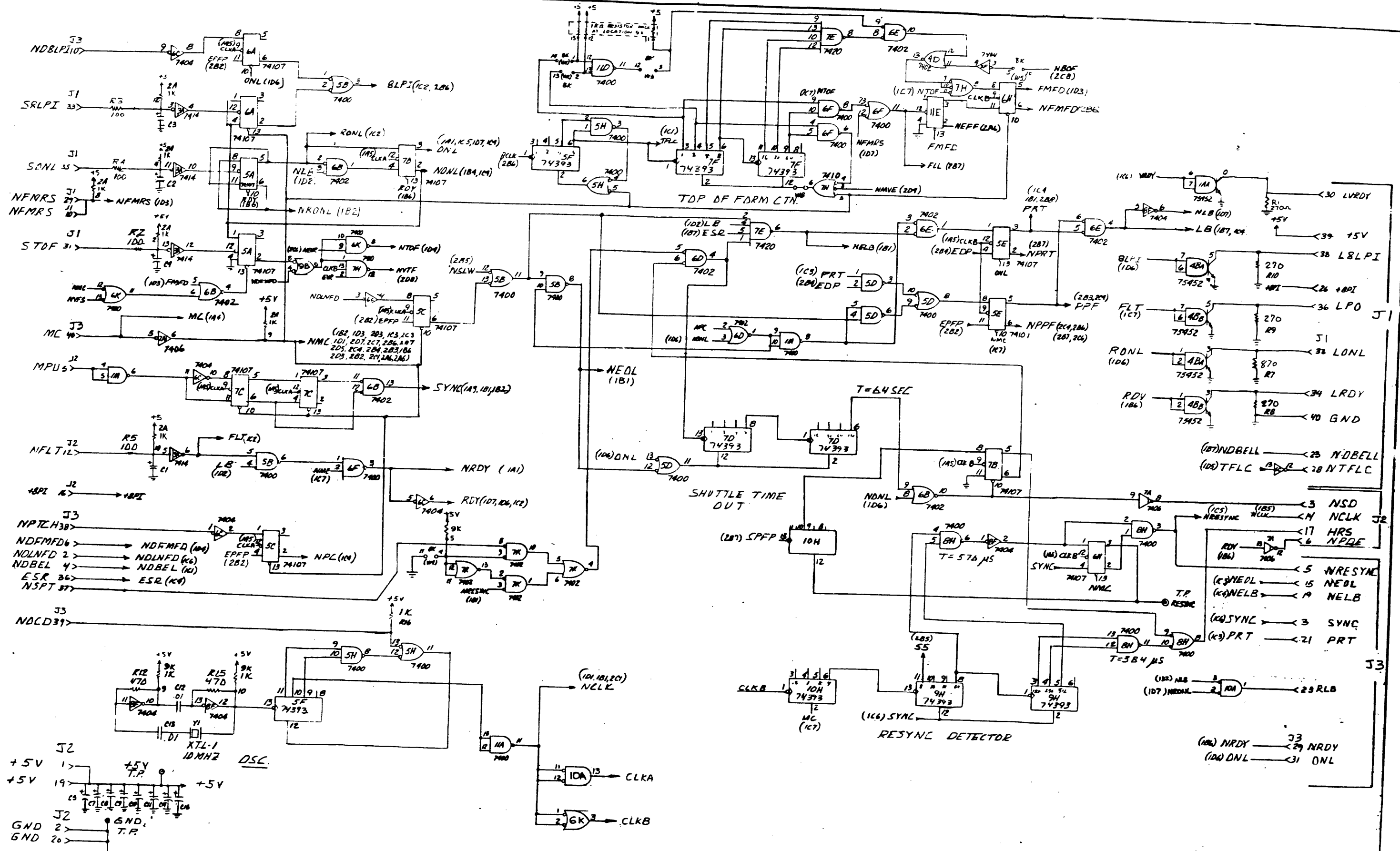


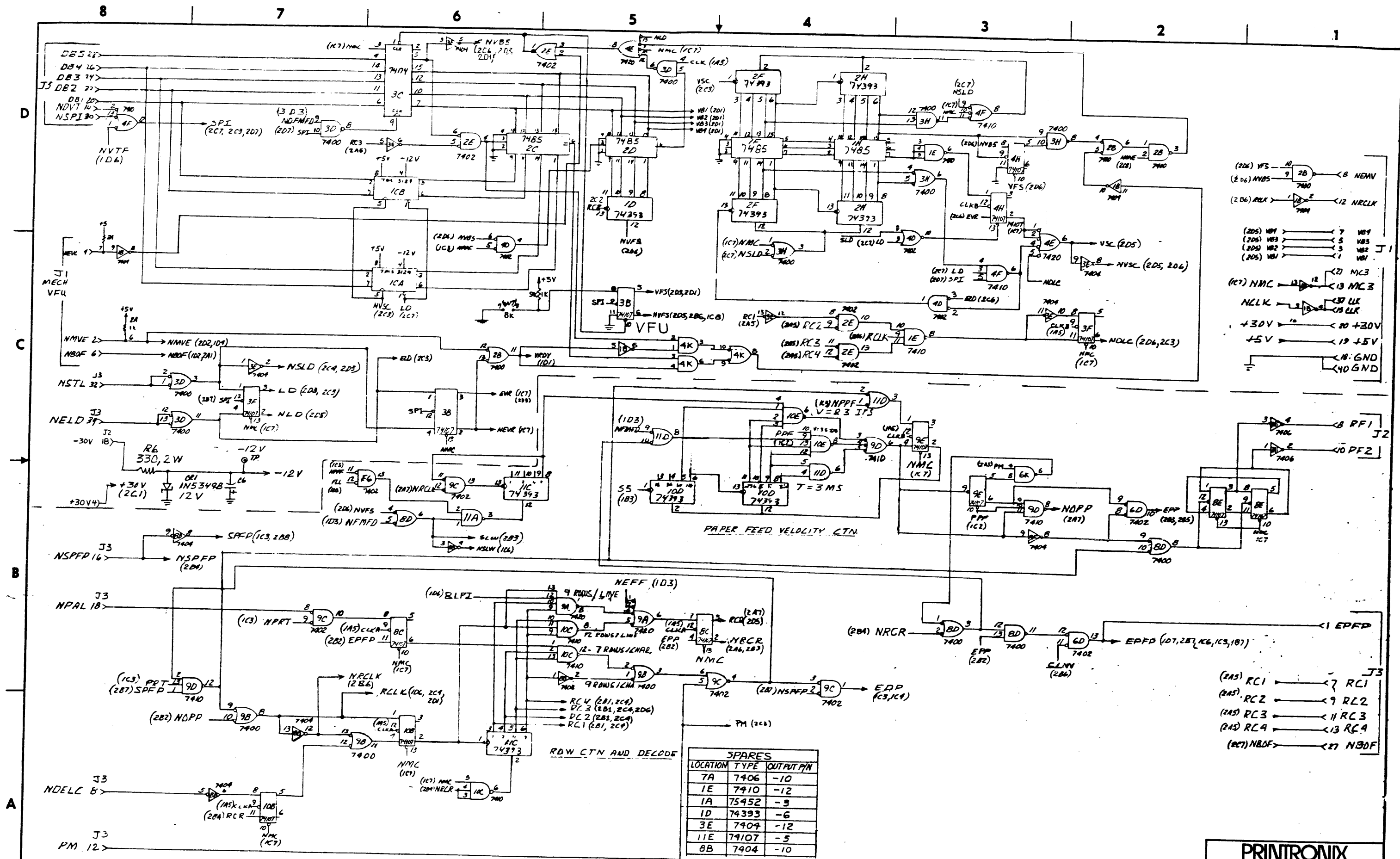


SHIFT REGISTER COUNTER

DOT COLUMN & CHARACTER COLUMN COUNTERS

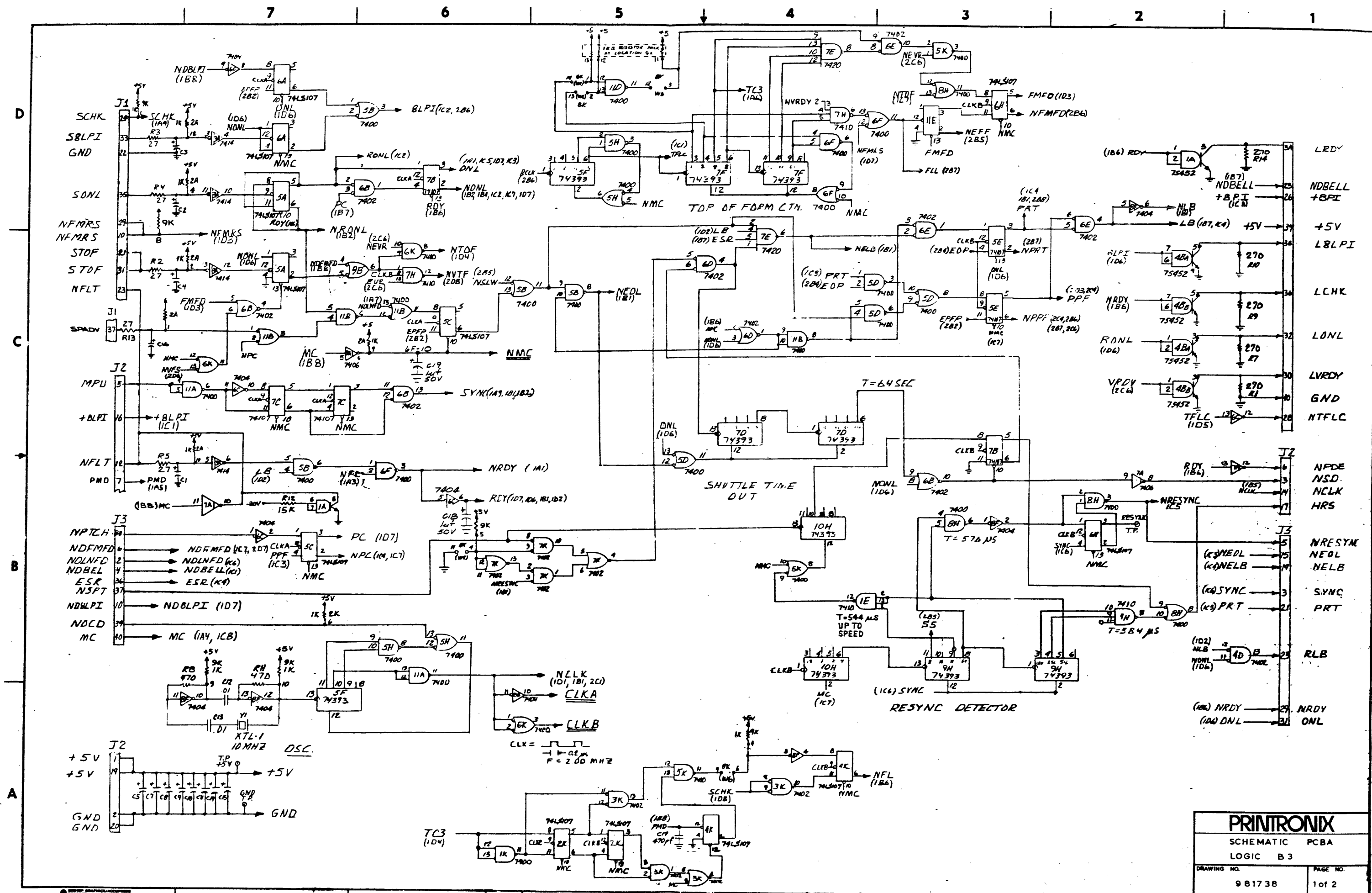
D  
C  
B  
A

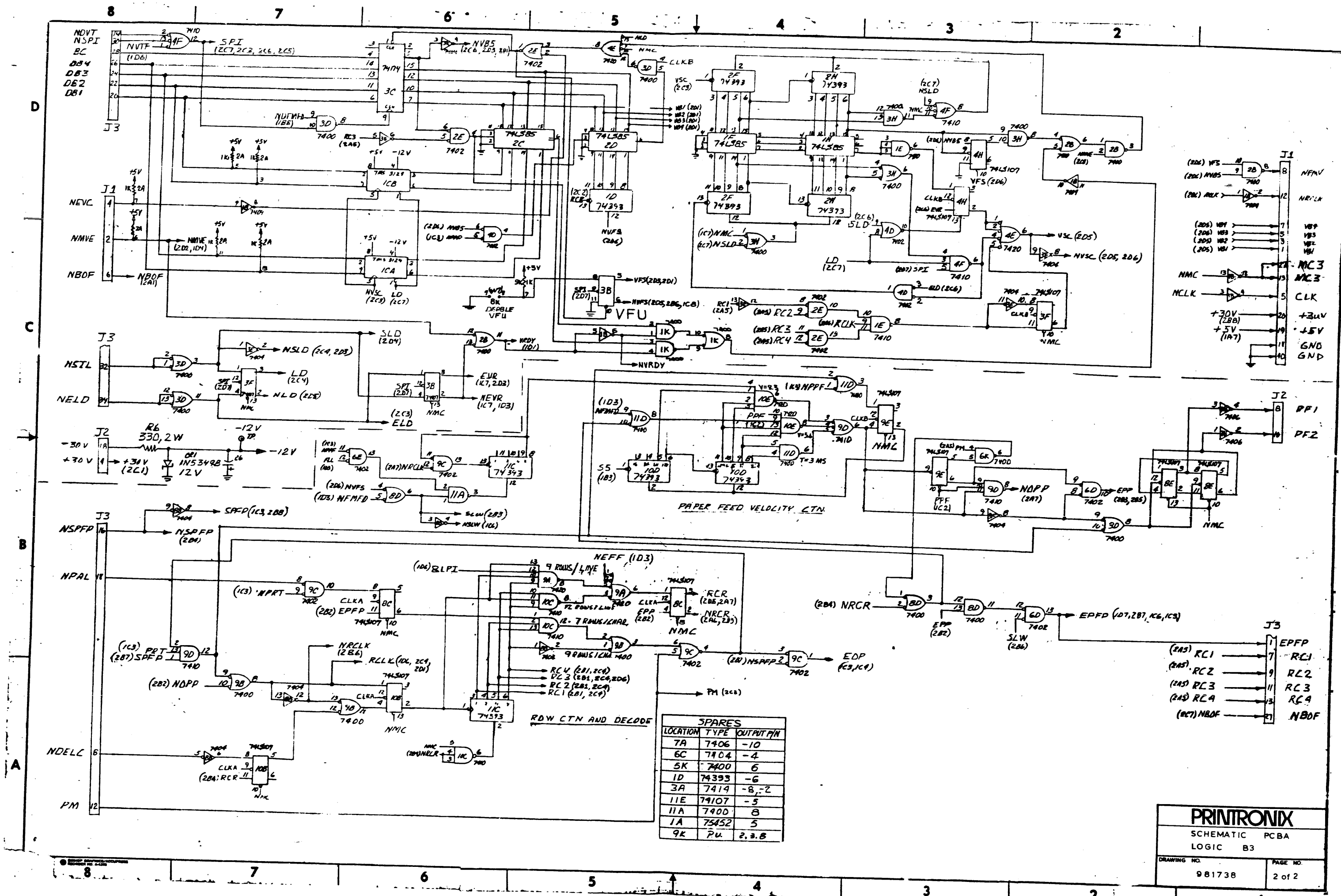




LOCATION	TYPE	OUTPUT PIN
7A	7406	-10
1E	7410	-12
1A	75452	-9
1D	74393	-6
3E	7404	-12
11E	74107	-5
8B	7404	-10

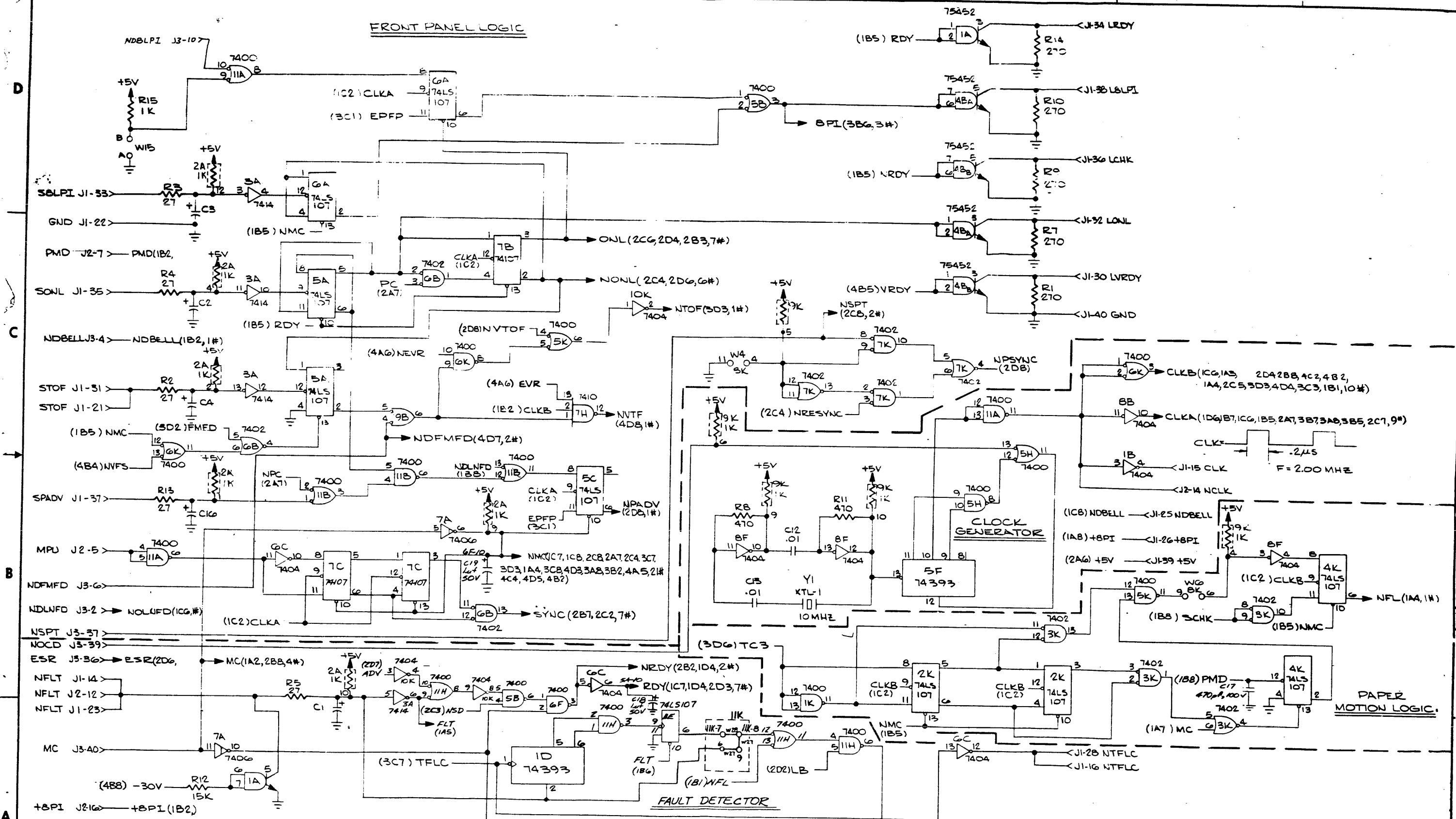
- (245) RC1 → 7 RC1
- (245) RC2 → 9 RC2
- (245) RC3 → 11 RC3
- (245) RC4 → 13 RC4
- (245) NBOF → 27 NBOF







FRONT PANEL LOGIC



JUMPER TABLE

W1	SKIP OVER PERF
W2	SKIP OVER PERF
W3	SKIP OVER PERF
W4	DOUBLE SPEED PLOT
W7	ELECTRONIC VFD DISABLE
W9	BLPI LINE COUNT
W10	IOLPI LINE COUNT

JUMPER TABLE

W12	BLPI LINE COUNT
W13	BLPI LINE COUNT
W14	IOLPI LINE COUNT
W15	BLPI ONLY
W27	NO LINE COUNT ON FLT
W28	12 LINE COUNT ON FLT

CONNECTOR BLOCK

J1	CONTROL PANEL MECH VFD
J2	POWER SUPPLY
J3	LOGIC A BD

LAST REF. DESIGNATION

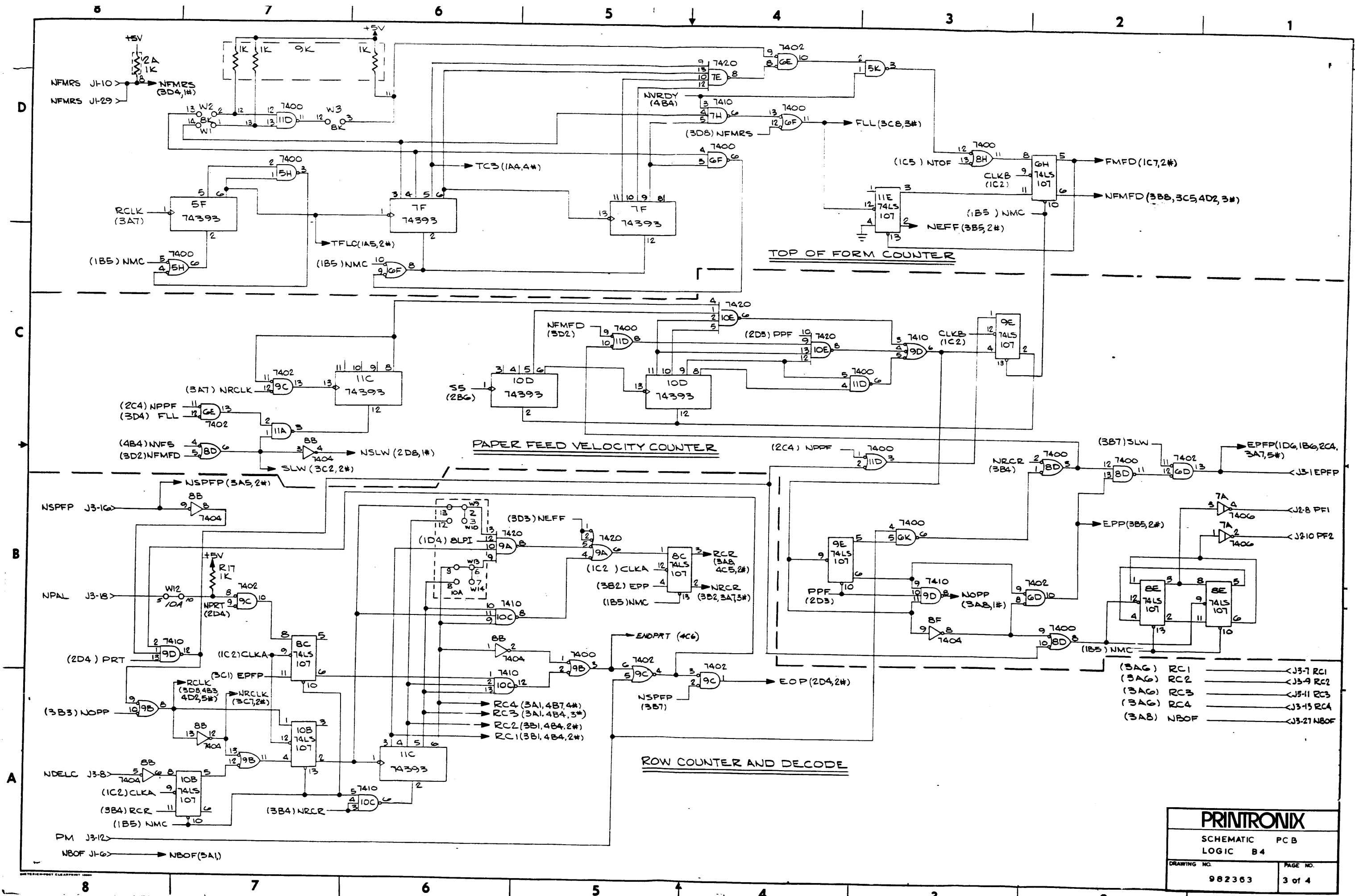
R17	
C15	
Y1	
CRI	

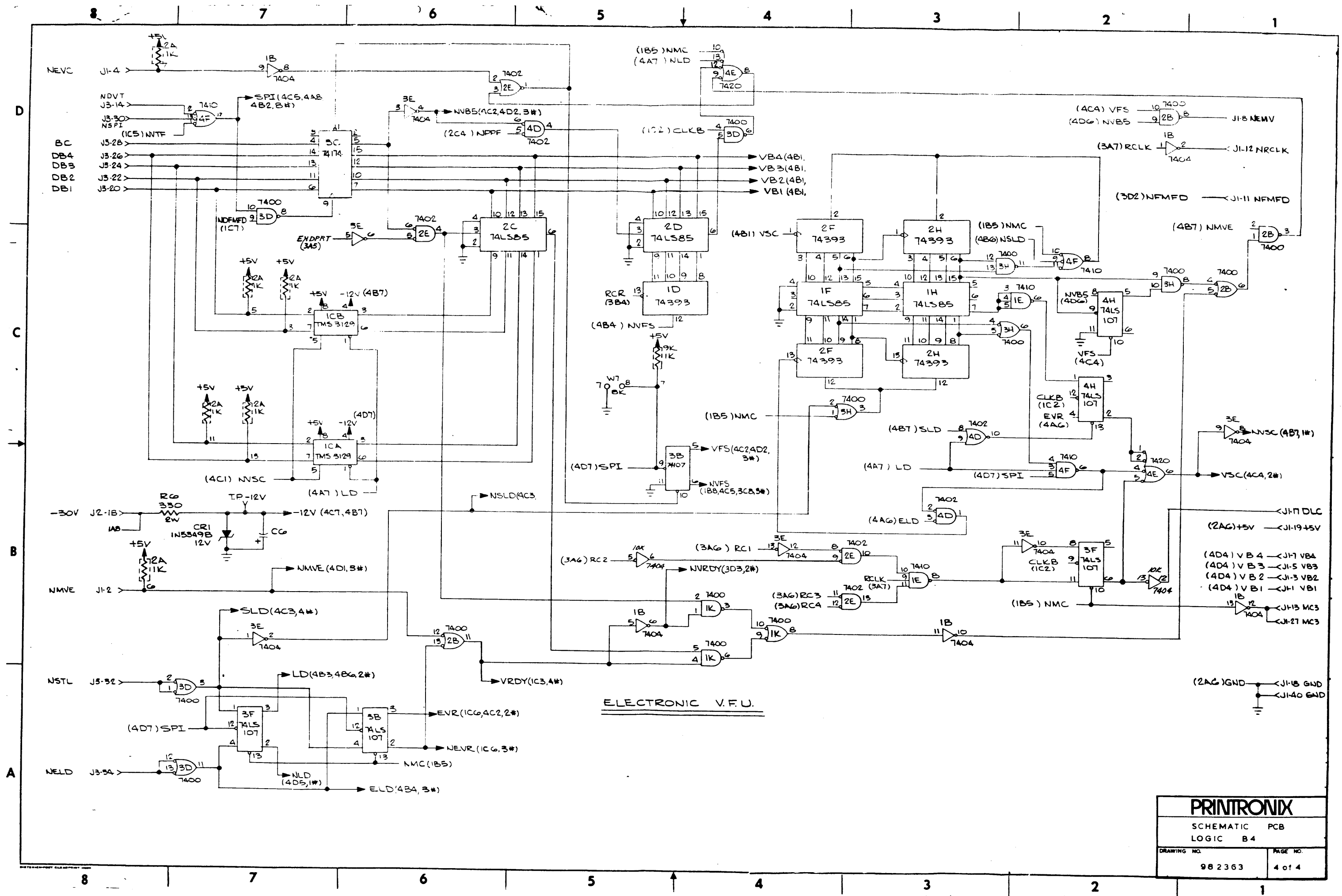
SPARE GATES

3A-7414-B,2	
10K-7404-.12,10	

**PRINTRONIX**  
 SCHEMATIC PCB  
 LOGIC B4  
 DRAWING NO. 982363 PAGE NO. 1 of 4

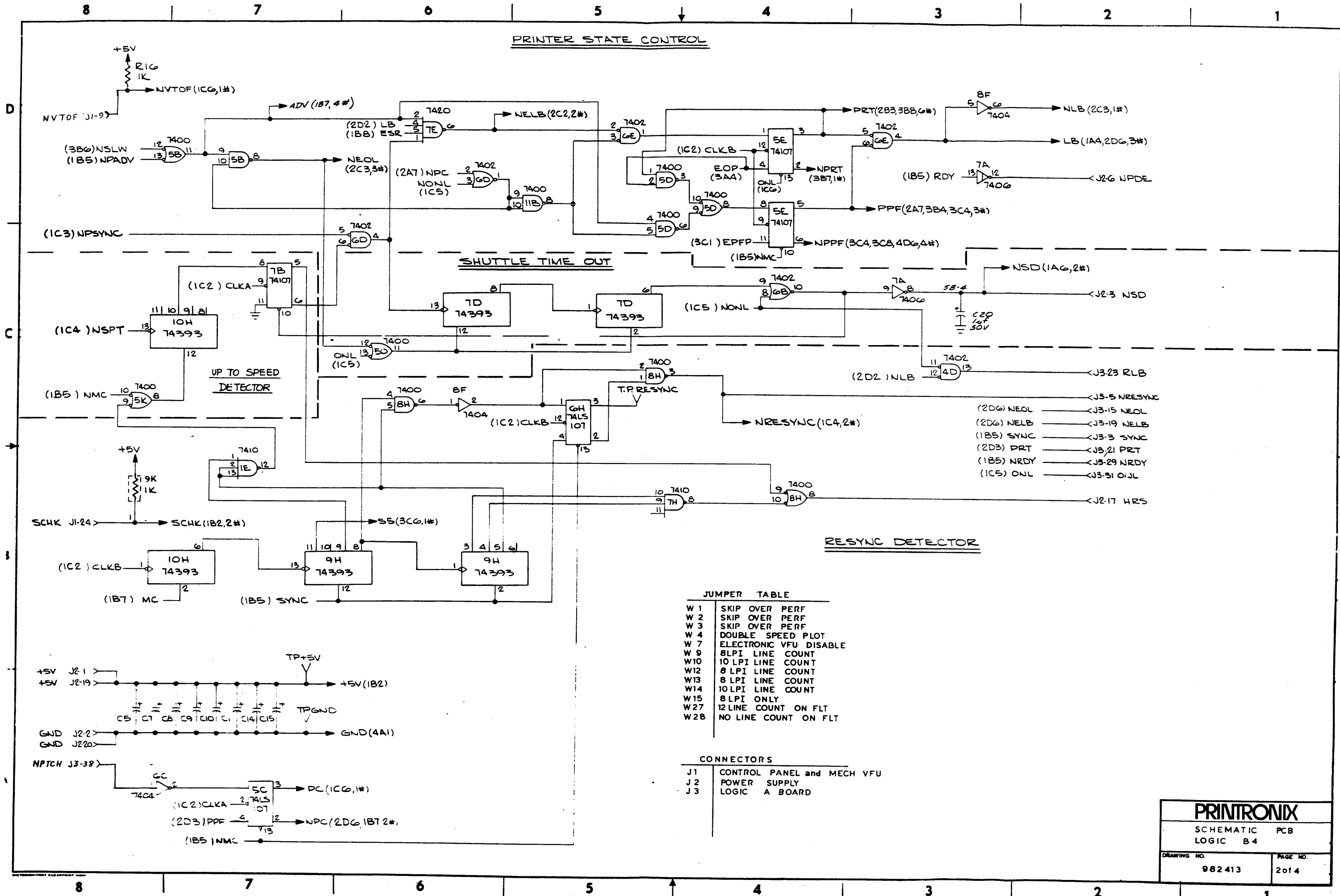






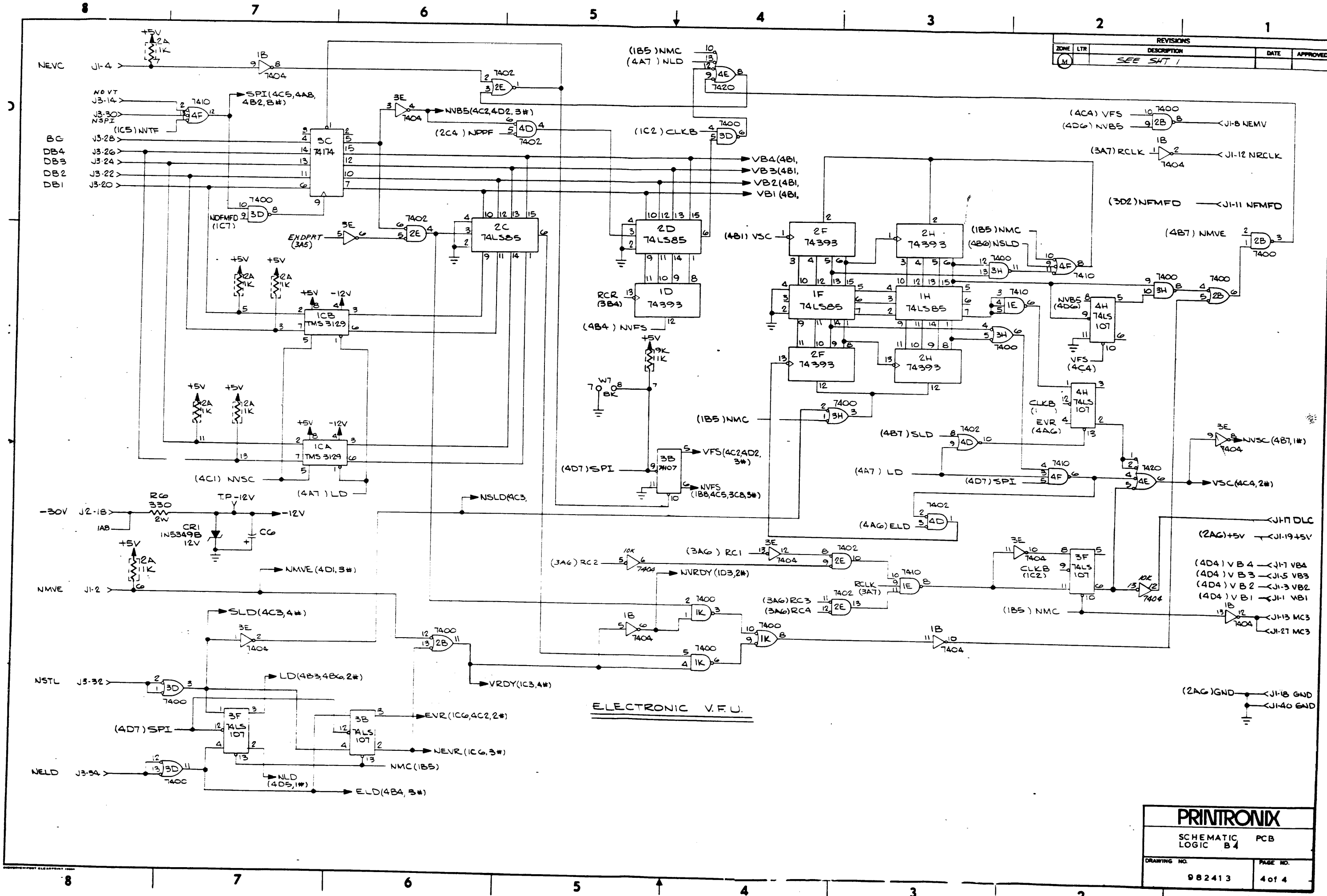


**PRINTER STATE CONTROL**





REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED
M		SEE SHT 1		

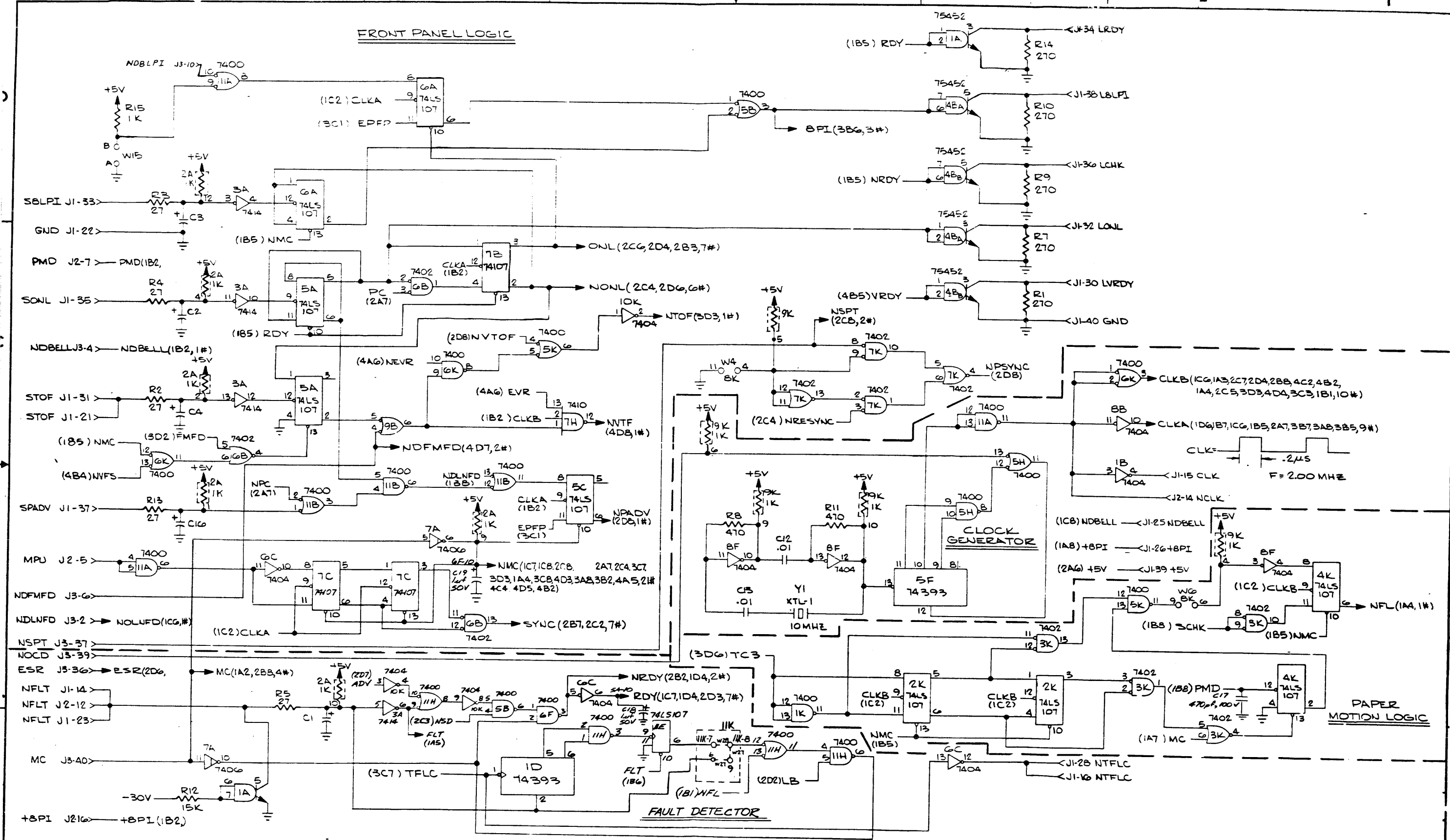


ELECTRONIC V.F.U.

<b>PRINTRONIX</b>	
SCHEMATIC	PCB
LOGIC	B4
DRAWING NO. 982413	PAGE NO. 4 of 4



FRONT PANEL LOGIC



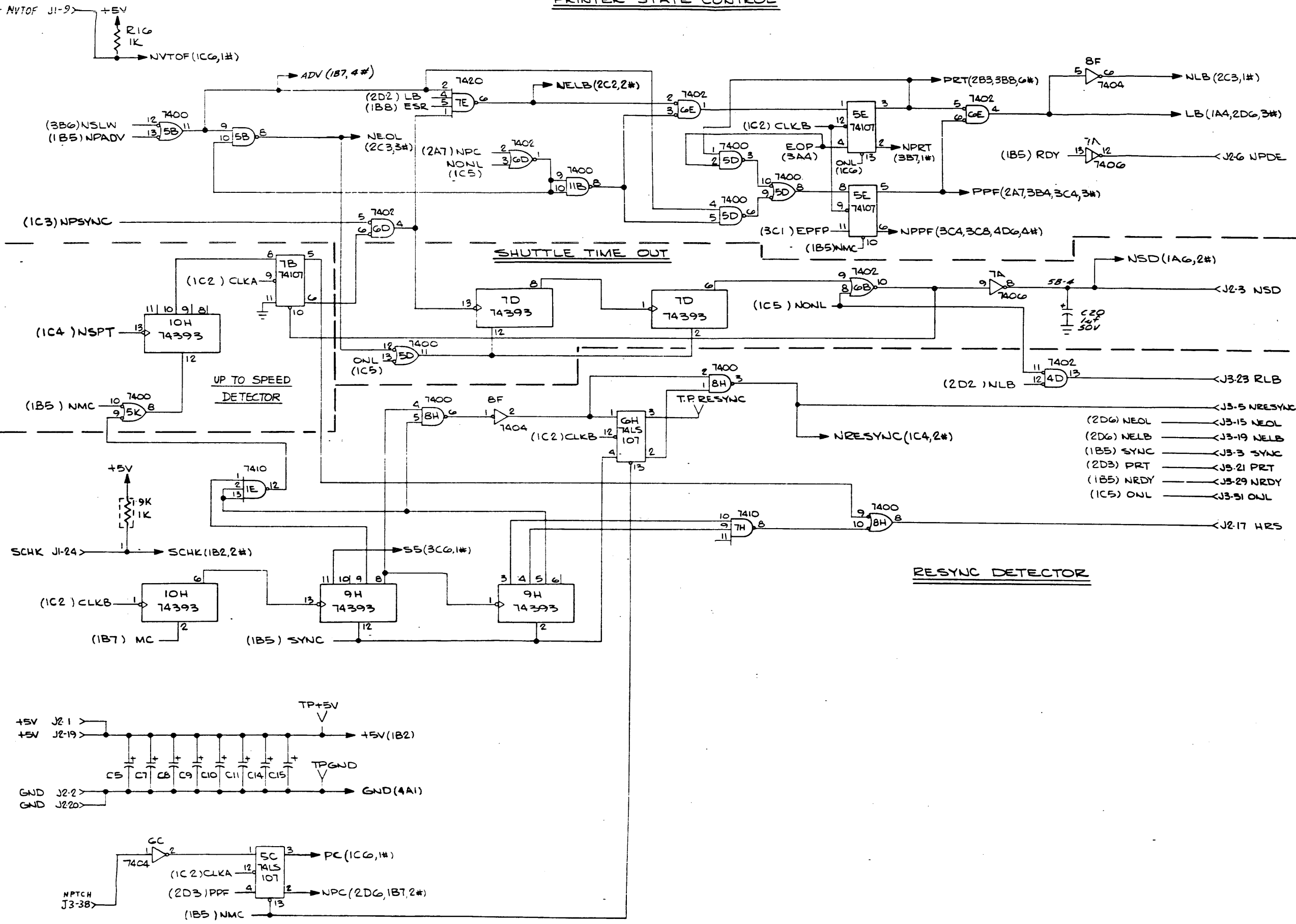
PRINTER STATE CONTROL

D

C

B

A



- (2D6) NEOL → J3-15 NEOL
- (2D6) NELB → J3-19 NELB
- (1B5) SYNC → J3-3 SYNC
- (2D3) PRT → J3-21 PRT
- (1B5) NRDY → J3-29 NRDY
- (1C5) ONL → J3-31 ONL
- (J3-5) NRESYNC
- (J3-19) NELB
- (J3-3) SYNC
- (J3-21) PRT
- (J3-29) NRDY
- (J3-31) ONL

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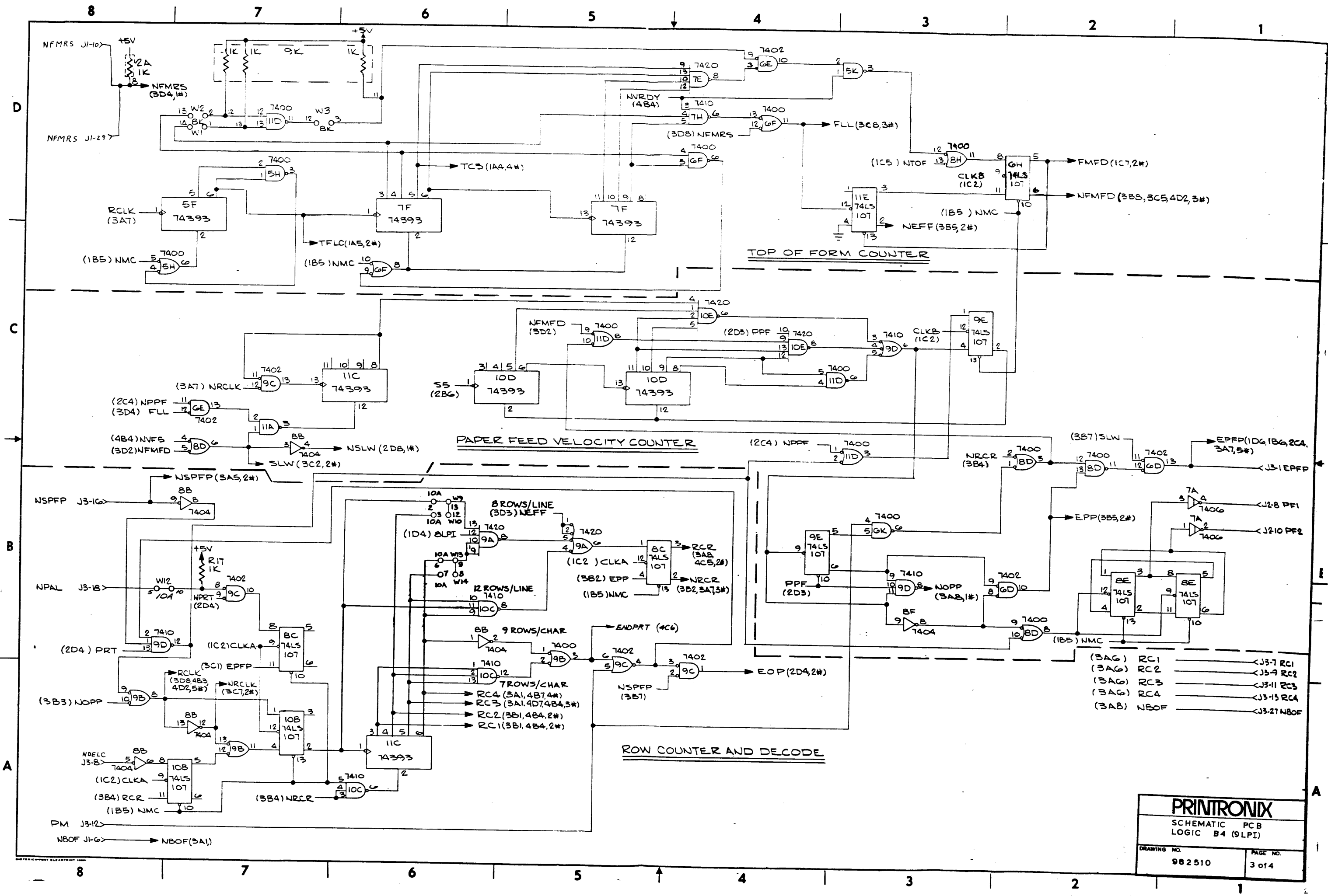
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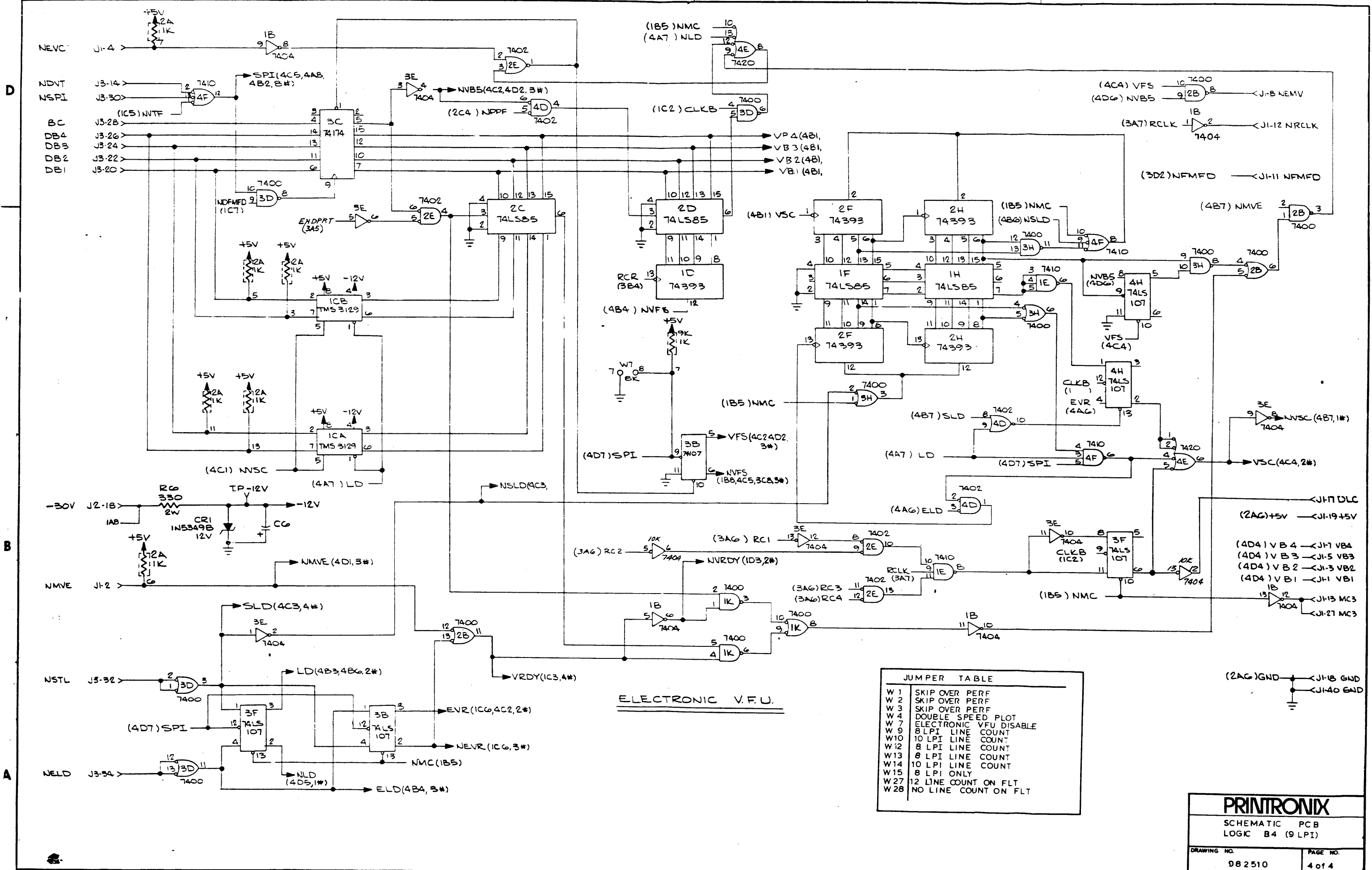
2

1



**PRINTRONIX**  
 SCHEMATIC PCB  
 LOGIC B4 (9LPI)

DRAWING NO.	PAGE NO.
982510	3 of 4



ELECTRONIC V.F.U.

JUMPER TABLE	
W 1	SKIP OVER PERF
W 2	SKIP OVER PERF
W 3	SKIP OVER PERF
W 4	DOUBLE SPEED PLOT
W 7	ELECTRONIC V.F.U. DISABLE
W 9	8 LPI LINE COUNT
W 10	10 LPI LINE COUNT
W 12	8 LPI LINE COUNT
W 13	8 LPI LINE COUNT
W 14	10 LPI LINE COUNT
W 15	8 LPI ONLY
W 27	12 LINE COUNT ON FLT
W 28	NO LINE COUNT ON FLT

**PRINTRONIX**  
 SCHEMATIC PCB  
 LOGIC B4 (9 LPI)  
 DRAWING NO. 98 2510      PAGE NO. 4 of 4

8

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1

D

C

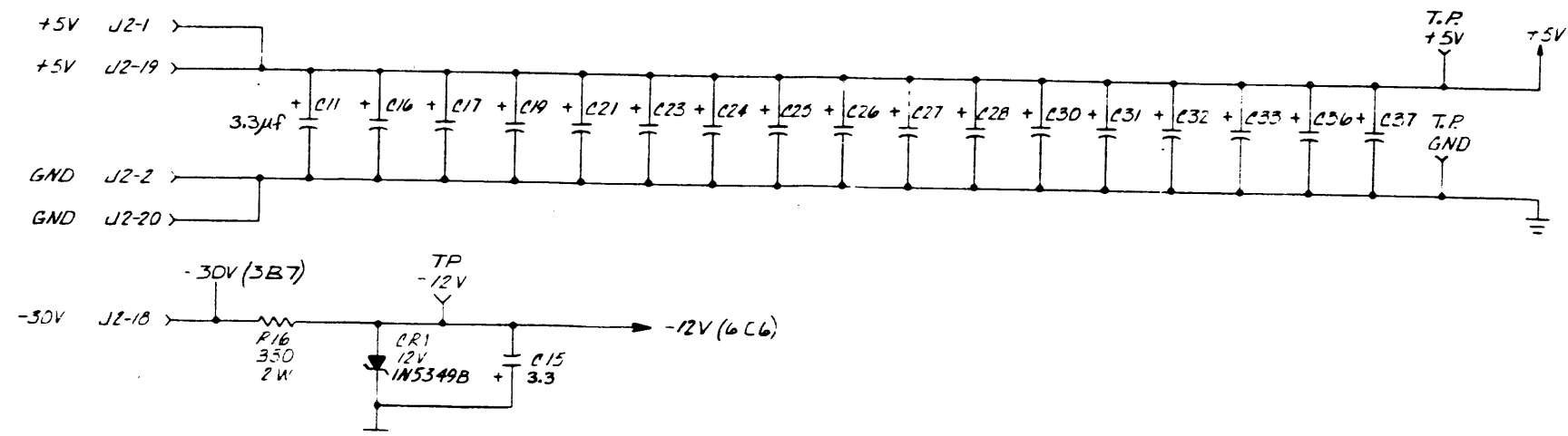
B

A

SPARE GATES TEST POINT		
3L	7400	11
3K	7420	5
5K	7486	3, 6, 8
3H	7410	6, 12
10F	7406	6, 8, 10, 12
5E	7402	15
12E	7420	8
2D	7404	2, 4
8C	7407	2, 6
10A	7410	6

JUMPER TABLE															
SKIP OVER FUNCTION	W1	W2	W3	W4	W5	W6	W7	W8	W9	W10	W11	W12	W13	W14	
SKIP OVER FUNCTION 6LPI/8LPI	X	X	X												
SKIP OVER PERF 3/4 LINES		X	X												
SKIP OVER PERF 4/5 LINES			X	X											
SKIP OVER PERF 7/8 LINES	X		X												
SKIP OVER PERF 9/8 LINES			X												
DOUBLE SPEED PLOT (P150)				X											
DOUBLE SPEED PLOT (P300)					X	X									
PMD ACTIVE *							X								
DISABLE VFU								X							
8 LPI									X						
10 LPI										X	X				
STOP ON FAULT *												X			
COUNT 12 LINES BEFORE FAULT STOP													X		
IN = MOVE 1 LINE BINARY (T: 000)														X	
OUT = MOVE 1 LINE BINARY (T: 001)															X
IN = 300 LPI MACHINE															X
OUT = 150 LPI MACHINE															X

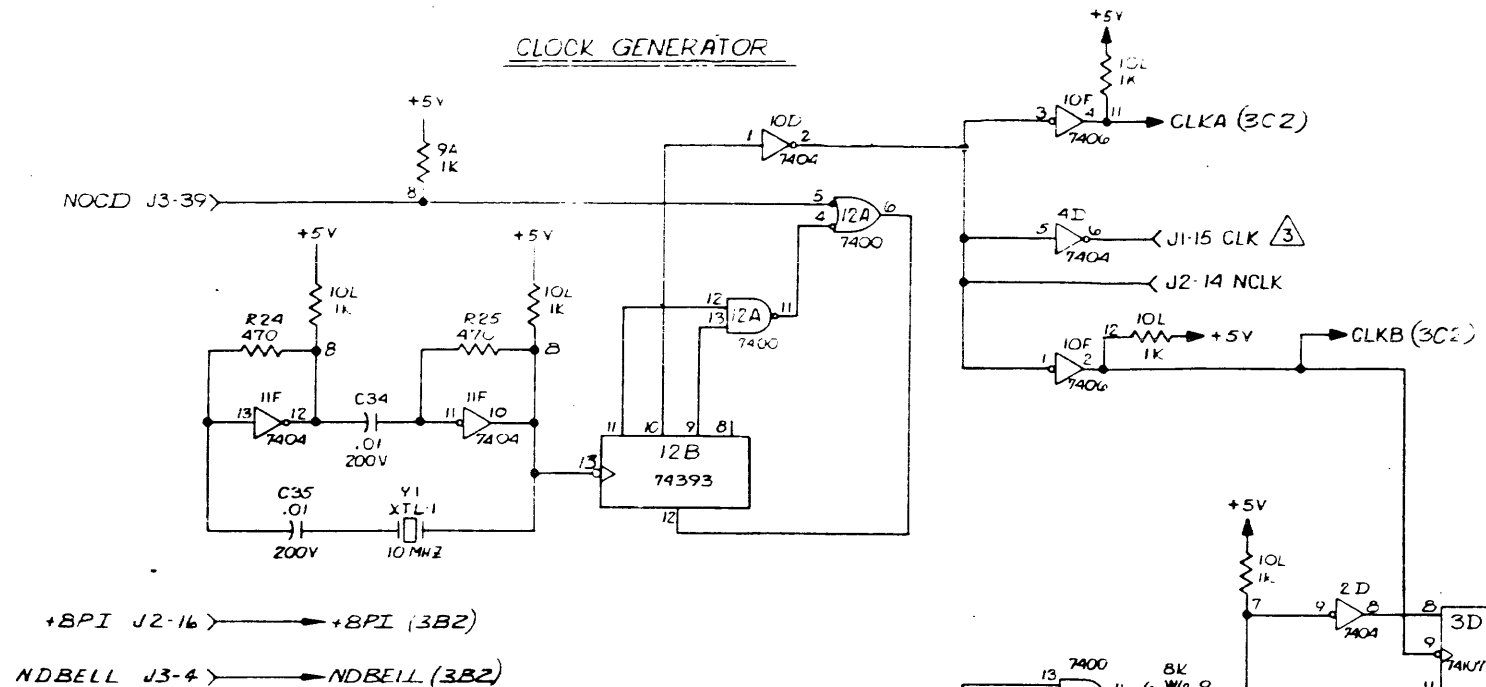
\* = INSTALLED AS REQ'D AT FACTORY.





D

CLOCK GENERATOR

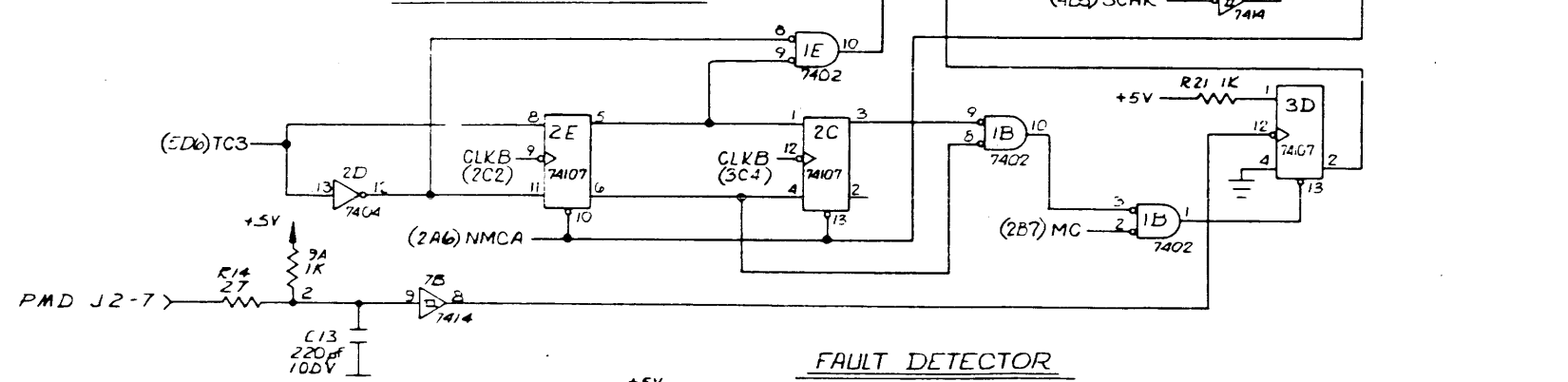


- (3C4) ○ — CLKB — (2D6, 2C6, 3B6, 2B6, 3B5, 4B8, 4C7, 4C5, 4D4)
- (3D4) ○ — CLKA — (2A7, 2C6, 4A7, 5B7, 5D3, 5A8, 5B5, 5C3, 6B2, 6D5, 6C2)
- (3B6) ○ — NMCH — (2B8, 3B6, 4A7, 4C4, 5C8, 4B6, 2C7, 2A6)

C

+BPI J2-16 → +BPI (3B2)  
 NDBELL J3-4 → NDBELL (3B2)

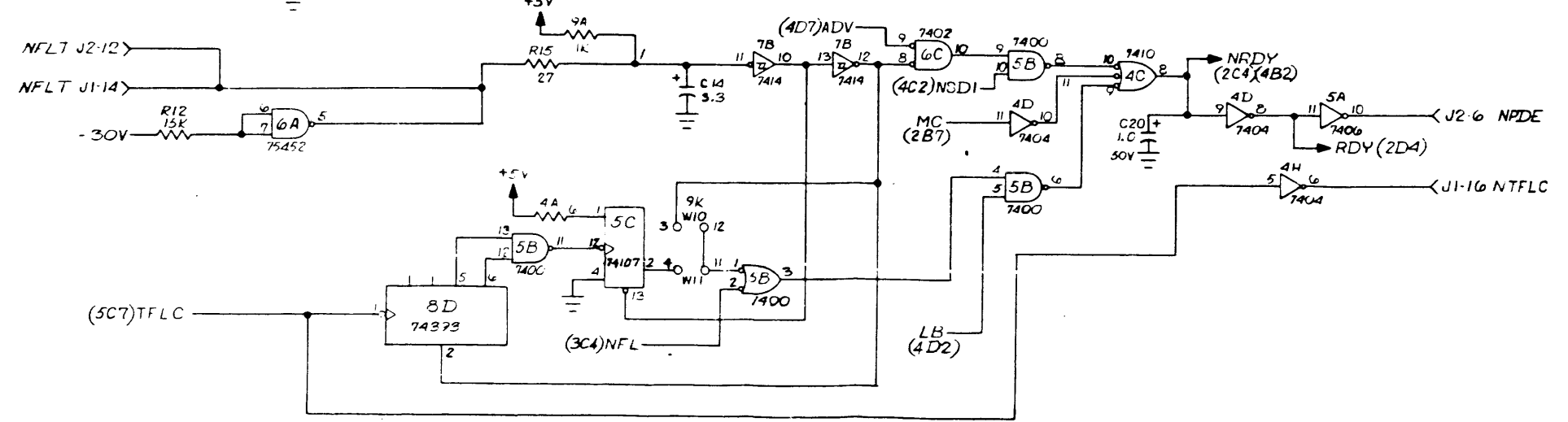
PAPER MOTION LOGIC



- (3C7) NDBELL — J1-25 NDBELL
- (5C7) +BPI — J1-26 +BPI
- (4A6) +5V — J1-39 +5V

B

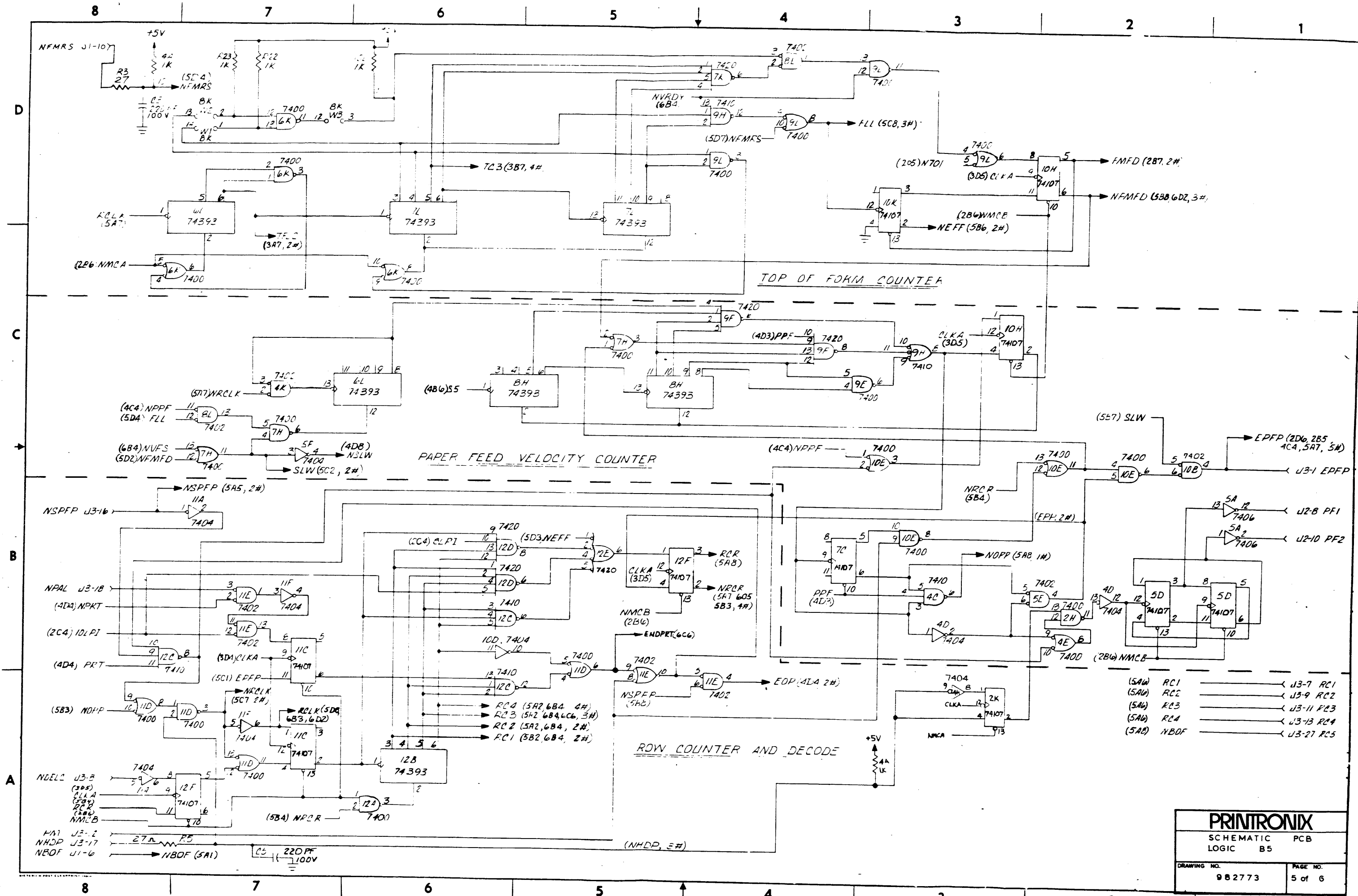
FAULT DETECTOR



A





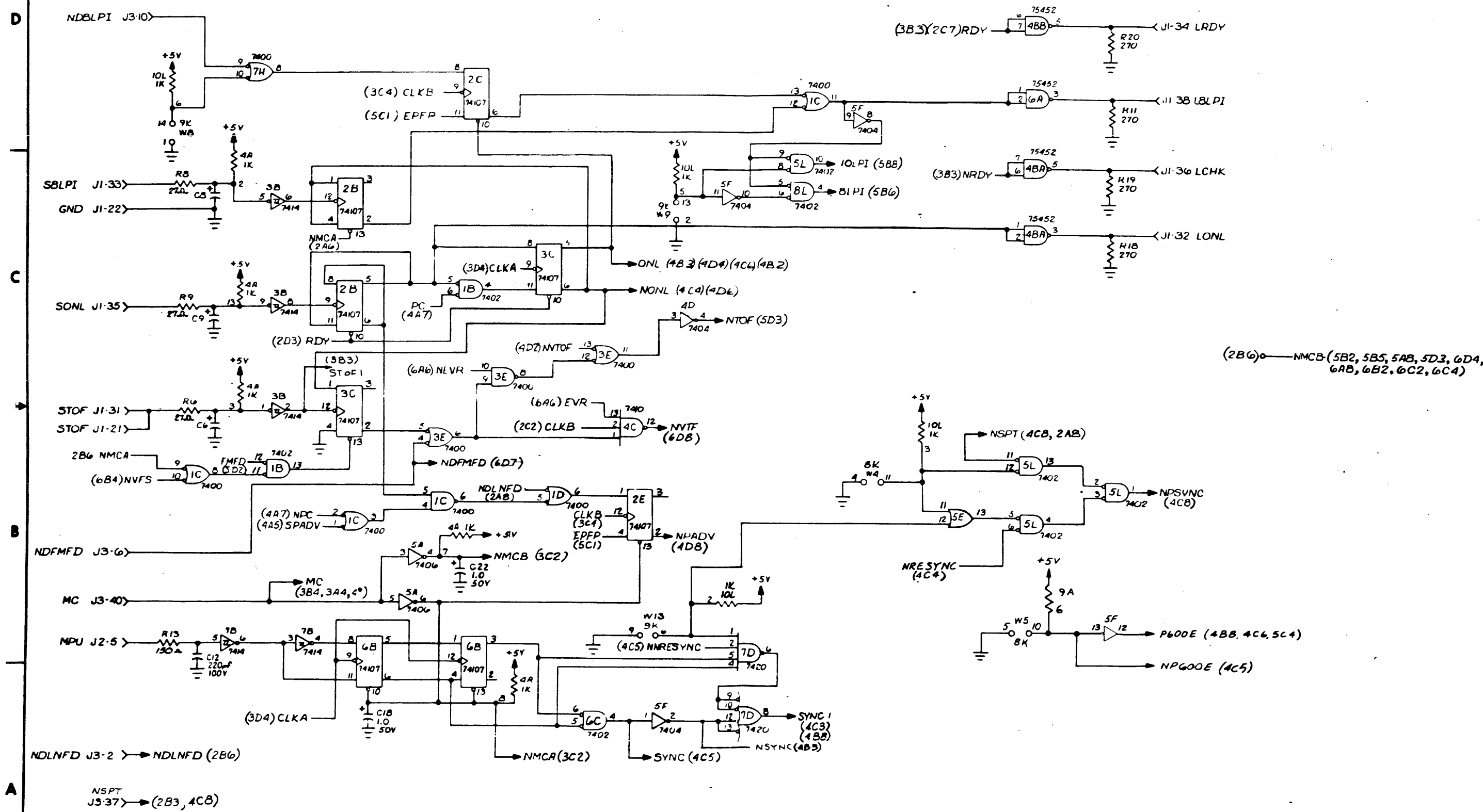






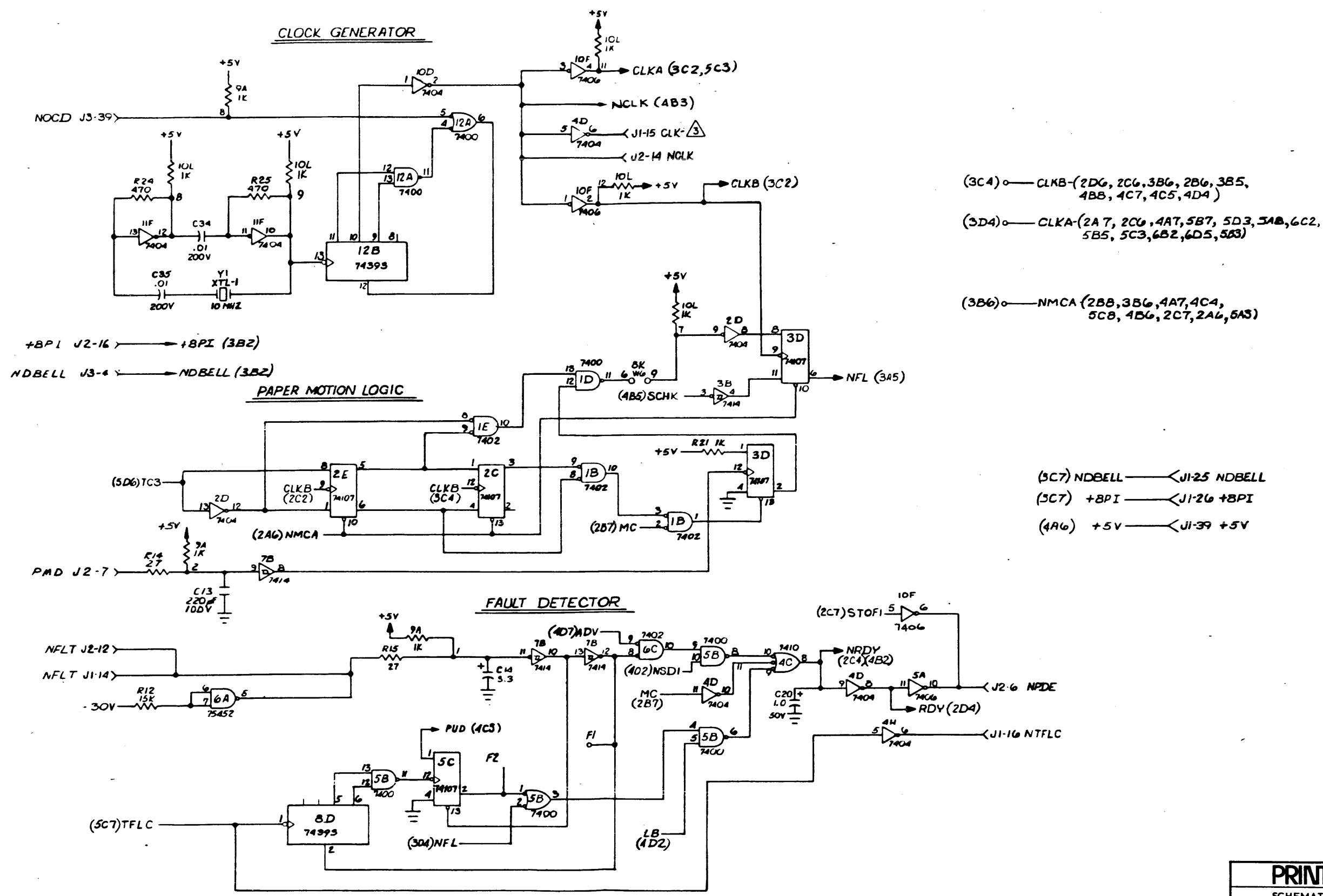
REVISIONS		DATE	APPROVED
ZONE	LTR		

FRONT PANEL LOGIC



(2B6) - NMCA (5B2, 5B5, 5AB, 5D3, 6D4, 6AB, 6B2, 6C2, 6C4)

REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED



- (3C4) ○ — CLKB (2D6, 2C6, 3B6, 2B6, 3B5, 4B8, 4C7, 4C5, 4D4)
- (3D4) ○ — CLKA (2A7, 2C6, 4A7, 5B7, 5D3, 5A8, 6C2, 5B5, 5C3, 6B2, 6D5, 5A3)
- (3B6) ○ — NMCA (2B8, 3B6, 4A7, 4C4, 5C8, 4B6, 2C7, 2A6, 5A5)

- (3C7) NDBELL — J1-25 NDBELL
- (3C7) +BPI — J1-26 +BPI
- (4A6) +5V — J1-39 +5V

PRINTER STATE CONTROL

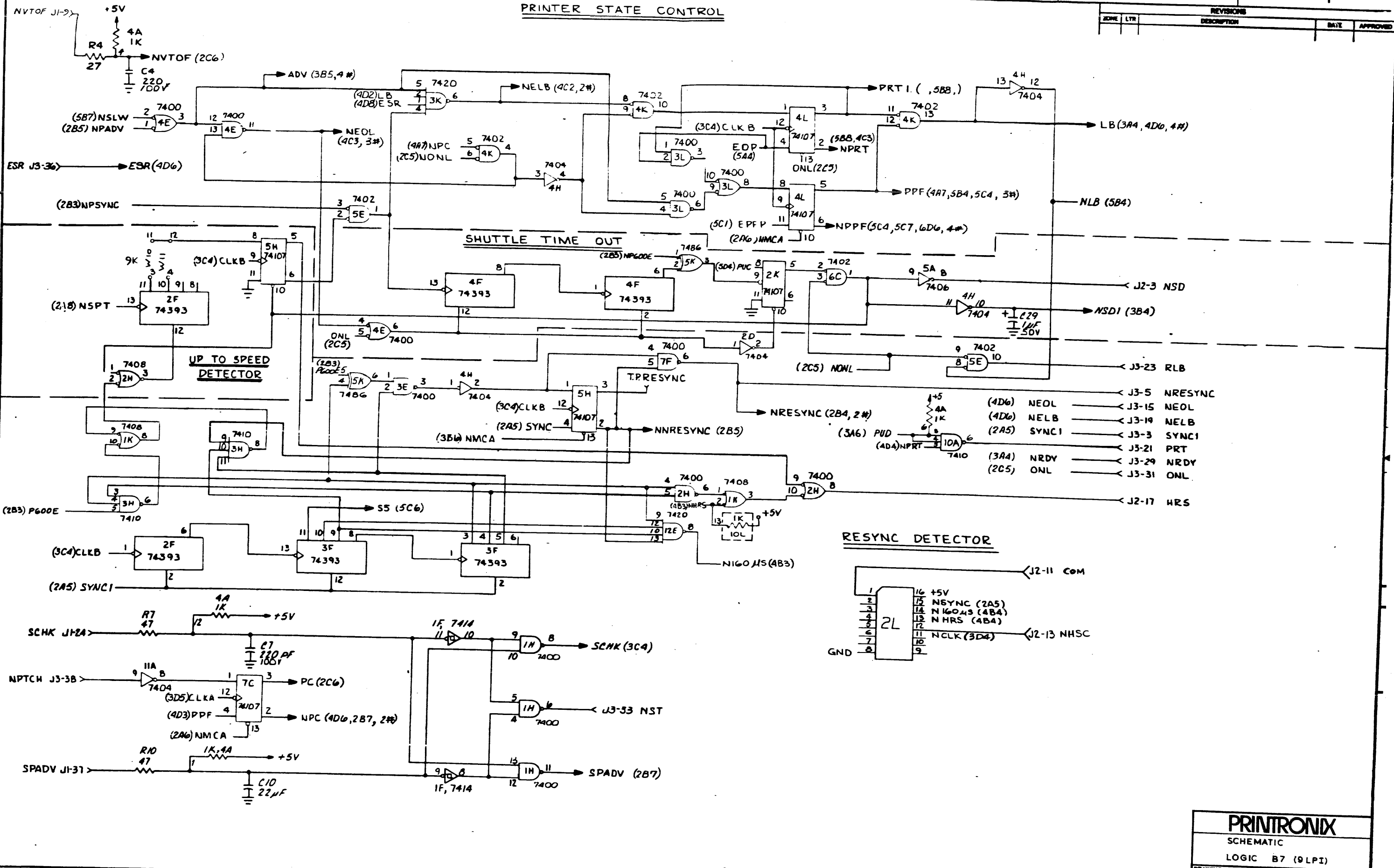
REVISIONS				
ZONE	LTR	DESCRIPTION	DATE	APPROVED

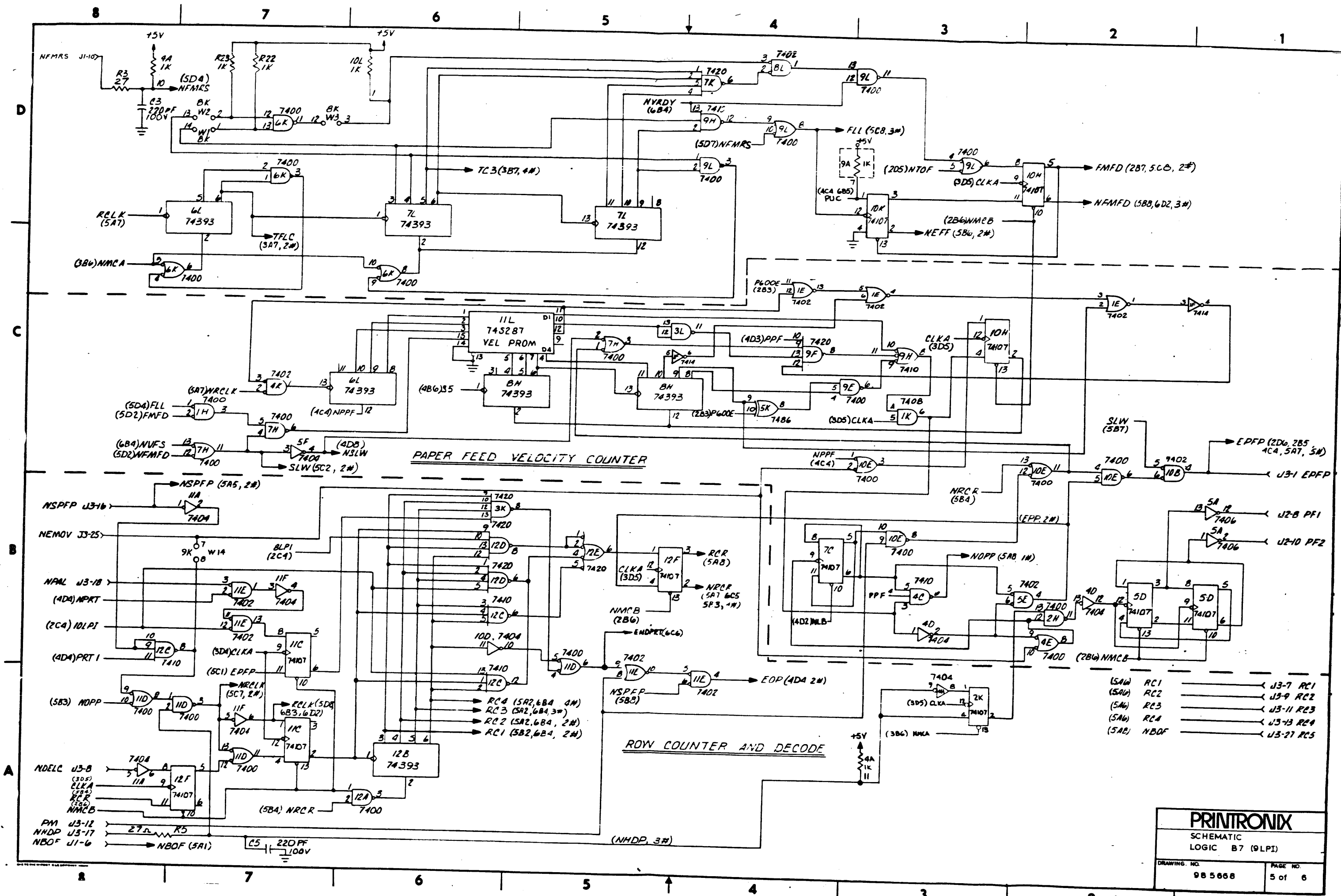
D

C

B

A






- (5A7) NRCLK
- (5A6) RC1
- (5A5) RC2
- (5A4) RC3
- (5A3) RC4
- (5A2) NBOF
- (5A1) NDEL
- (5A0) NMOV
- (4A9) NPRT
- (4A8) NPRT
- (4A7) PRT 1
- (4A6) PRT 1
- (4A5) PRT 1
- (4A4) PRT 1
- (4A3) PRT 1
- (4A2) PRT 1
- (4A1) PRT 1
- (4A0) PRT 1
- (3A9) NPPF
- (3A8) NPPF
- (3A7) NPPF
- (3A6) NPPF
- (3A5) NPPF
- (3A4) NPPF
- (3A3) NPPF
- (3A2) NPPF
- (3A1) NPPF
- (3A0) NPPF
- (2A9) NRCR
- (2A8) NRCR
- (2A7) NRCR
- (2A6) NRCR
- (2A5) NRCR
- (2A4) NRCR
- (2A3) NRCR
- (2A2) NRCR
- (2A1) NRCR
- (2A0) NRCR
- (1A9) NRCR
- (1A8) NRCR
- (1A7) NRCR
- (1A6) NRCR
- (1A5) NRCR
- (1A4) NRCR
- (1A3) NRCR
- (1A2) NRCR
- (1A1) NRCR
- (1A0) NRCR

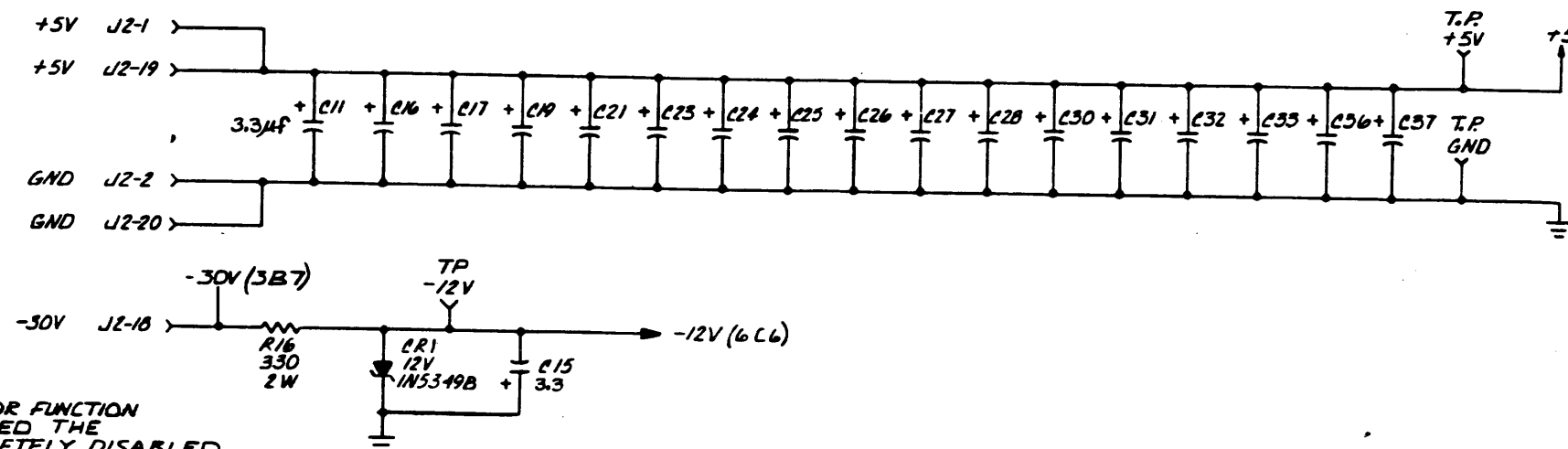





CONNECTOR BLOCK	
J1	CONTROL PANEL
J2	POWER
J3	A BOARD TRANSFER

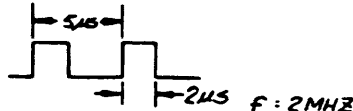
JUMPER TABLE 															
	W1	W2	W3	W4	W5	W6	W7	W8	W9	W10	W11	W12	W13	W14	F1-F2
SKIP OVER FUNCTION 6LPI/8LPI															
SKIP OVER PERF 3/4 LINES	X	X	X												
SKIP OVER PERF 4/6 LINES		X	X												
SKIP OVER PERF 7/6 LINES	X		X												
SKIP OVER PERF 9/6 LINES			X												
DOUBLE SPEED PLOT				X											
IMMEDIATE PAPER OUT FAULT															X
PMD ACTIVE *						X									
DISABLE VFU							X								
8 LPI								X							
10 LPI									X	X					
P-300											X	X	X		
IN = MOVE 1 LINE BINARY CT=000												X			
OUT = MOVE 1 LINE BINARY CT=001													X		
P-600					X					X	X				
P-150											X			X	

\* = INSTALLED AT FACTORY



 X INDICATES JUMPER INSTALLED FOR FUNCTION SPECIFIED. IF W3 IS NOT INSTALLED THE SKIP OVER PERFORMANCE IS COMPLETELY DISABLED.

 CLOCK WAVE FORM



2. ALL CAPACITORS - 3.3  $\mu$ F, 15V,  $\pm$ 20%  
 1. ALL RESISTORS - 1/4W,  $\pm$ 5%

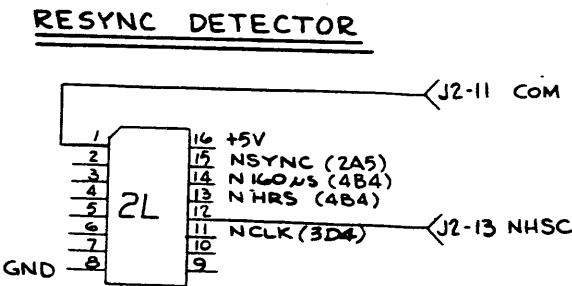
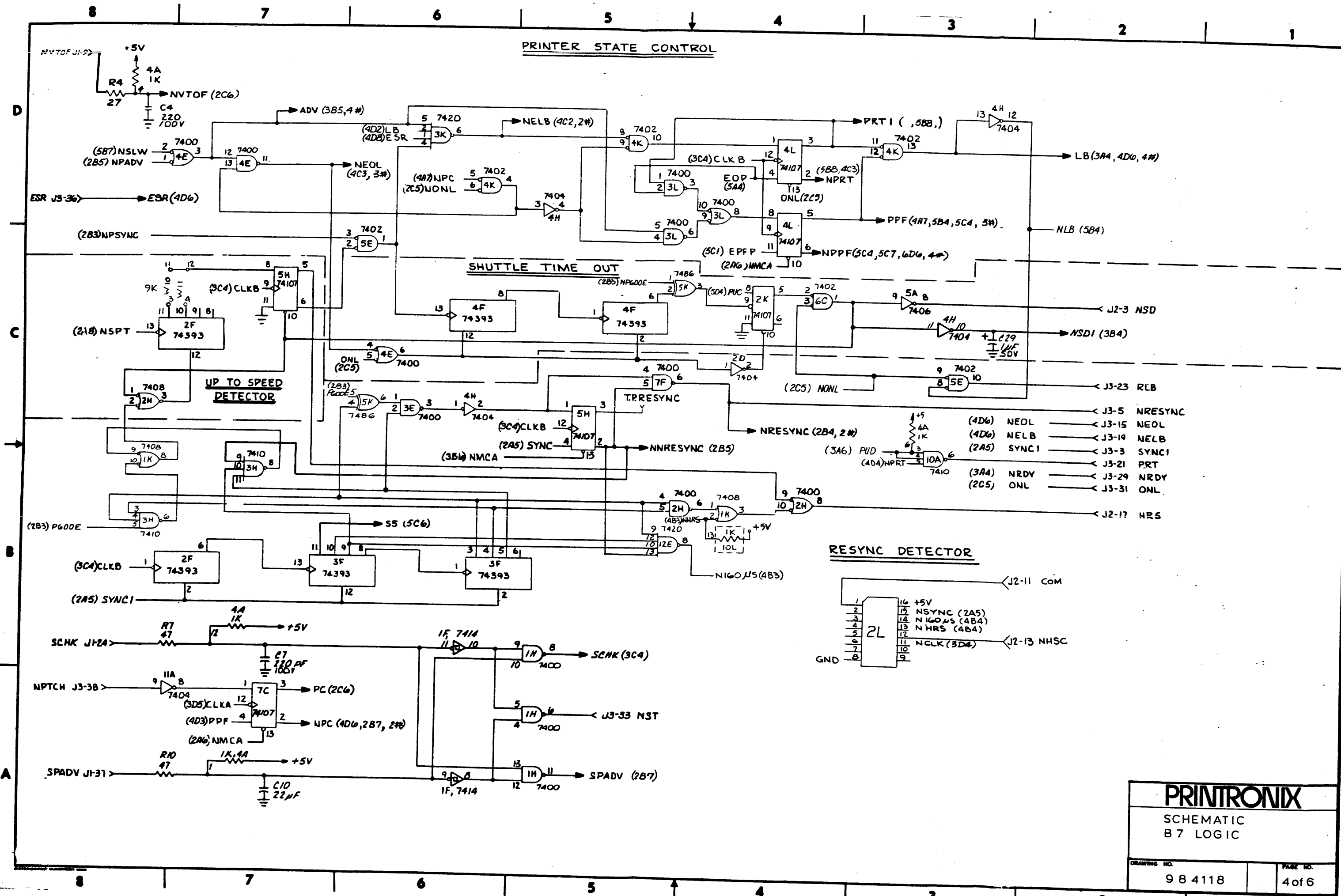
NOTES: UNLESS OTHERWISE SPECIFIED

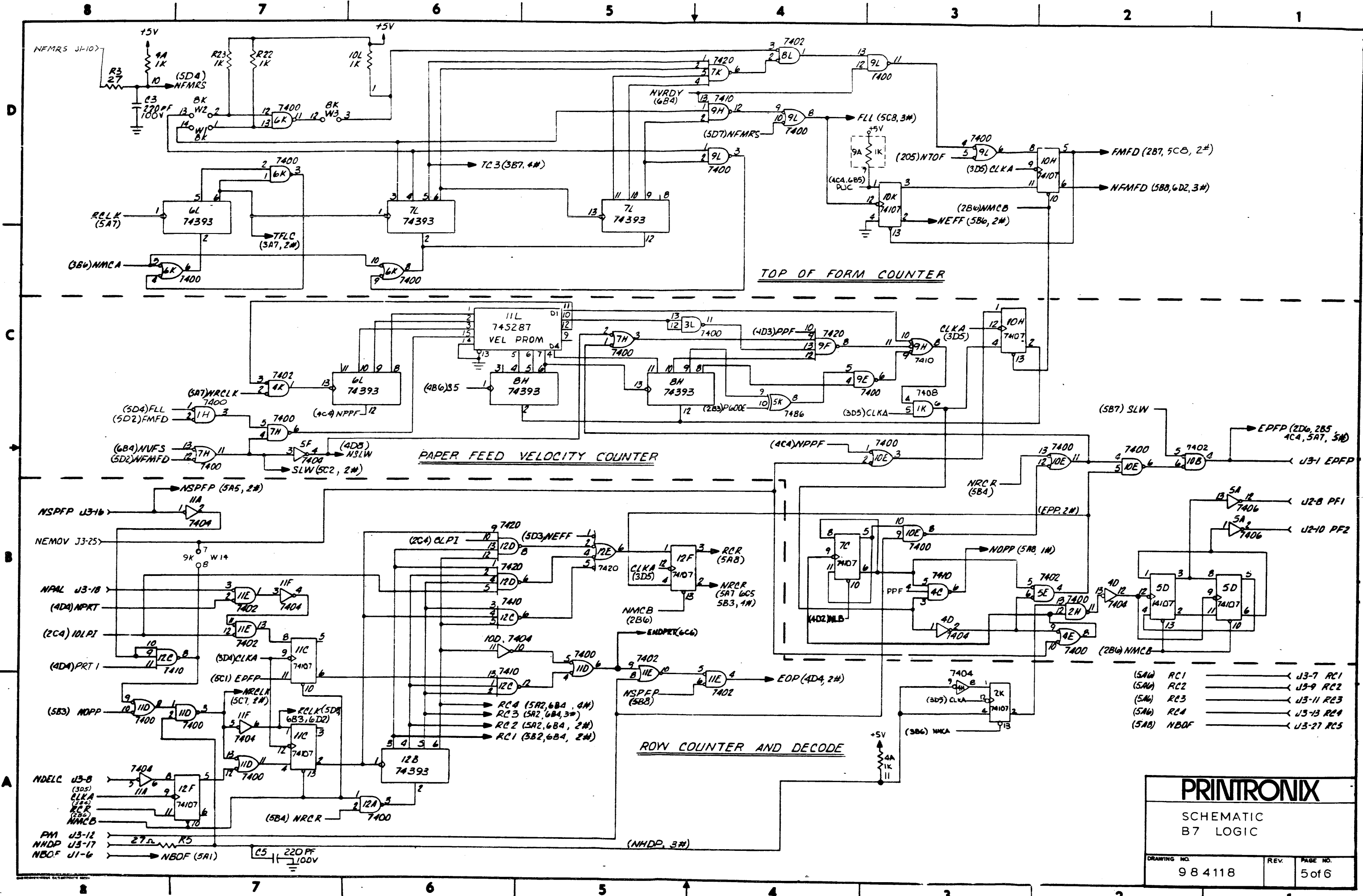
<b>PRINTRONIX</b>	
SCHEMATIC B7 LOGIC	
DRAWING NO. 984118	PAGE NO. 1 of 6





PRINTER STATE CONTROL





- (5A6) RC1
- (5A6) RC2
- (5A6) RC3
- (5A6) RC4
- (5A8) NBOF
- (J3-7) RC1
- (J3-9) RC2
- (J3-11) RC3
- (J3-13) RC4
- (J3-27) RC5

**PRINTRONIX**

SCHEMATIC  
B7 LOGIC

DRAWING NO. 984118	REV.	PAGE NO. 5 of 6
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8

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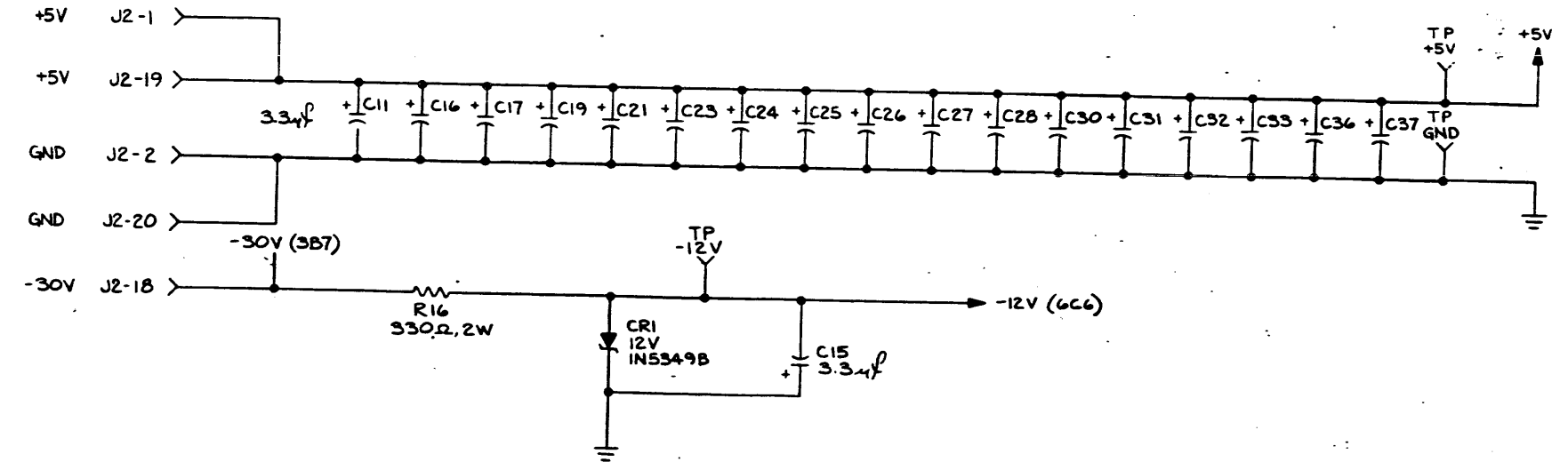
2

1

### JUMPER TABLE $\Delta$

W1,W2,W3,W4,W5,W6,W7, W8,W9,W10,W11,W12,W13,W14	SKIP OVER FUNCTION 6LPI/8LPI
W1,W2,W3	SKIP OVER PERF 3/4 LINES
W2,W3	SKIP OVER PERF 4/8 LINES
W1,W3	SKIP OVER PERF 5/6 LINES
W3	SKIP OVER PERF 6/8 LINES
W4	DOUBLE SPEED PLOT
W6	PMD ACTIVE *
W7	DISABLE VFU
W8	8 LPI
W8,W9	10 LPI
W11,W13,W14	P300
W12	IN = MOVE 1 LINE BINARY CT = 000 OUT = MOVE 1 LINE BINARY CT = 001
W5,W11,W13	P600
W10,W14	PI50

\*INSTALLED AS REQ'D AT FACTORY



# PRINTRONIX

SCHEMATIC  
LOGIC B7 (MOD PLOT)

DRAWING NO. 985520	PAGE NO. 1 of 6
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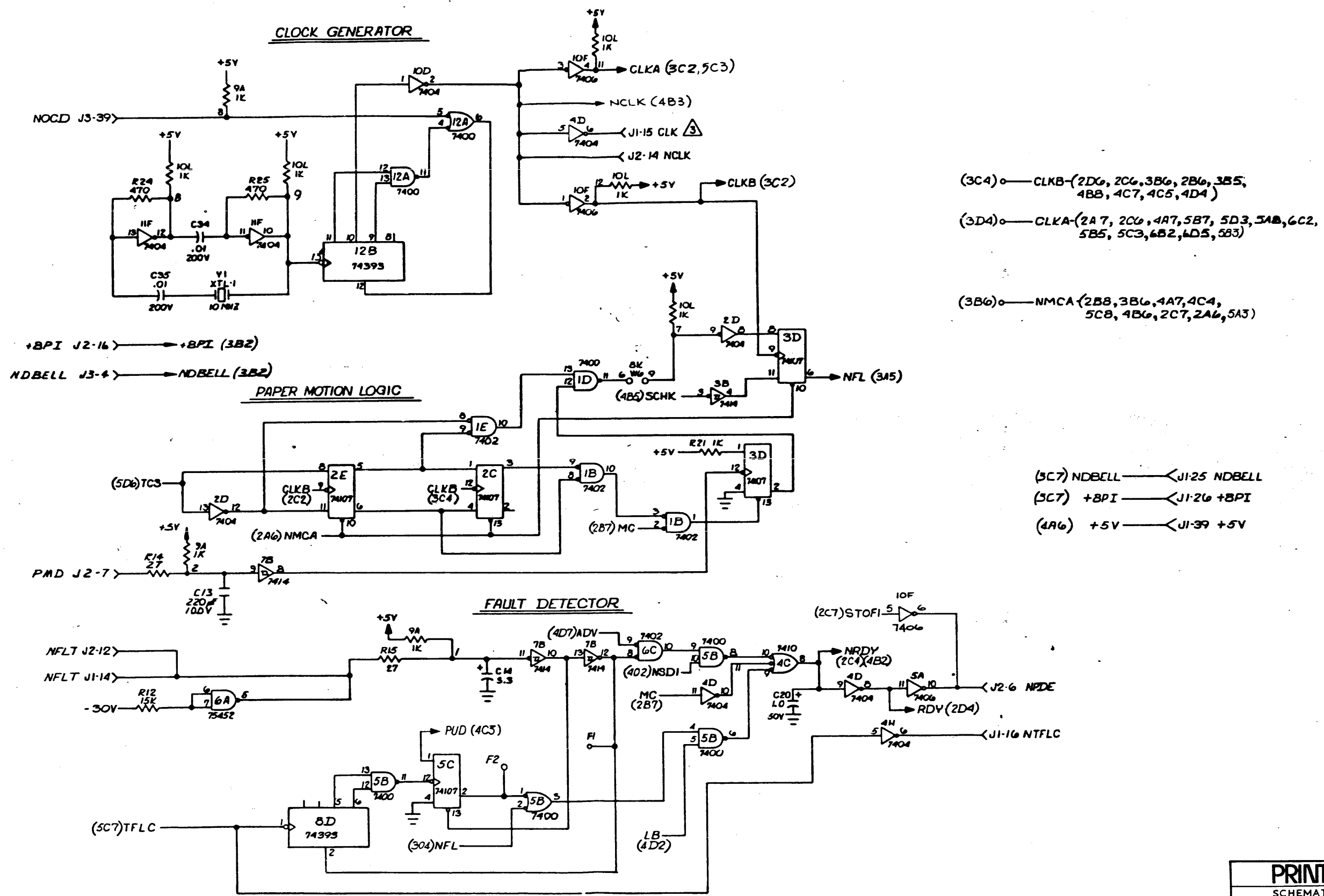


D

C

B

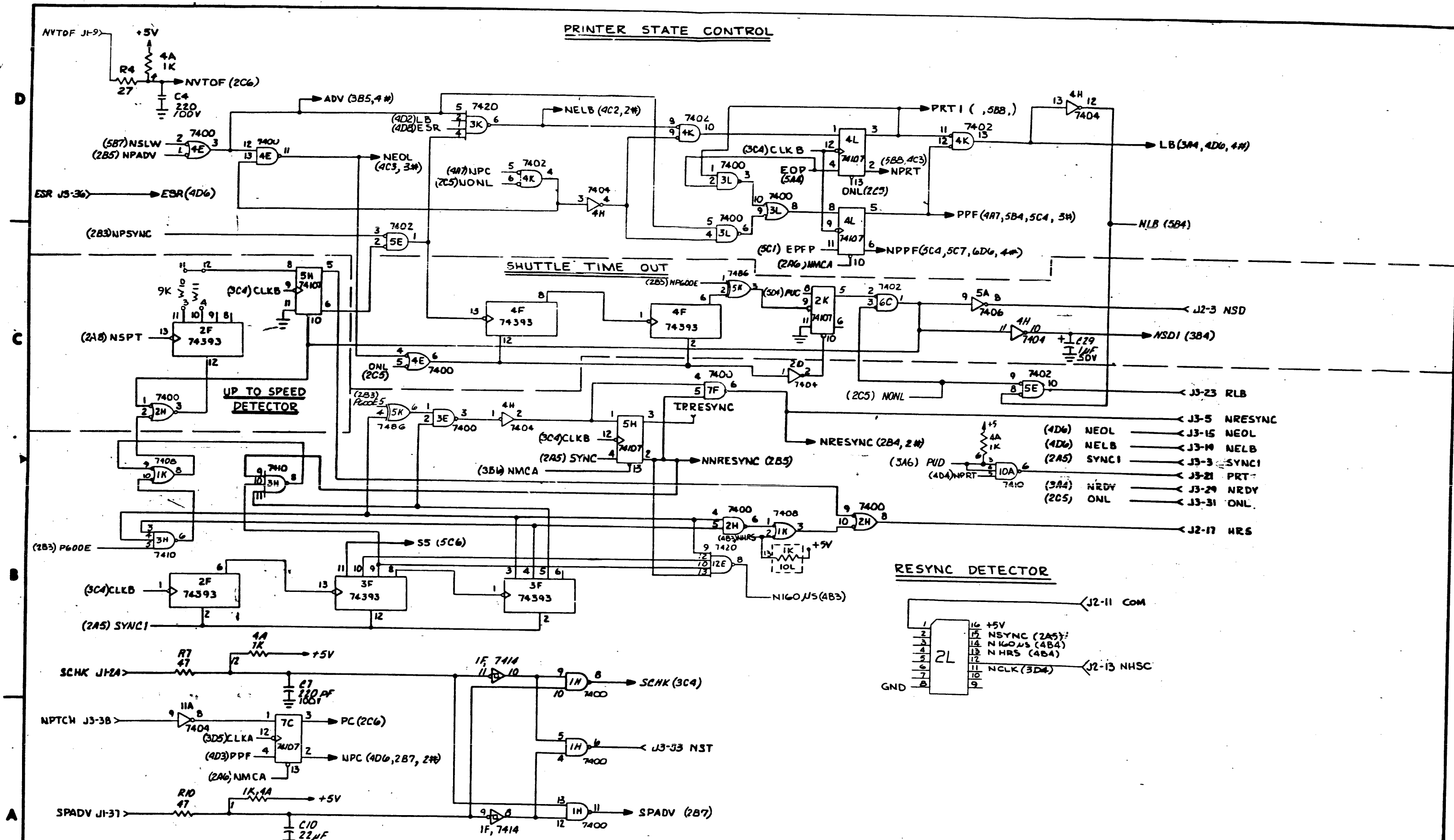
A



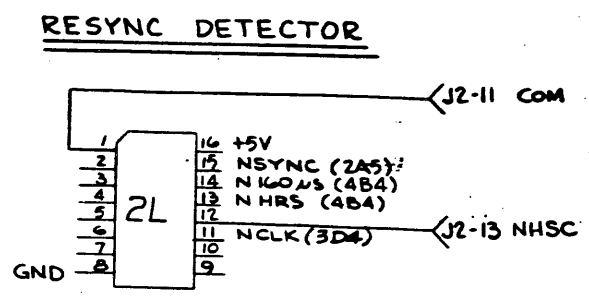
- (3C4) ○ CLKB (2D6, 2C6, 3B6, 2B6, 3B5, 4B8, 4C7, 4C5, 4D4)
- (3D4) ○ CLKA (2A7, 2C6, 4A7, 5B7, 5D3, 5A8, 6C2, 5B5, 5C3, 6B2, 6D5, 5B3)
- (3B6) ○ NMCA (2B8, 3B6, 4A7, 4C4, 5C8, 4B6, 2C7, 2A6, 5A3)

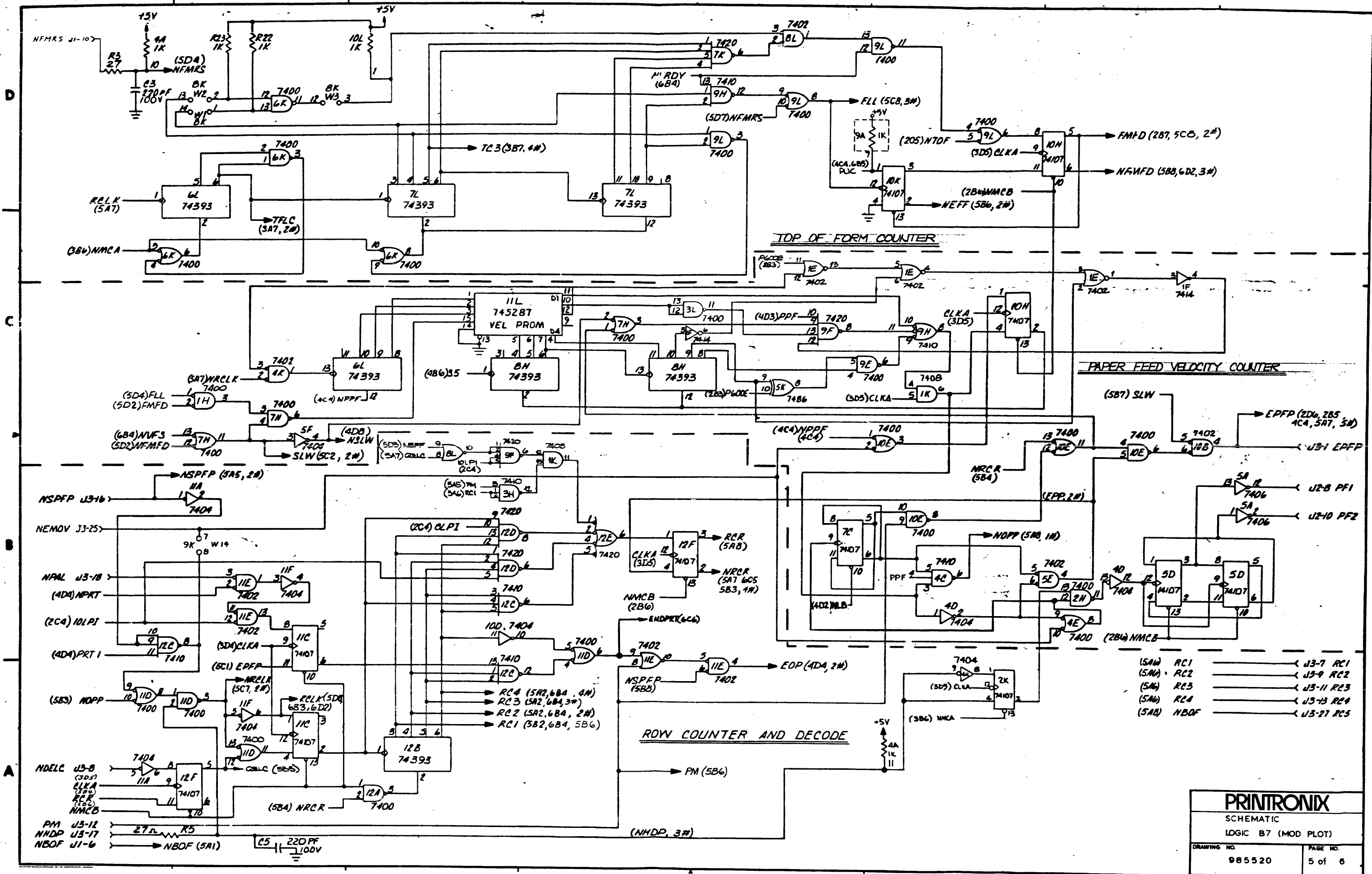
- (5C7) NDBELL ← J1-25 NDBELL
- (5C7) +BPI ← J1-26 +BPI
- (4A6) +5V ← J1-39 +5V

PRINTER STATE CONTROL



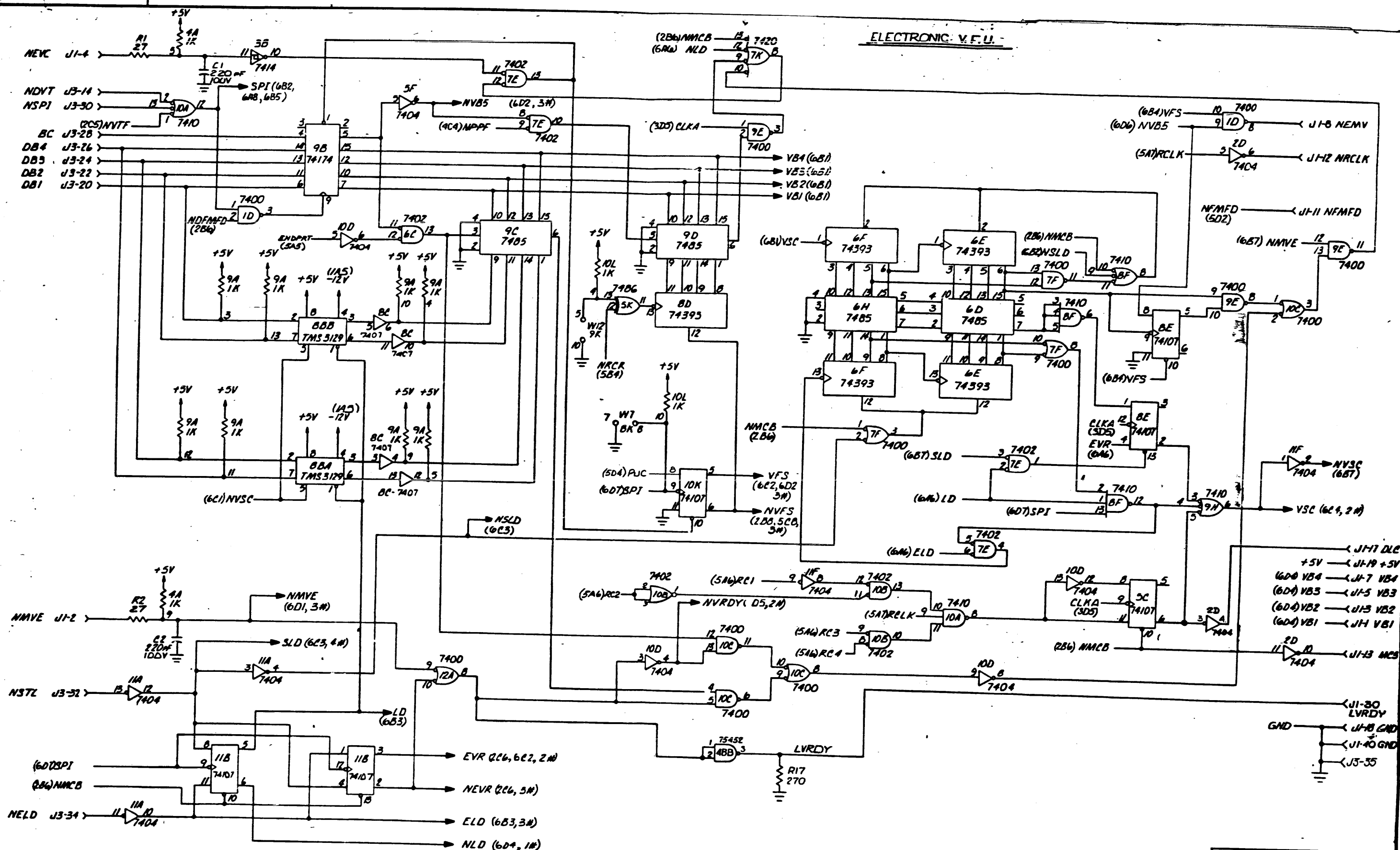
- (4D6) NEOL → J3-15 NEOL
- (4D6) NELB → J3-19 NELB
- (2A5) SYNC1 → J3-3 SYNC1
- (3A4) NRDY → J3-29 NRDY
- (2C5) ONL → J3-31 ONL





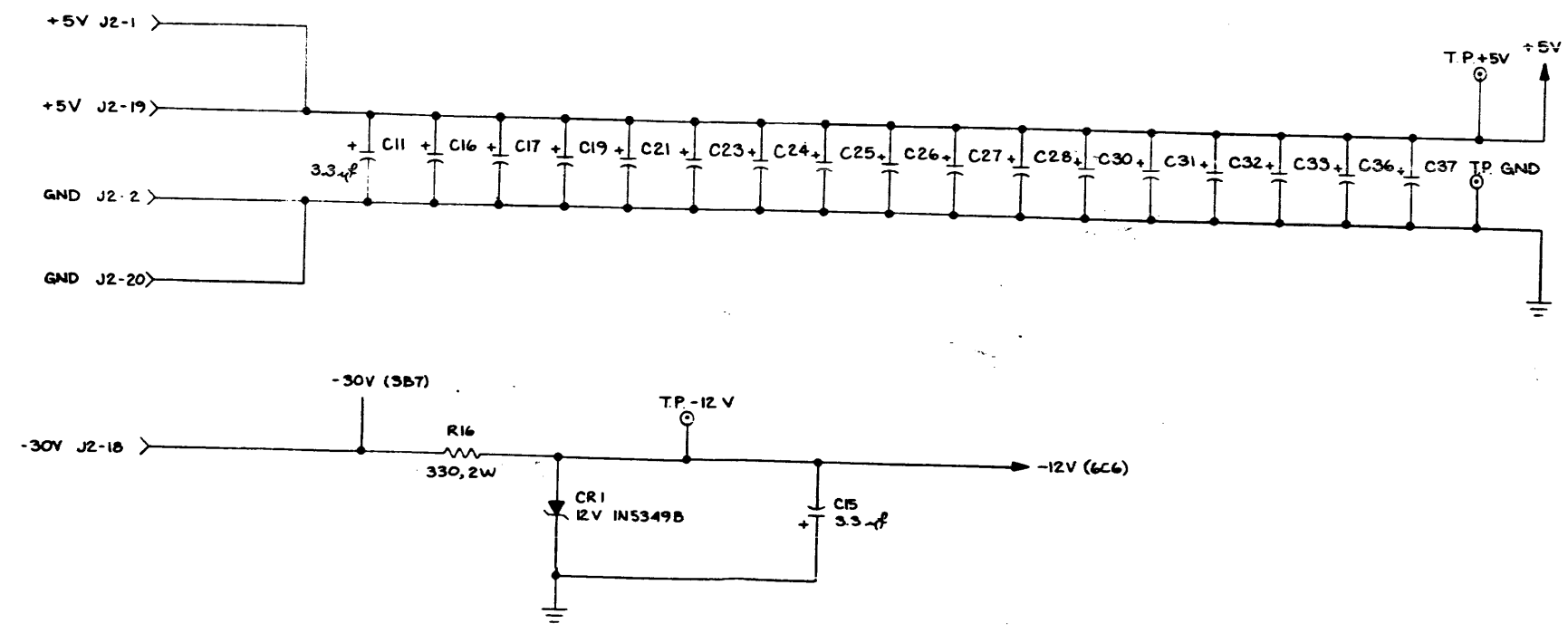
- (5A6) RC1
- (5A7) RC2
- (5A8) RC3
- (5A9) RC4
- (5A0) NBOF
- (J3-7) RC1
- (J3-9) RC2
- (J3-11) RC3
- (J3-13) RC4
- (J3-27) RC5

ELECTRONIC V.F.U.



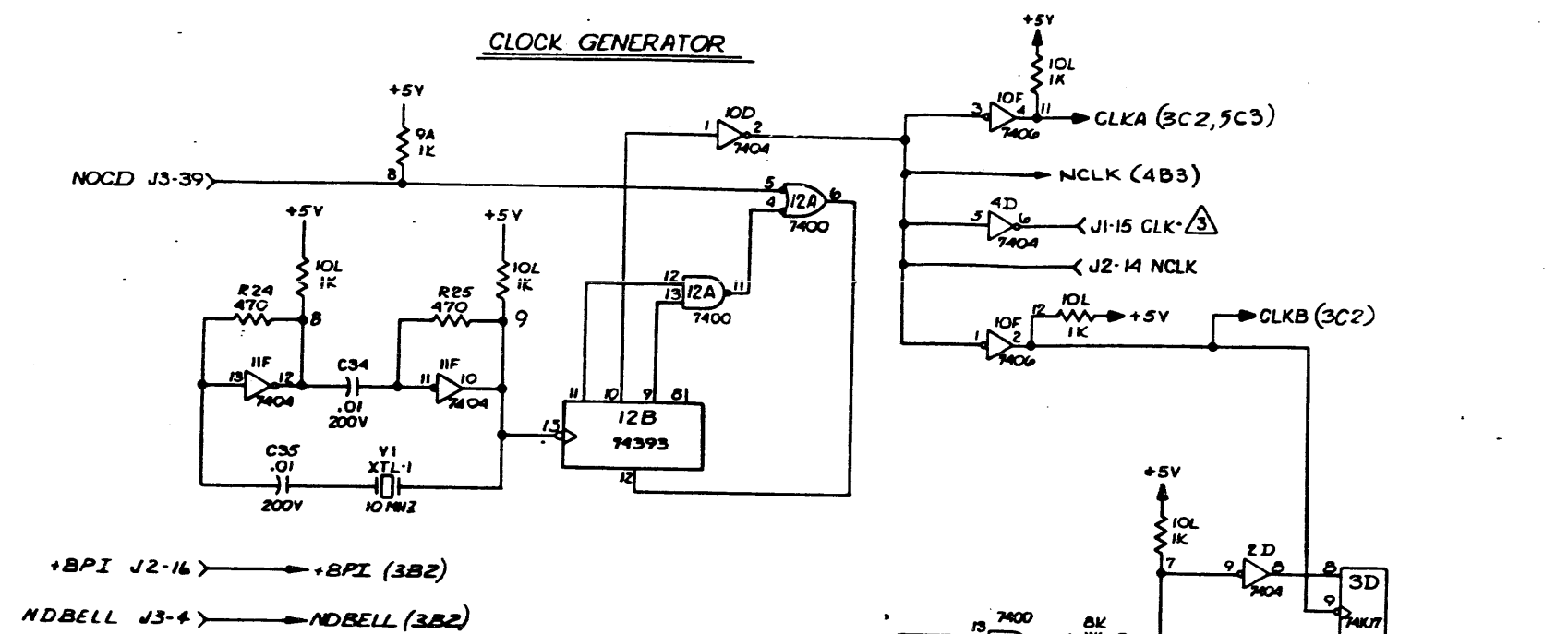
8		7		6		5		4		3		2		1	
JUMPER TABLE $\Delta$		SPARE CIRCUITS		I.C. GND AND VOLTAGE INFO											
W1, W2, W3, W4, W5, W6, W7, W8, W9, W10, W11, W12, W13, W14	SKIP OVER FUNCTION 6LPI/6LPI	TYPE	LOCATION	OUTPUT PIN	TYPE	GND PIN	+5V PIN								
W1, W2, W3	SKIP OVER PERF 3/4 LINES	7420	3K	8	7400	7	14								
W2, W3	SKIP OVER PERF 4/4 LINES	7406	10F	8, 10, 12	7402	7	14								
W1, W3	SKIP OVER PERF 5/6 LINES	7402	8L	10, 13	7404	7	14								
W3	SKIP OVER PERF 6/8 LINES				7406	7	14								
W4	DOUBLE SPEED PLOT				7407	7	14								
W6	PMD ACTIVE *	7420	9F	6	7408	7	14								
W7	DISABLE VFU	7407	8C	2, 8	7410	7	14								
W8	8 LPI	7414	1F	2, 12	7414	7	14								
W8, W9	10 LPI		3B	12	7420	7	14								
W11, W13, W14	P300		7B	2	7485	8	14								
W12	IN-MOVE 1 LINE BINARY COUNT = 000 OUT-MOVE 1 LINE BINARY COUNT = 001	7410	3H	12	7486	7	14								
W10, W14	P150				74107	7	14								
W5, W11, W13	P60C	7408	1K	11	74174	8	14								
					74393	7	14								
					74452	4	8								

\* INSTALLED AS REQUIRED AT FACTORY



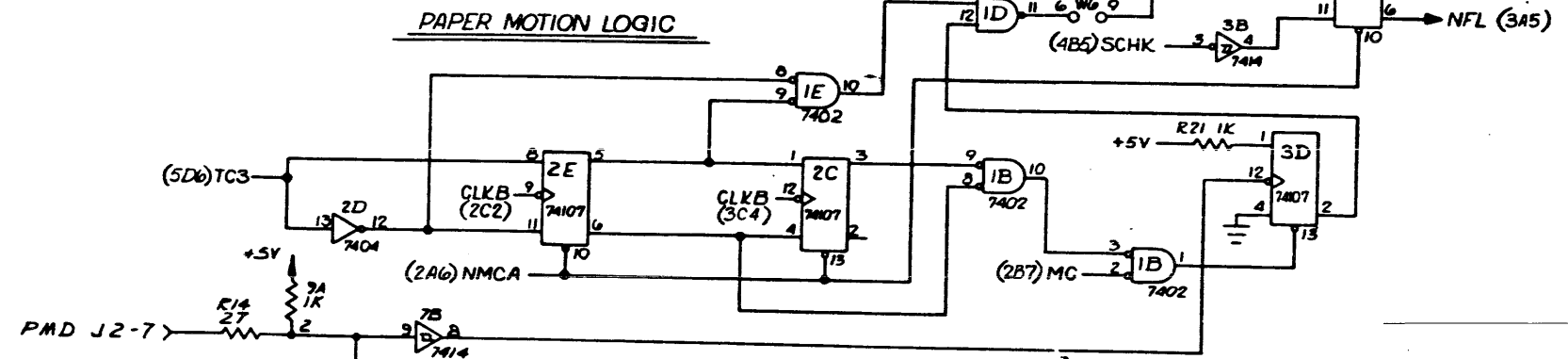


CLOCK GENERATOR



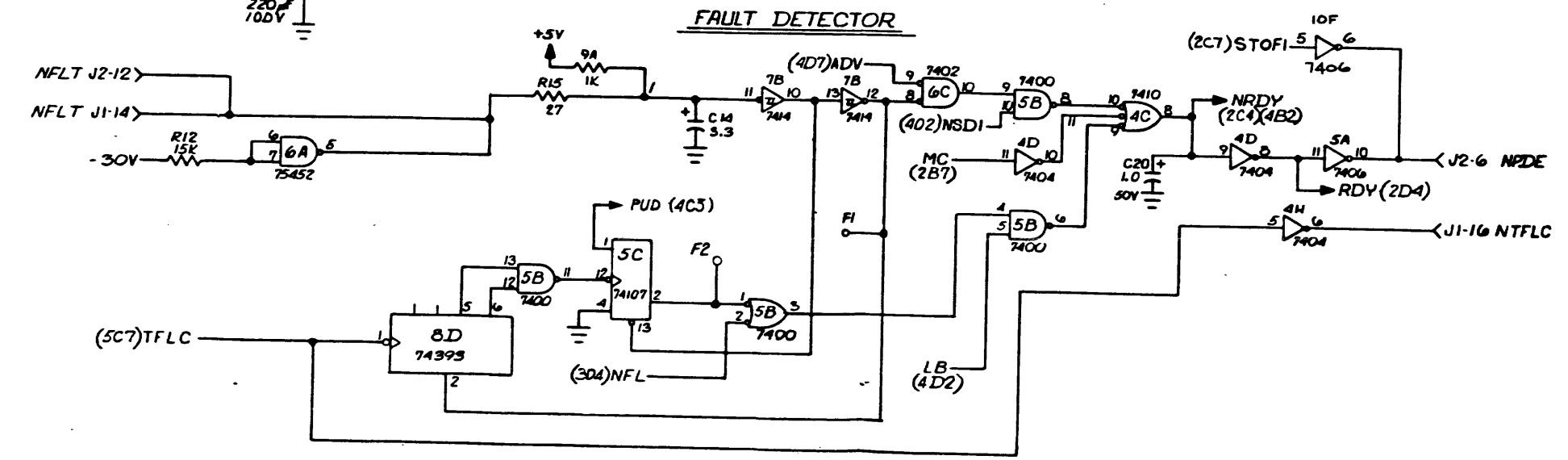
- (3C4) ○ — CLKB — (2D6, 2C6, 3B6, 2B6, 3B5, 4B8, 4C7, 4C5, 4D4)
- (3D4) ○ — CLKA — (2A7, 2C6, 4A7, 5B7, 5D3, 5A8, 6C2, 5B5, 5C3, 6B2, 6D5, 5B3)
- (3B6) ○ — NMCA — (2B8, 3B6, 4A7, 4C4, 5C8, 4B6, 2C7, 2A6, 5A3)

PAPER MOTION LOGIC



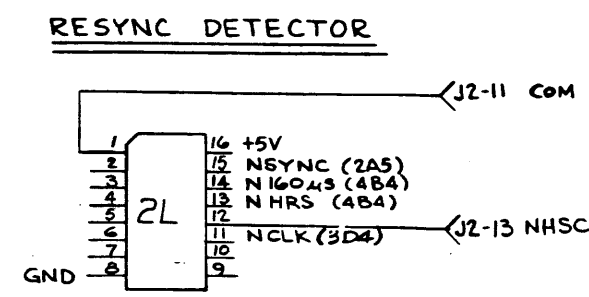
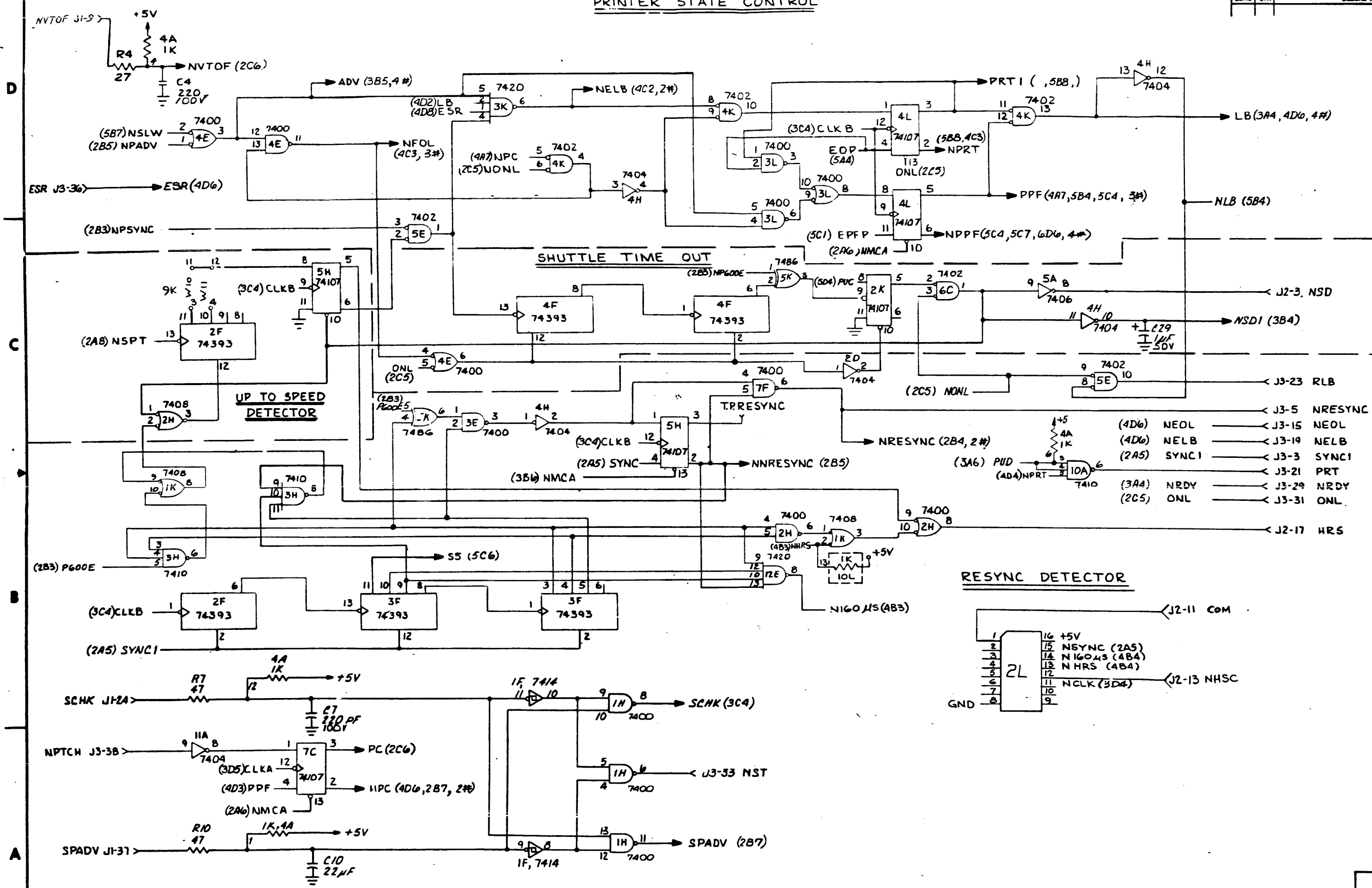
- (5C7) NDBELL — J1-25 NDBELL
- (5C7) +BPI — J1-26 +BPI
- (4A6) +5V — J1-39 +5V

FAULT DETECTOR

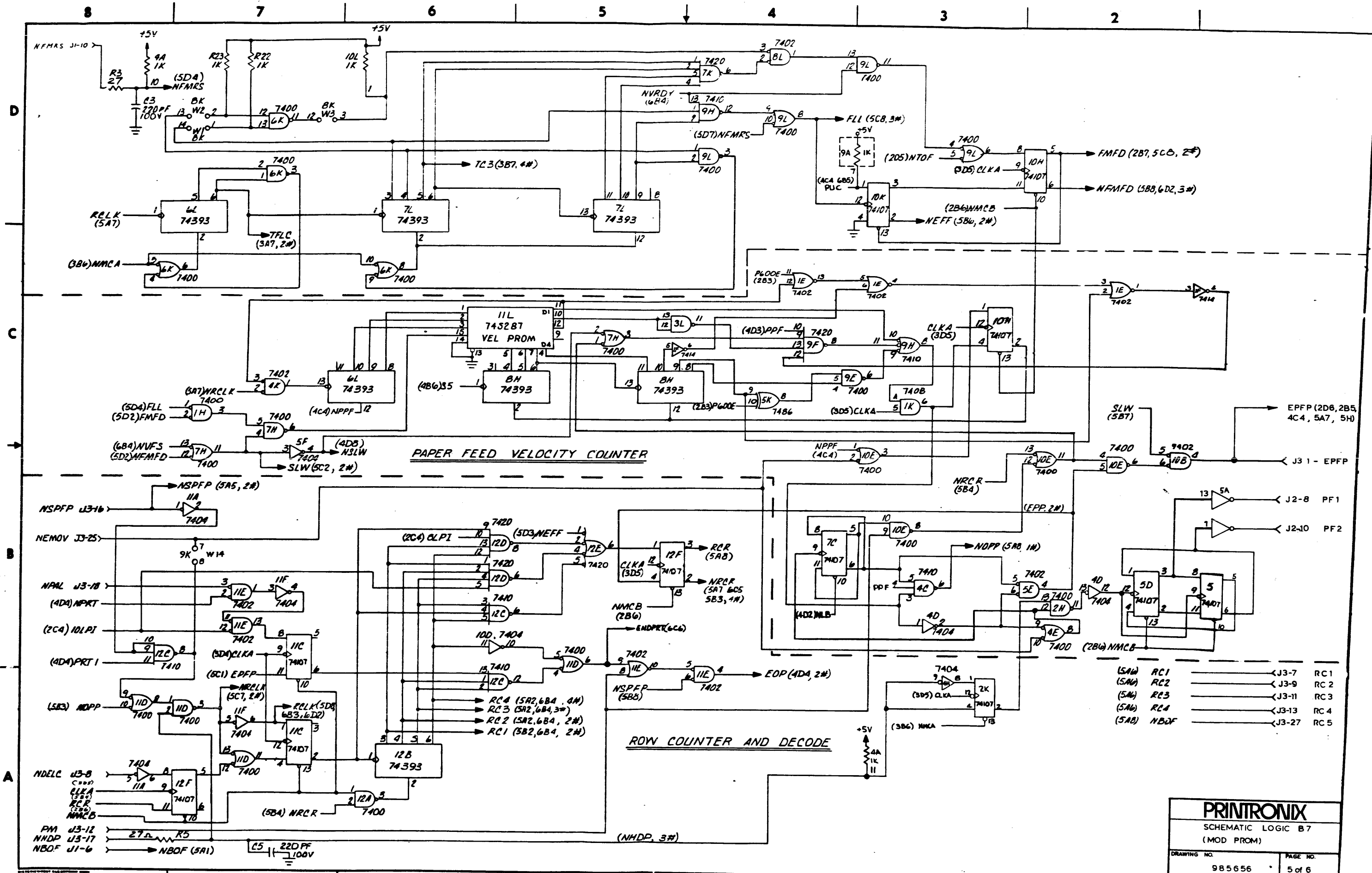


PRINTER STATE CONTROL

REVISIONS		DATE	APPROVAL
ZONE	LTR	DESCRIPTION	





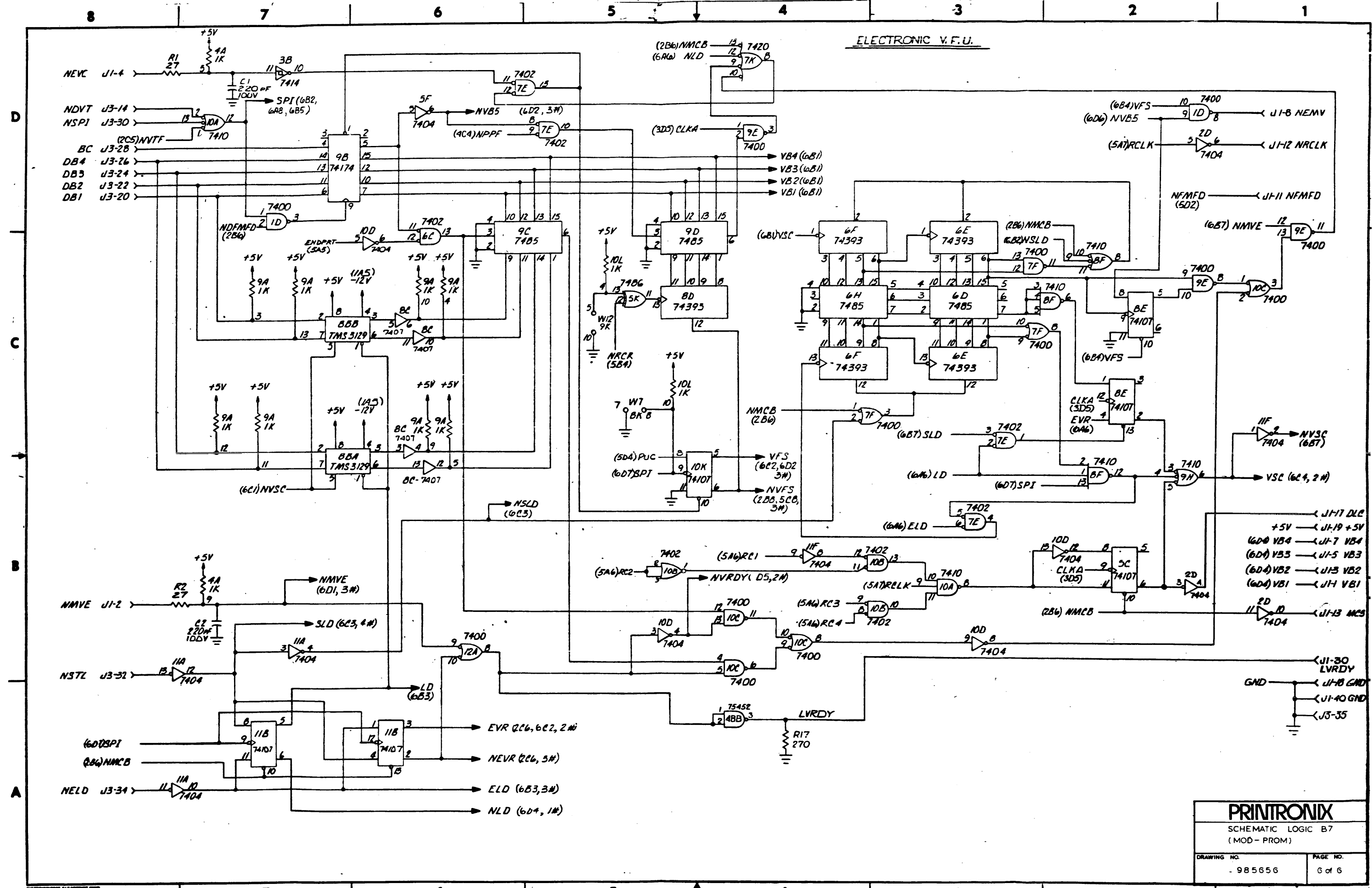


**PAPER FEED VELOCITY COUNTER**

**ROW COUNTER AND DECODE**

- (5A) RC1 → J3-7 RC 1
- (5A) RC2 → J3-9 RC 2
- (5A) RC3 → J3-11 RC 3
- (5A) RC4 → J3-13 RC 4
- (5A) NBOF → J3-27 RC 5

ELECTRONIC V.F.U.



**PRINTRONIX**  
 SCHEMATIC LOGIC B7  
 (MOD - PROM)

DRAWING NO.	PAGE NO.
985656	6 of 6

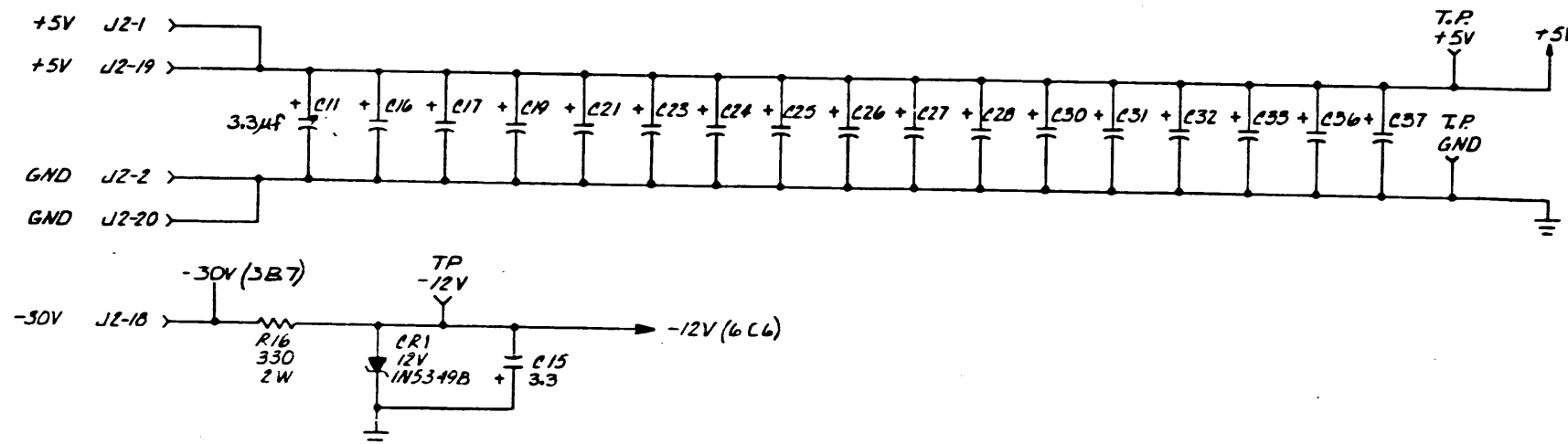
CONNECTOR BLOCK	
J1	
J2	
J3	

LAST REF. DESIGNATION	
RESISTOR - R25	
CAPACITOR - C37	
CRYSTAL - Y1	
DIODE - CR1	

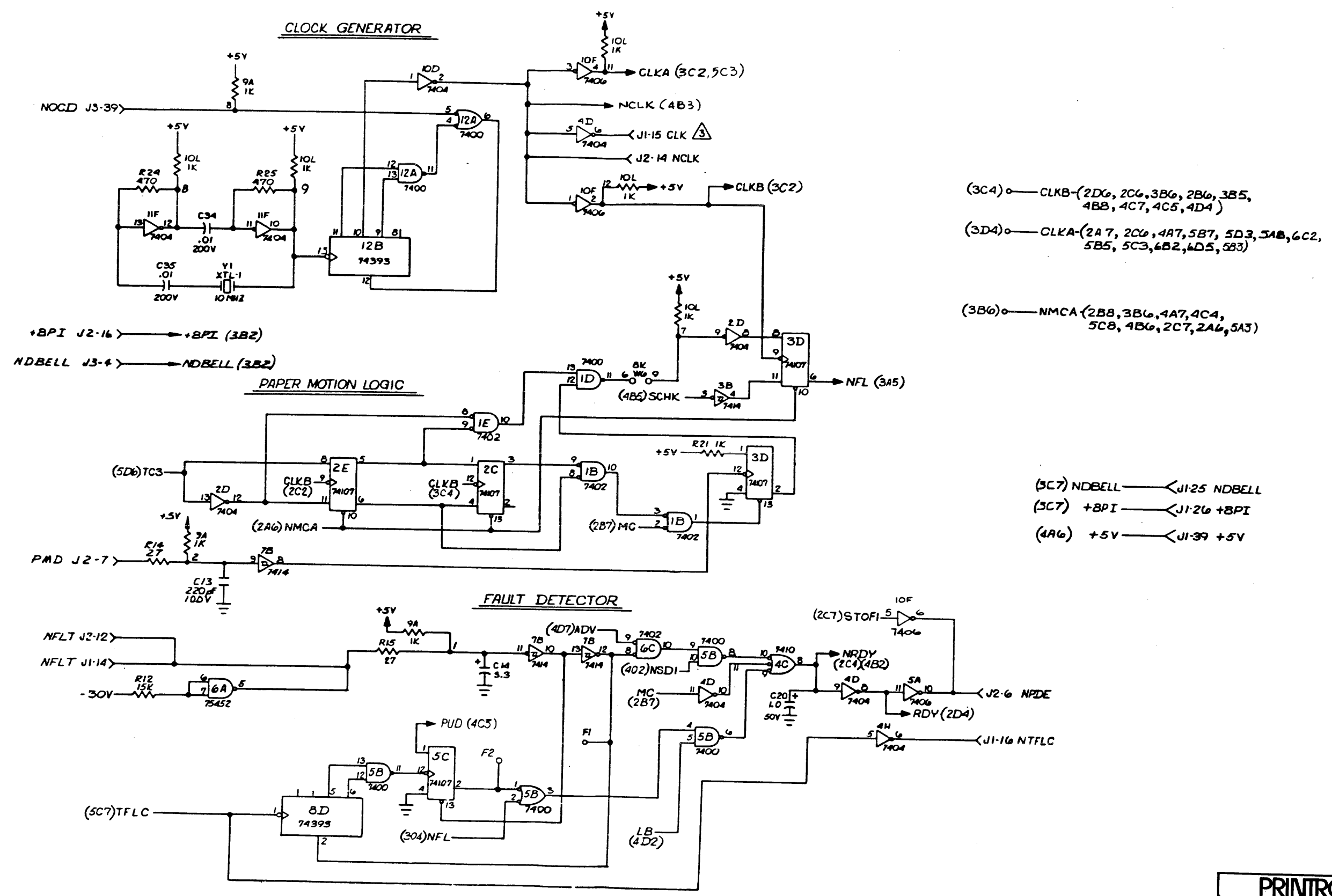
SPARE GATES CONTENT PINS		
5K	7420	8
5K	7486	11
3H	7410	6, 12
10F	7406	8, 10, 12
8L	7402	13
5E	7402	13
12E	7420	8
8C	7407	2, 8
1K	7408	11

JUMPER TABLE															
	W1	W2	W3	W4	W5	W6	W7	W8	W9	W10	W11	W12	W13	W14	W15
SKIP OVER FUNCTION 6LPI/8LPI															
SKIP OVER PERF 3/4 LINES	X	X	X												
SKIP OVER PERF 1/2 LINES		X	X												
SKIP OVER PERF 3/8 LINES	X		X												
SKIP OVER PERF 1/8 LINES			X												
DOUBLE SPEED PLOT				X											
PMD ACTIVE *						X									
DISABLE VFLU							X								
8 LPI								X							
10 LPI									X	X					
P-300											X			X	
IN = MOVE 1 LINE BINARY LT=000												X			
OUT = MOVE 1 LINE BINARY CT=001													X		
6/8 LPI ONLINE LOCKOUT														X	
P-600					X					X					
P-150											X				X

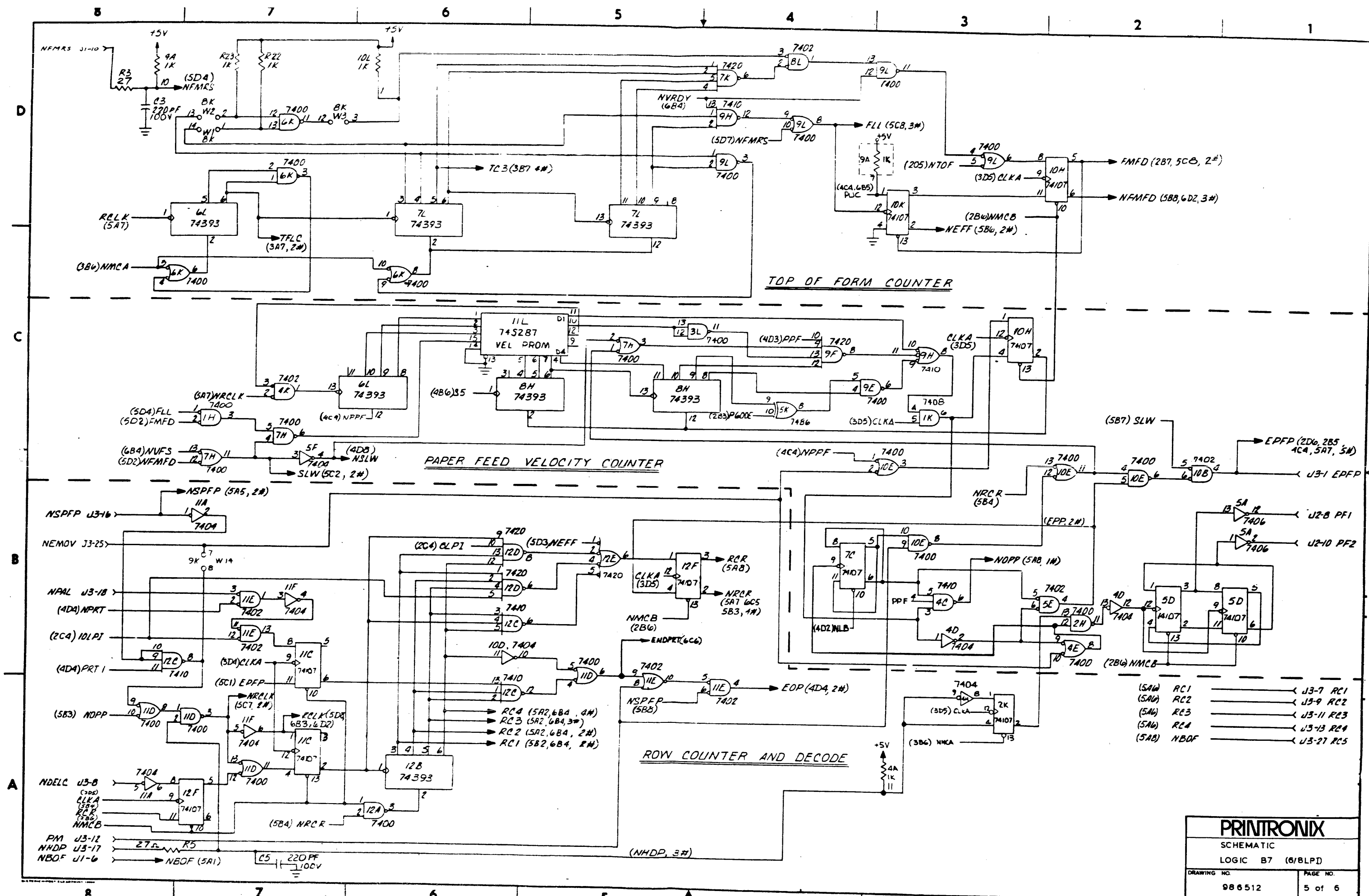
\* = INSTALLED AS REQ'D AT FACTORY.







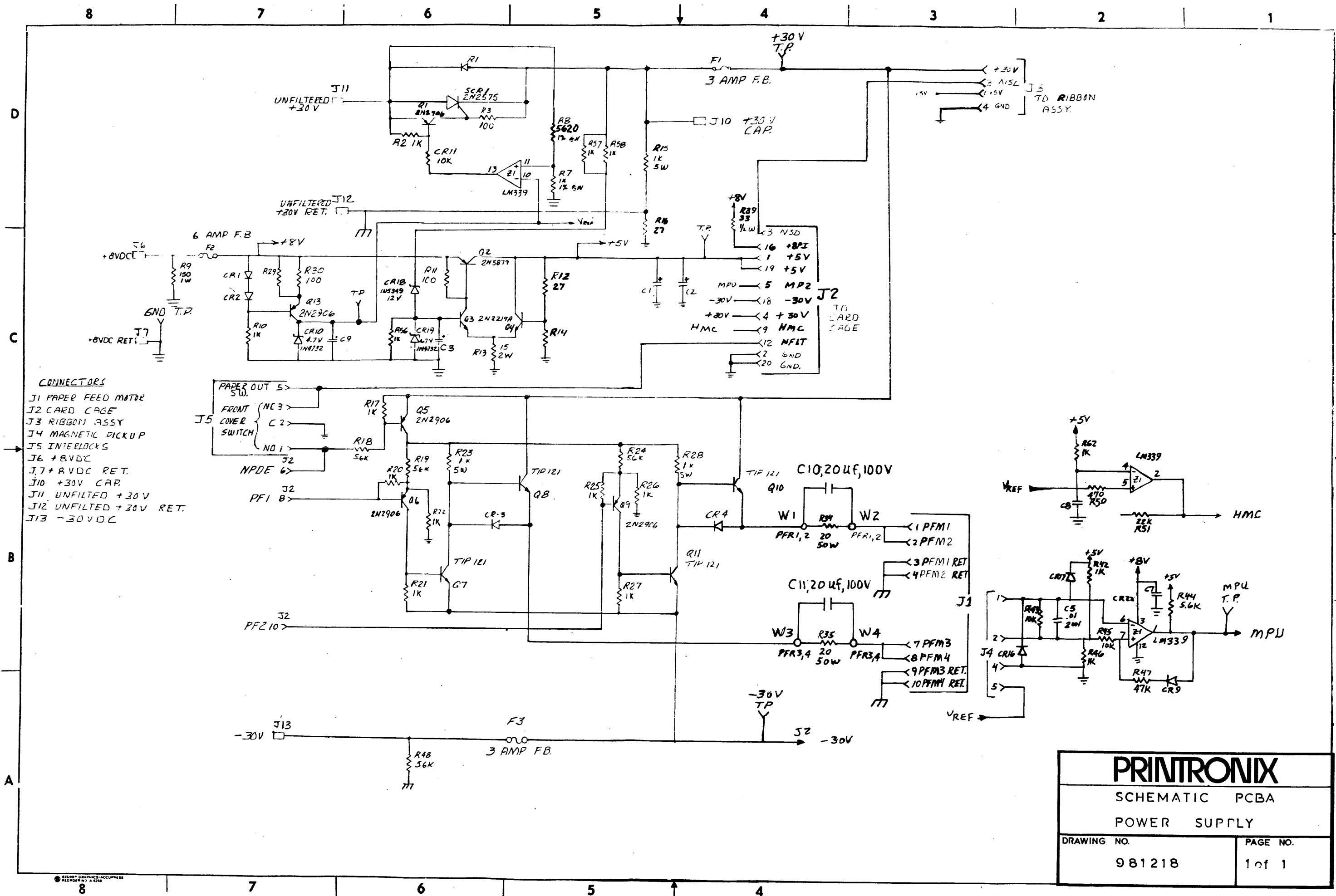




- (5A6) RC1
- (5A6) RC2
- (5A6) RC3
- (5A6) RC4
- (5A6) NBDF
- (J3-7) RC1
- (J3-9) RC2
- (J3-11) RC3
- (J3-13) RC4
- (J3-27) RC5







- CONNECTORS**
- J1 PAPER FEED MOTOR
  - J2 CARD CAGE
  - J3 RIBBON ASSY
  - J4 MAGNETIC PICKUP
  - J5 INTERLOCKS
  - J6 +8VDC
  - J7 +8VDC RET.
  - J10 +30V CAP.
  - J11 UNFILTERED +30V
  - J12 UNFILTERED +30V RET.
  - J13 -30VDC

<b>PRINTRONIX</b>	
SCHEMATIC PCBA	
POWER SUPPLY	
DRAWING NO.	PAGE NO.
981218	1 of 1

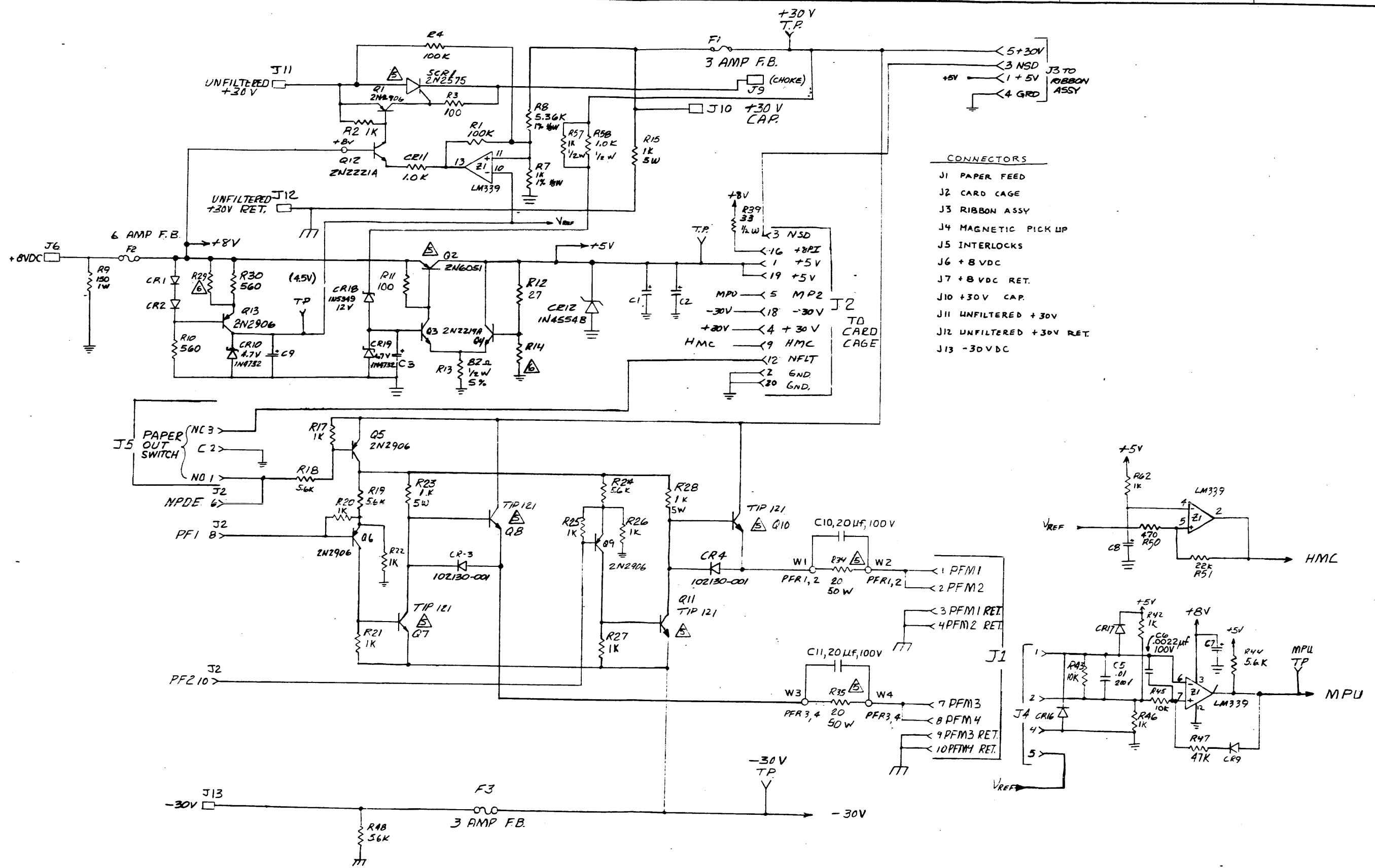
8 7 6 5 4 3 2 1

D

C

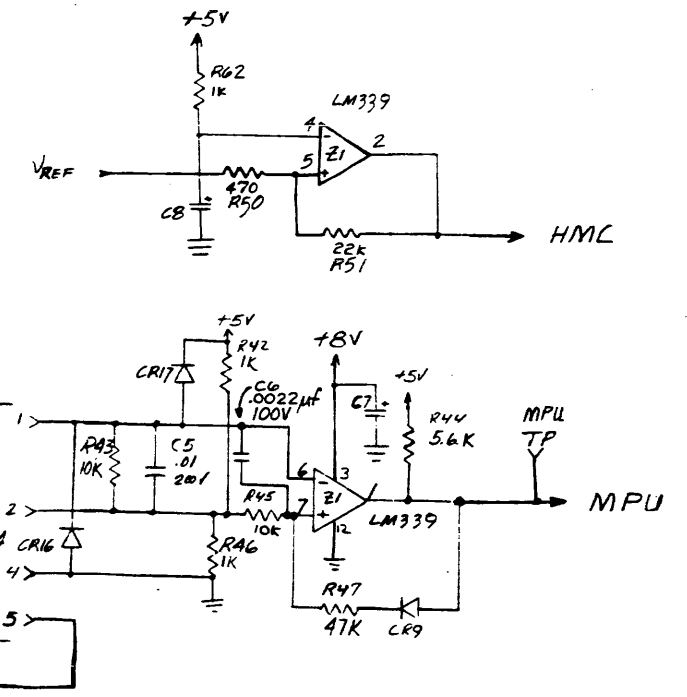
B

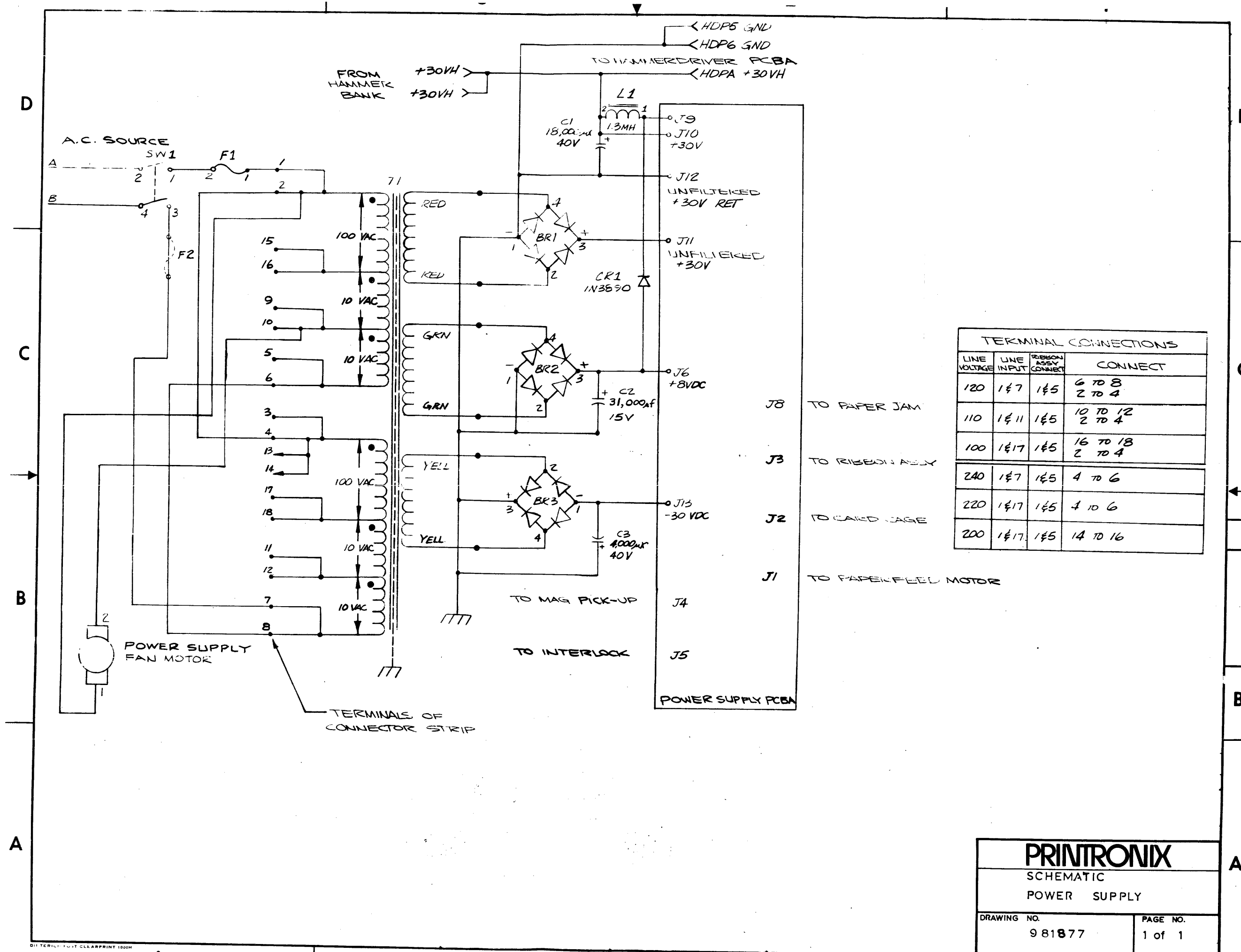
A



CONNECTORS

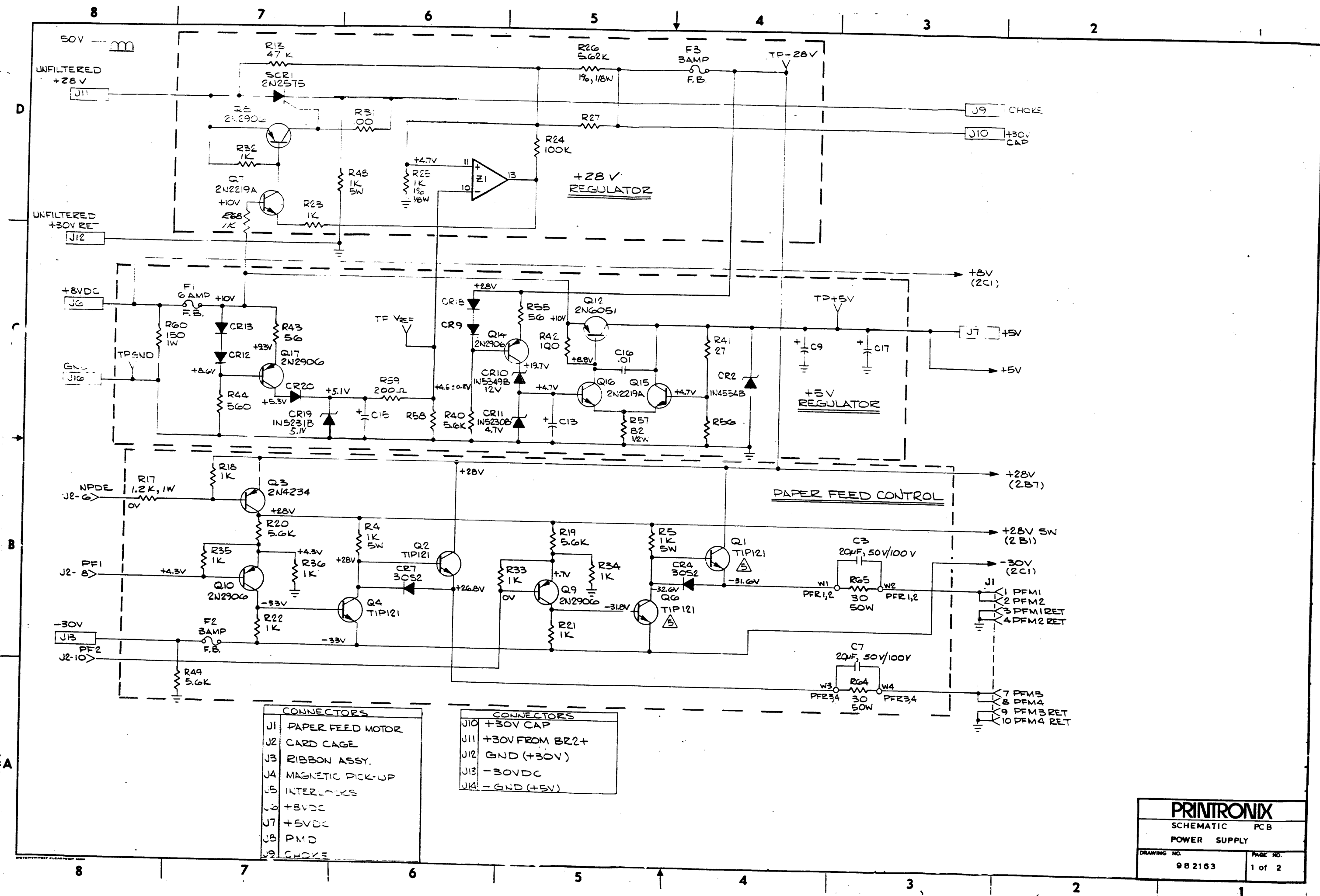
- J1 PAPER FEED
- J2 CARD CAGE
- J3 RIBBON ASSY
- J4 MAGNETIC PICK UP
- J5 INTERLOCKS
- J6 + 8 VDC
- J7 + 8 VDC RET.
- J10 + 30V CAP.
- J11 UNFILTERED + 30V
- J12 UNFILTERED + 30V RET.
- J13 - 30VDC





TERMINAL CONNECTIONS			
LINE VOLTAGE	LINE INPUT	RIBBON ASSY CONNECT	CONNECT
120	1&7	1&5	6 TO 8 2 TO 4
110	1&11	1&5	10 TO 12 2 TO 4
100	1&17	1&5	16 TO 18 2 TO 4
240	1&7	1&5	4 TO 6
220	1&17	1&5	4 TO 6
200	1&17	1&5	14 TO 16

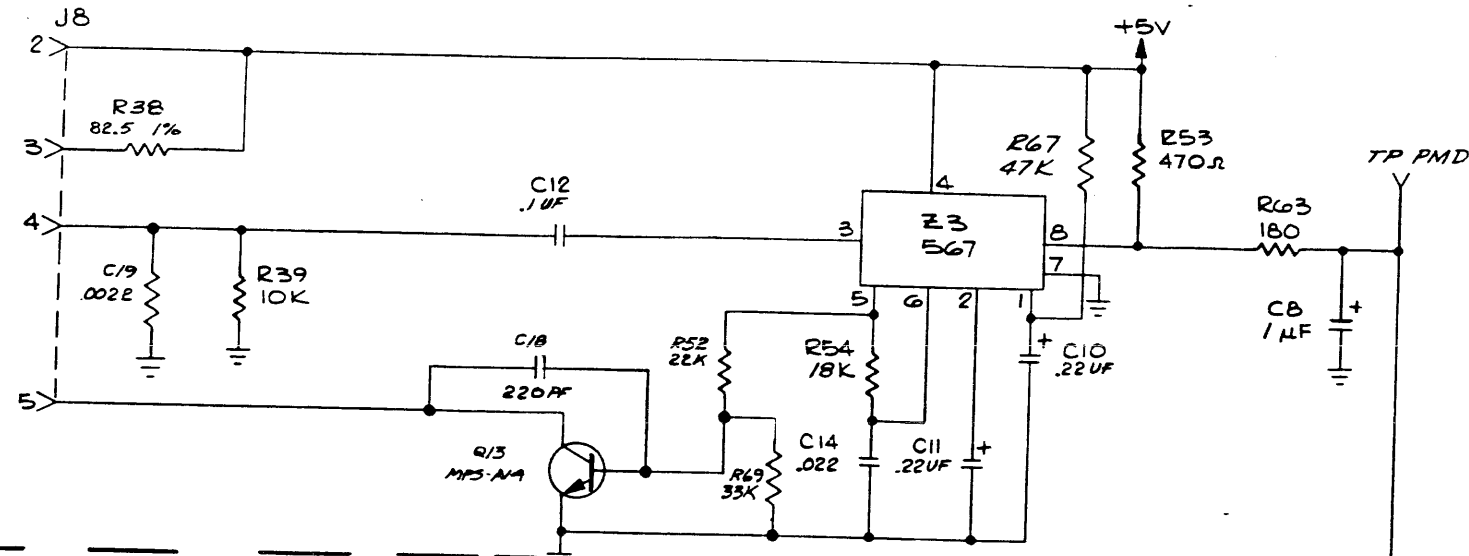
- J8 TO PAPER JAM
- J3 TO RIBBON ASSEMBLY
- J2 TO CARTRIDGE
- J1 TO PAPER FEED MOTOR



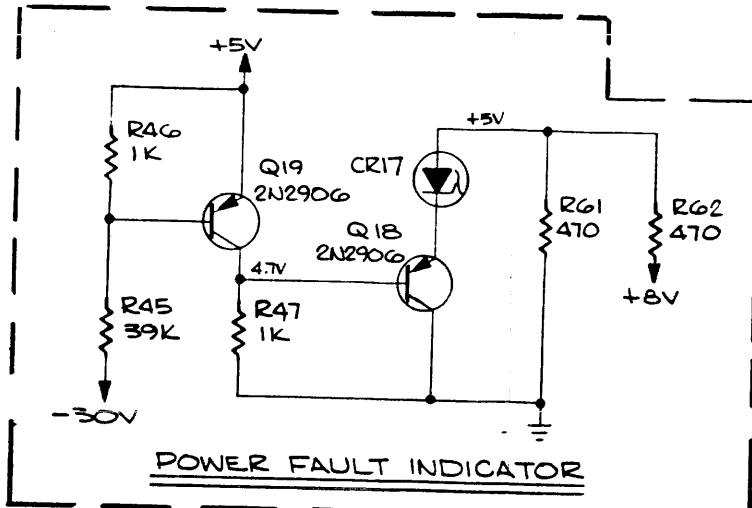
CONNECTORS	
J1	PAPER FEED MOTOR
J2	CARD CAGE
J3	RIBBON ASSY.
J4	MAGNETIC PICK-UP
J5	INTERLOCKS
J6	+8VDC
J7	+5VDC
J8	PMD
J9	CHOKE

CONNECTORS	
J10	+30V CAP
J11	+30V FROM BR2+
J12	GND (+30V)
J13	-30VDC
J14	-GND (+5V)

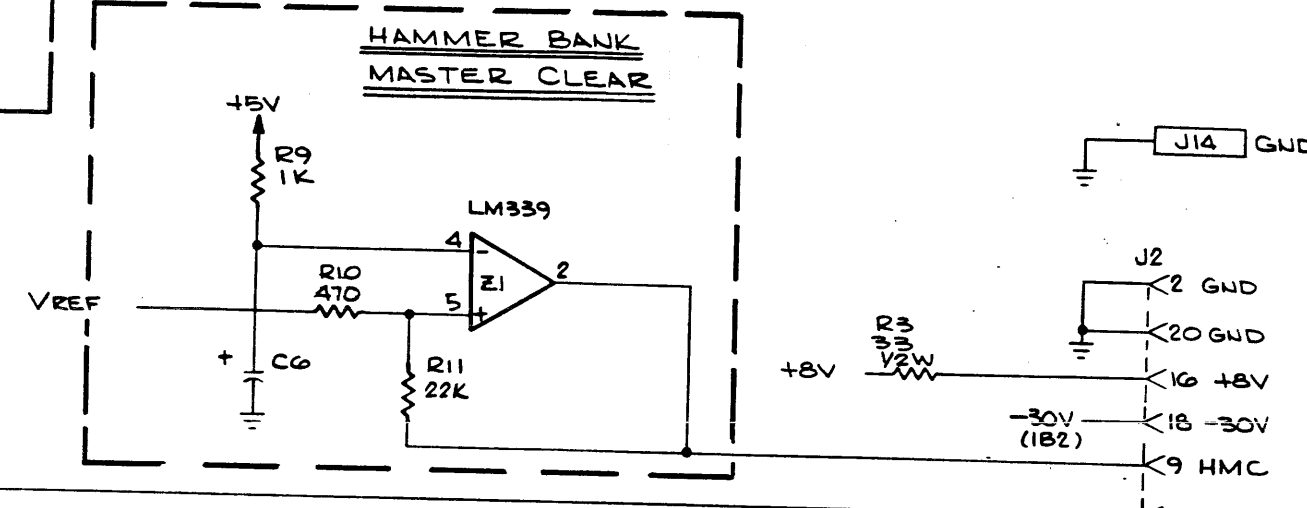
PAPER MOTION DETECTOR



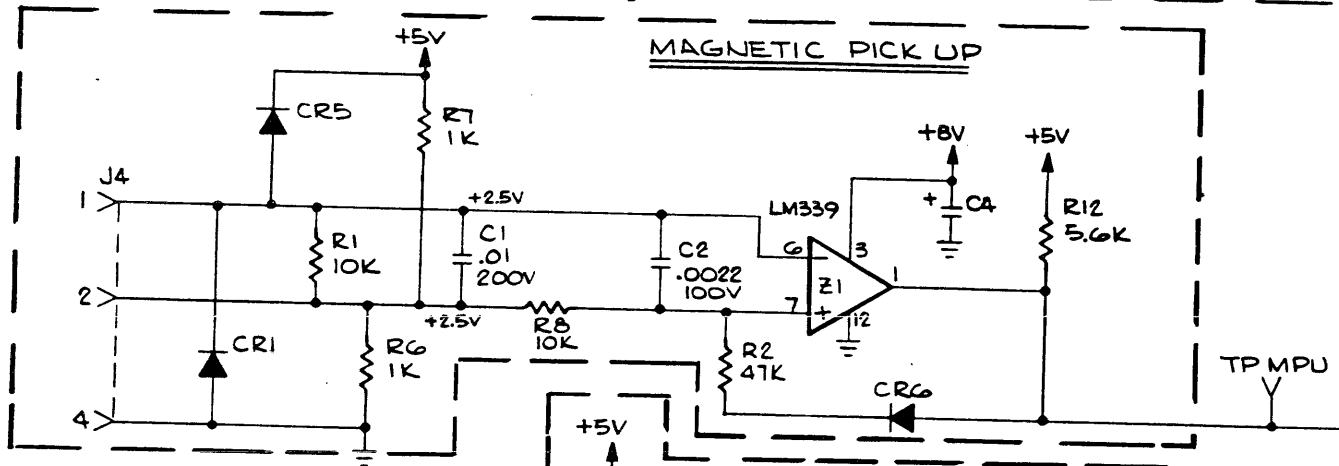
POWER FAULT INDICATOR



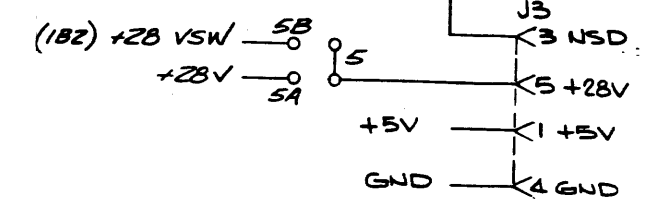
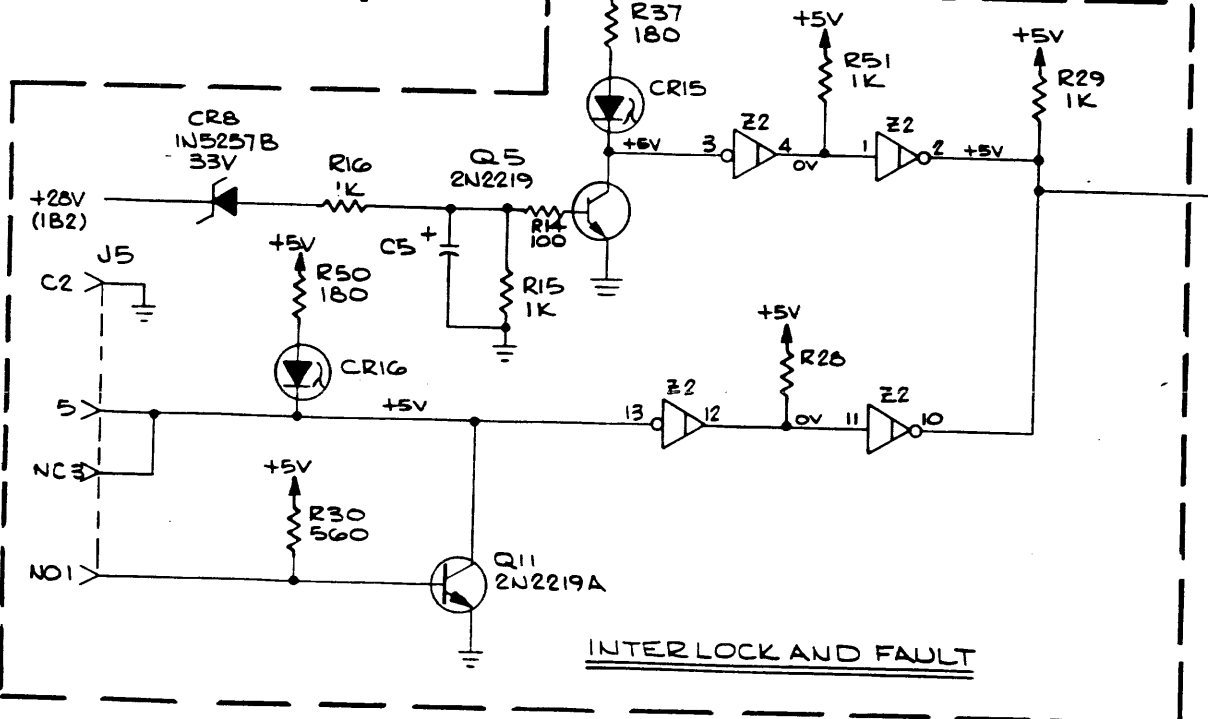
HAMMER BANK MASTER CLEAR

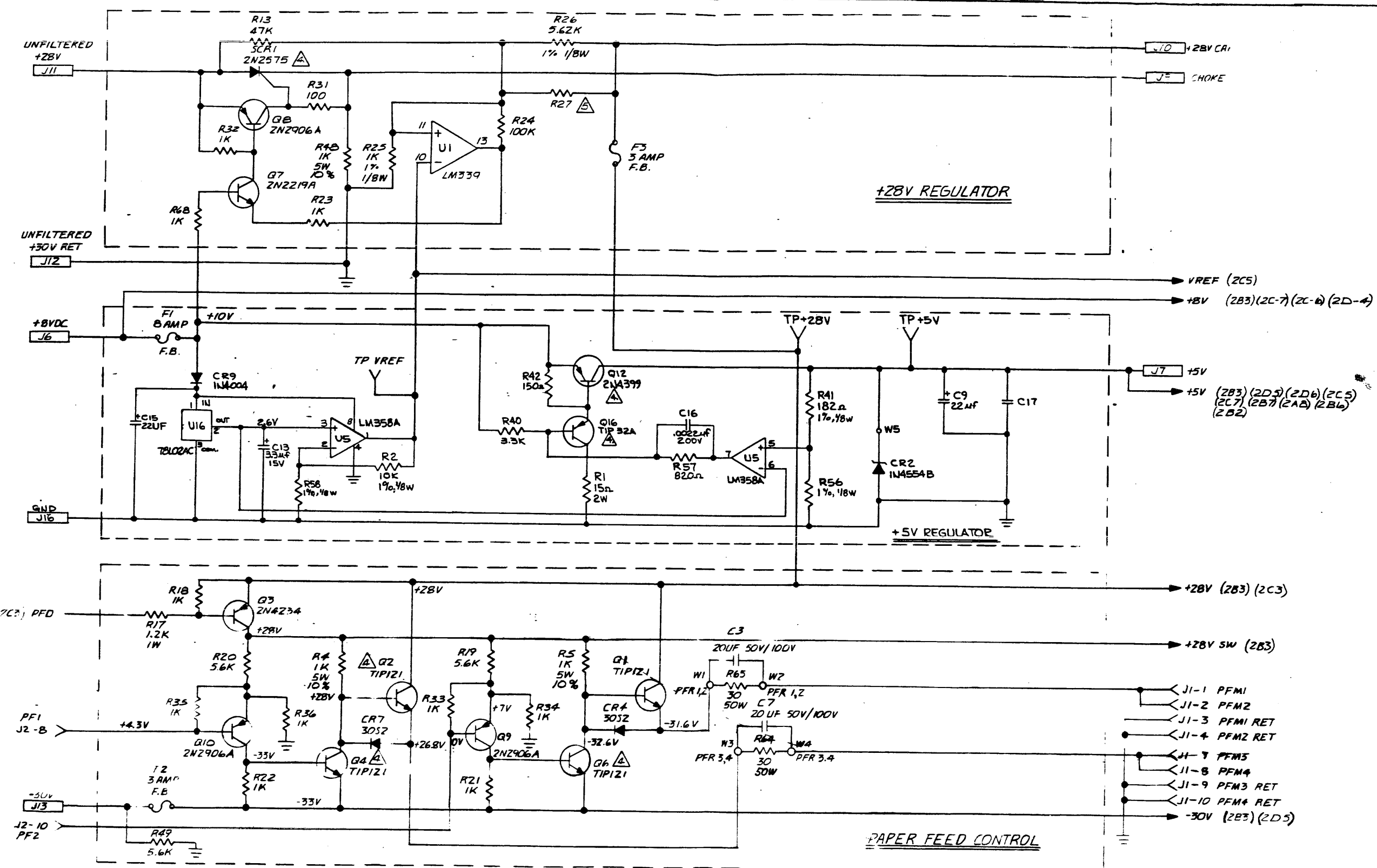


MAGNETIC PICK UP

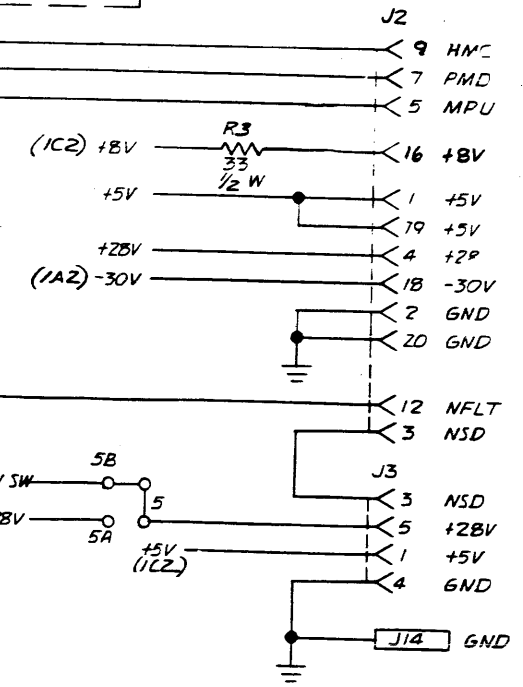
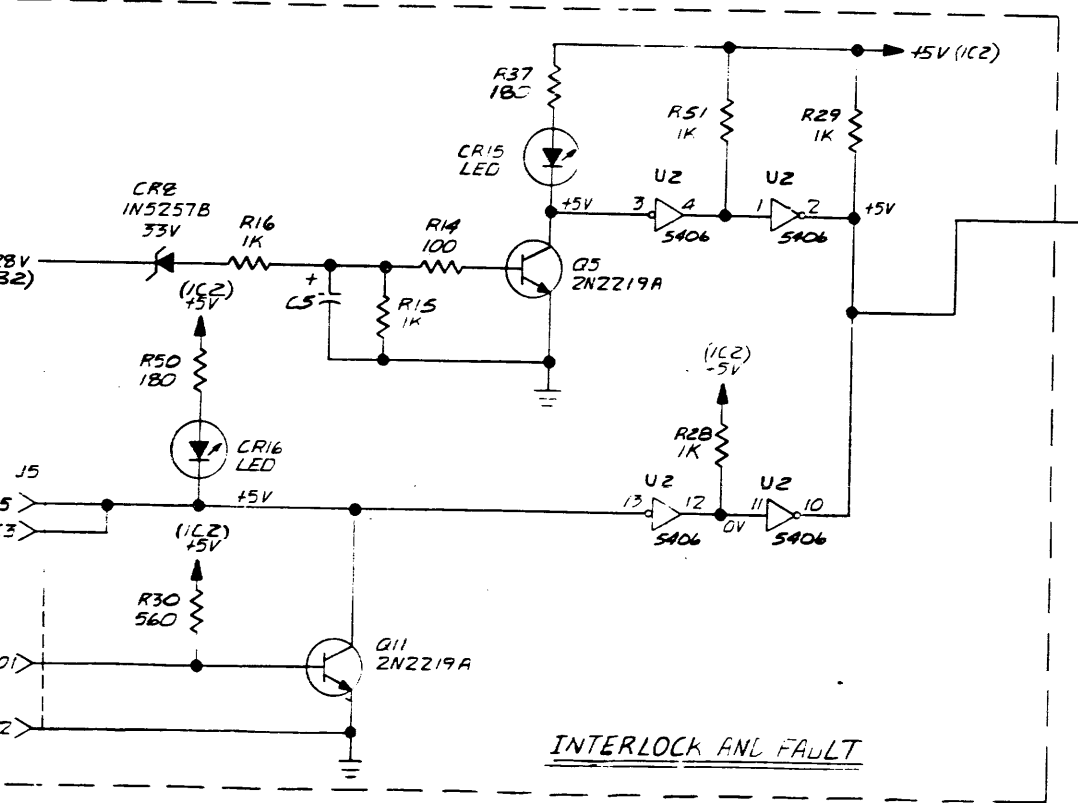
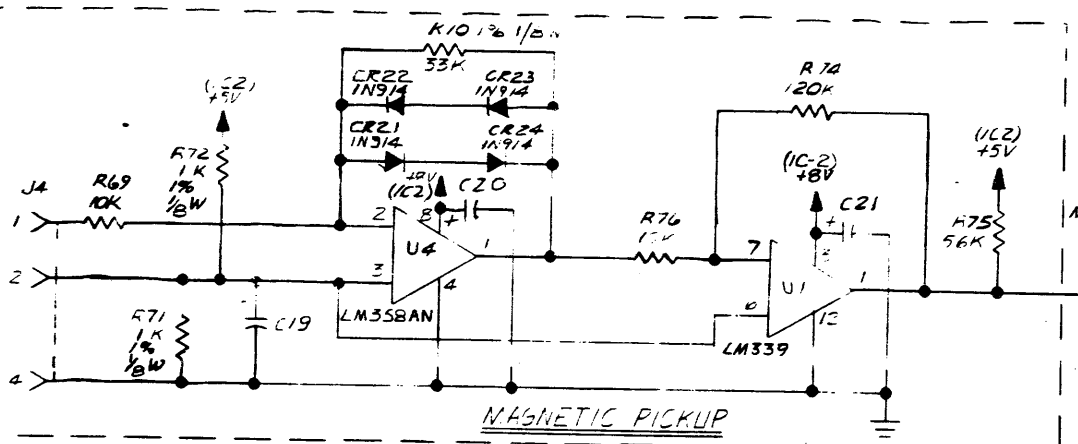
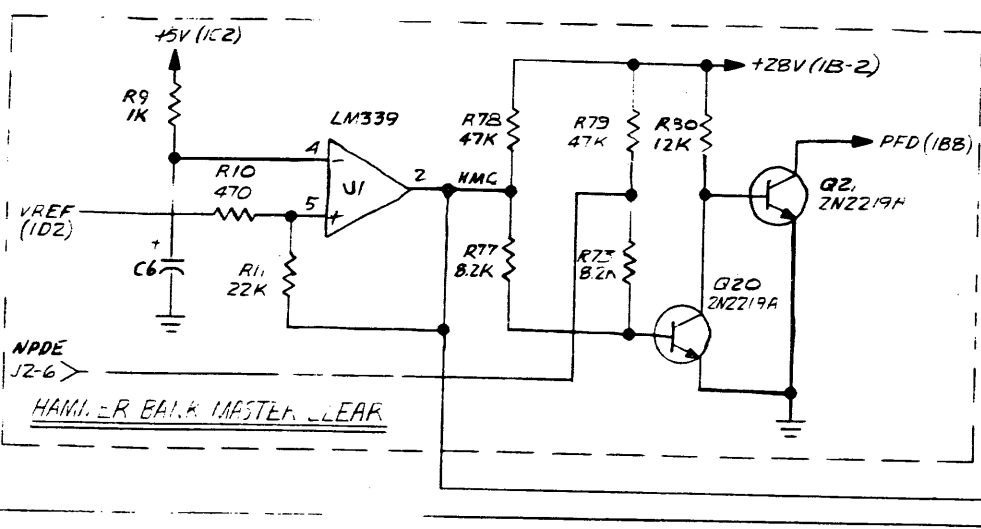
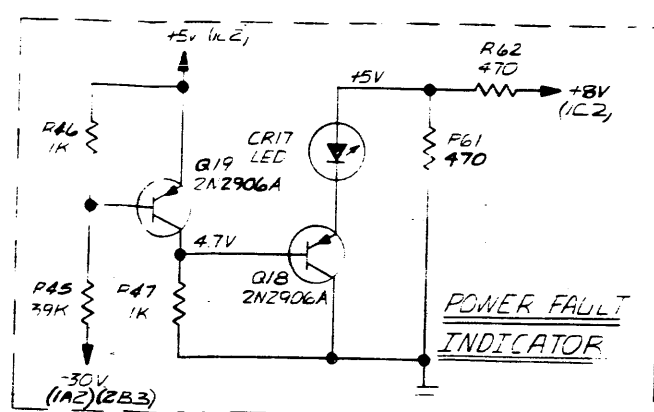
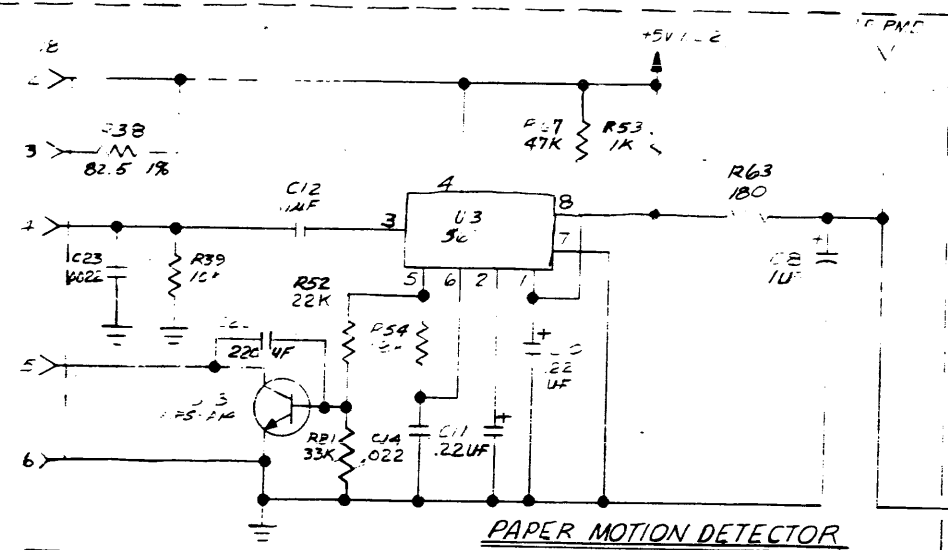


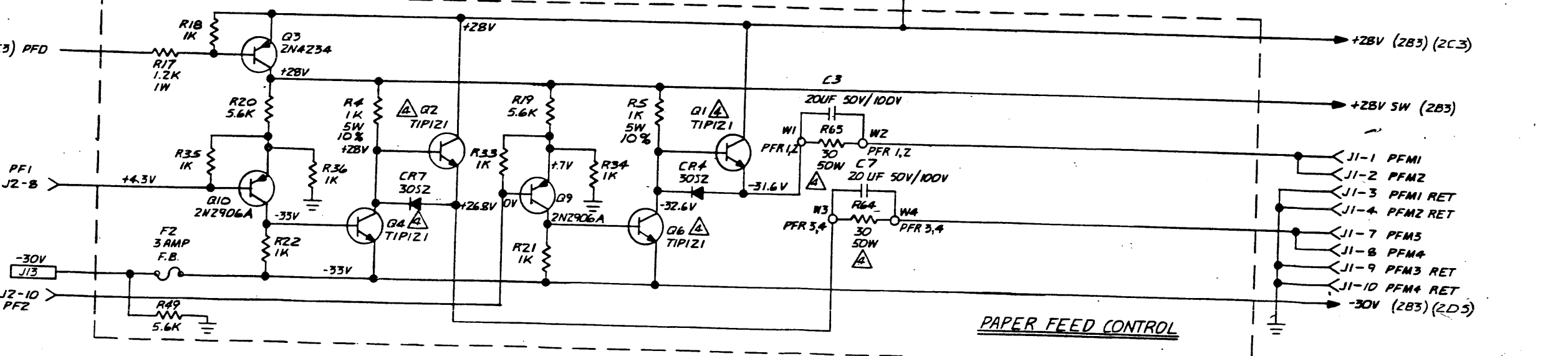
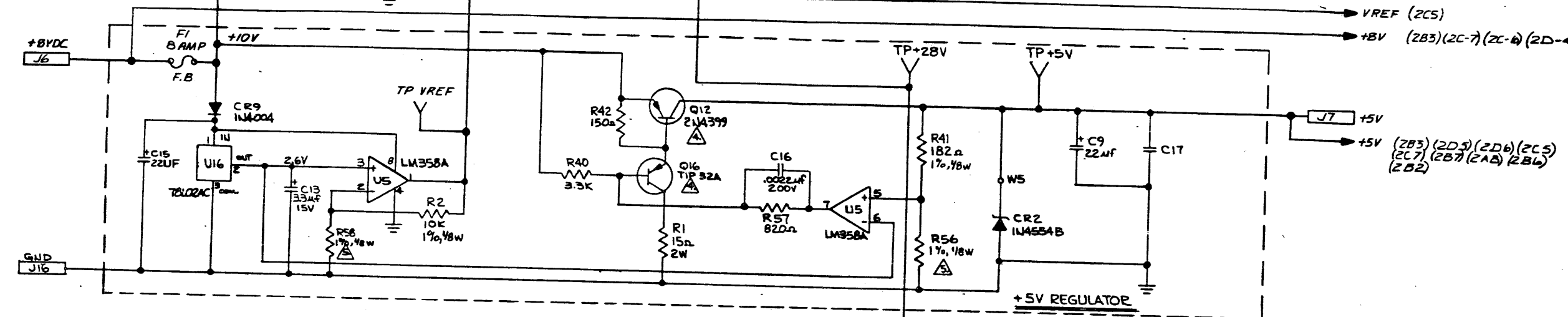
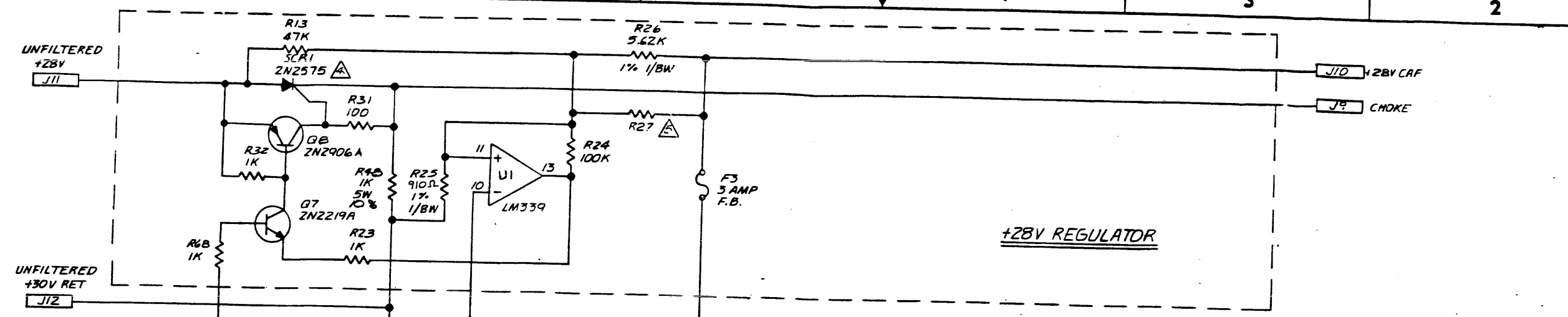
INTERLOCK AND FAULT





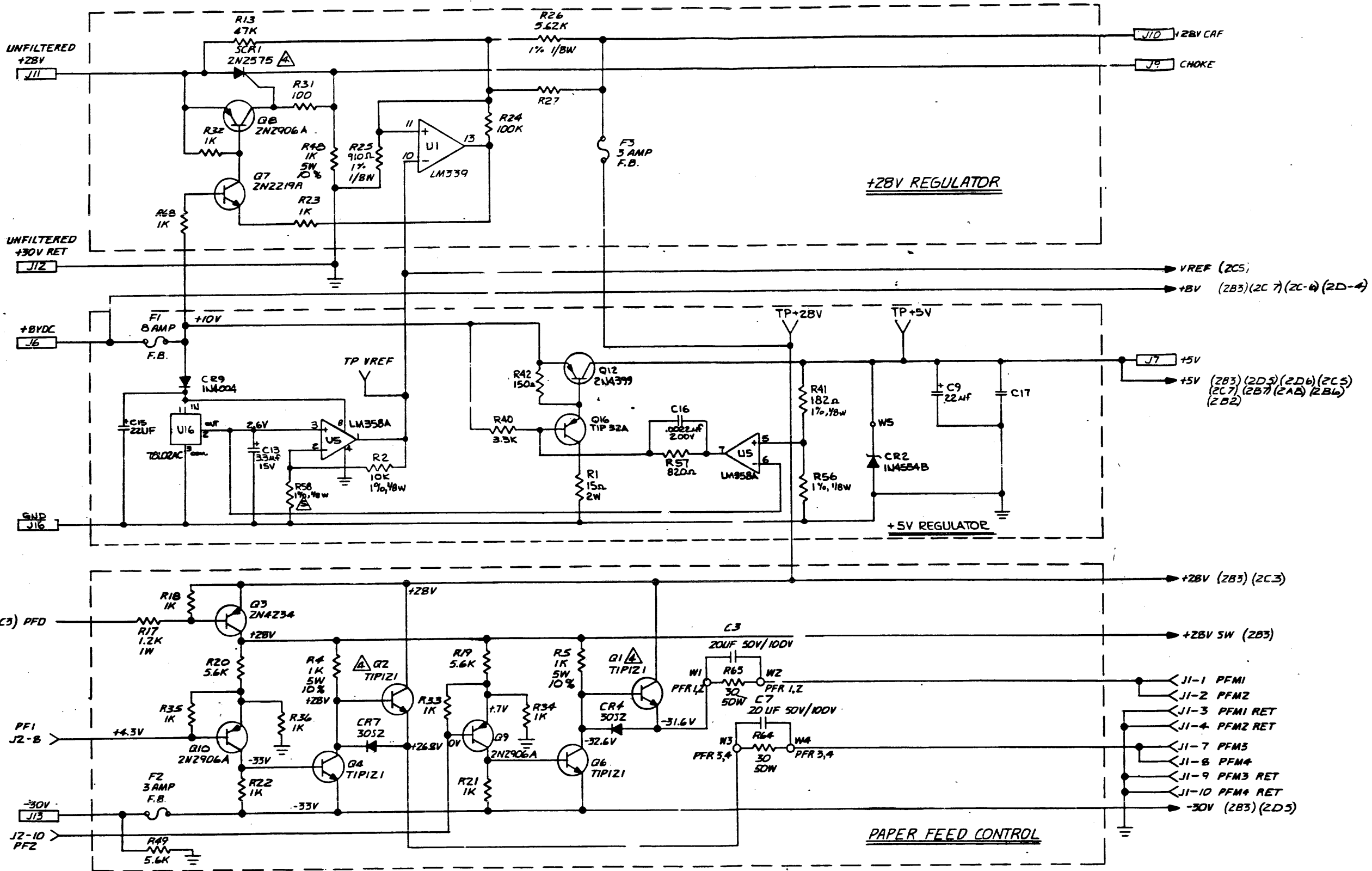
CONNECTORS	
J1	PAPER FEED MOTOR
J2	CARD CAGE
J3	RIBBON ASSY
J4	MAGNETIC PICKUP
J5	INTERLOCKS
J6	+8VDC
J7	+5VDC
J8	PMD
J9	CHOKE
J10	+30V CAP
J11	+30V FROM BR2 +
J12	GND (+30V)
J13	-30VDC
J14	-GND (+5V)



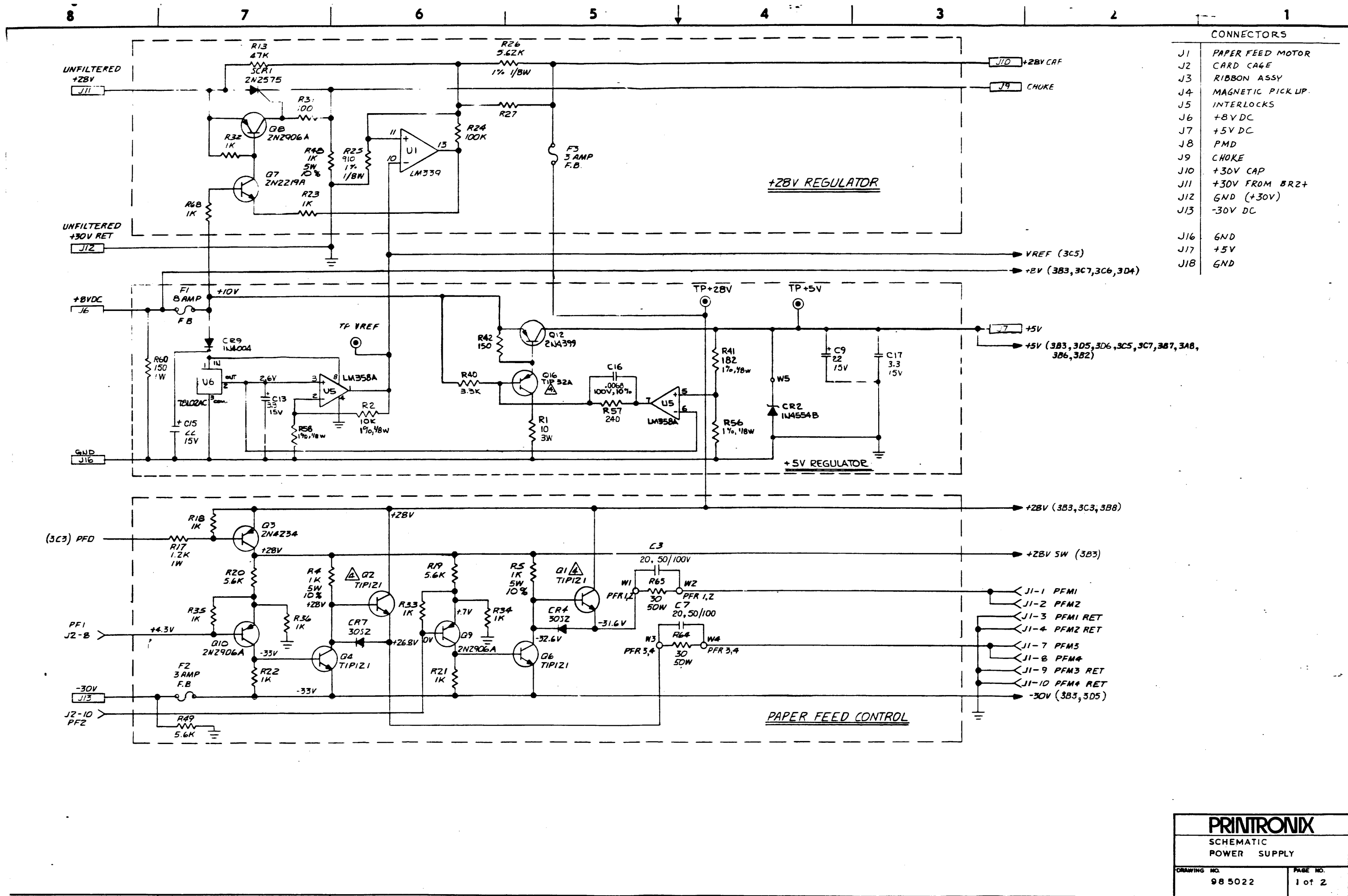


CONNECTORS	
J1	PAPER FEED MOTOR
J2	CARD CAGE
J3	RIBBON ASSY
J4	MAGNETIC PICKUP
J5	INTERLOCKS
J6	+8VDC
J7	+5VDC
J8	PMD
J9	CHOKER
J10	+30V CAP
J11	+30V FROM BR2 +
J12	GND (+30V)
J13	-30VDC
J14	-GND (+5V)





CONNECTORS	
J1	PAPER FEED MOTOR
J2	CARD CAGE
J3	RIBBON ASSY
J4	MAGNETIC PICKUP
J5	INTERLOCKS
J6	+8VDC
J7	+5VDC
J8	PMD
J9	CHOKE
J10	+30V CAP
J11	+30V FROM BR2+
J12	GND (+30V)
J13	-30VDC
J14	-GND (+5V)





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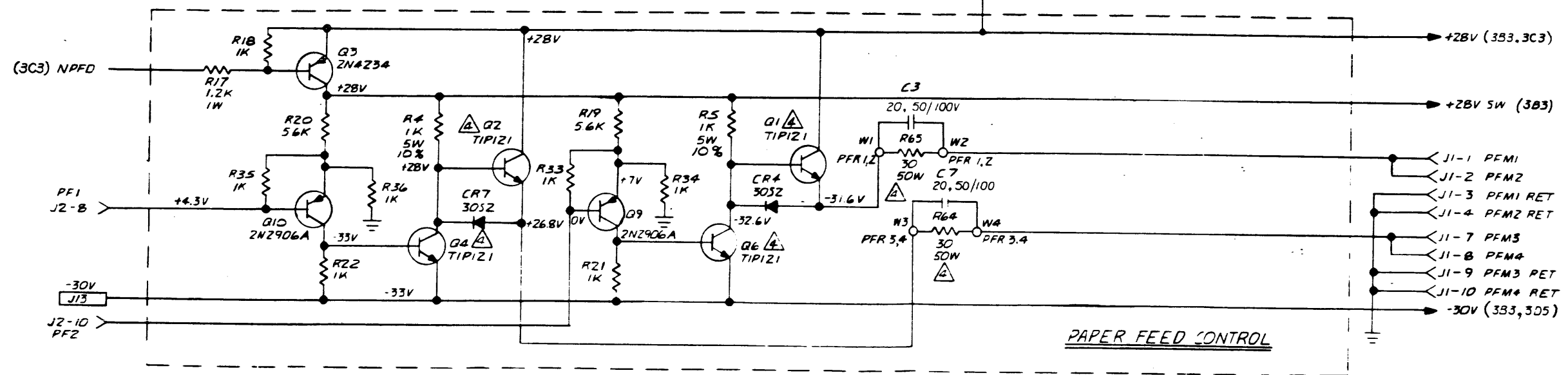
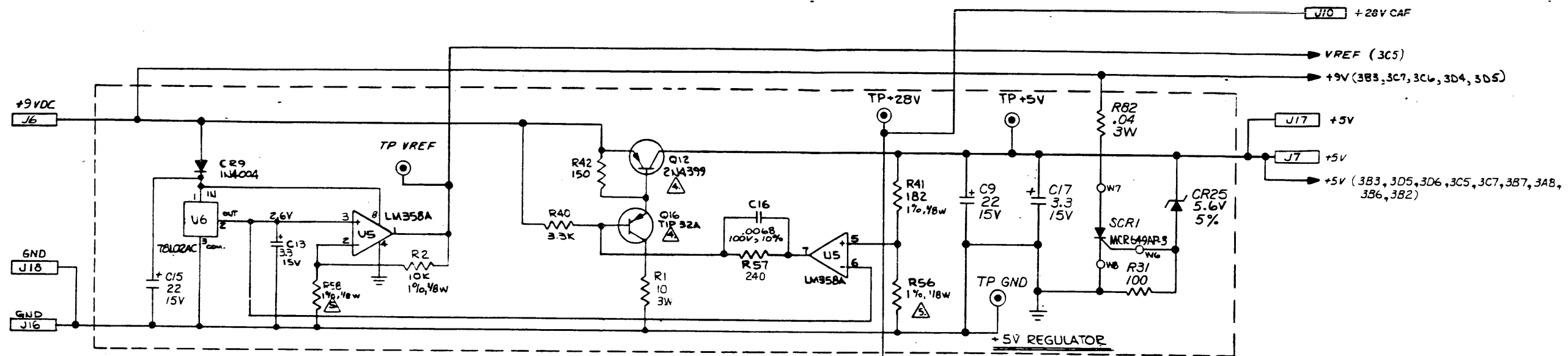
2

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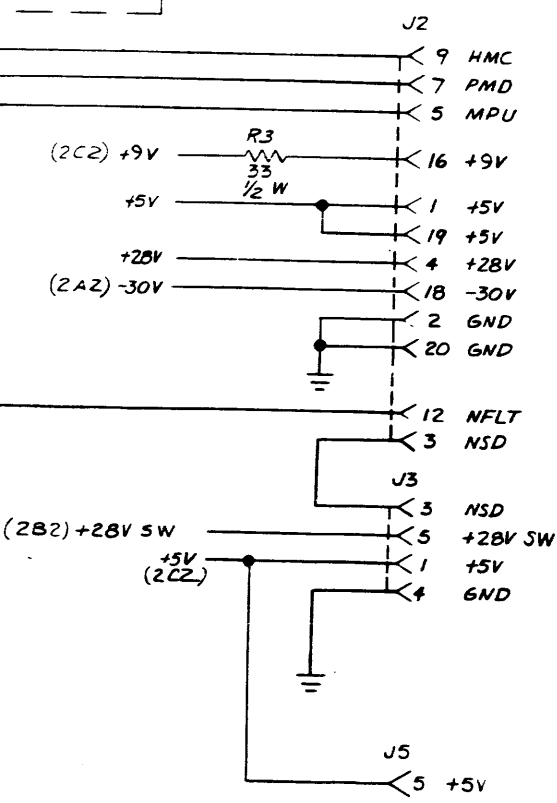
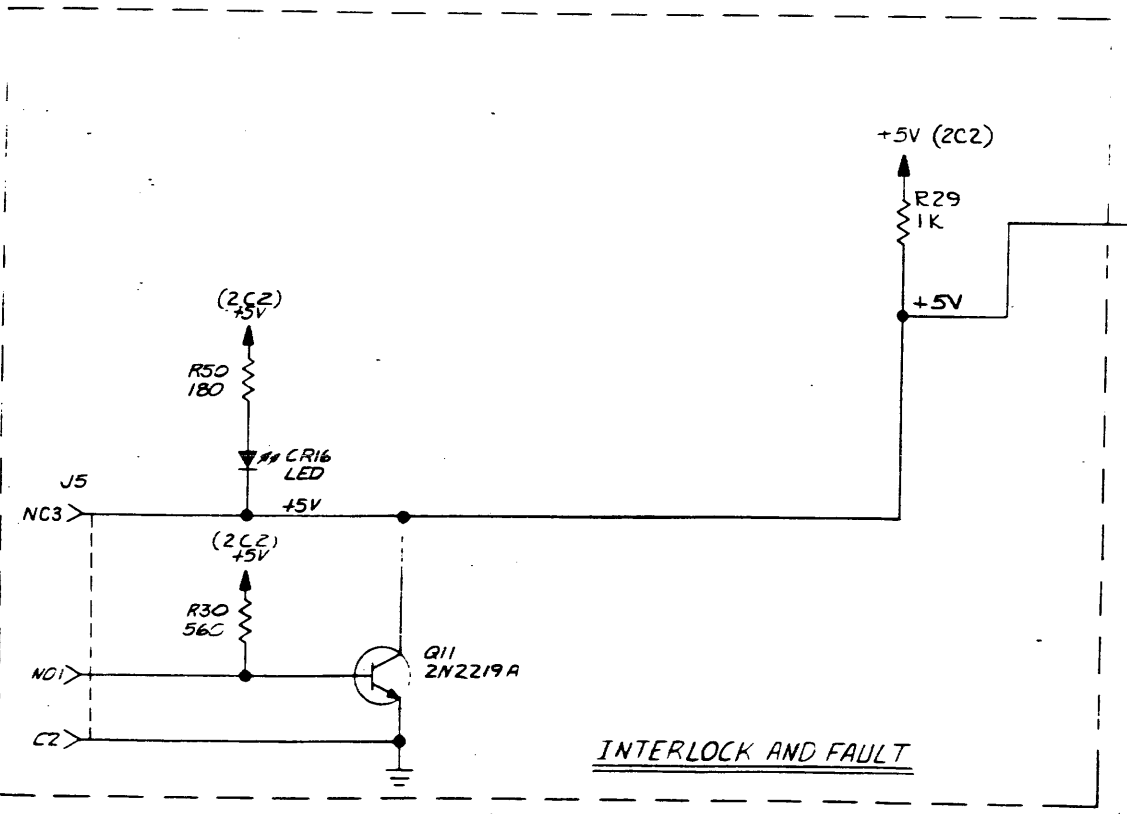
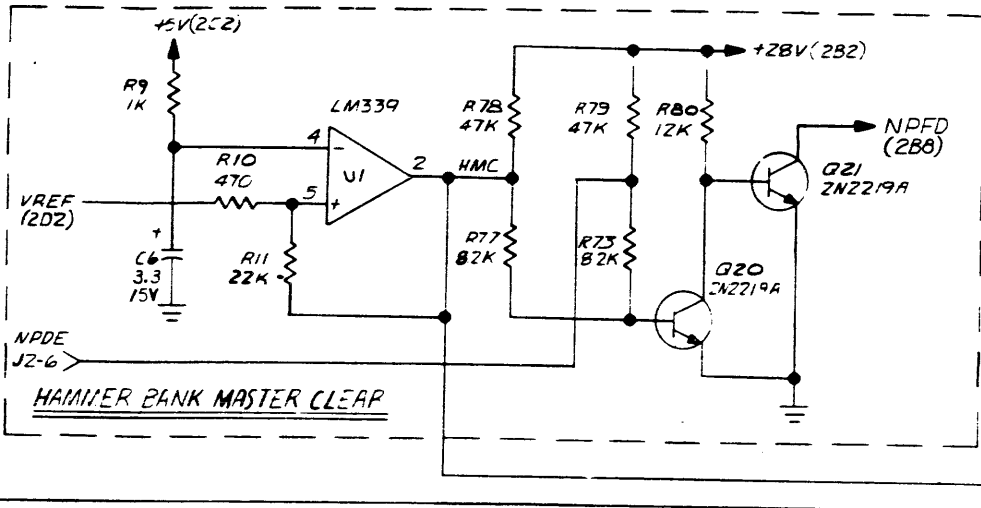
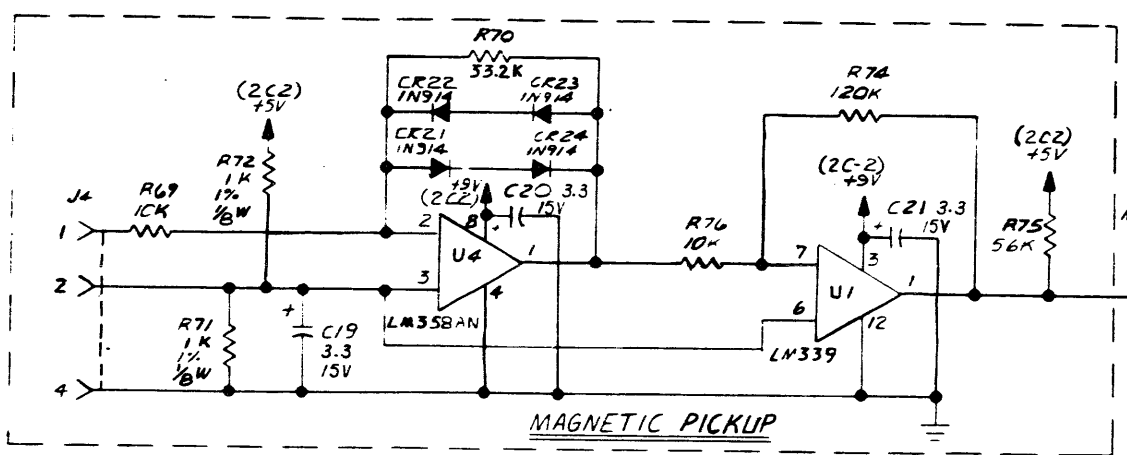
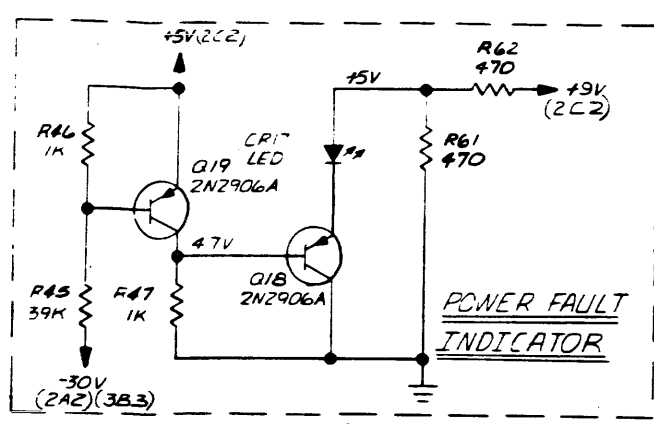
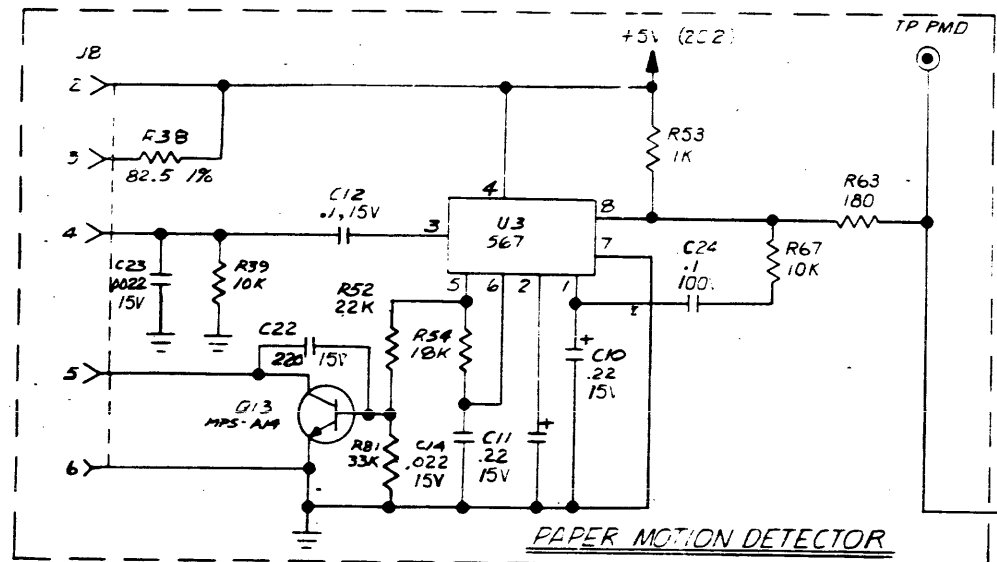
SPARE CIRCUITS		
TYPE	LOCATION	OUTPUT PIN
LM339	U1	13,14
LM358AN	U4	7

I.C. GND AND VOLTAGE INFO.		
TYPE	GND PIN	+5V PIN
567	7	4
78L02AC	3	1
LM339	12	3
LM358AN	4	8

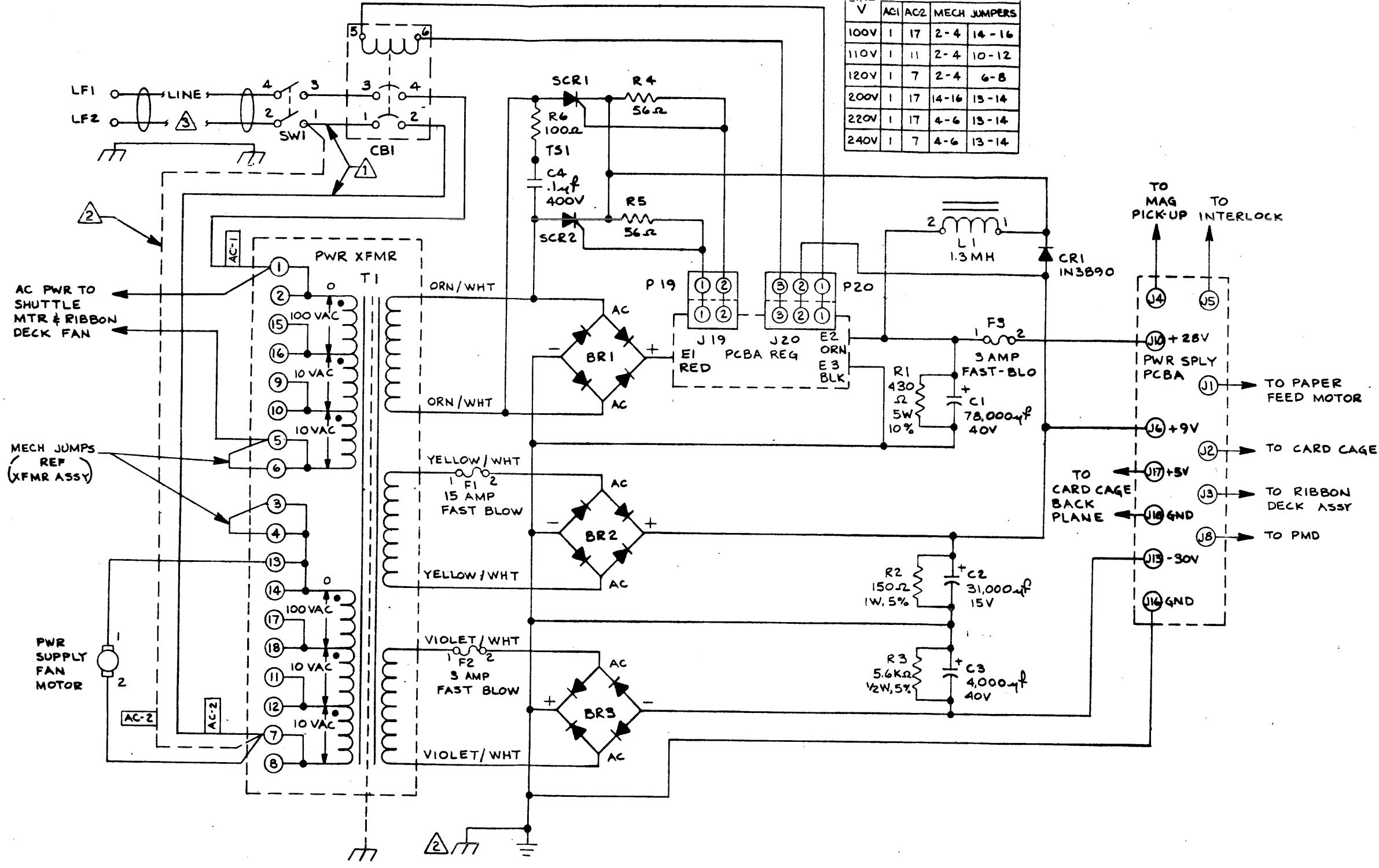
CONNECTORS	
J1	PAPER FEED MOTOR
J2	CARD CAGE
J3	RIBBON ASSY
J4	MAGNETIC PICKUP
J5	INTERLOCKS
J6	+9V DC
J7	+5V DC
J8	PMD
J10	+30V CAP
J13	-30V DC
J16	GND
J17	+5V
J18	GND



8 7 6 5 4 3 2 1

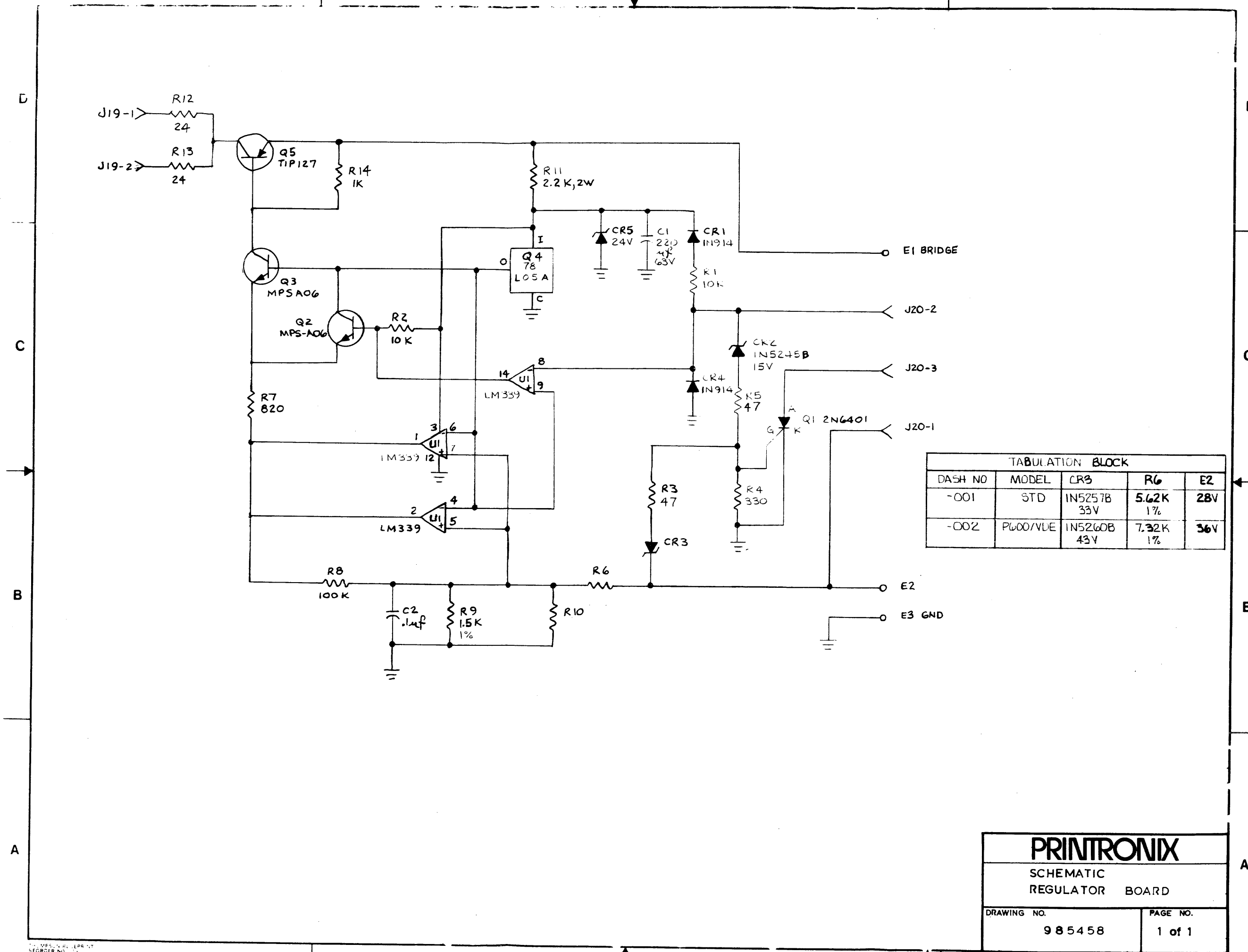


LINE V	TO TERMINALS			
	AC1	AC2	MECH JUMPERS	
100V	1	17	2-4	14-16
110V	1	11	2-4	10-12
120V	1	7	2-4	6-8
200V	1	17	14-16	13-14
220V	1	17	4-6	13-14
240V	1	7	4-6	13-14



⚠ LINE VOLTAGE CONFIGURATION SHOWN IS 240V.  
 ⚠ FOR 120V LINE VOLTAGE CONFIGURATION.

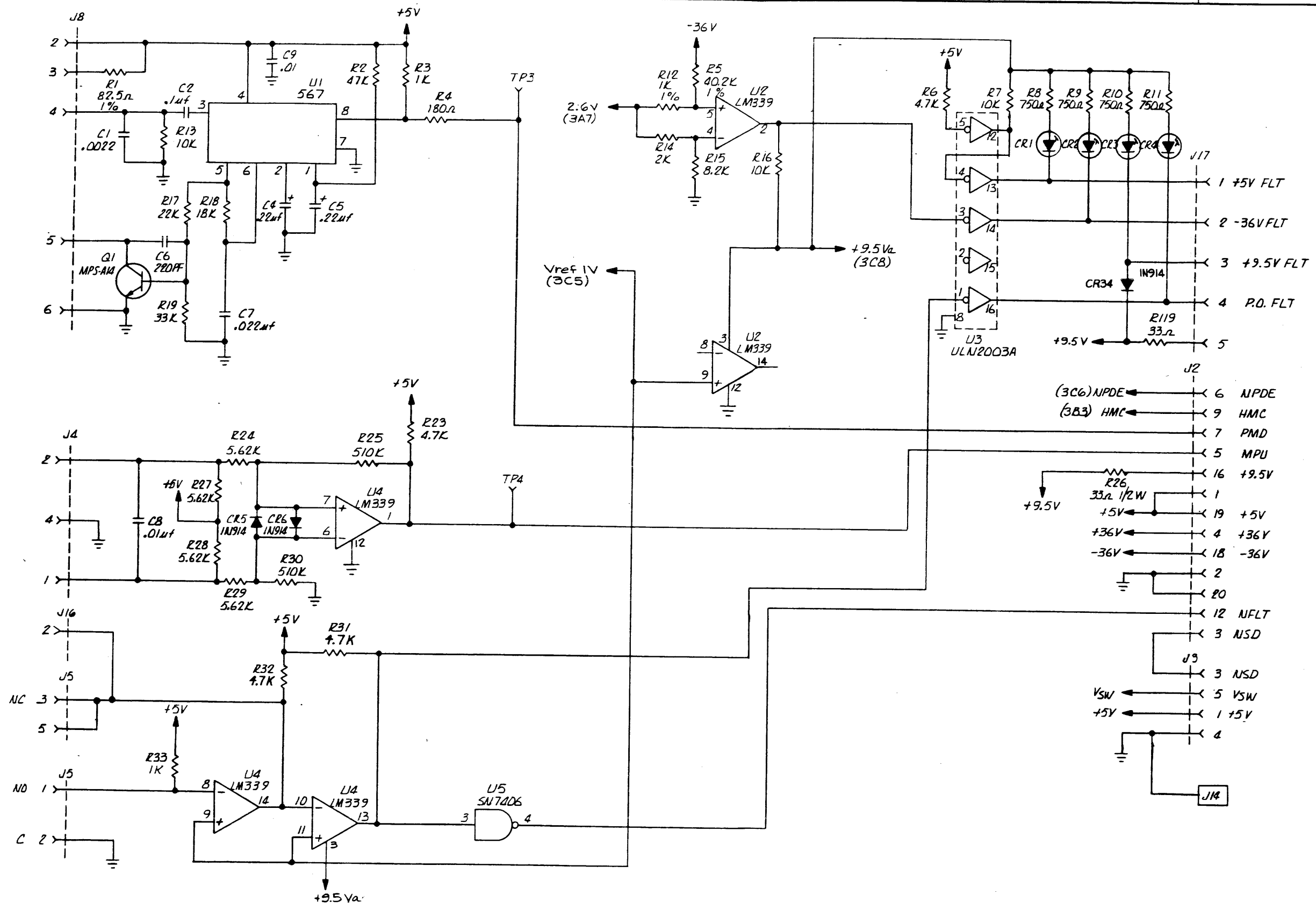
<b>PRINTRONIX</b>	
SCHEMATIC POWER SUPPLY P300 VDE	
DRAWING NO. 985447	PAGE NO. 1 of 1

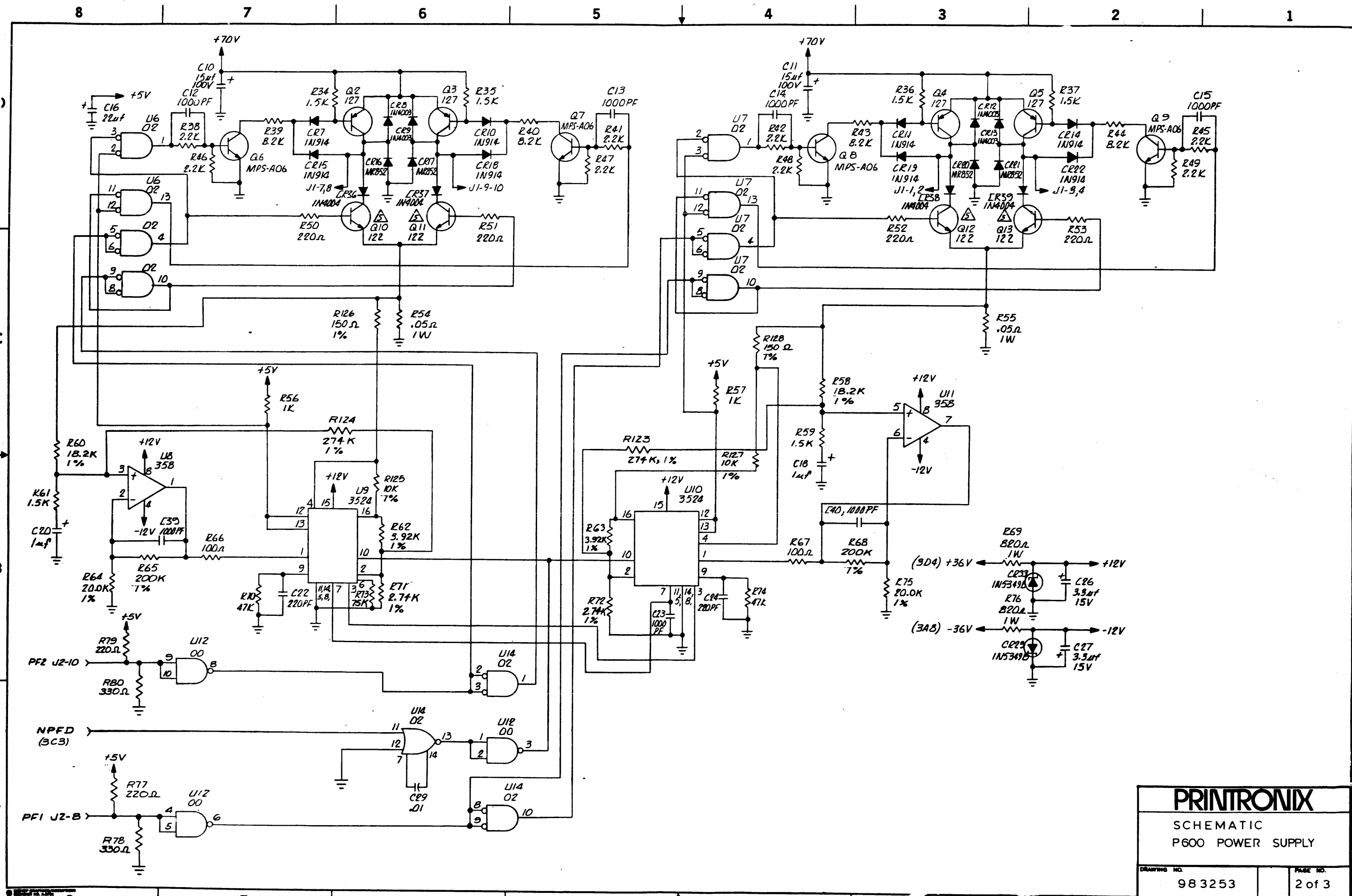


TABULATION BLOCK				
DASH NO	MODEL	CR3	R6	E2
-001	STD	1N5257B 33V	5.62K 1%	28V
-002	PL00/VLE	1N5260B 43V	7.32K 1%	36V

<b>PRINTRONIX</b>	
SCHEMATIC REGULATOR BOARD	
DRAWING NO. 985458	PAGE NO. 1 of 1



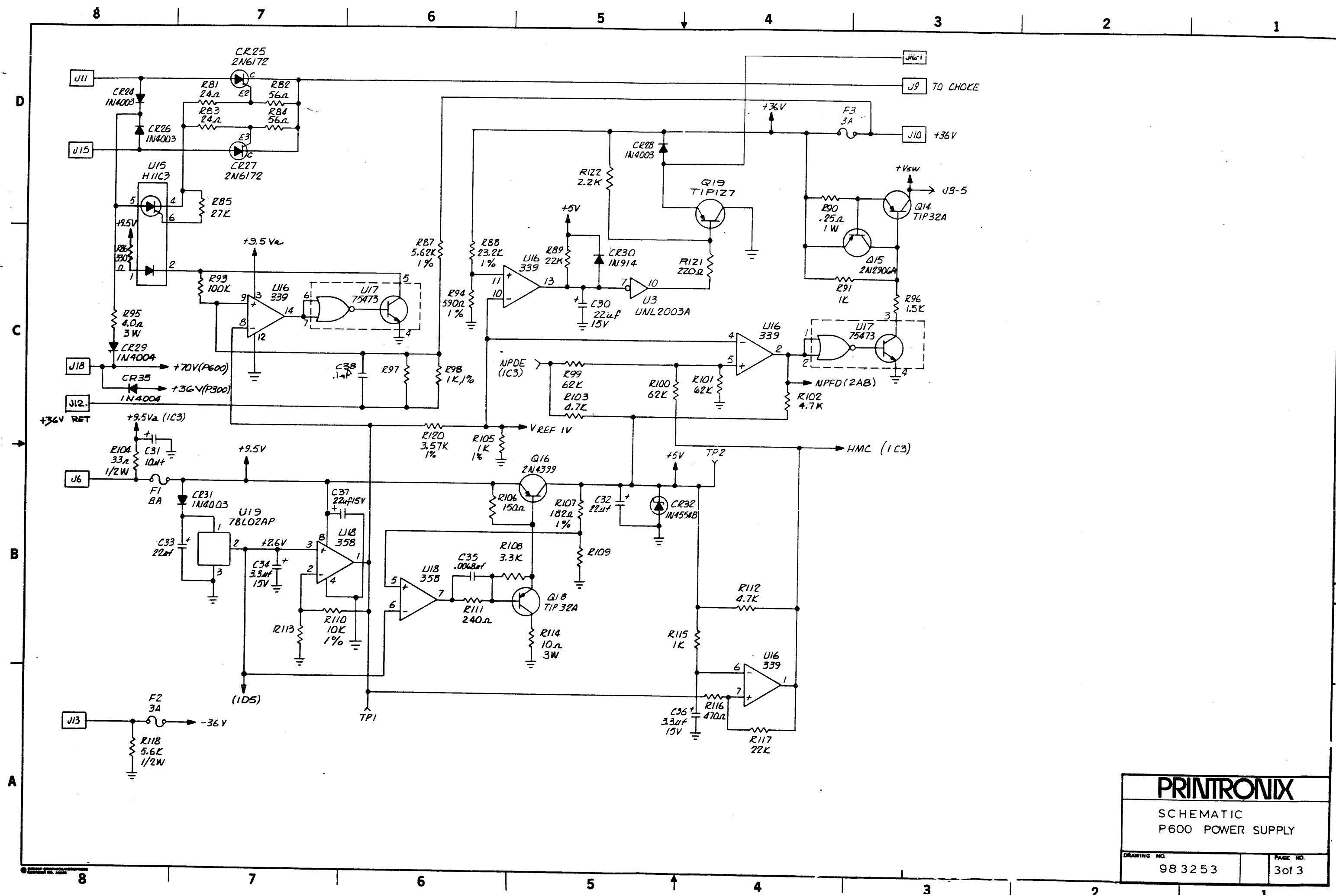




**PRINTRONIX**

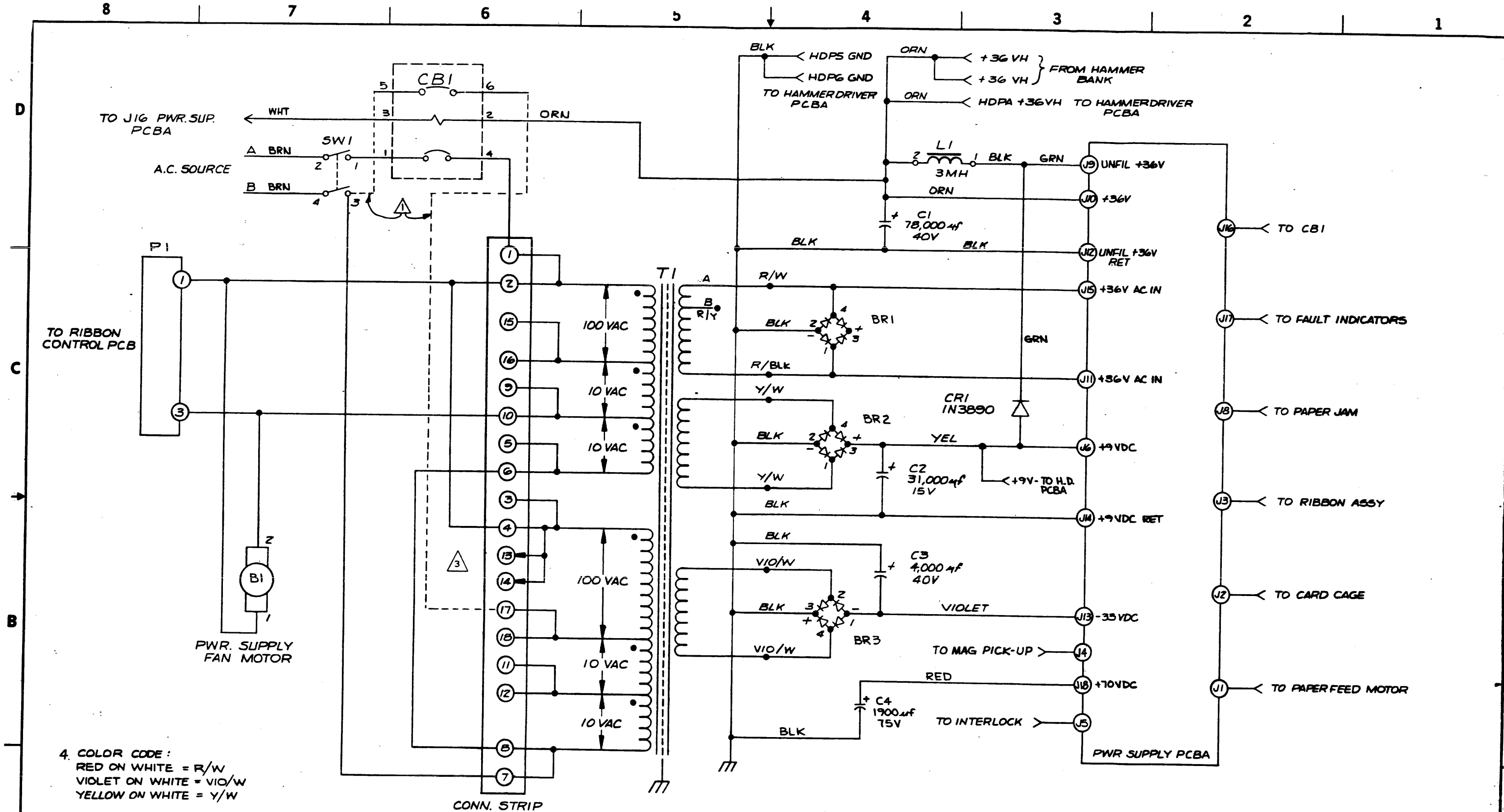
SCHEMATIC  
P600 POWER SUPPLY

DRAWING NO. 98 3253	PAGE NO. 2 of 3
------------------------	--------------------



**PRINTRONIX**  
 SCHEMATIC  
 P600 POWER SUPPLY

DRAWING NO.	PAGE NO.
98 32 53	3 of 3



4. COLOR CODE:  
 RED ON WHITE = R/W  
 VIOLET ON WHITE = VIO/W  
 YELLOW ON WHITE = Y/W

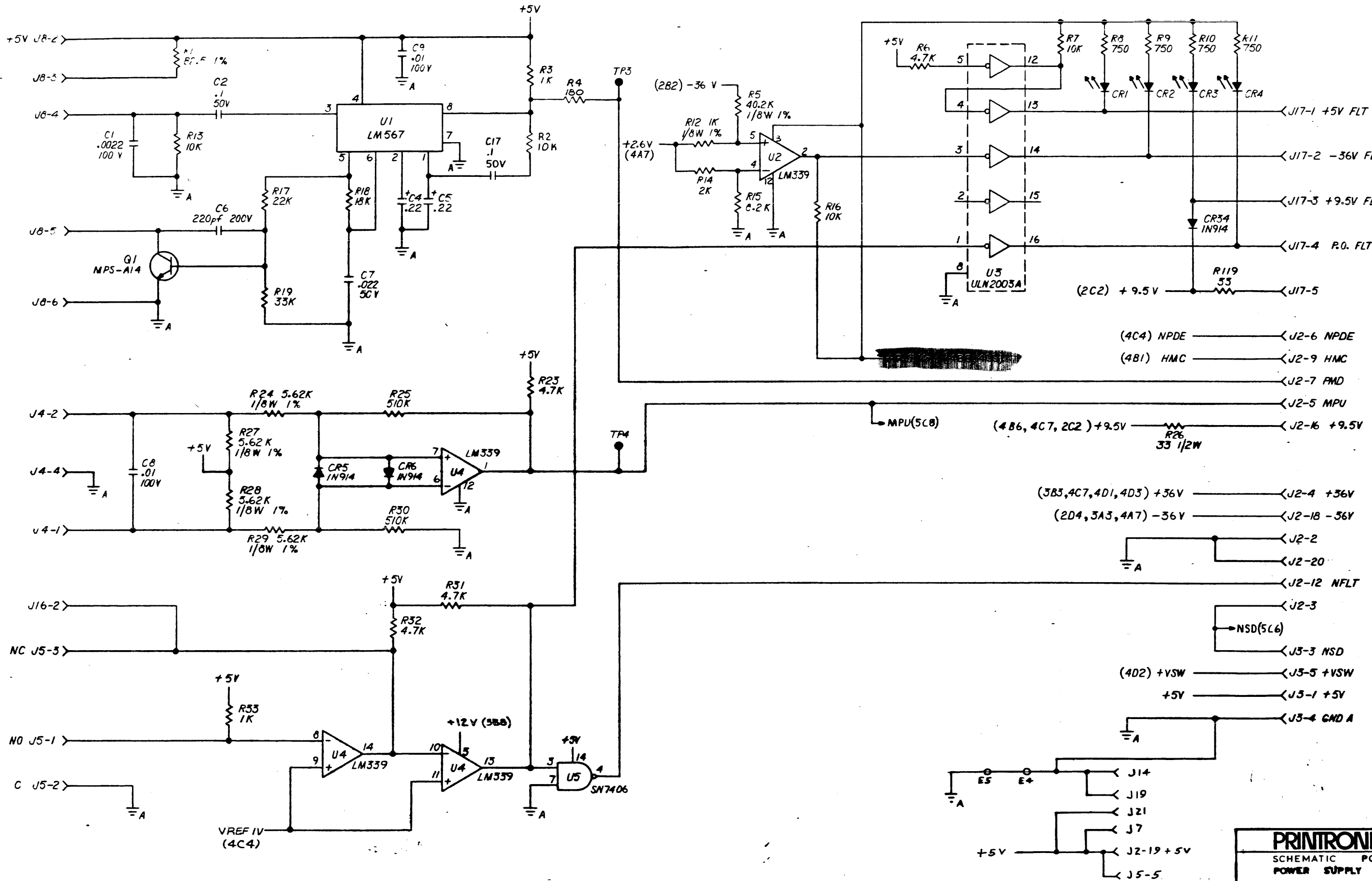
- ⚠️ TERMINAL CONNECTION SHOWN ARE FOR 120 VOLTS SOURCE ONLY CONNECTIONS FOR OTHER VOLTAGES (SEE TABLE)
2. RECTIFIER BRIDGES BR1 & BR3 ARE MDA-980 OR EQUIV. BR2 IS MDA-990-2 OR EQUIV.
- ⚠️ FOR 220 VAC IN
- NOTES: UNLESS OTHERWISE SPECIFIED

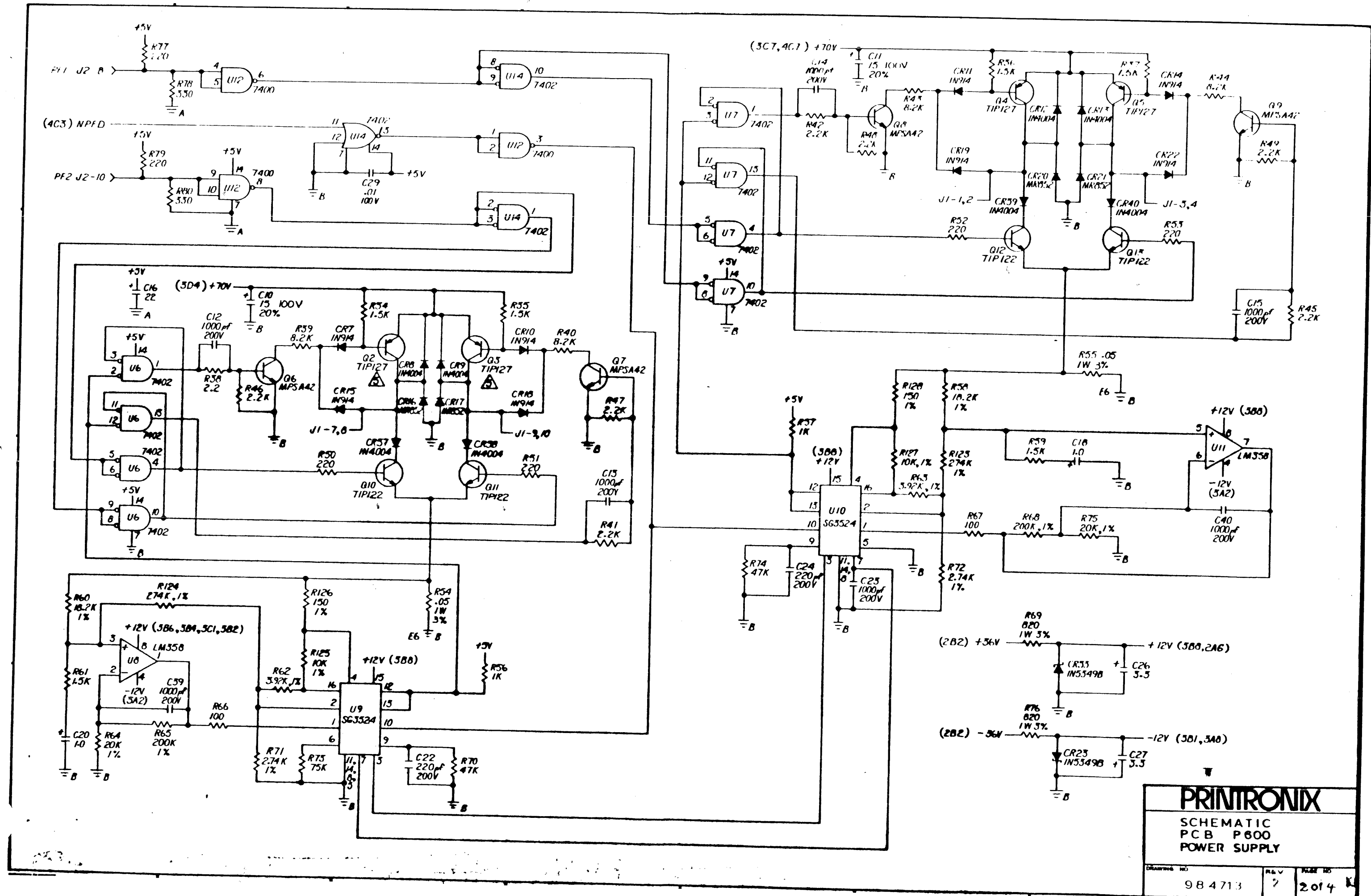
TERMINAL CONNECTIONS			
LINE VOLTAGE	LINE INPUT	RIBBON ASSY. CONNECT	TERMINATIONS
120	1 & 7	1 & 5	6 TO 8, 2 TO 4
110	1 & 11	1 & 5	10 TO 12, 2 TO 4
100	1 & 17	1 & 5	16 TO 18, 2 TO 4
240	1 & 7	1 & 5	4 TO 6
220	1 & 11	1 & 5	4 TO 6
200	1 & 17	1 & 5	14 TO 16

**PRINTRONIX**

SCHEMATIC  
 P600 POWER SUPPLY

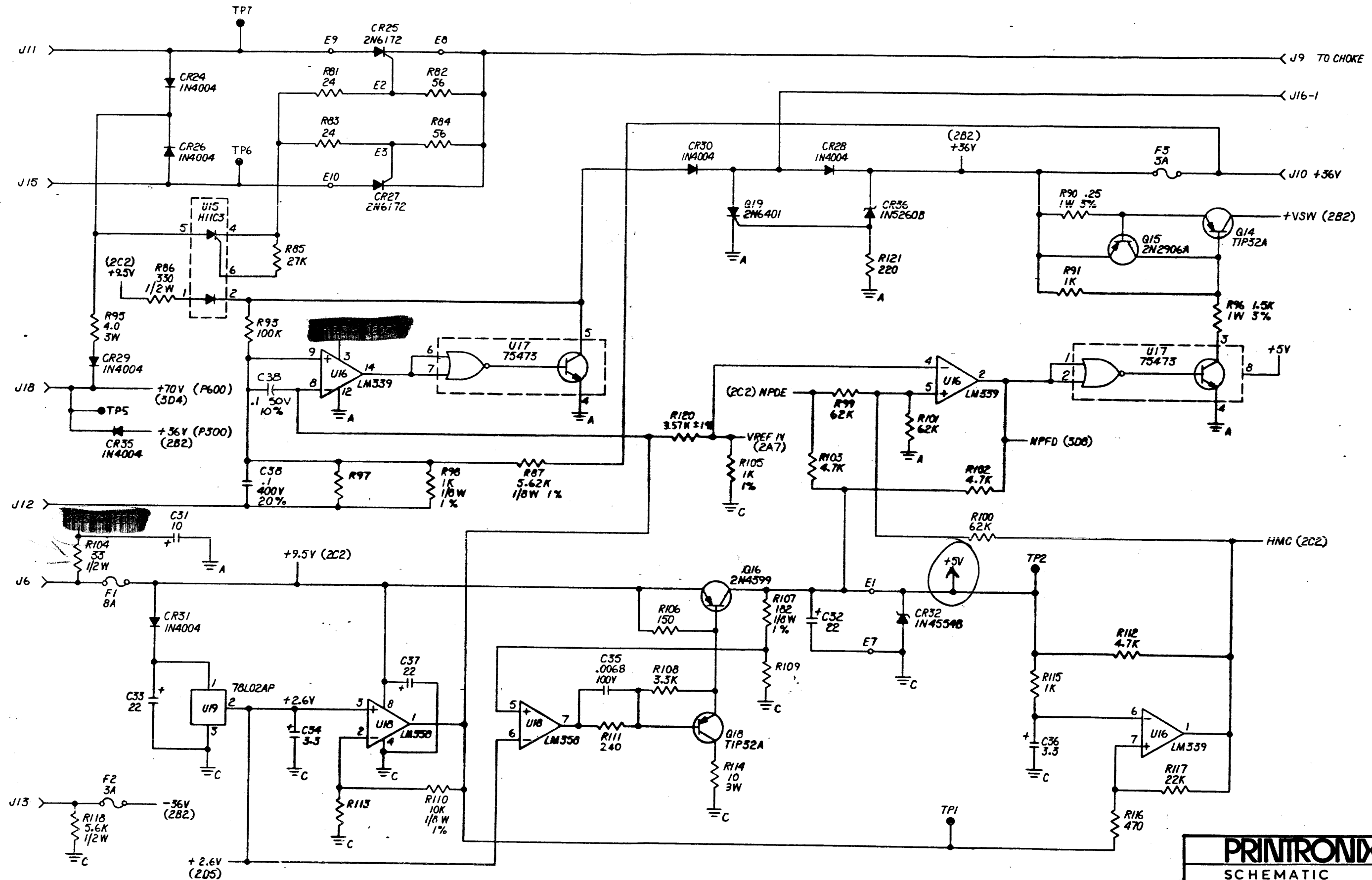
DRAWING NO. 983295 PAGE NO. 1 of 1





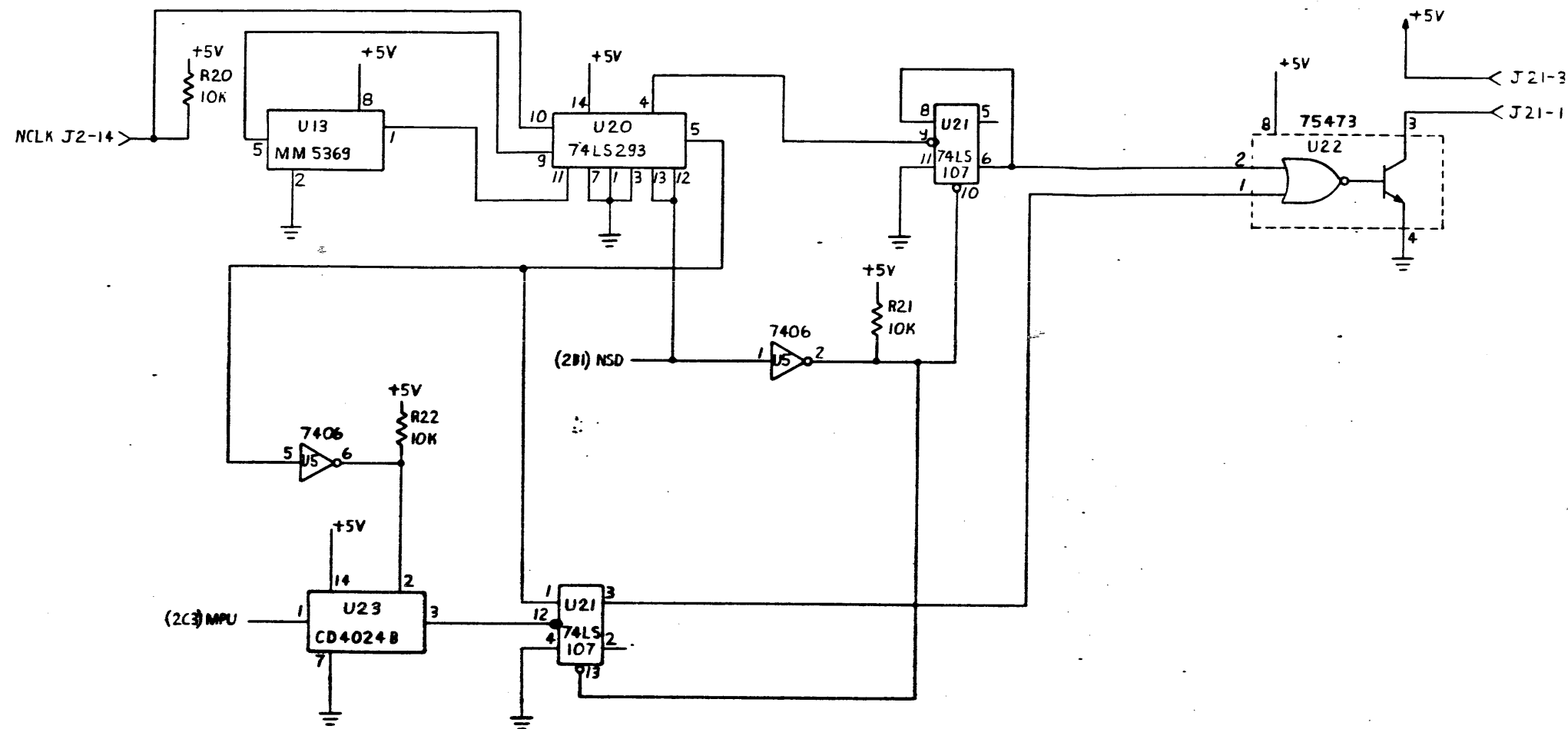
**PRINTRONIX**  
 SCHEMATIC  
 PCB P600  
 POWER SUPPLY

DRAWING NO	REV	DATE
984713	?	2014



**PRINTRONIX**  
 SCHEMATIC  
 PCB P600  
 POWER SUPPLY

DRAWING NO. 984713	PAGE NO. 3 of 4
-----------------------	--------------------



#### CONNECTORS

J1	PAPER FEED MOTOR
J2	LOGIC A, B AND G. D. (CARD CASE)
J3	RIBBON DECK
J4	M. P. U.
J5	PAPER OUT / PLATEN SWITCH
J6	+9.5V SUPPLY
J7	+5V
J8	FMD
J9	+36V CHORE
J10	+36V CAPACITOR
J11	+36V AC IN
J12	+36V RETURN
J13	-36V
J14	GND
J15	+36V AC M
J16	CIRCUIT BREAKER
J17	FAULT MONITOR
J18	+70V
J19	GND
J20	NOT USED
J21	MOTOR START RELAY

**PRINTRONIX**

SCHMATIC  
PCB P600  
POWER SUPPLY

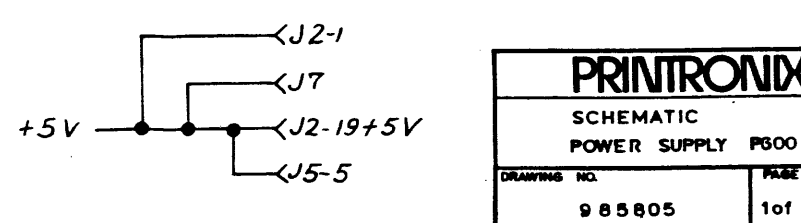
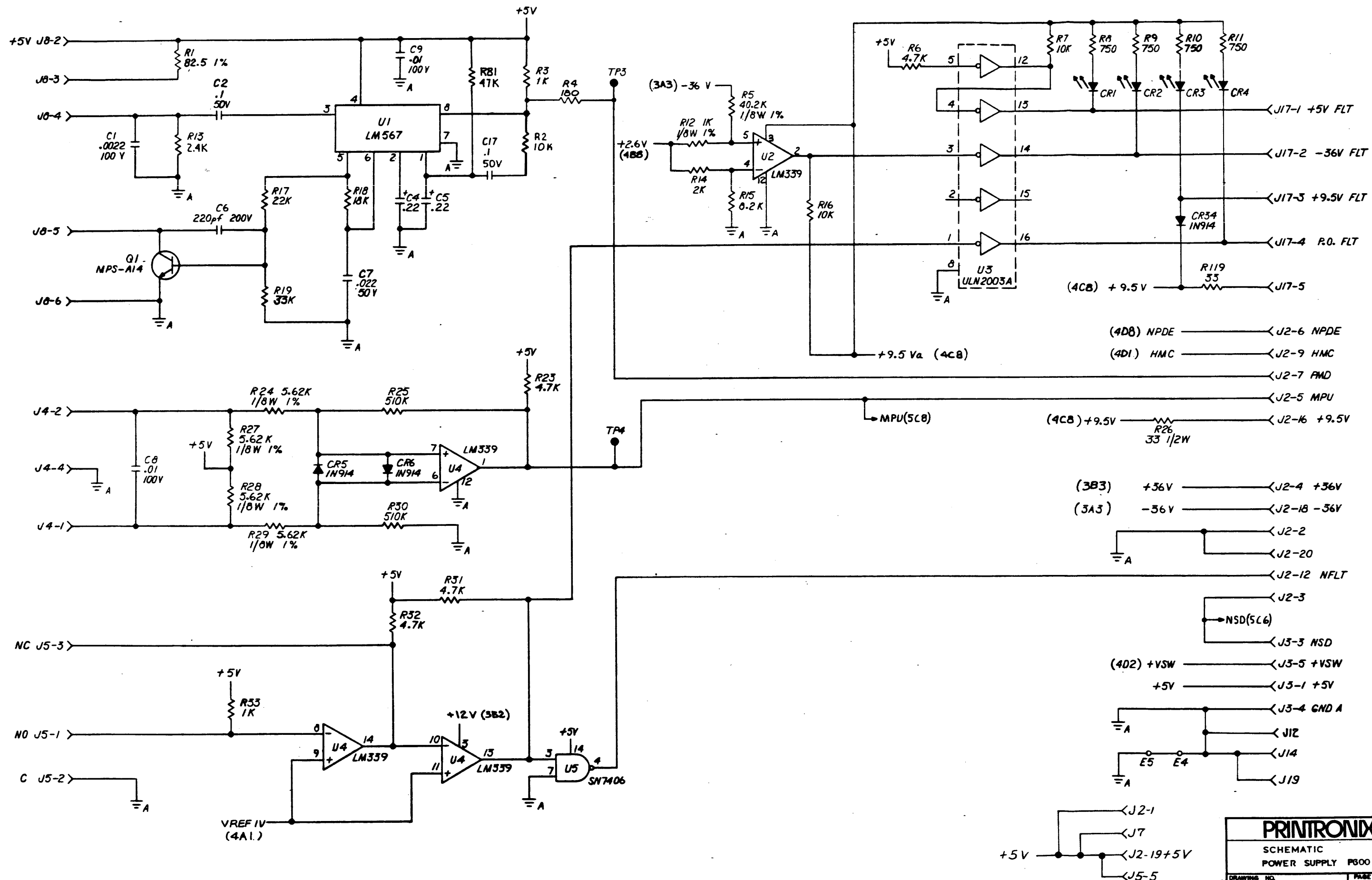
DRAWING NO.

984713

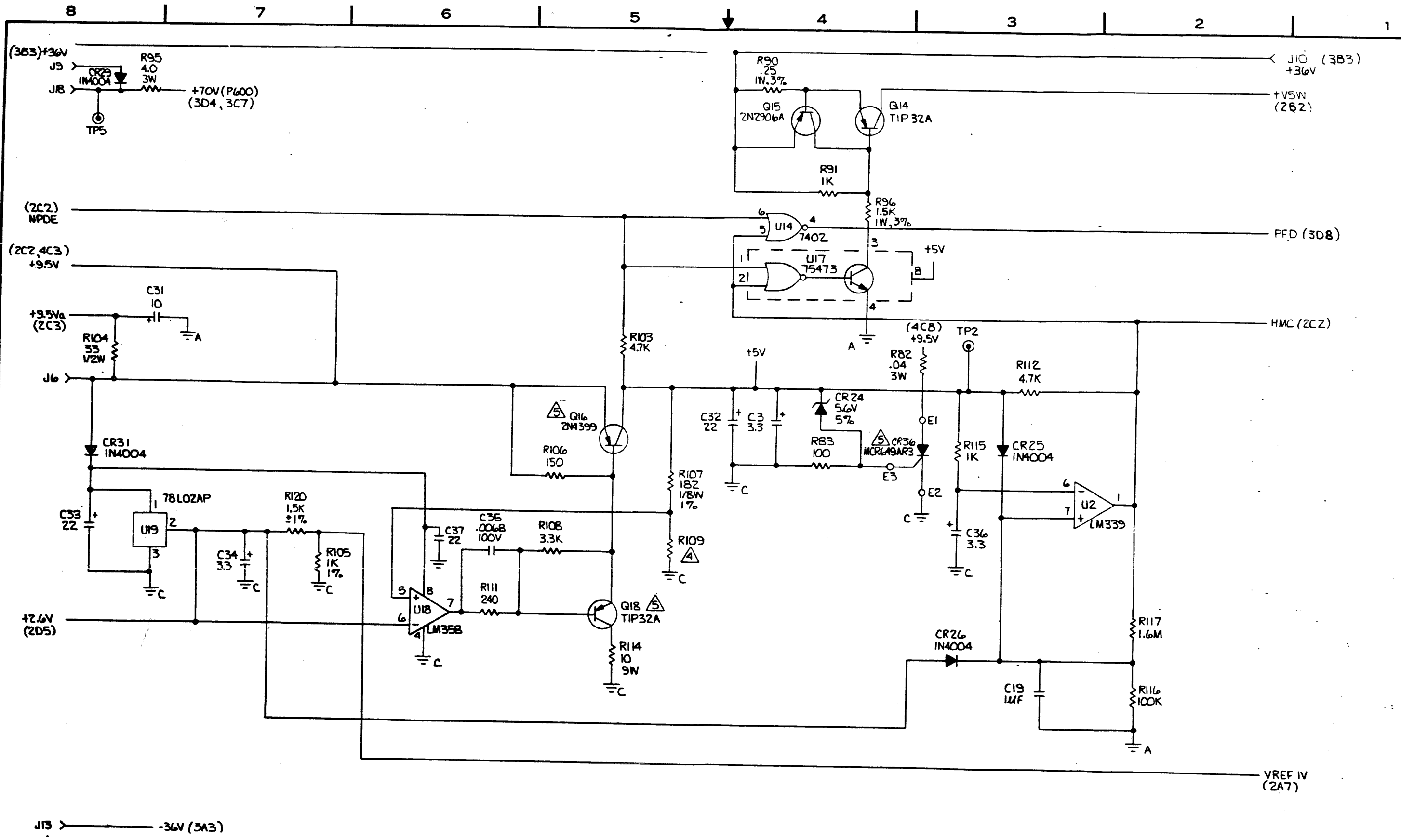
PAGE NO.

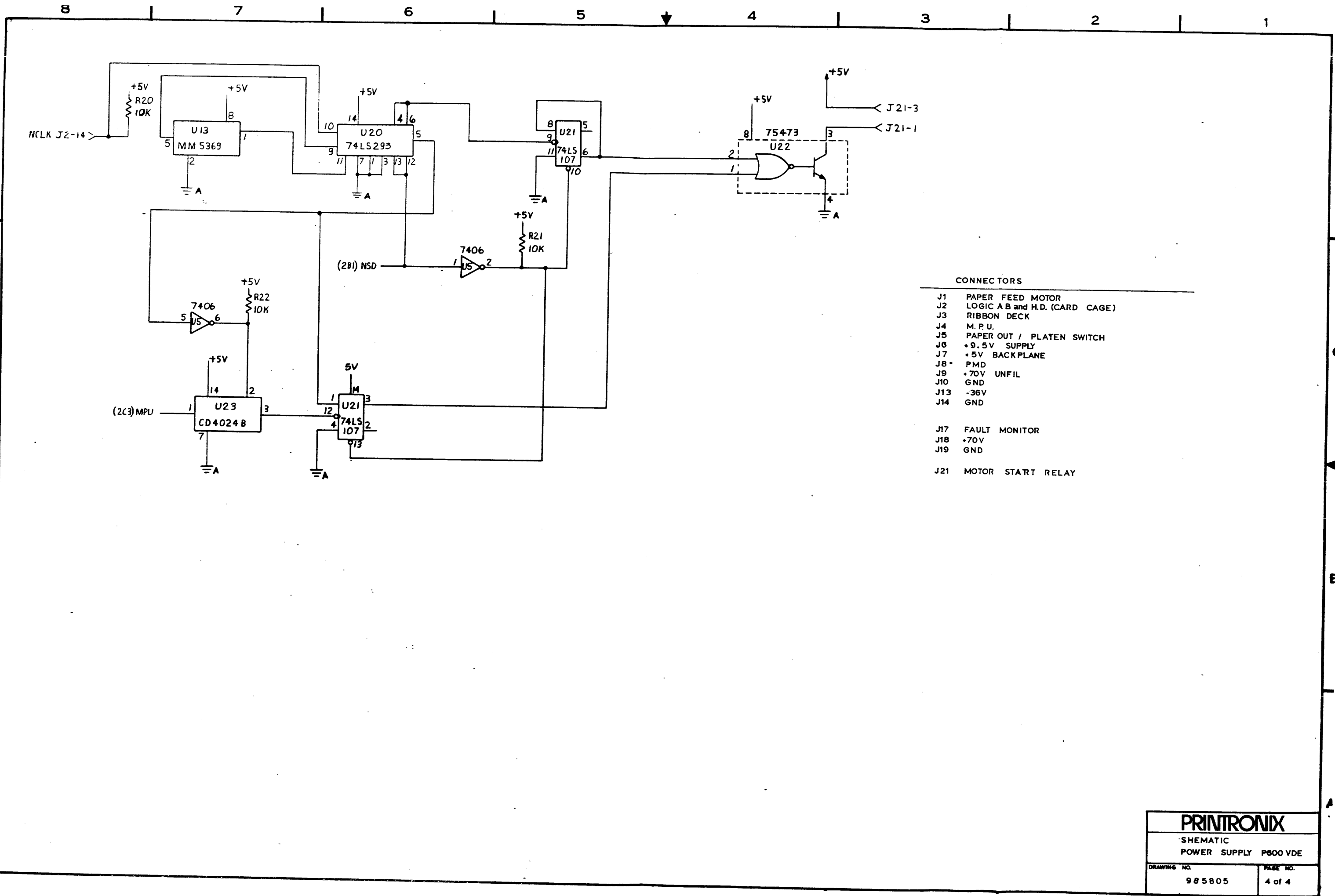
5 4 of 4





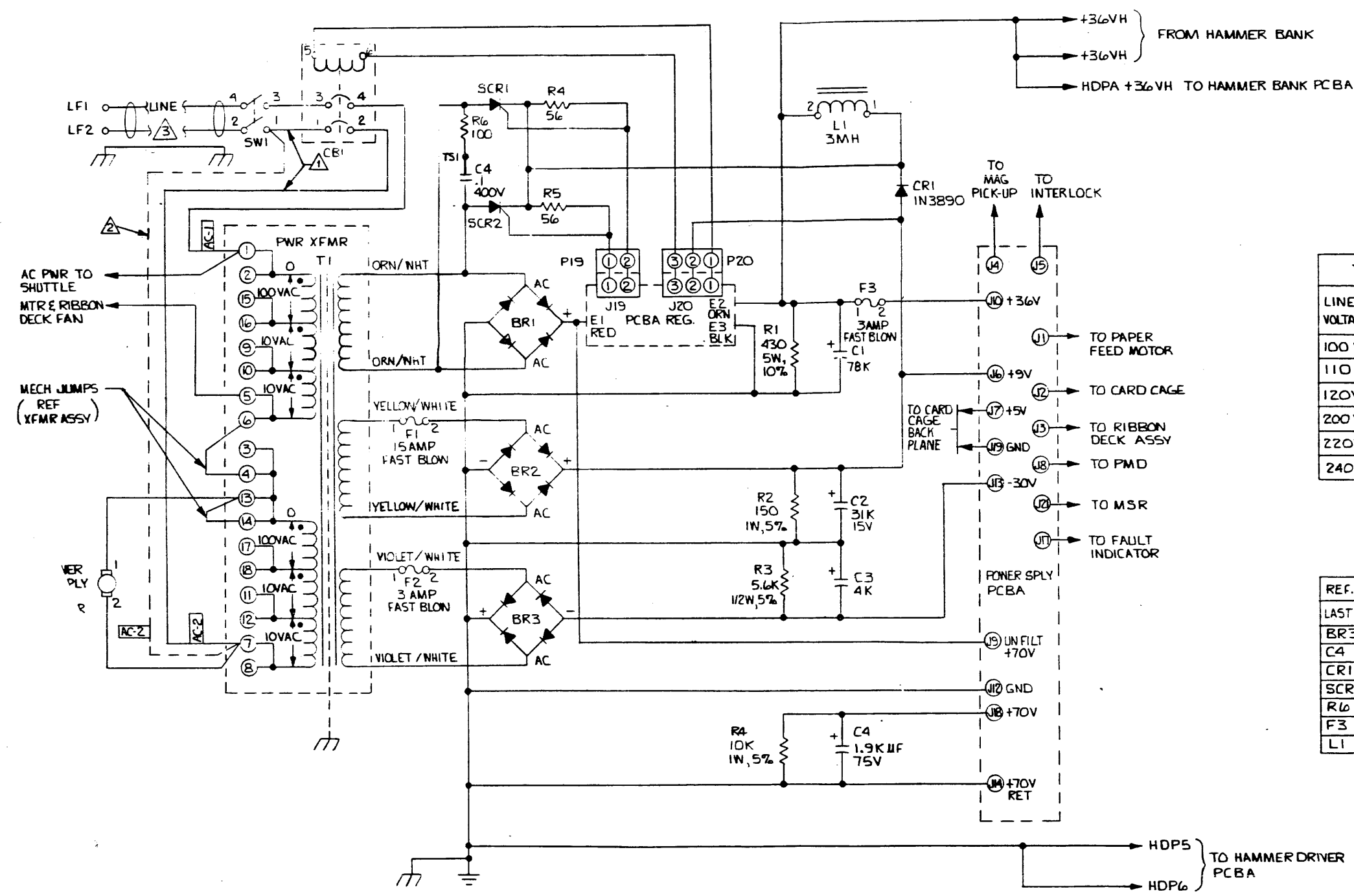






- CONNECTORS
- J1 PAPER FEED MOTOR
  - J2 LOGIC A B and H.D. (CARD CAGE)
  - J3 RIBBON DECK
  - J4 M. P. U.
  - J5 PAPER OUT / PLATEN SWITCH
  - J6 +0.5V SUPPLY
  - J7 +5V BACKPLANE
  - J8 - PMD
  - J9 +70V UNFIL
  - J10 GND
  - J13 -36V
  - J14 GND
  
  - J17 FAULT MONITOR
  - J18 +70V
  - J19 GND
  
  - J21 MOTOR START RELAY

D



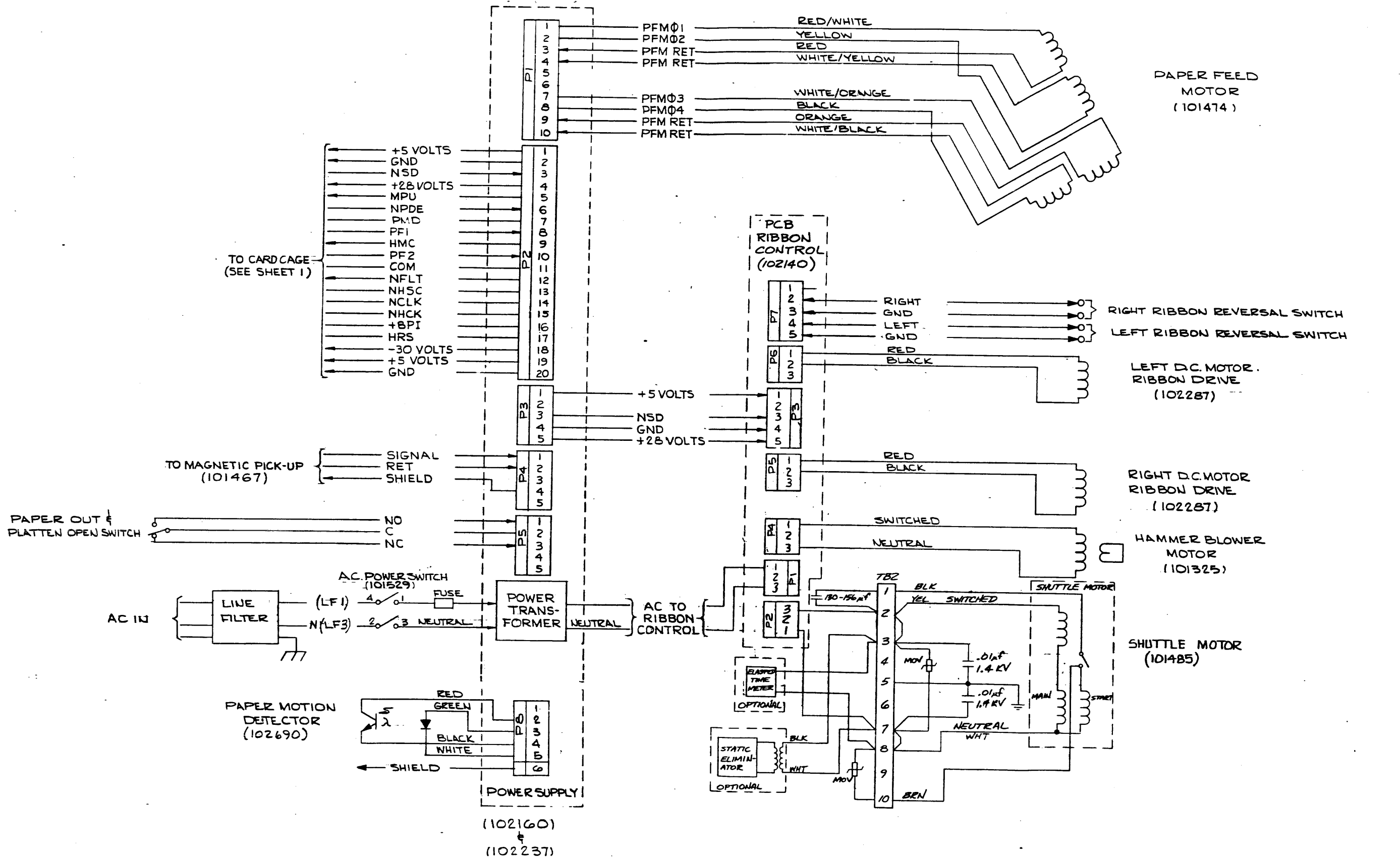
VOLTAGE CONFIGURATION				
LINE VOLTAGE	TO TERMINALS			
	AC-1	AC-2	MECH JUMPERS	
100 V	1	17	2-4	16-18
110 V	1	11	2-4	10-12
120 V	1	7	2-4	6-8
200 V	1	17	14-16	13-14
220 V	1	17	4-6	13-14
240 V	1	7	4-6	13-14

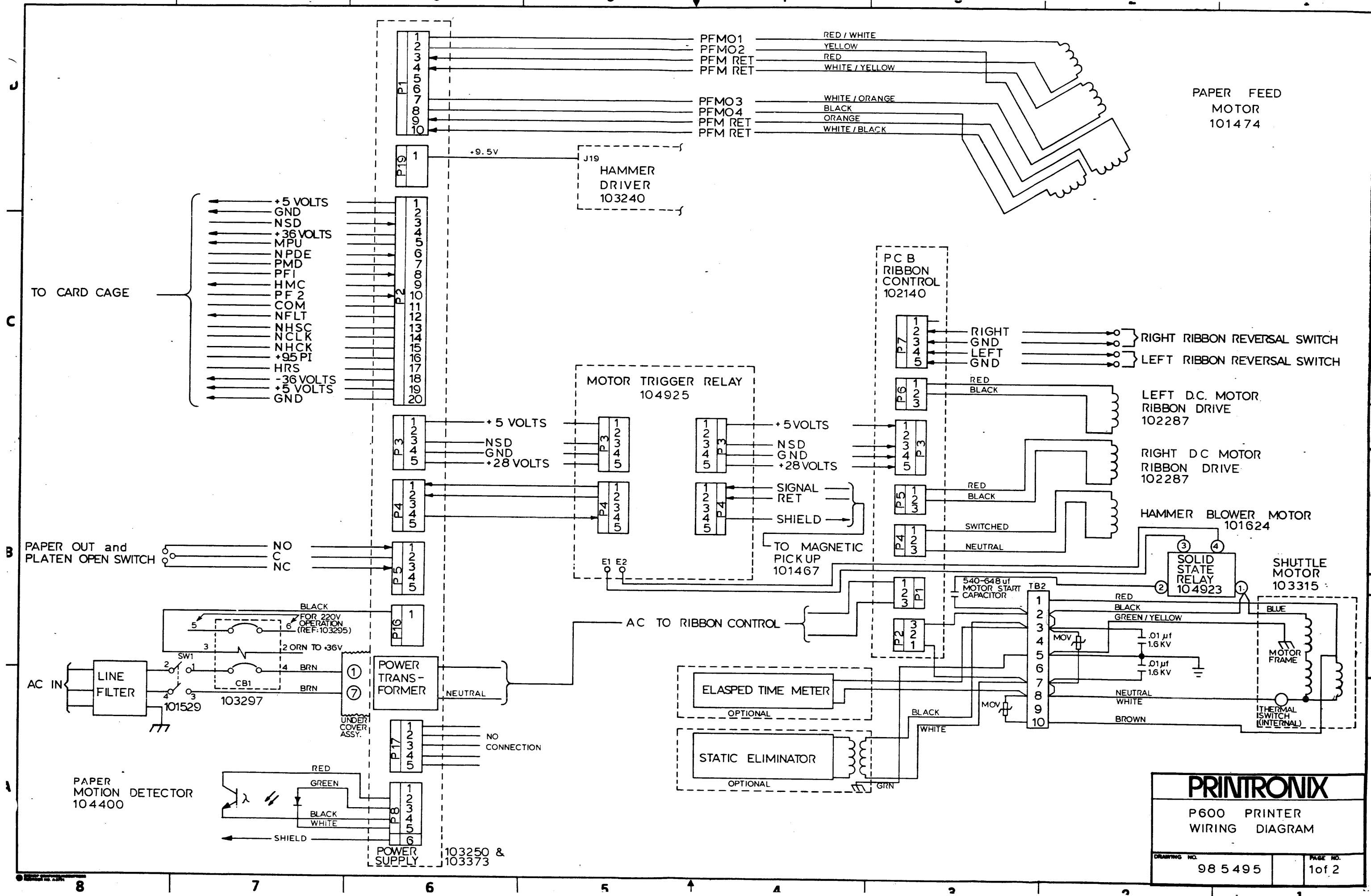
REF. DESIG. CHART	
LAST NO. USED	NOT USED
BR3	
C4	
CR1	
SCR2	
R6	
F3	
L1	

△ LINE VOLTAGE CONFIGURATION SHOWN 240V.  
 △ FOR 120V LINE VOLTAGE CONFIGURATION.

A

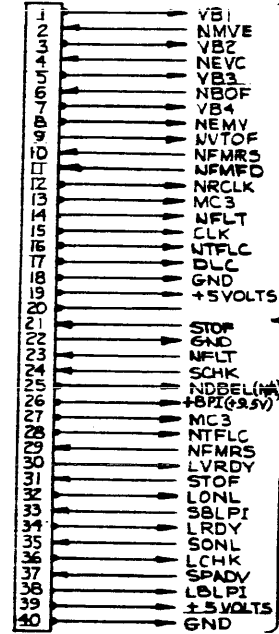




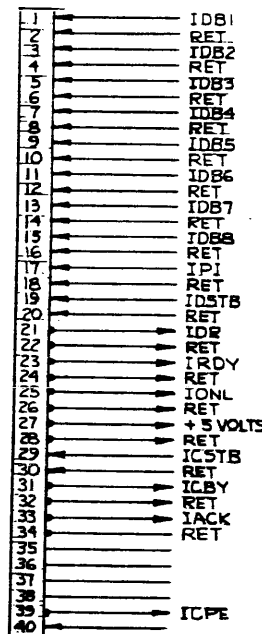




LOGIC B  
LBPI 103830



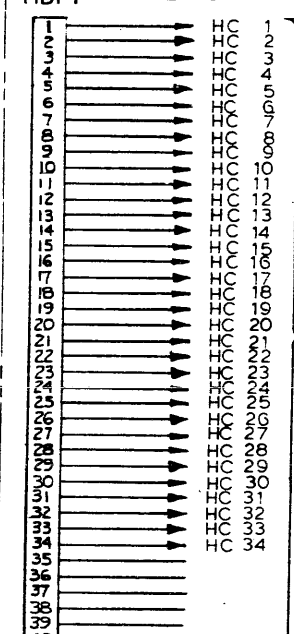
LOGIC A  
LAPI 103831



SPARE  
SSPI



HAMMER  
DRIVER  
HDPI 103240



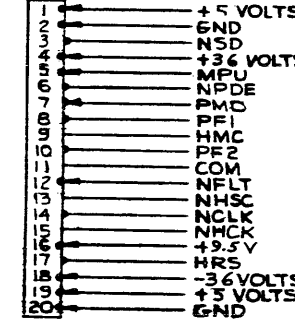
TO MECHANICAL  
YFU(NA)  
(102286)  
OR  
FORMS LENGTH  
SELECTOR  
(102347)

TO CONTROL  
PANEL  
(101578)

TO I/O  
CONNECTOR  
101500(DPC)  
OR  
101501(CC)

TO HAMMER  
BANK  
HAMMERS  
1 TO 20

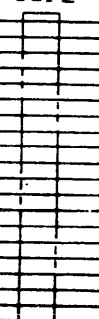
LOGIC B  
LBP2



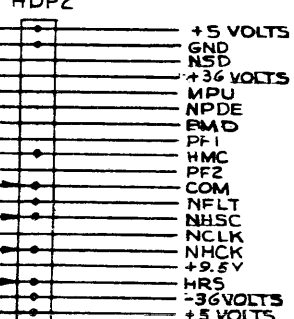
LOGIC A  
LAP2



SPARE  
SSP2

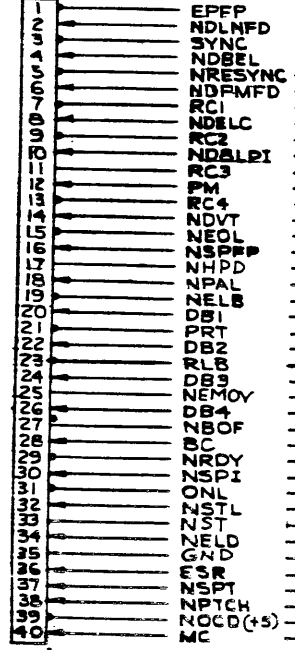


HAMMER  
DRIVE  
HDP2



POWER  
SUPPLY  
PSP2  
103250 &  
103273

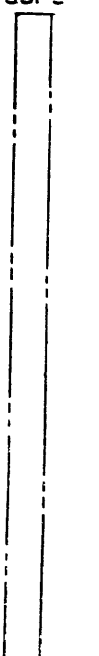
LOGIC B  
LBP3



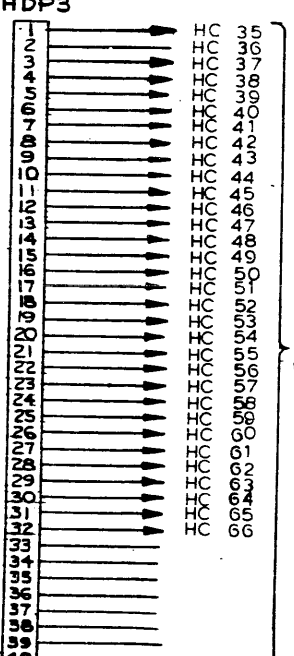
LOGIC A  
LAP3



SPARE  
SSP3



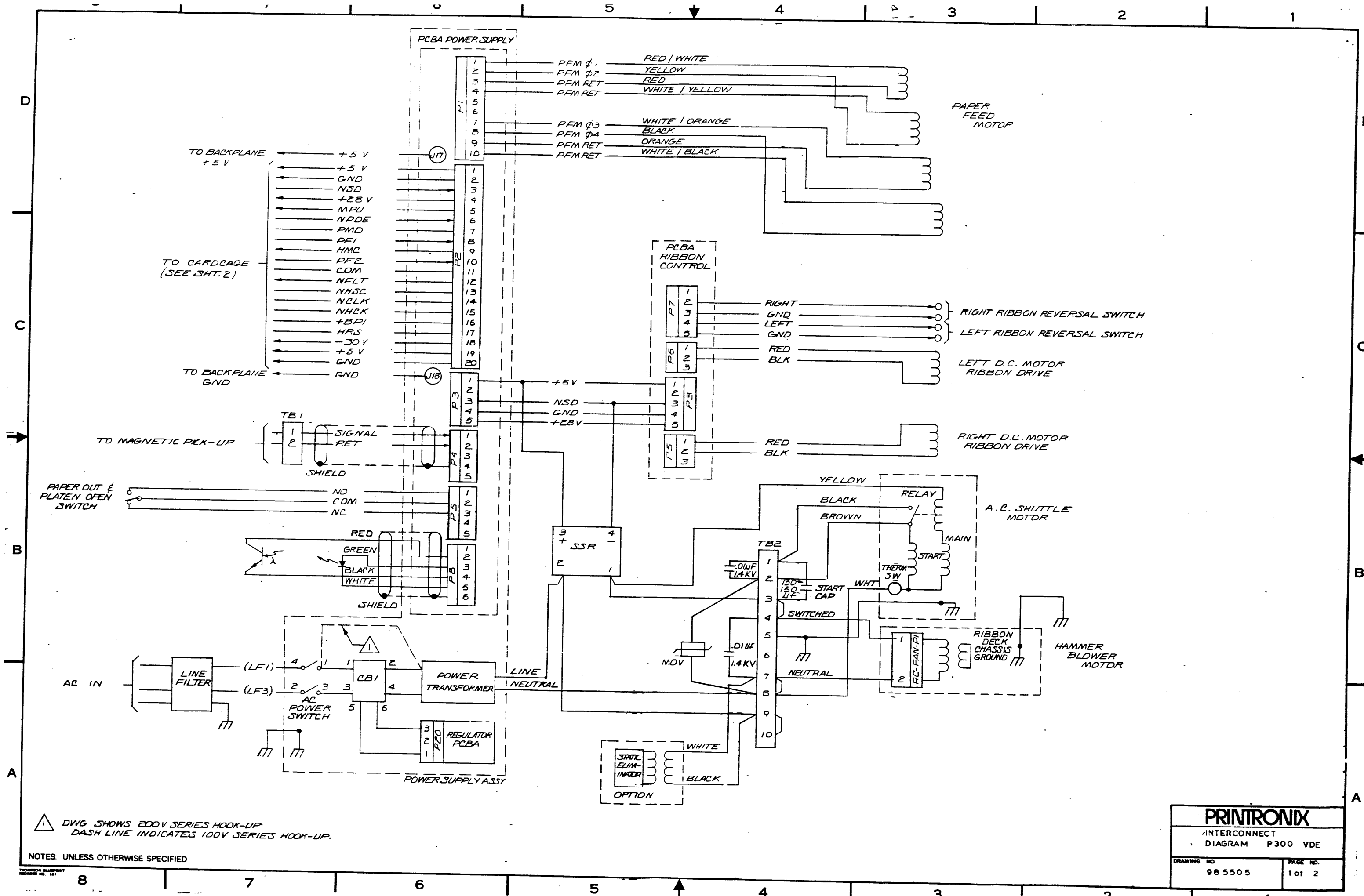
HAMMER  
DRIVER  
HDP3



TO HAMMER  
BANK  
HAMMERS  
21 TO 44

**PRINTRONIX**  
P600 PRINTER  
WIRING DIAGRAM

DRAWING NO.	REV.	PAGE NO.
985495	-	2 of 2

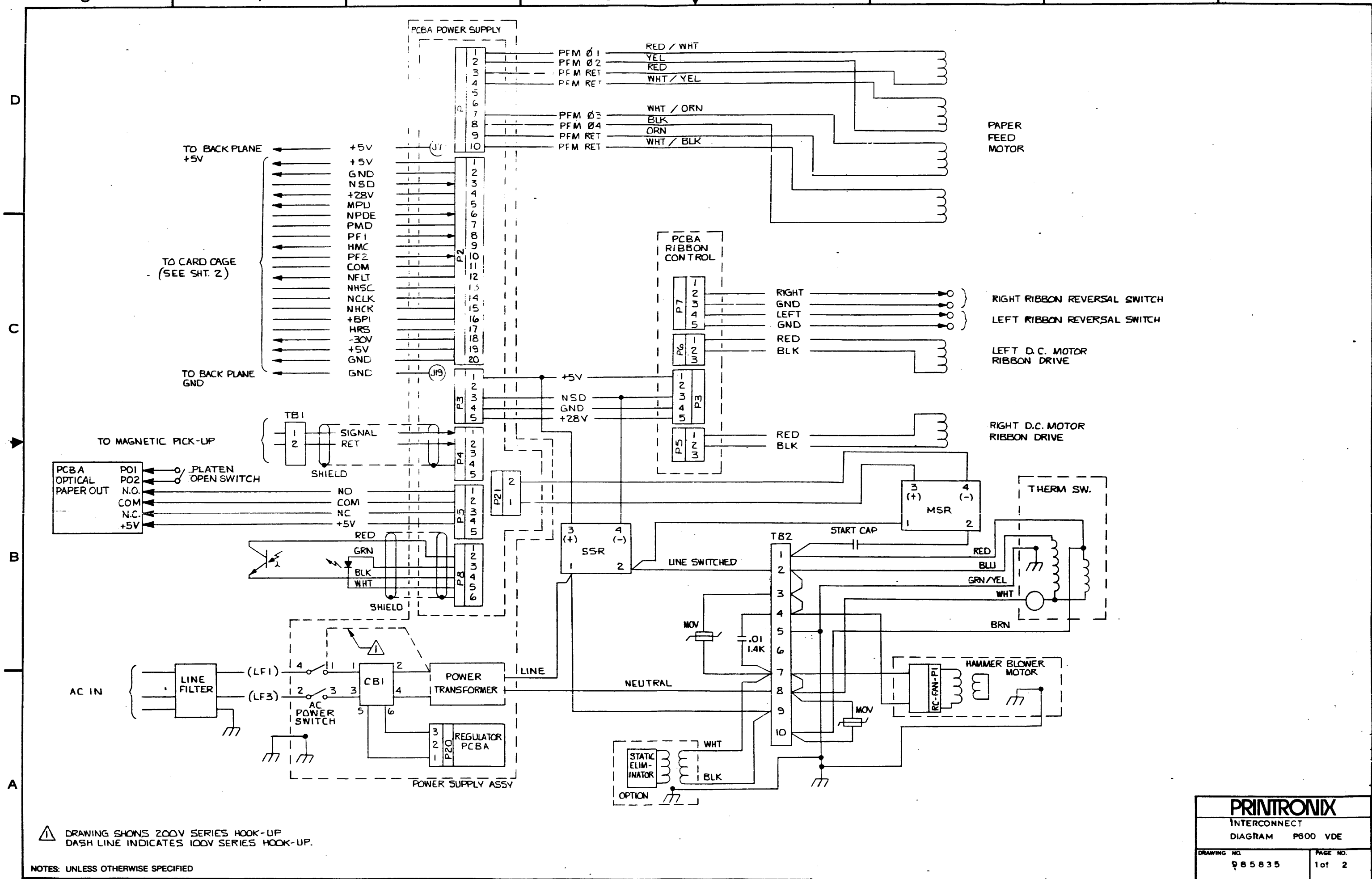


△ DWG SHOWS 200V SERIES HOOK-UP  
 DASH LINE INDICATES 100V SERIES HOOK-UP.

NOTES: UNLESS OTHERWISE SPECIFIED

<b>PRINTRONIX</b>	
INTERCONNECT DIAGRAM P300 VDE	
DRAWING NO. 985505	PAGE NO. 1 of 2



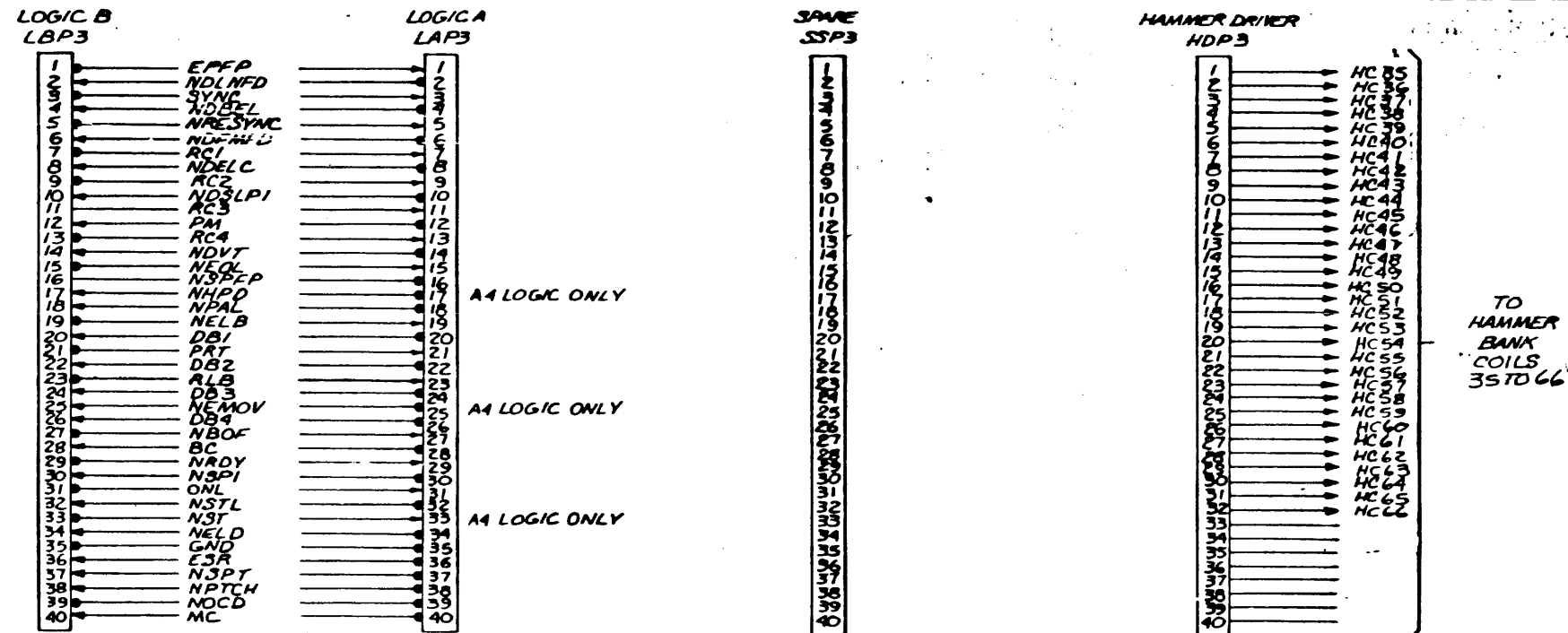
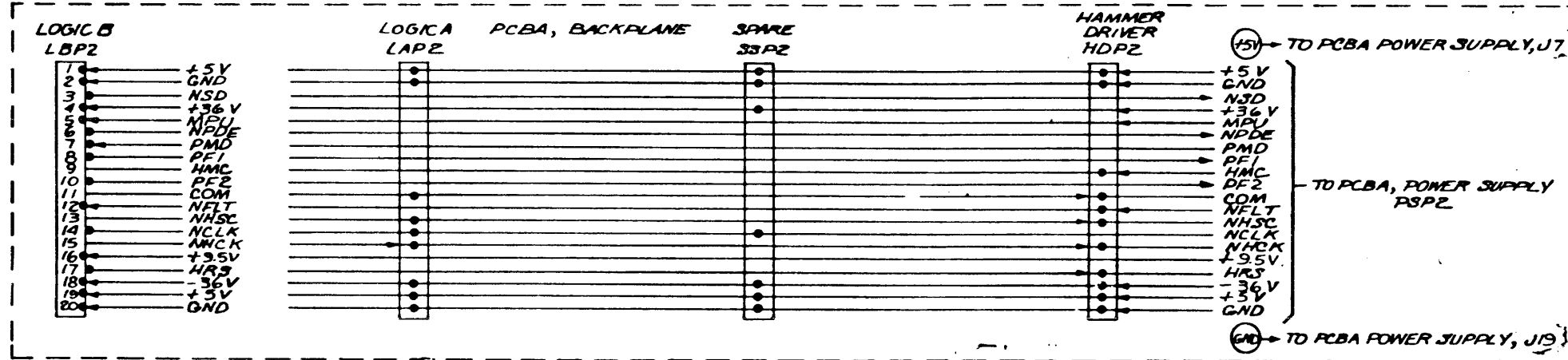
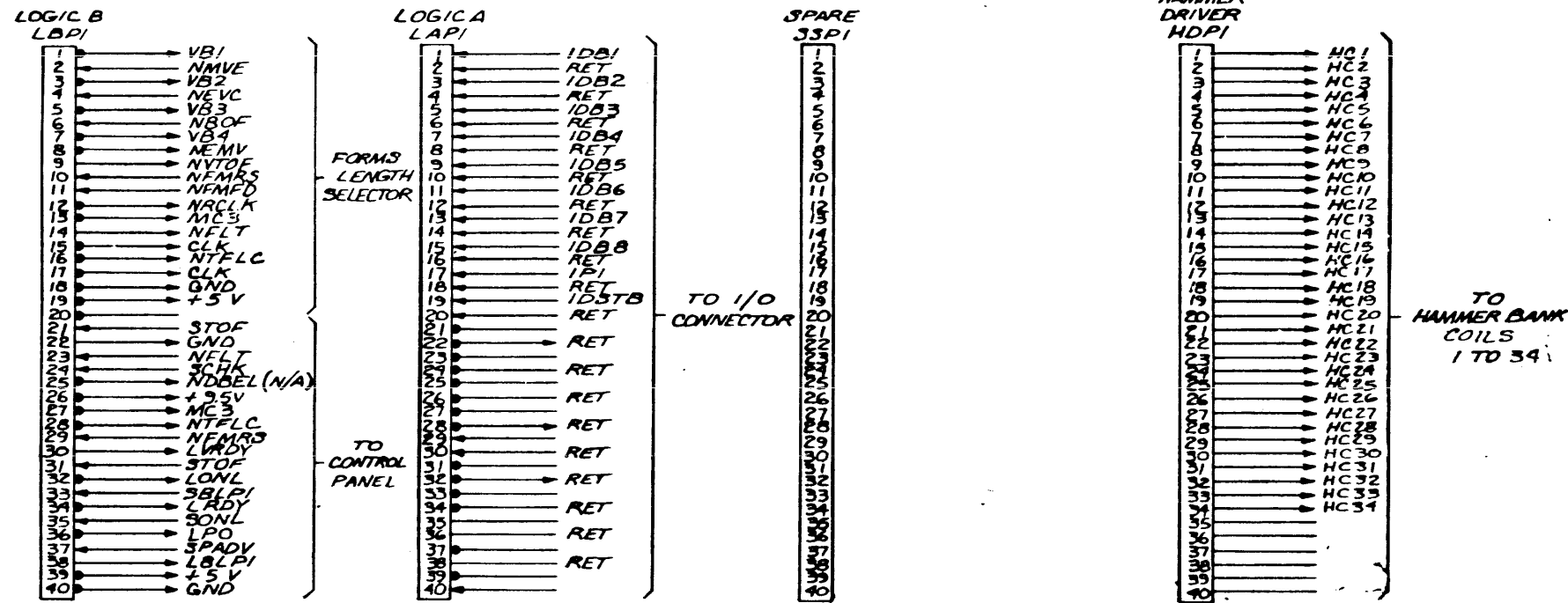


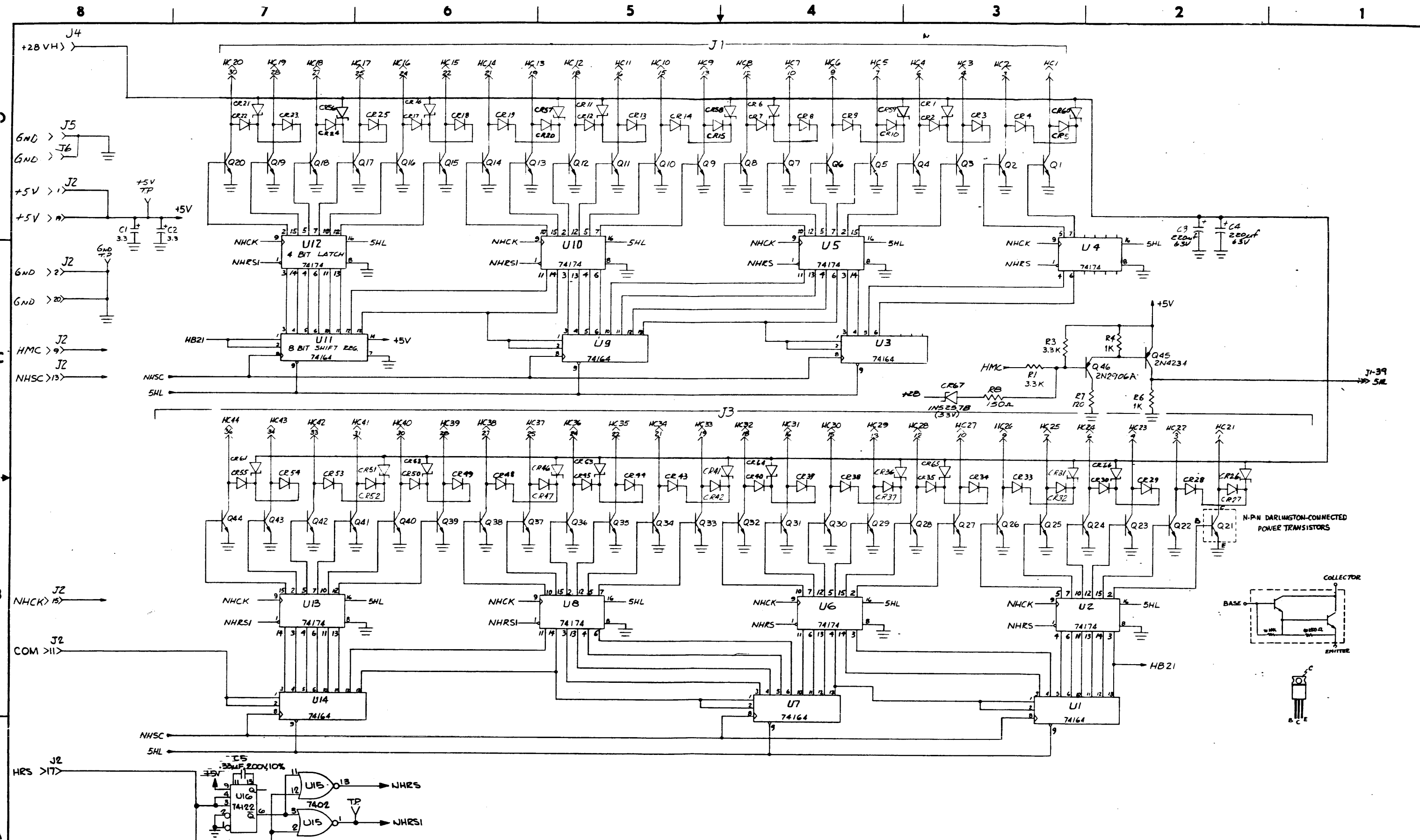
⚠ DRAWING SHOWS 200V SERIES HOOK-UP  
DASH LINE INDICATES 100V SERIES HOOK-UP.

NOTES: UNLESS OTHERWISE SPECIFIED

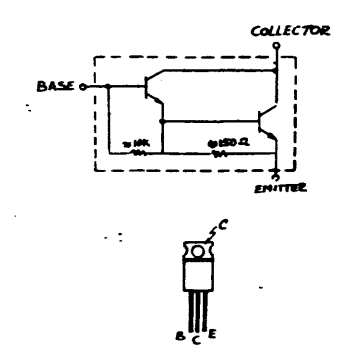
<b>PRINTRONIX</b>	
INTERCONNECT	
DIAGRAM P600 VDE	
DRAWING NO.	PAGE NO.
985835	1 of 2

8      7      6      5      4      3      2      1



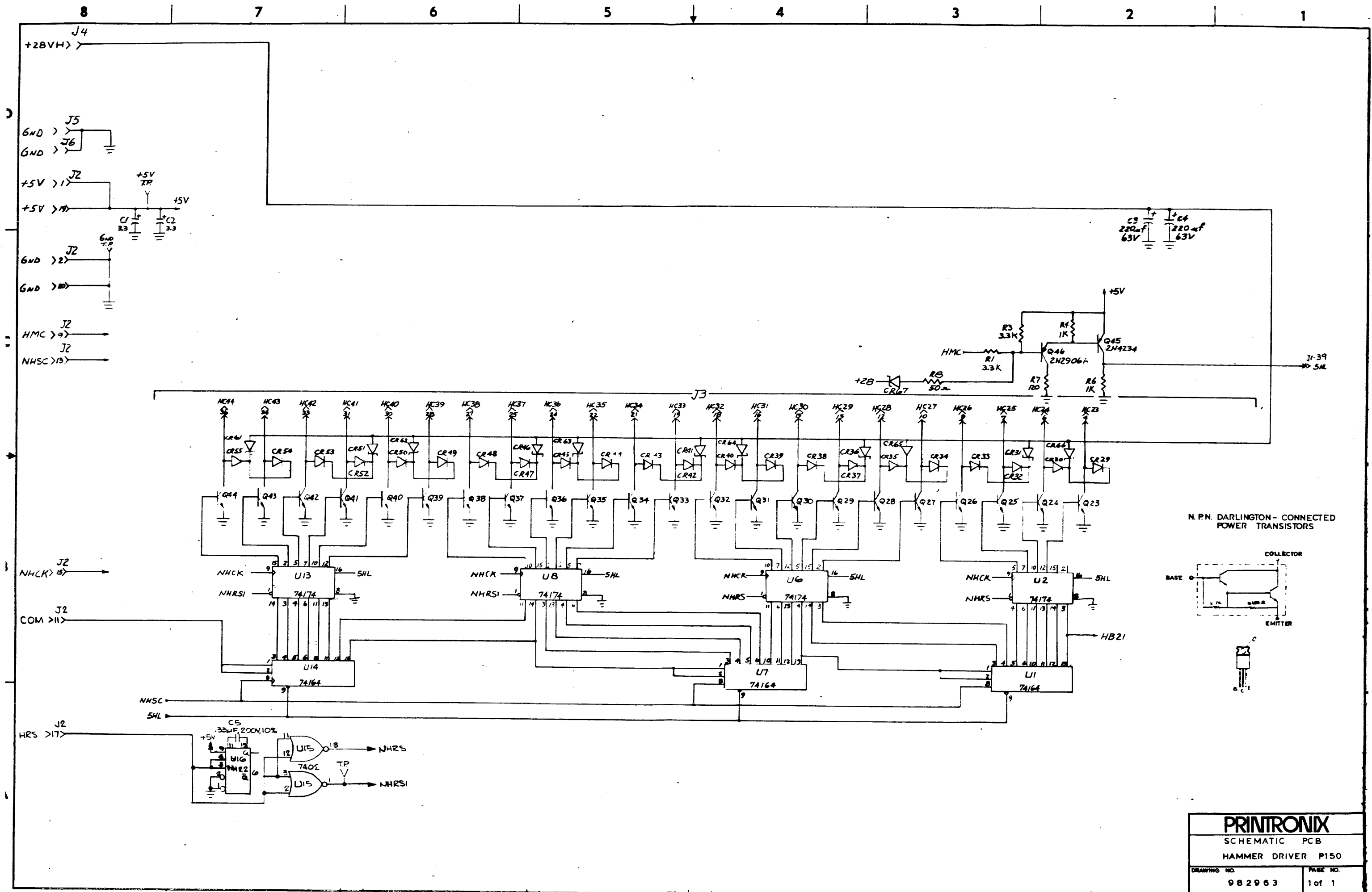


N-P-N DARLINGTON-CONNECTED POWER TRANSISTORS

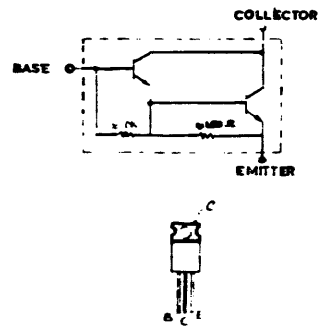


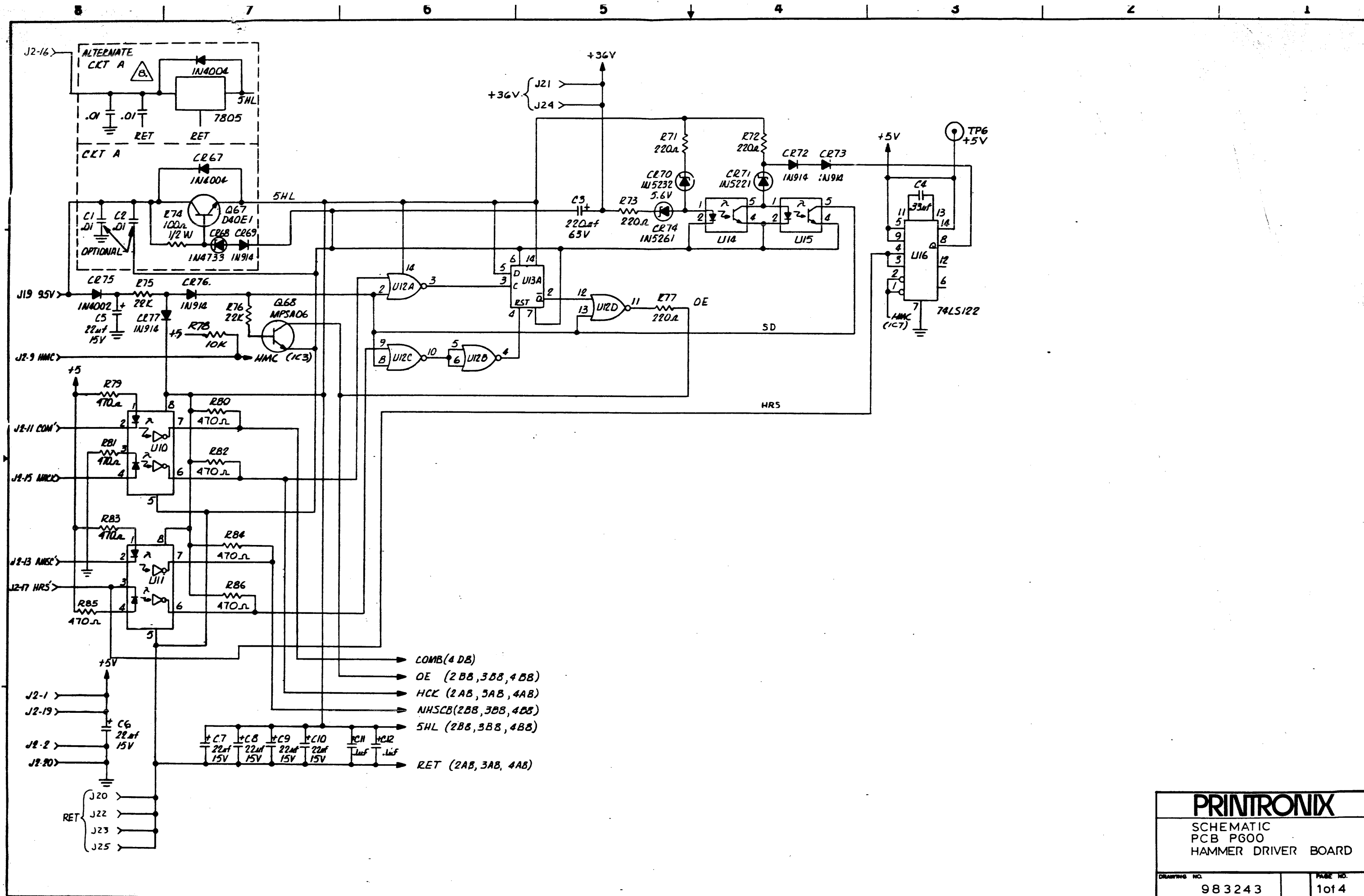
CONNECTORS	
J1	HAMMER BANK
J2	POWER SUPPLY
J3	HAMMER BANK
J4	+28V
J5	GND
J6	GND

<b>PRINTRONIX</b>	
SCHEMATIC HAMMER DRIVER	
DRAWING NO. 981203	PAGE NO. 1 of 1

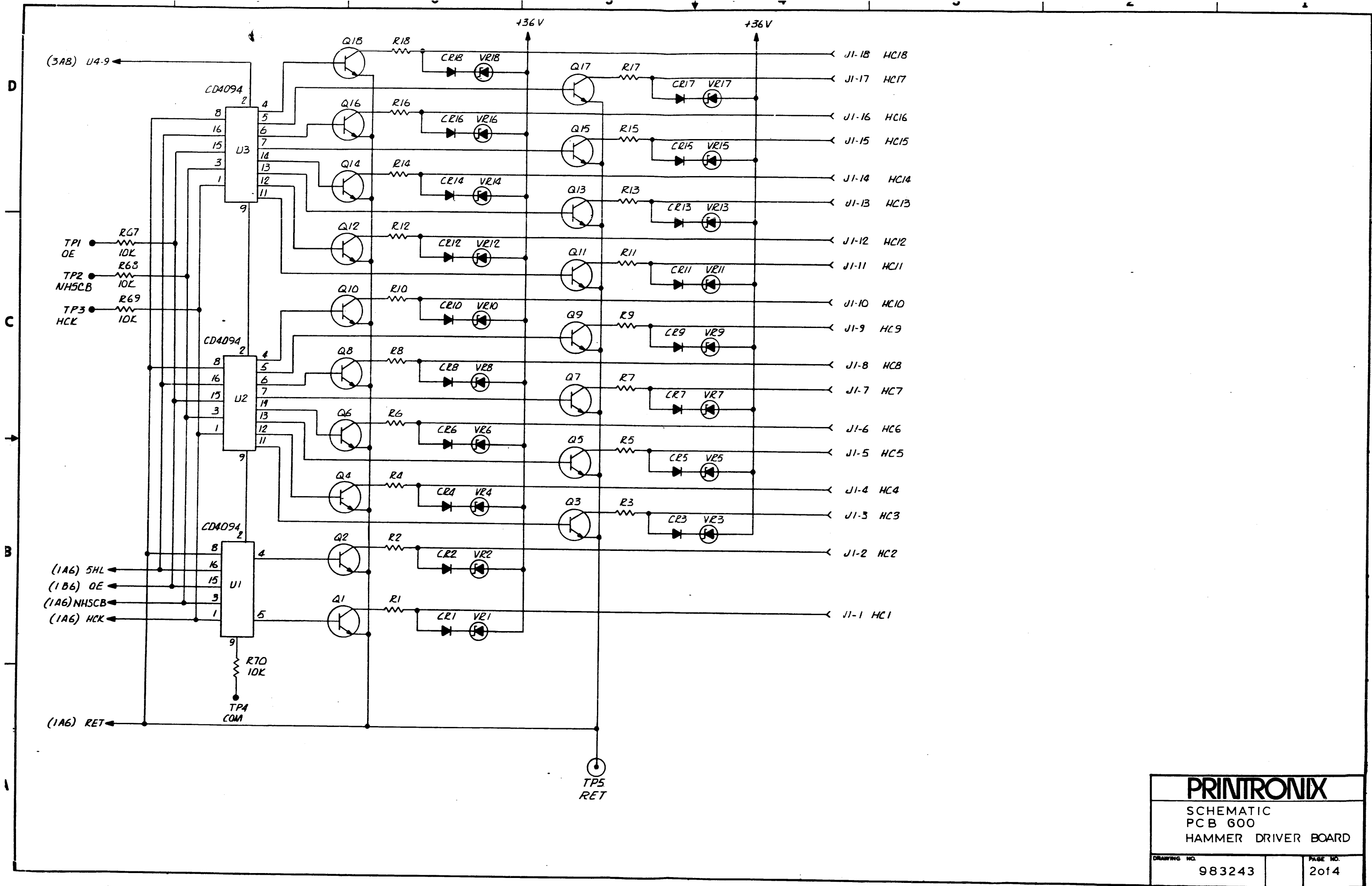


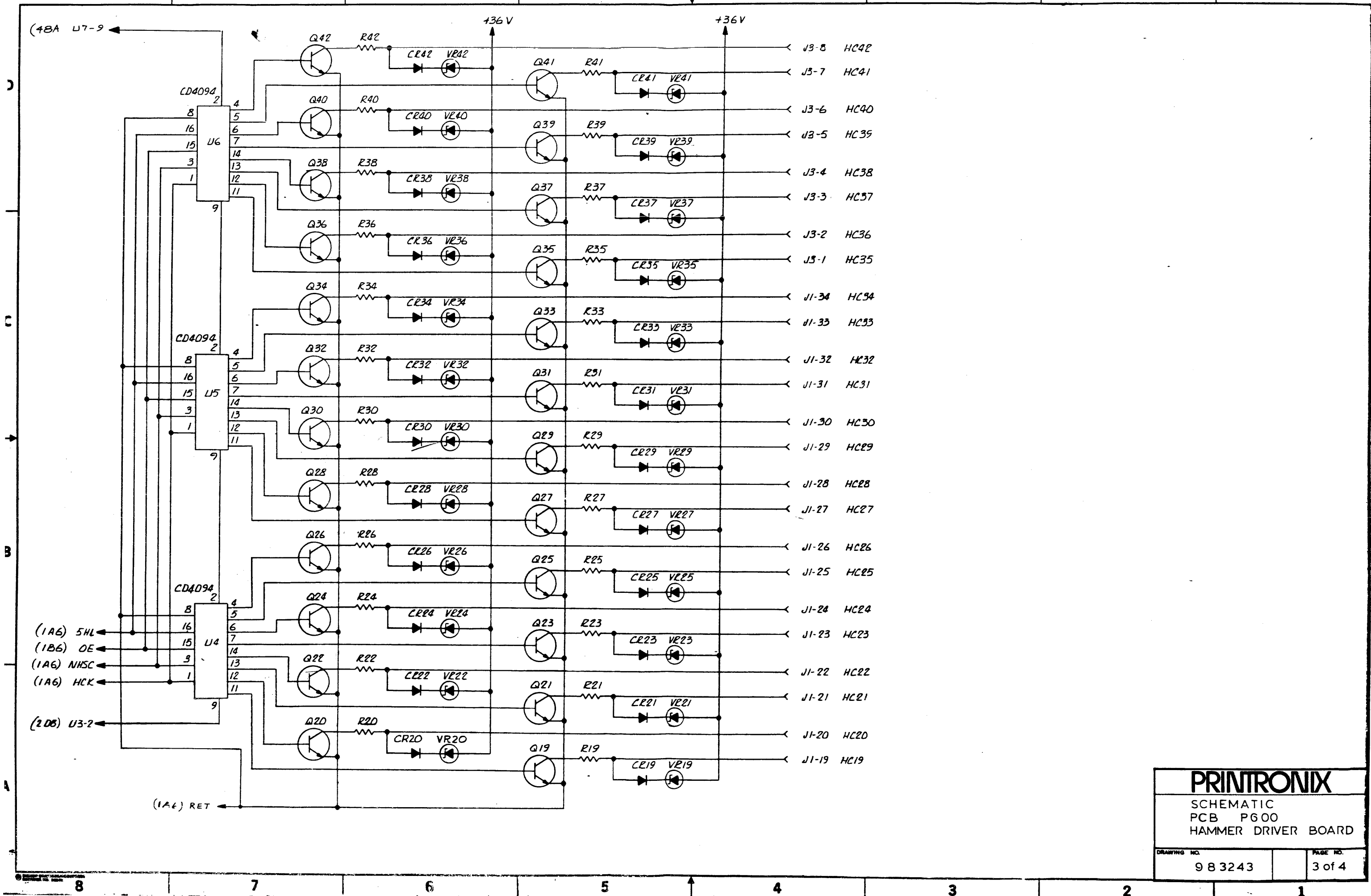
N. P.N. DARLINGTON - CONNECTED  
POWER TRANSISTORS



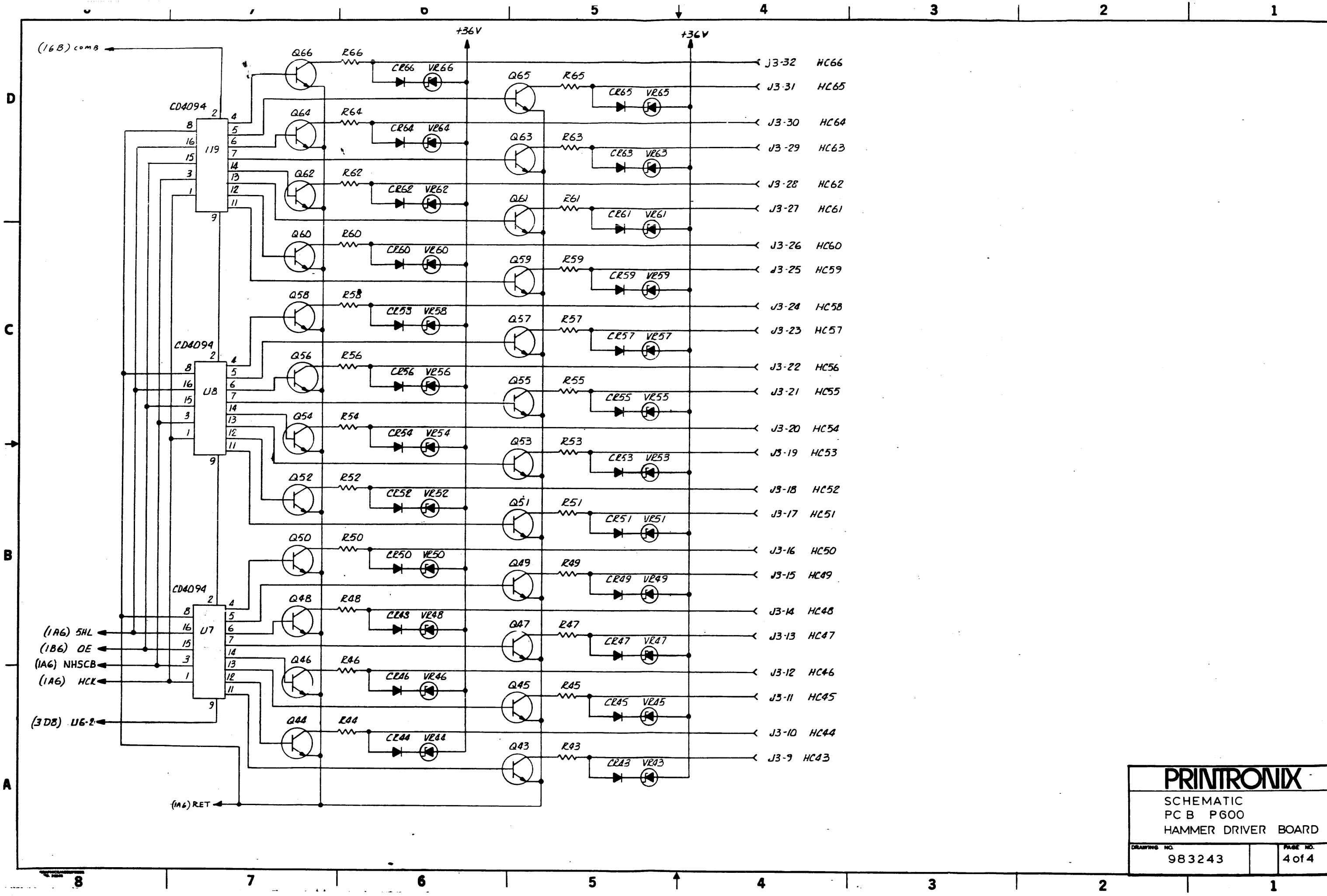




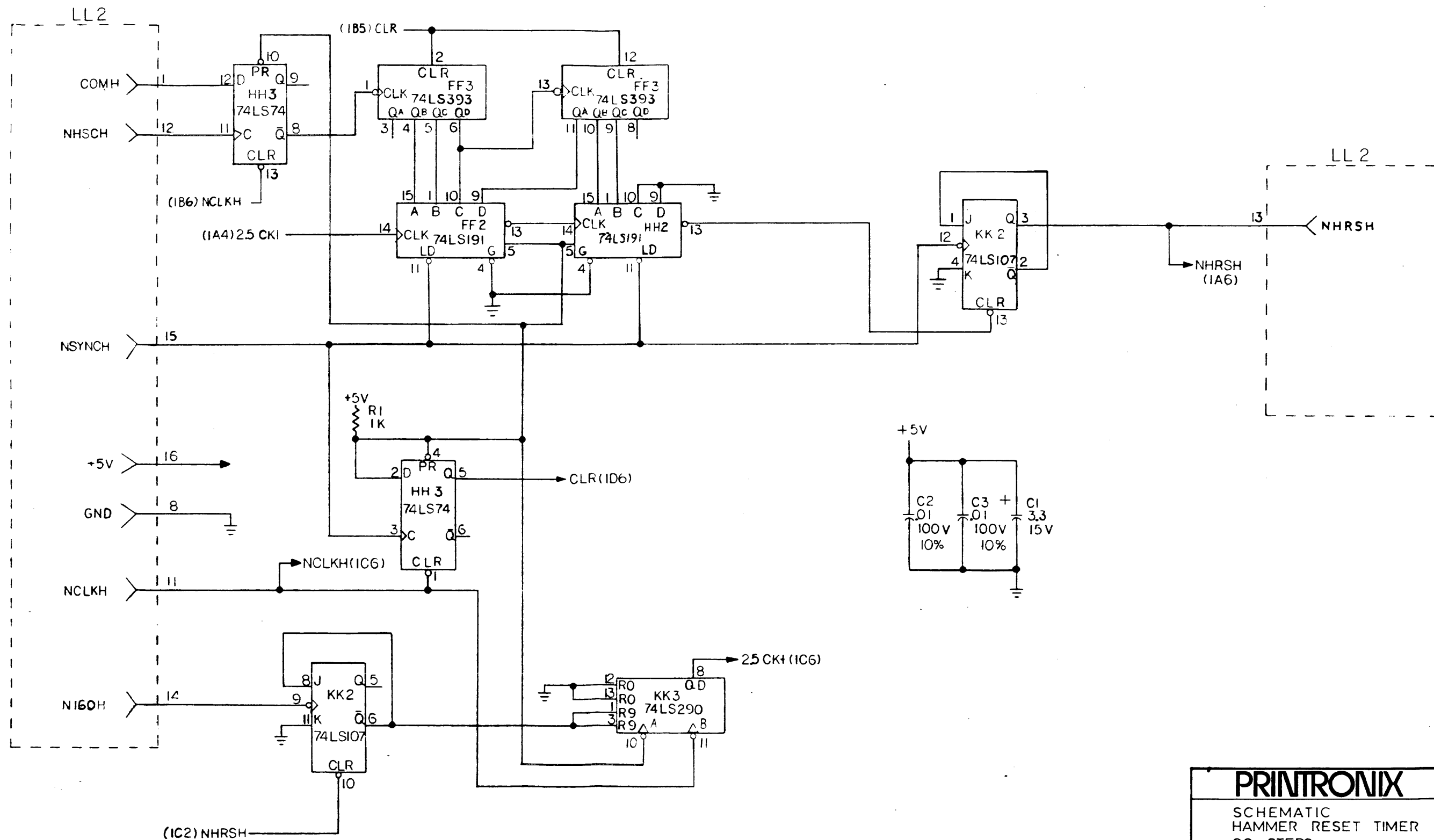


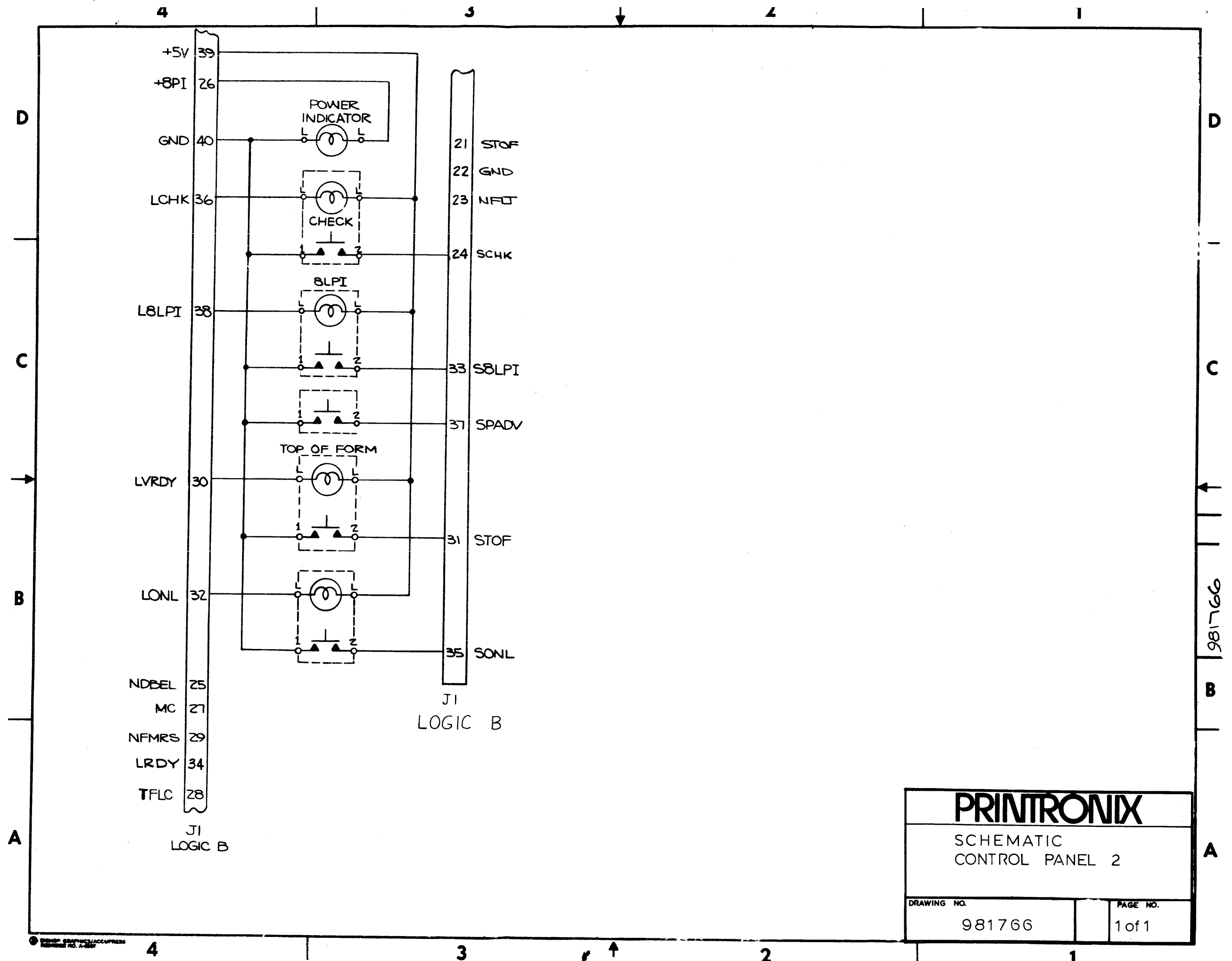


<b>PRINTRONIX</b>	
SCHEMATIC PCB P600 HAMMER DRIVER BOARD	
DRAWING NO.	PAGE NO.
983243	3 of 4



<b>PRINTRONIX</b>	
SCHEMATIC PC B P600 HAMMER DRIVER BOARD	
DRAWING NO.	PAGE NO.
983243	4 of 4

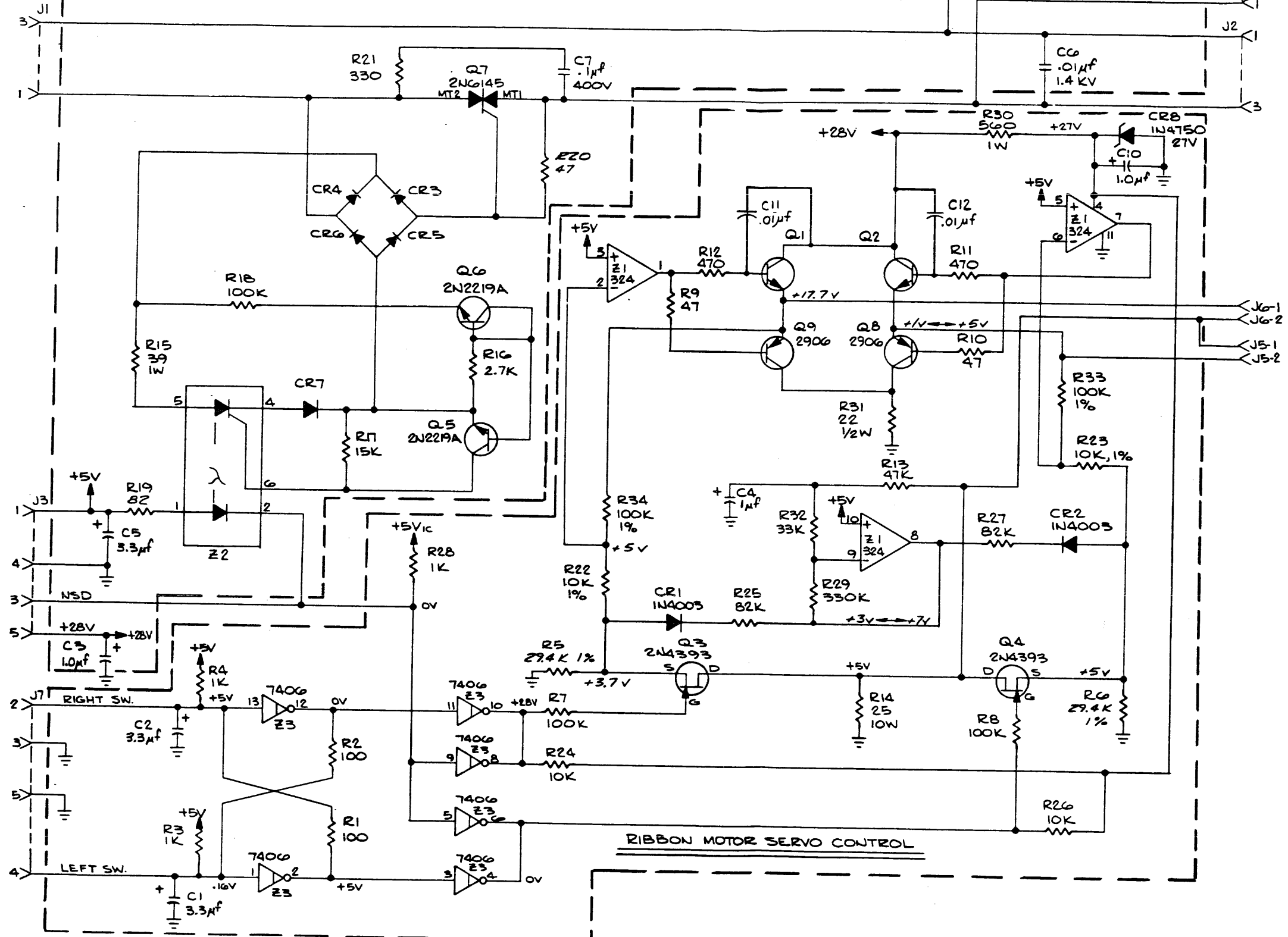




<b>PRINTRONIX</b>	
SCHEMATIC CONTROL PANEL 2	
DRAWING NO.	PAGE NO.
981766	1 of 1

SHUTTLE DRIVE & BLOWER CONTROL

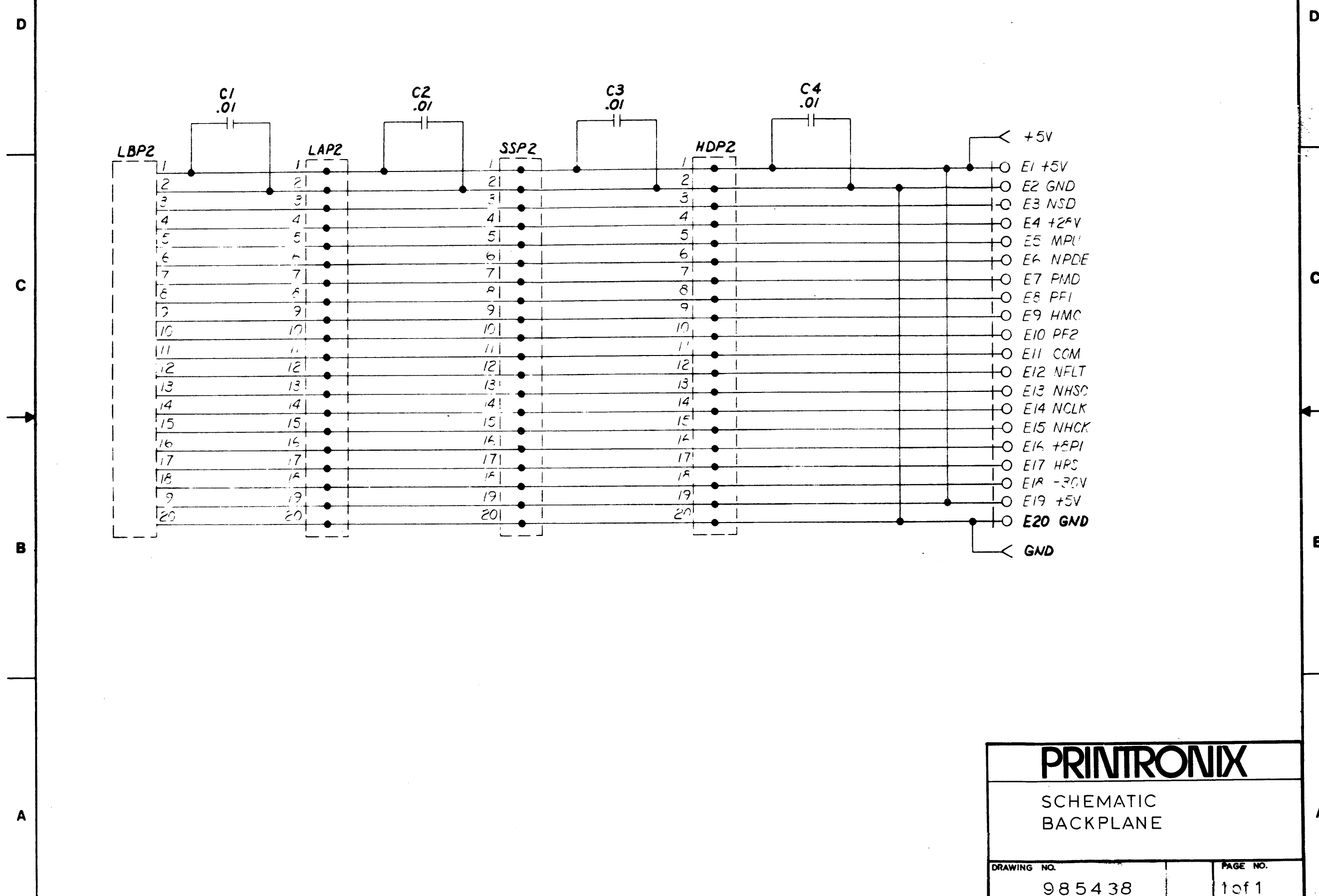
RIBBON MOTOR SERVO CONTROL



CONNECTORS	
J1	AC POWER IN
J2	TO SHUTTLE MOTOR
J3	TO POWER SUPPLY
J4	TO HAMMER BANK BLOWER
J5	LEFT MOTOR
J6	RIGHT MOTOR
J7	LIMIT SWITCHES

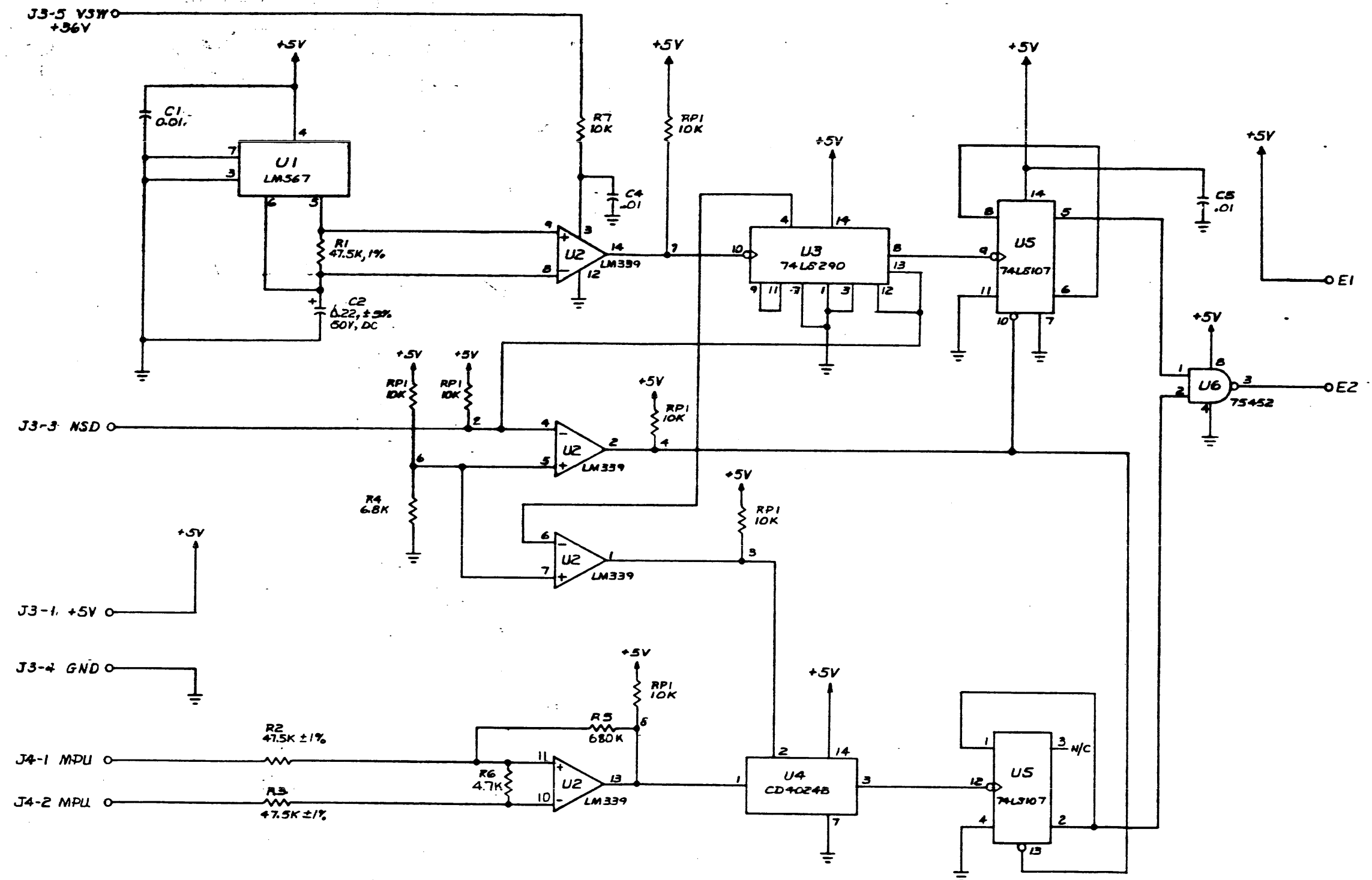
<b>PRINTRONIX</b>	
SCHEMATIC PCB RIBBON CONTROL	
DRAWING NO. 982143	PAGE NO. 1 of 1





<b>PRINTRONIX</b>	
SCHEMATIC BACKPLANE	
DRAWING NO.	PAGE NO.
985438	1 of 1

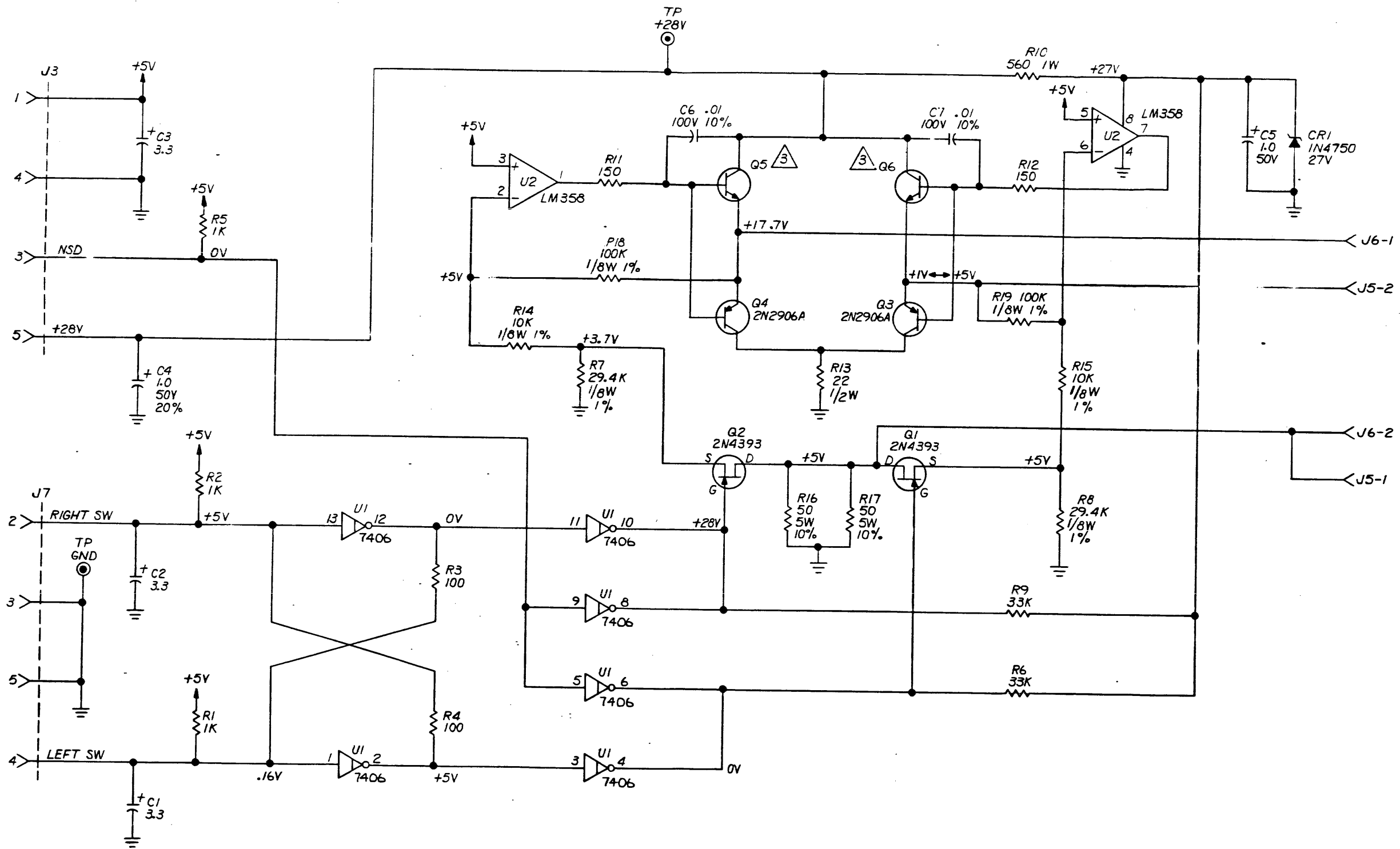




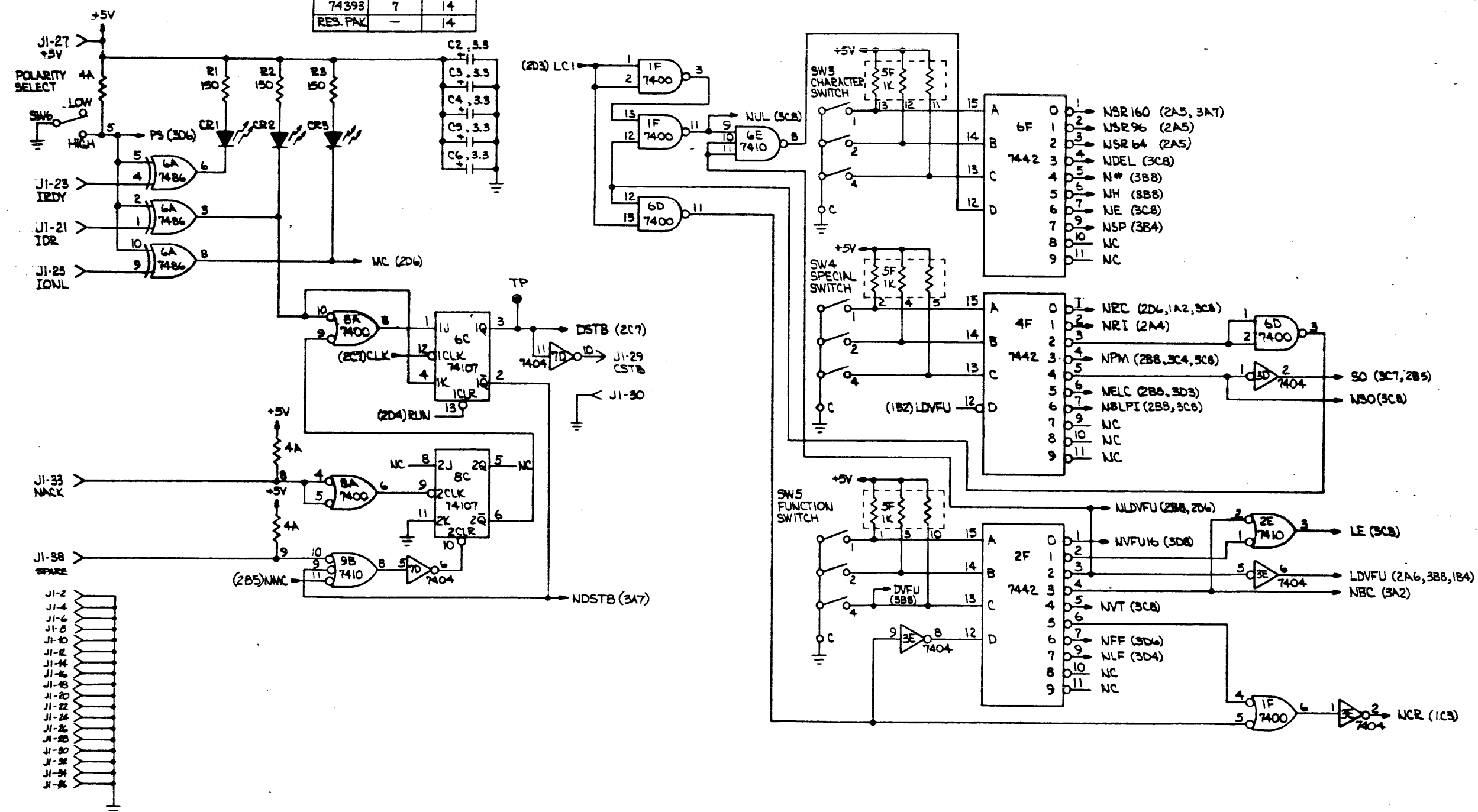
CONNECTORS

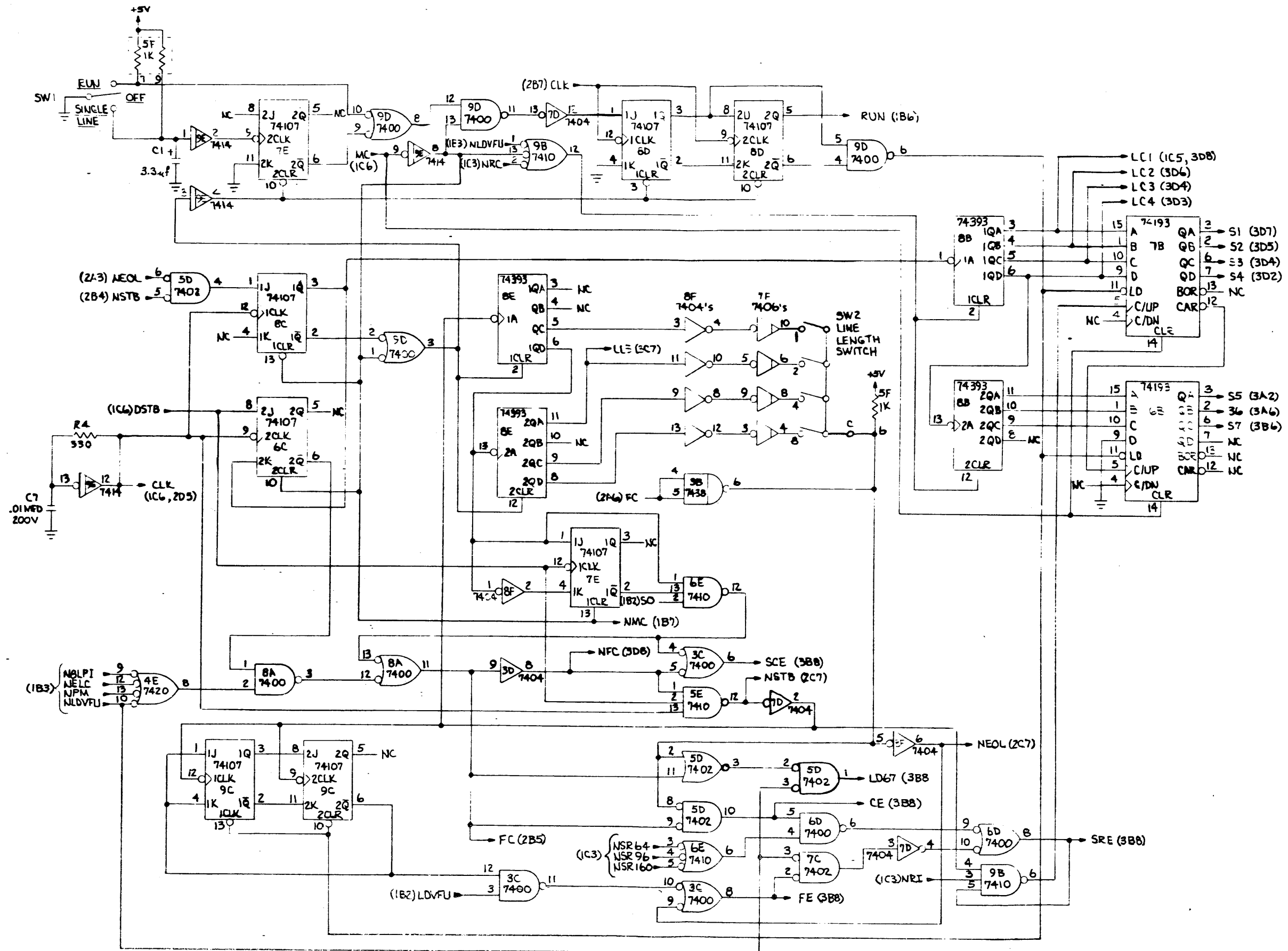
J3-1	+5V
J3-4	GND
J3-5	+36V

<b>PRINTRONIX</b>	
SCHEMATIC MOTOR RELAY TRIGGER P600	
DRAWING NO. 984926	PAGE NO. 1 of 1



SPARE CIRCUITS			I.C. GROUND AND VOLTAGE INFO.		
TYPE	LOCATION	OUTPUT PIN	TYPE	GND	+5V
7402	7C	4,10,13	7402	7	14
7406	7F	2,12	7404	7	14
7414	9E	6,10	7410	7	14
7486	3A	8,11	7414	7	14
7486	6A	11	7420	7	14
RES.PAK	4A	10	7438	7	14
RES.PAK	5F	8	7442	8	16
			7486	7	14
			74107	7	14
			74193	8	16
			74393	7	14
			RES.PAK	-	14





8

7

6

5

4

3

2

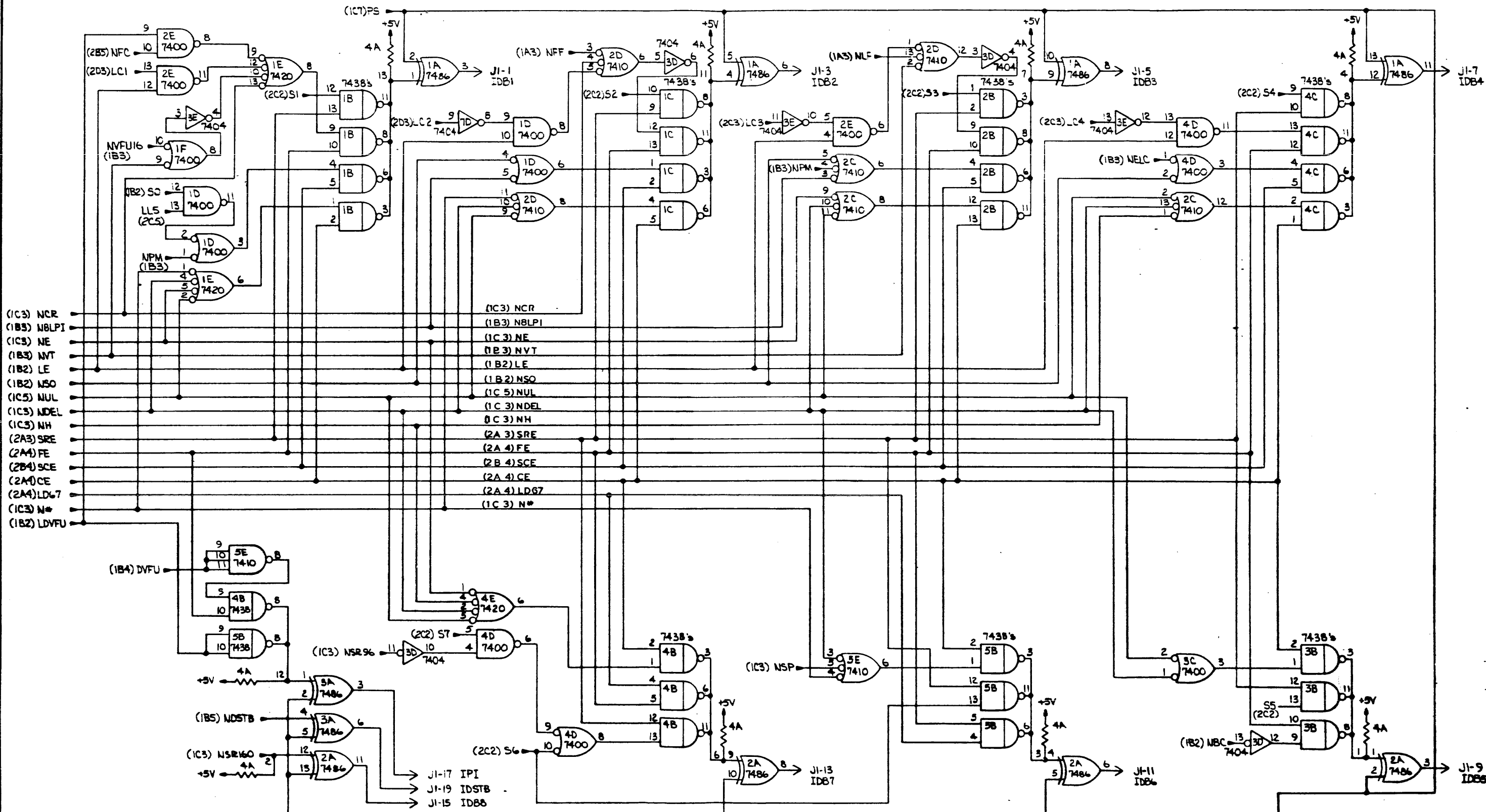
1

D

C

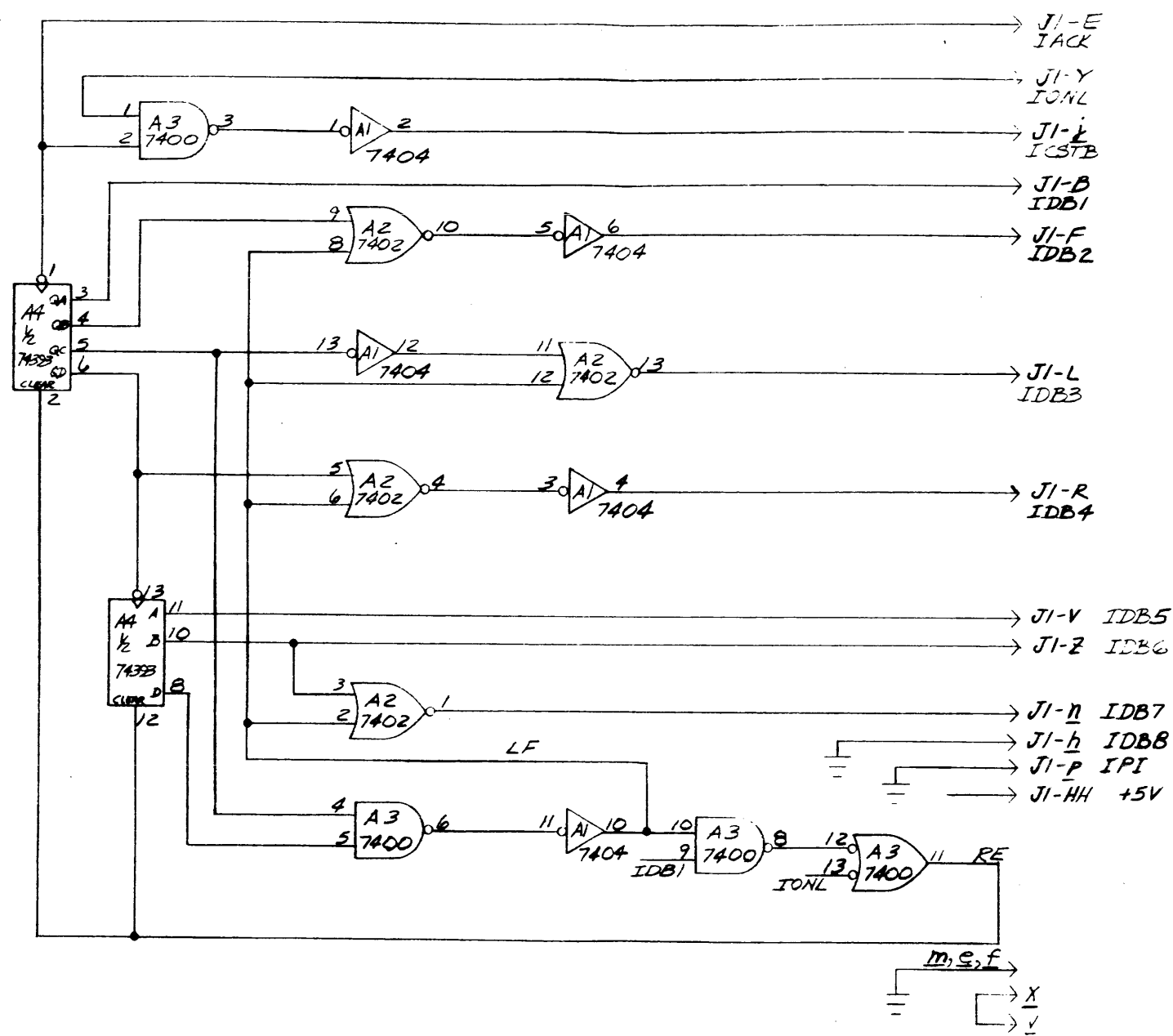
B

A



D  
C  
B  
A

D  
C  
B  
A



2673

**PRINTRONIX**

SCHEMATIC PCB  
MINI EXERCISER  
DATA PRODUCTS

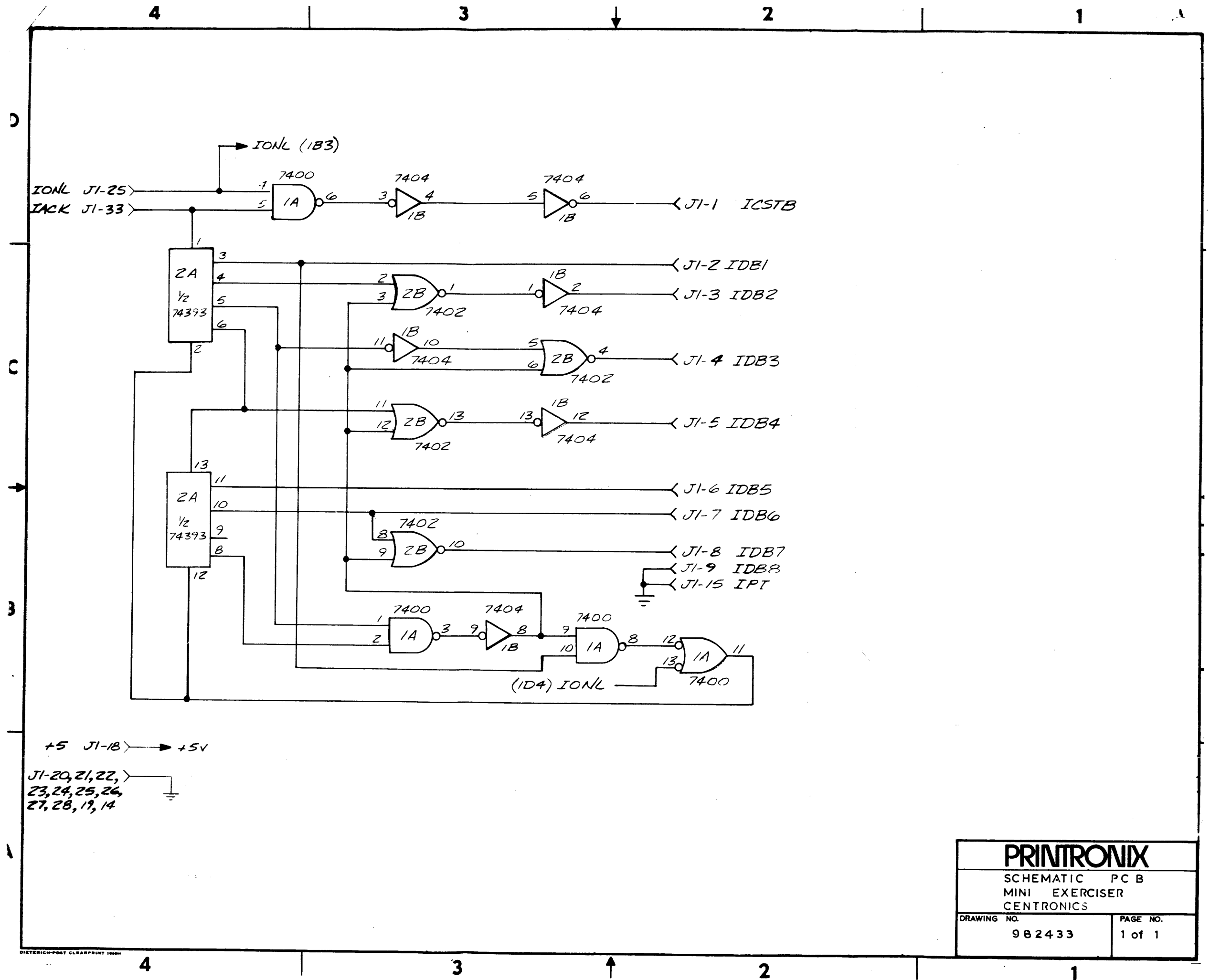
DRAWING NO.	PAGE NO.
9 8 2 6 7 3	1 of 1

4

3

2

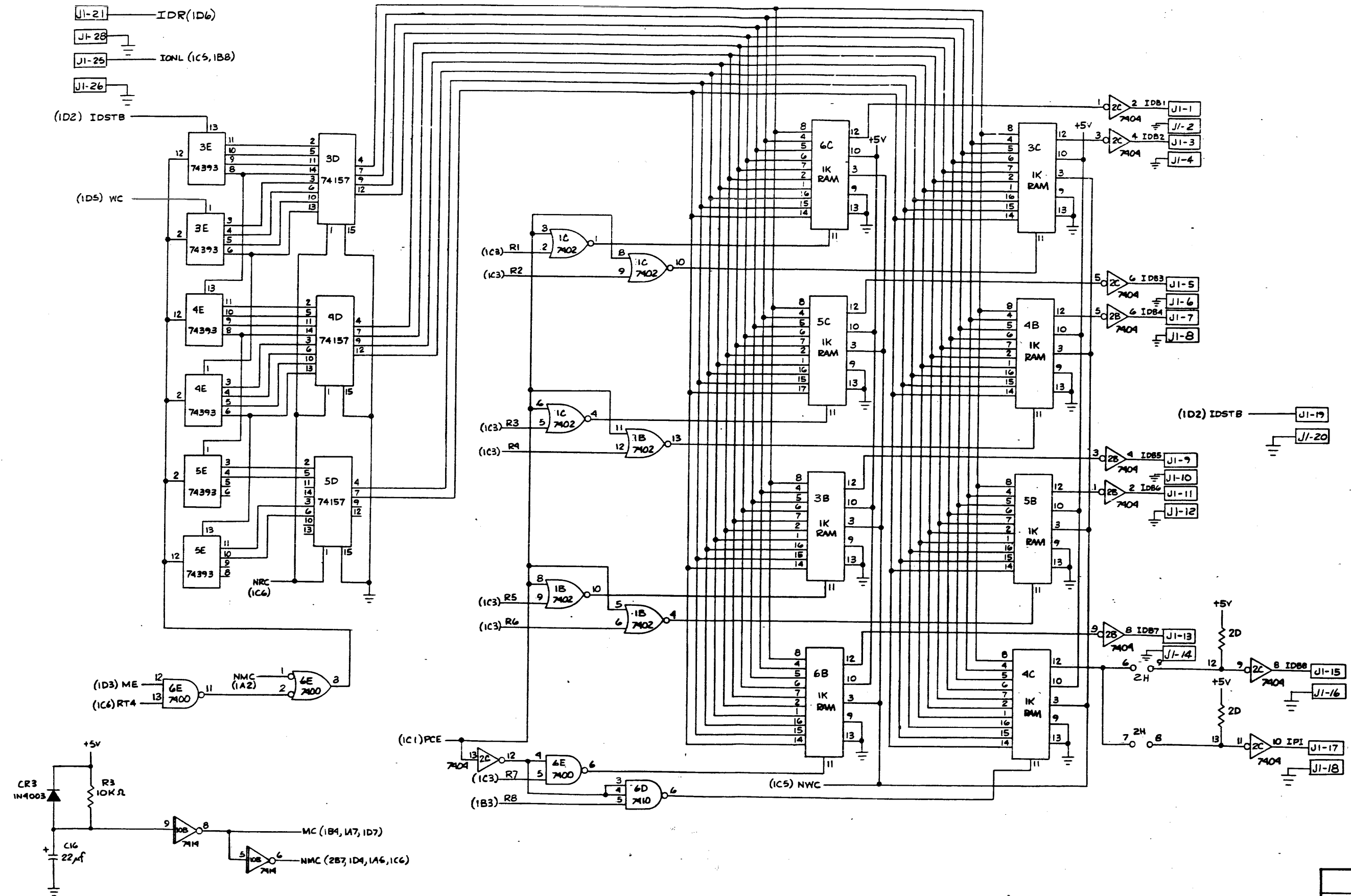
1



<b>PRINTRONIX</b>	
SCHEMATIC PCB MINI EXERCISER CENTRONICS	
DRAWING NO. 982433	PAGE NO. 1 of 1



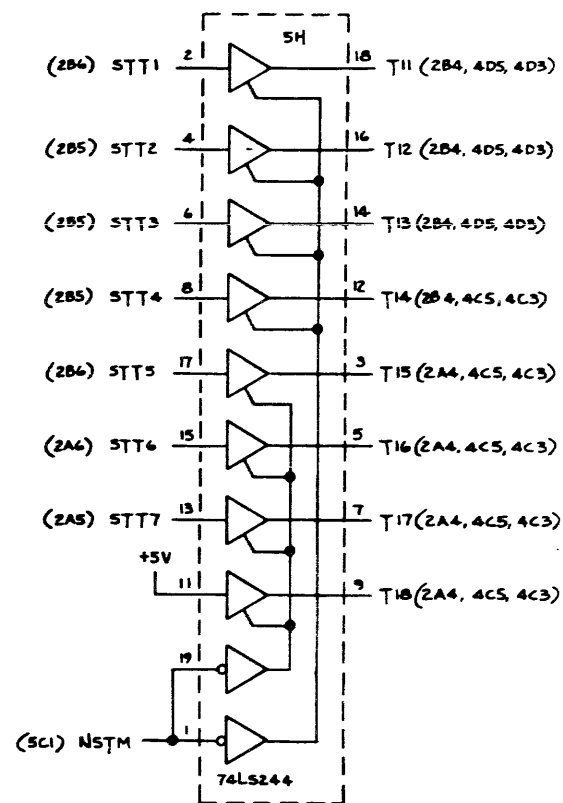




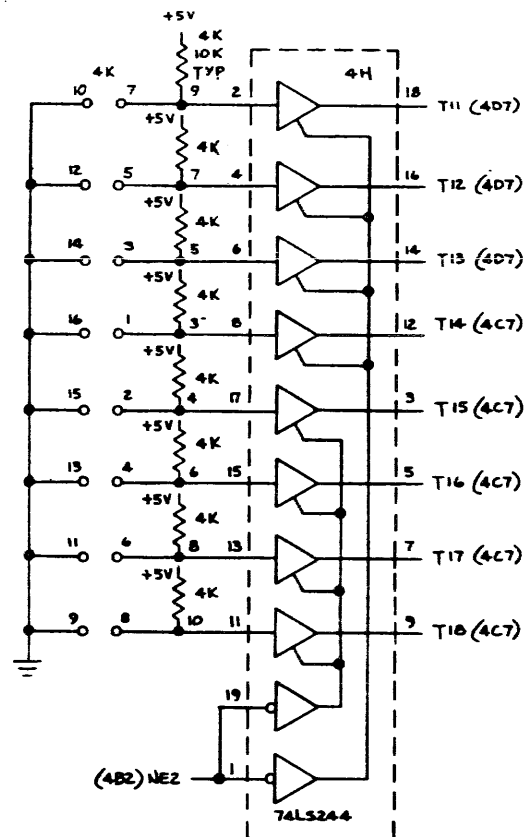




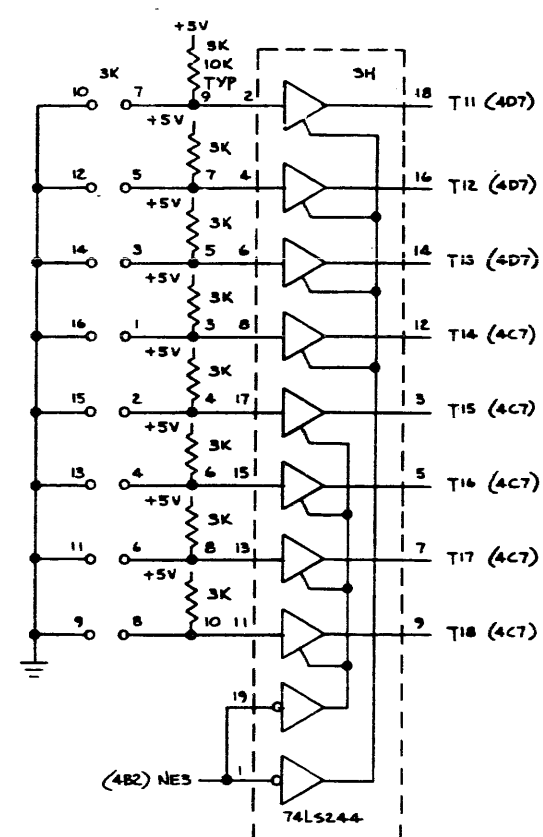
CHARACTER SWITCH  
SELF TEST



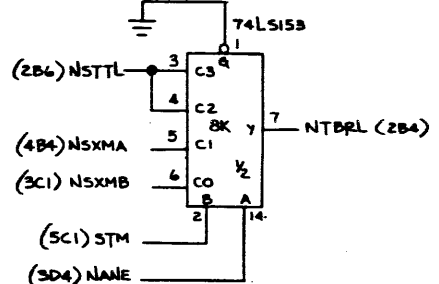
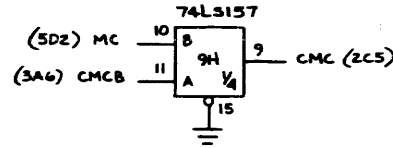
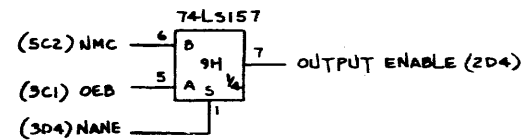
CHARACTER SWITCH  
FIRST CHARACTER (X-ON, ACK)



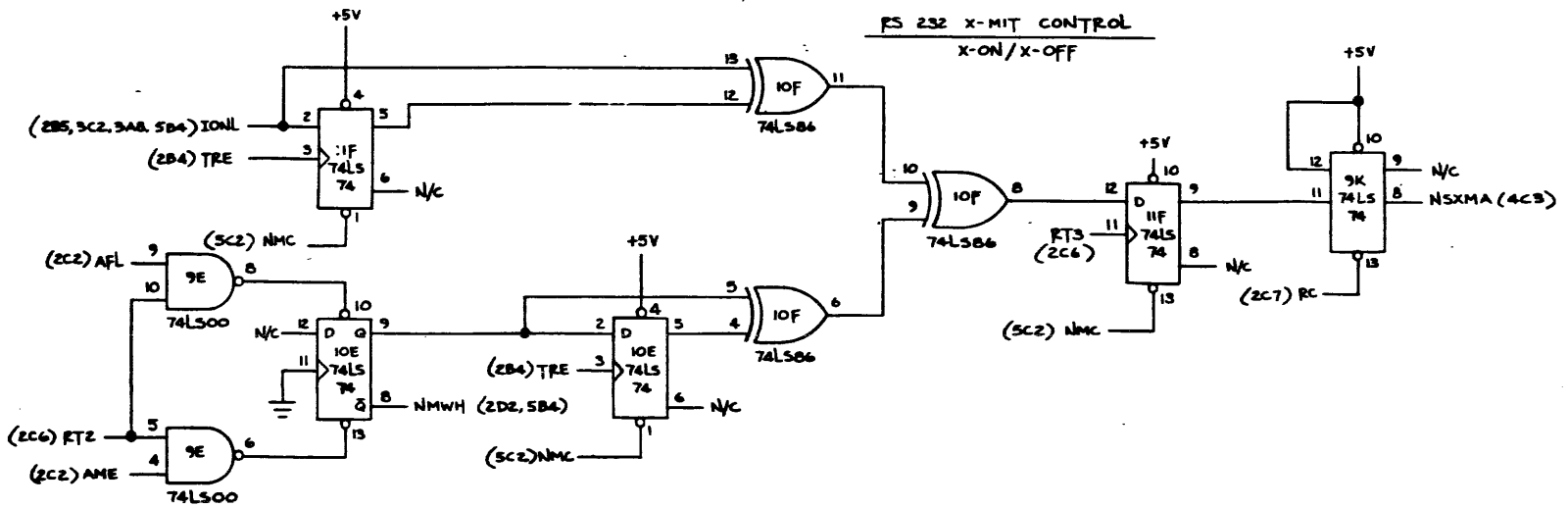
CHARACTER SWITCH  
SECOND CHARACTER (X OFF, NACK)



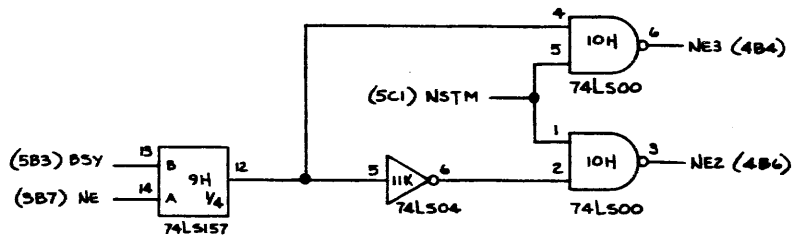
MX CONTROL LINES



RS 232 X-MIT CONTROL  
X-ON/X-OFF

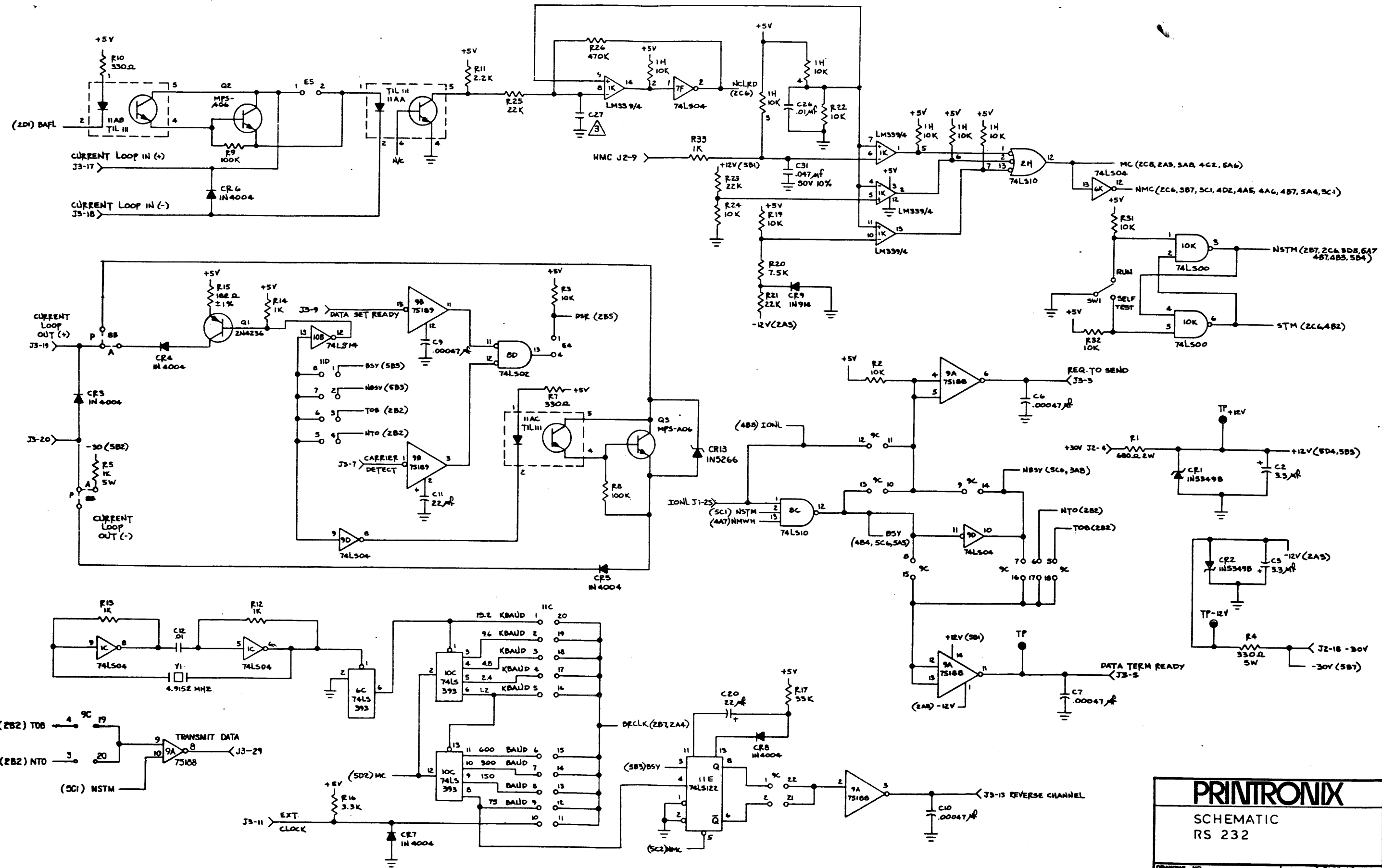


CHARACTER SELECT CONTROL

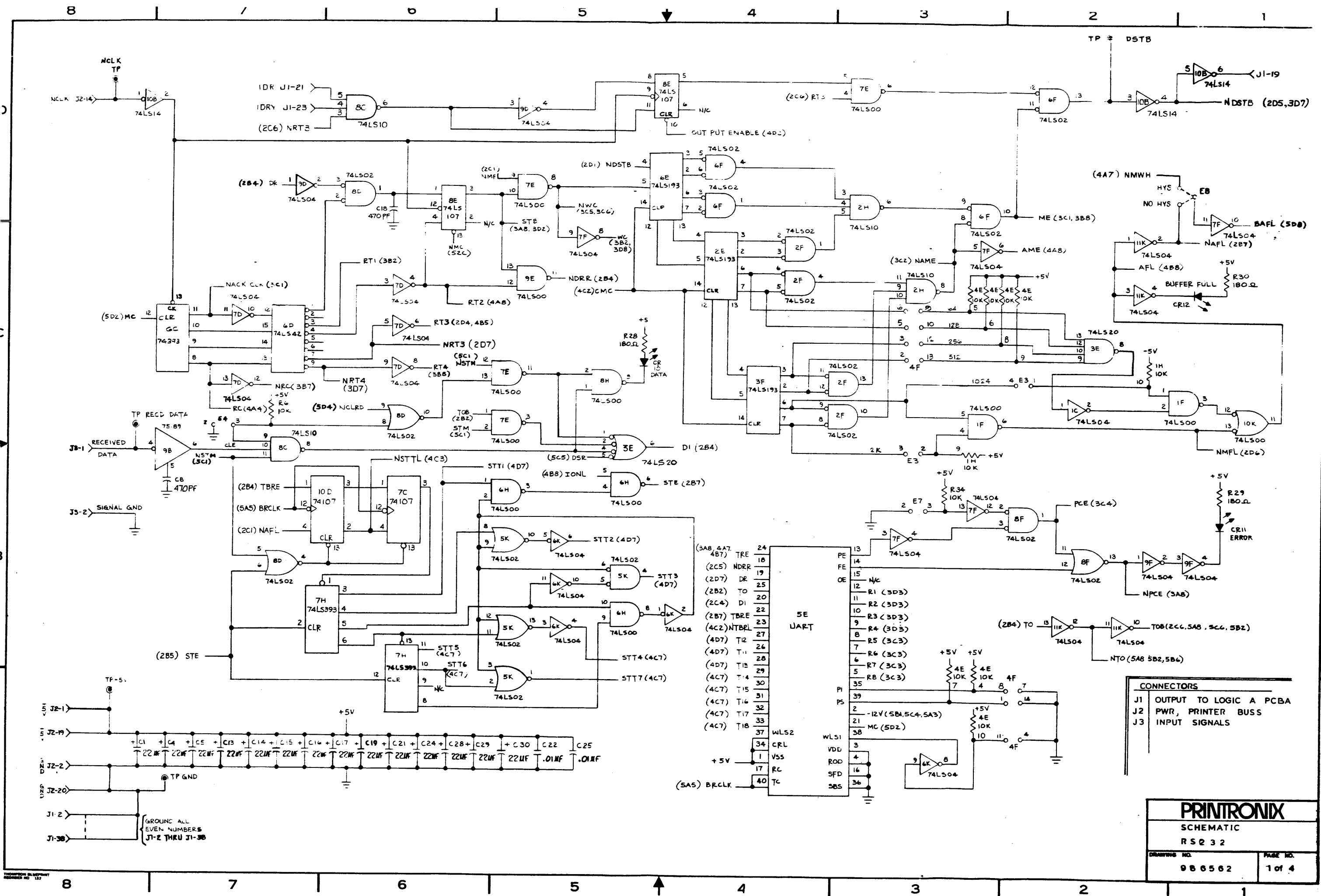


<b>PRINTRONIX</b>	
SCHEMATIC RS 232	
DRAWING NO.	PAGE NO.
984379	3 of 4

D  
C  
B  
A  
8 7 6 5 4 3 2 1

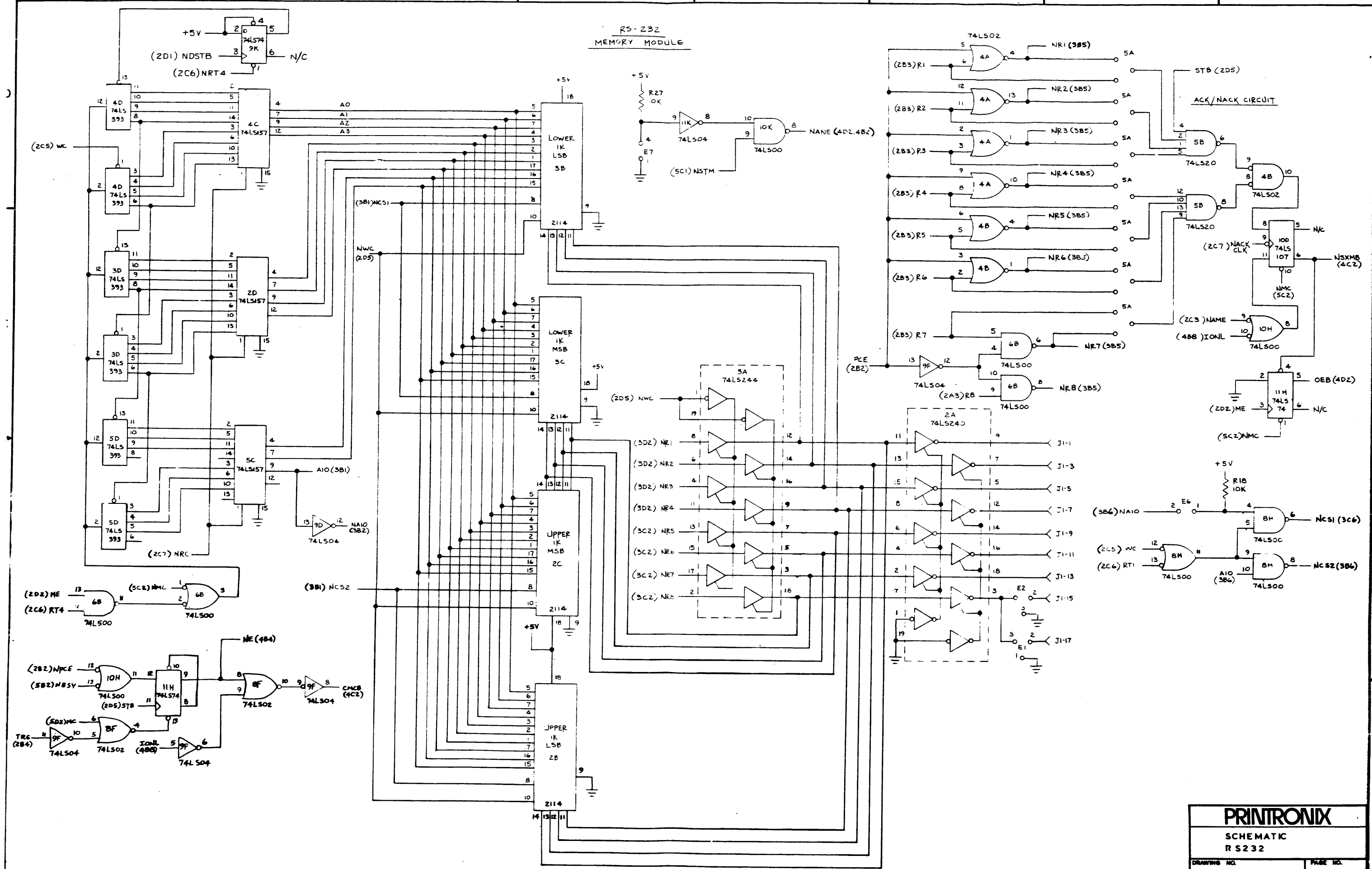


**PRINTRONIX**  
SCHEMATIC  
RS 232  
DRAWING NO. 984379 PAGE NO. 4 of 4

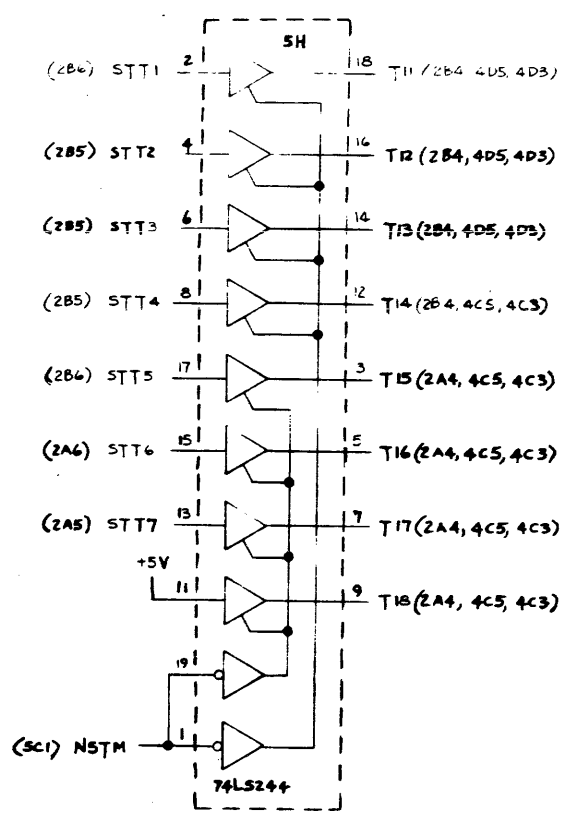


- CONNECTORS**
- J1 OUTPUT TO LOGIC A PCBA
  - J2 PWR, PRINTER BUSS
  - J3 INPUT SIGNALS

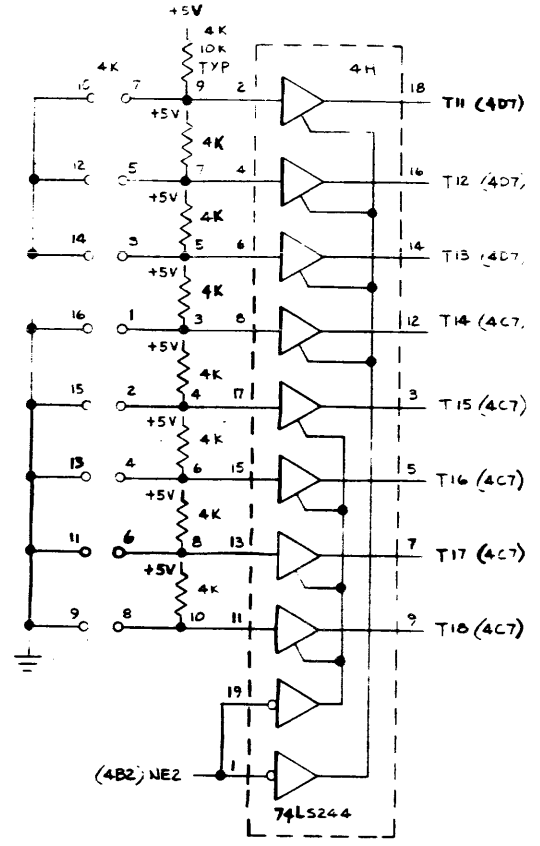
R5-232  
MEMORY MODULE



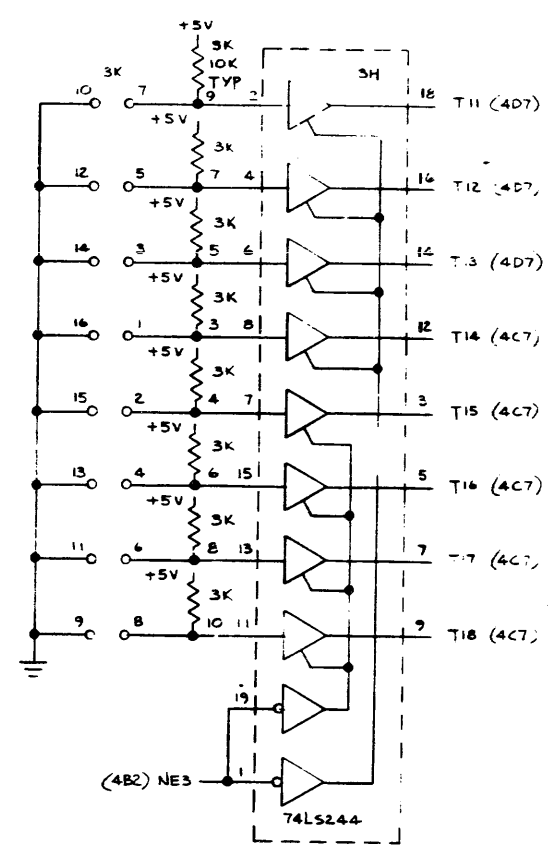
CHARACTER SWITCH  
SELF TEST



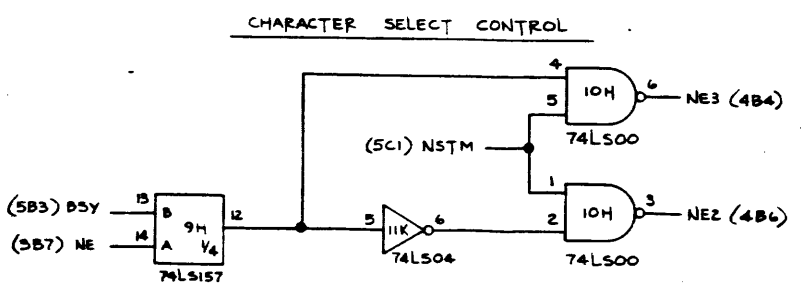
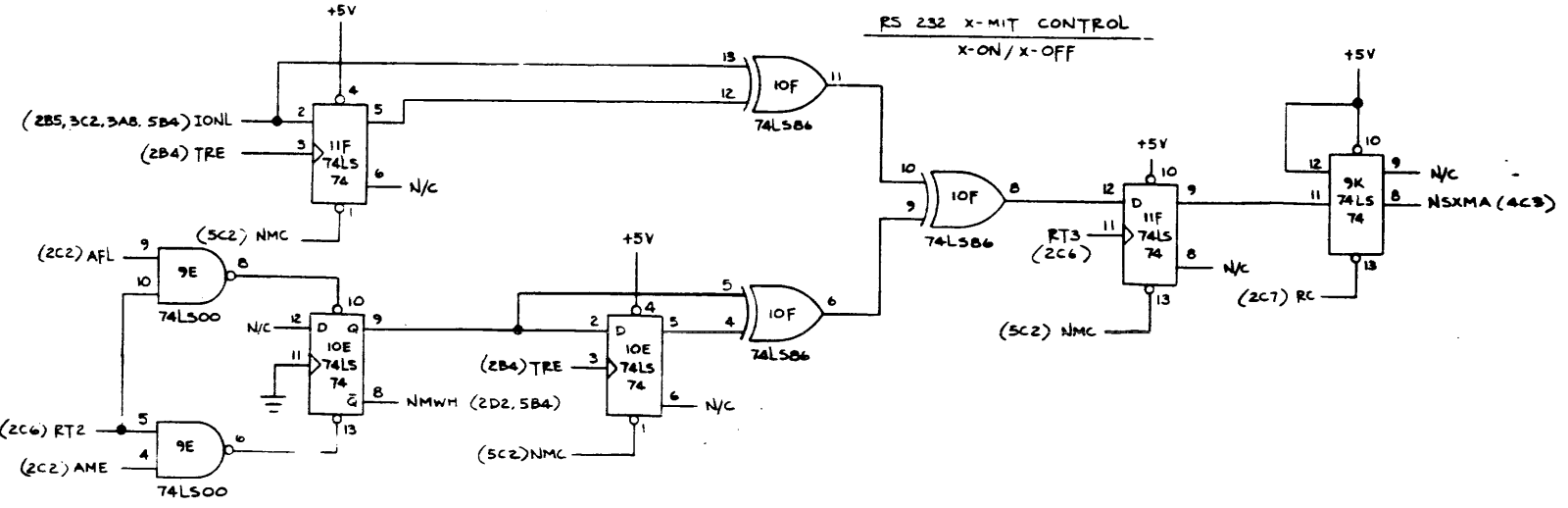
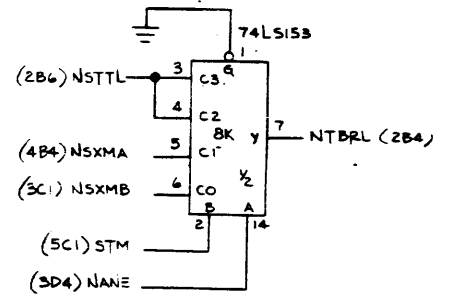
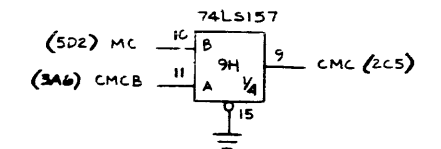
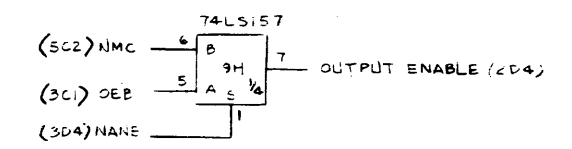
CHARACTER SWITCH  
FIRST CHARACTER (X-ON ACK)



CHARACTER SWITCH  
SECOND CHARACTER (X-OFF NACK)



MY CONTROL LINES



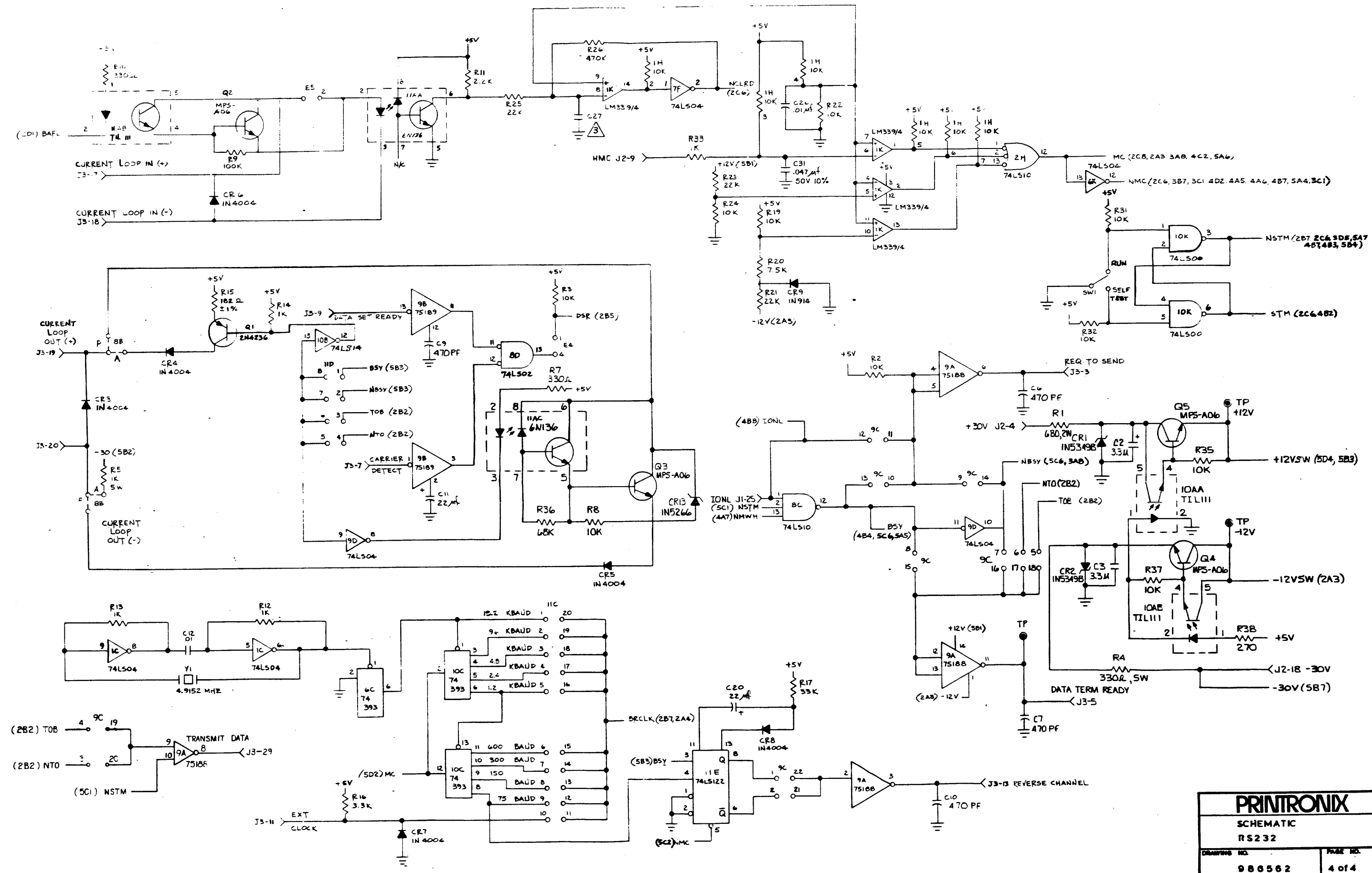


D

C

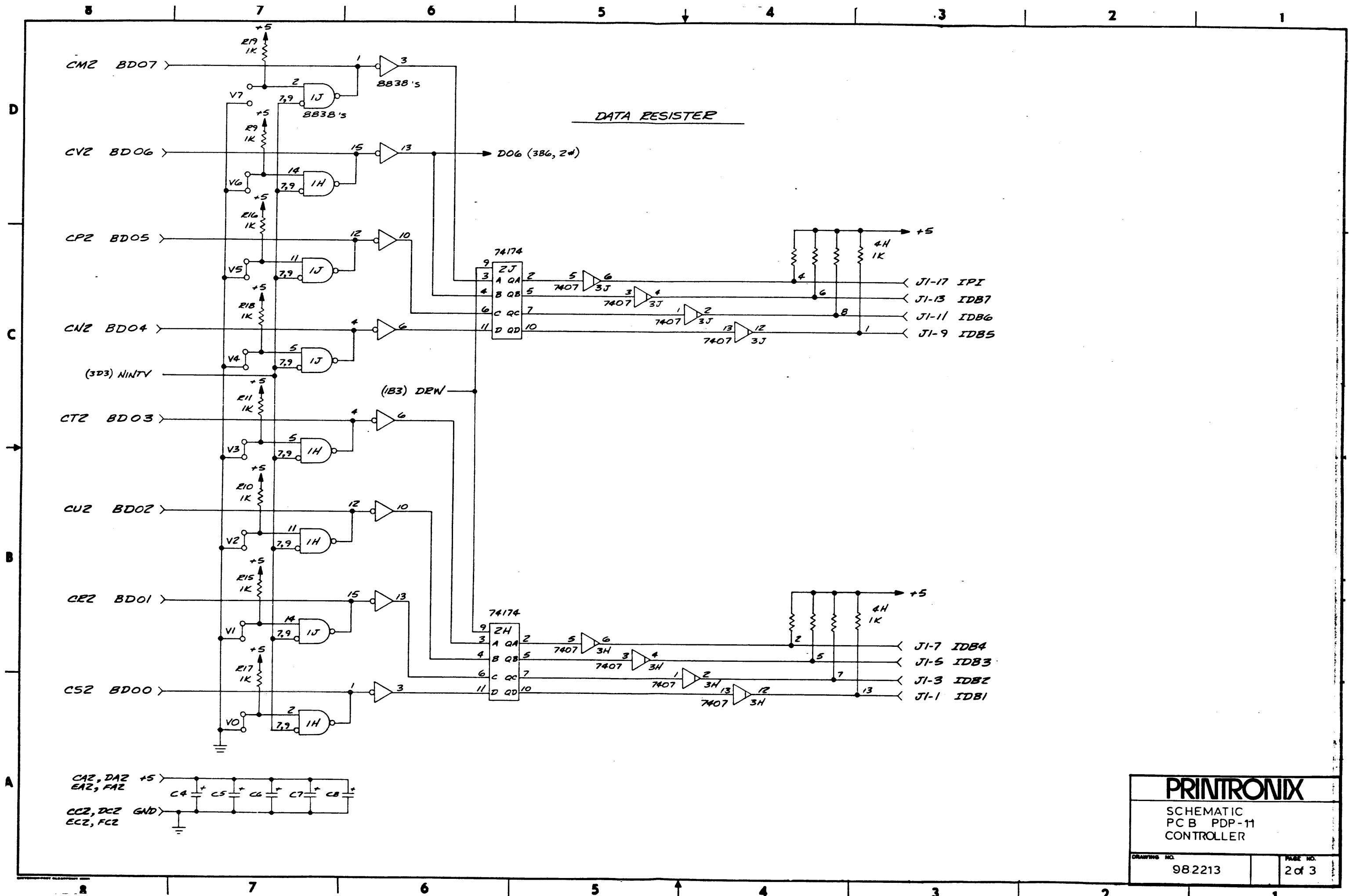
B

A



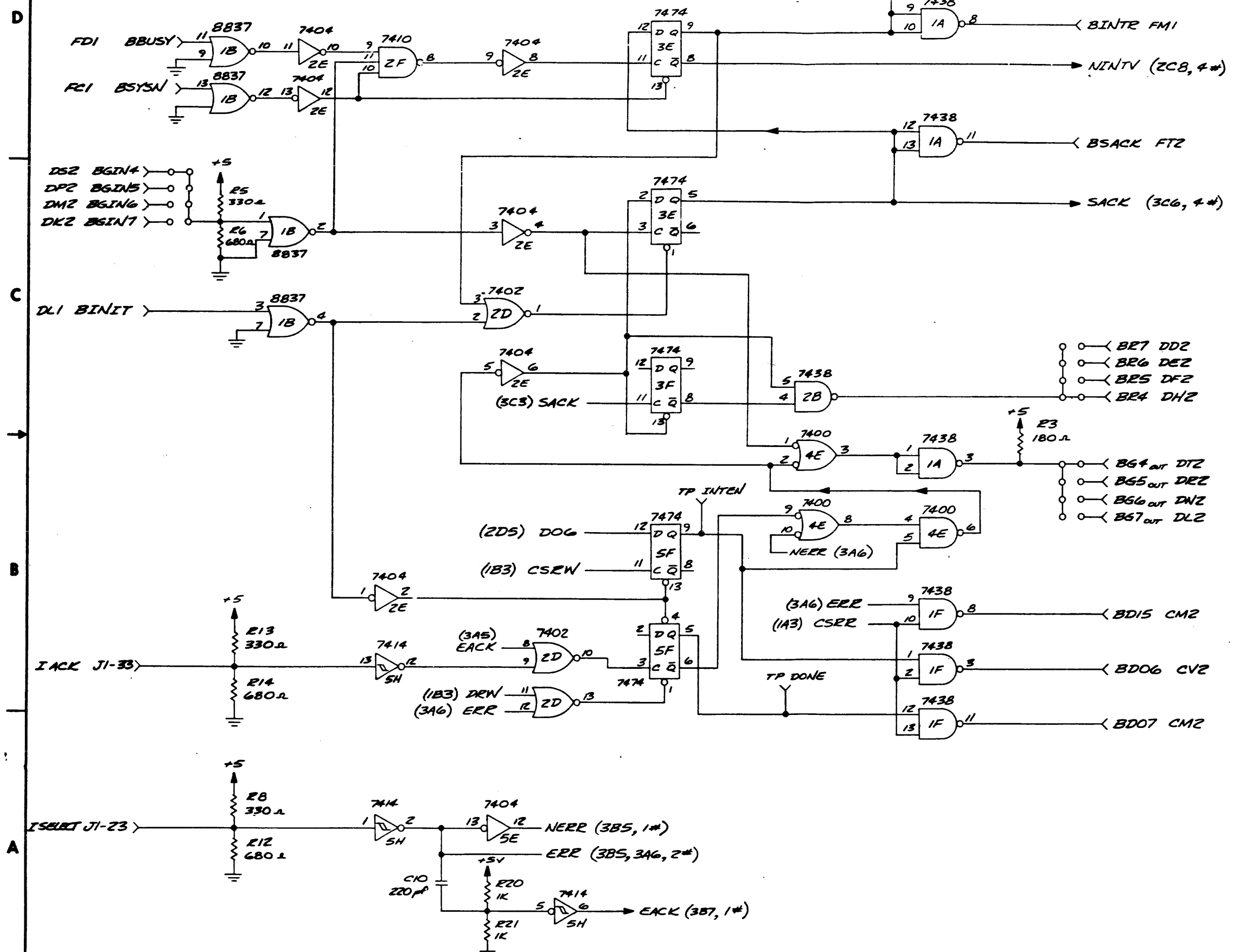
<b>PRINTRONIX</b>	
SCHEMATIC RS232	
DRAWING NO. 986562	PAGE NO. 4 of 4





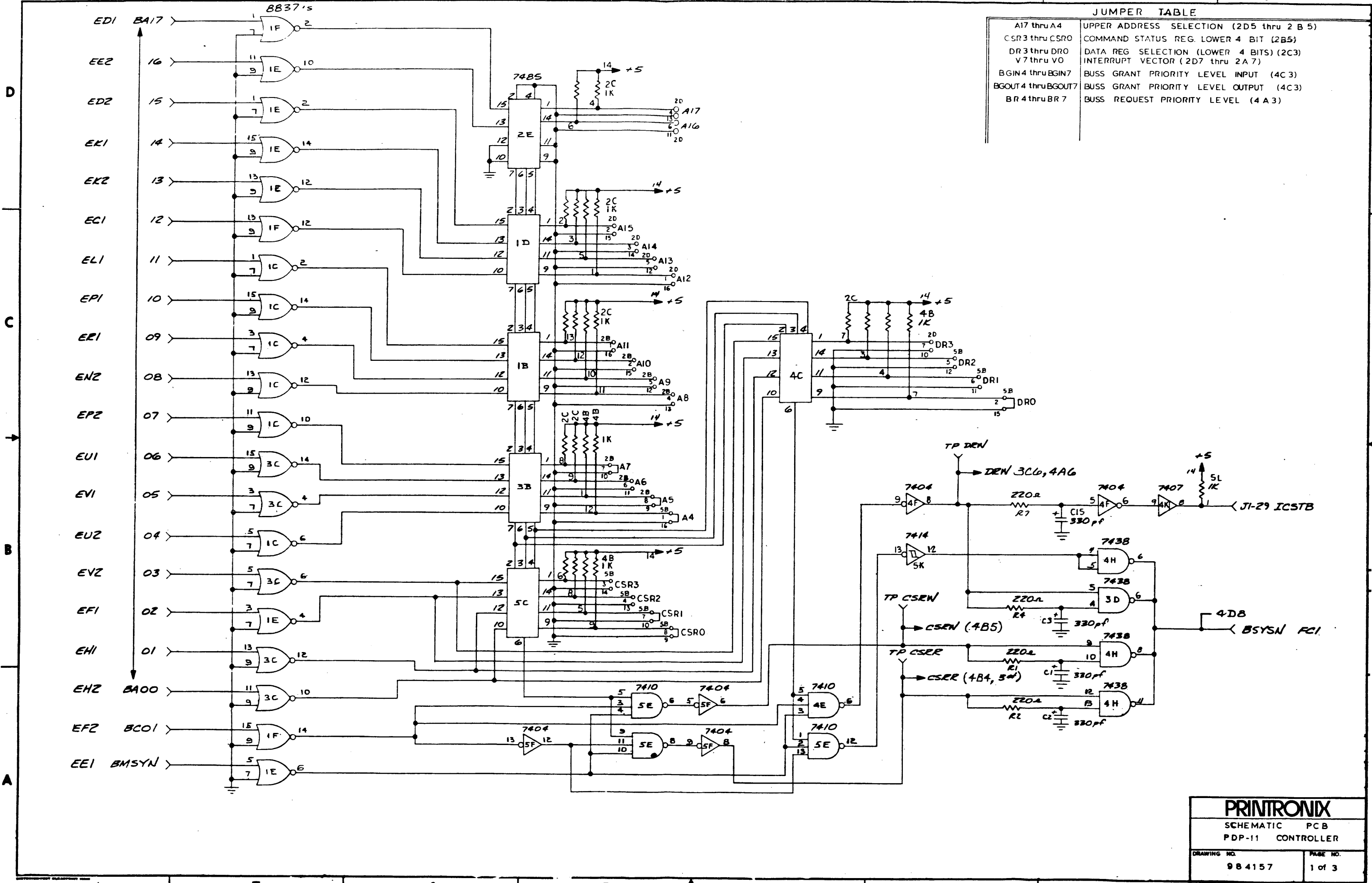
<b>PRINTRONIX</b>	
SCHEMATIC PC B PDP-11 CONTROLLER	
DRAWING NO.	PAGE NO.
98.2213	2 of 3

INTERRUPT CONTROL SECTION

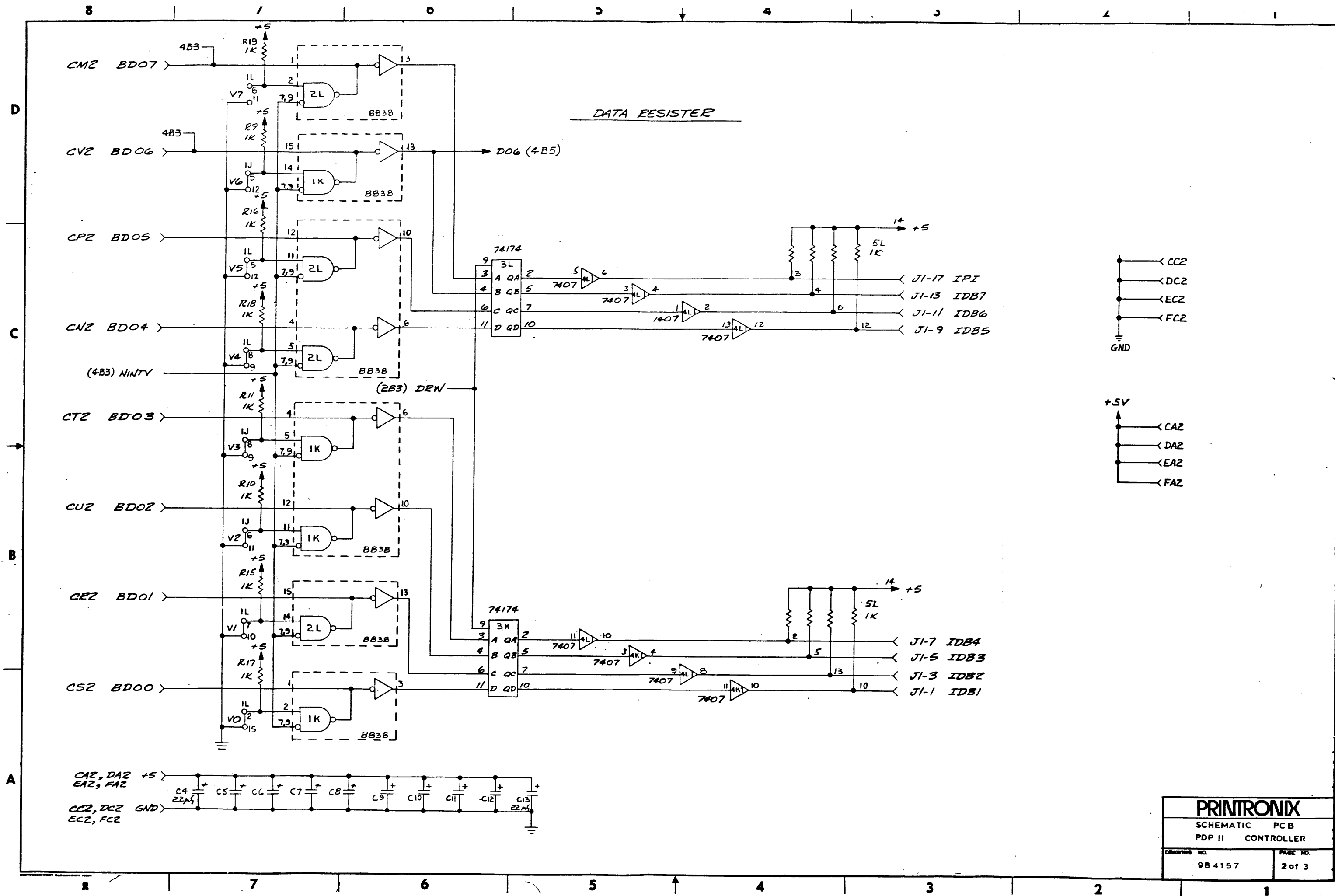


8 7 6 5 4 3 2 1

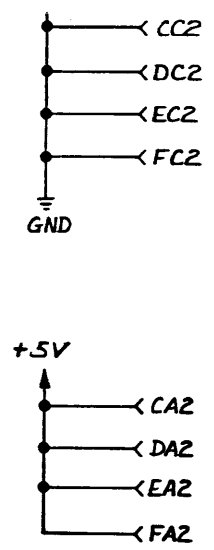
JUMPER TABLE	
A17 thru A4	UPPER ADDRESS SELECTION (2D5 thru 2B5)
CSR3 thru CSR0	COMMAND STATUS REG. LOWER 4 BIT (2B5)
DR3 thru DR0	DATA REG SELECTION (LOWER 4 BITS) (2C3)
V7 thru V0	INTERRUPT VECTOR (2D7 thru 2A7)
BGIN4 thru BGIN7	BUSS GRANT PRIORITY LEVEL INPUT (4C3)
BGOUT4 thru BGOUT7	BUSS GRANT PRIORITY LEVEL OUTPUT (4C3)
BR4 thru BR7	BUSS REQUEST PRIORITY LEVEL (4A3)

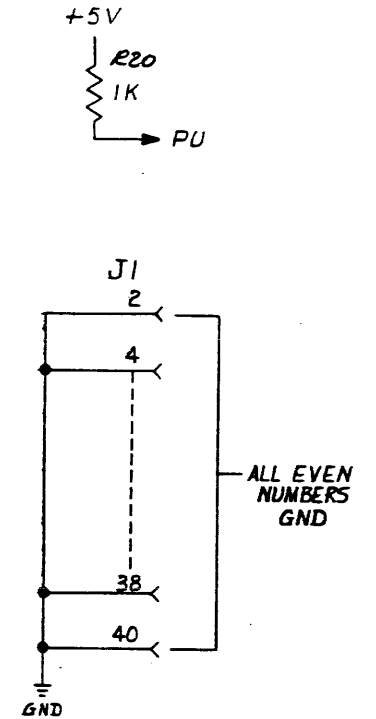
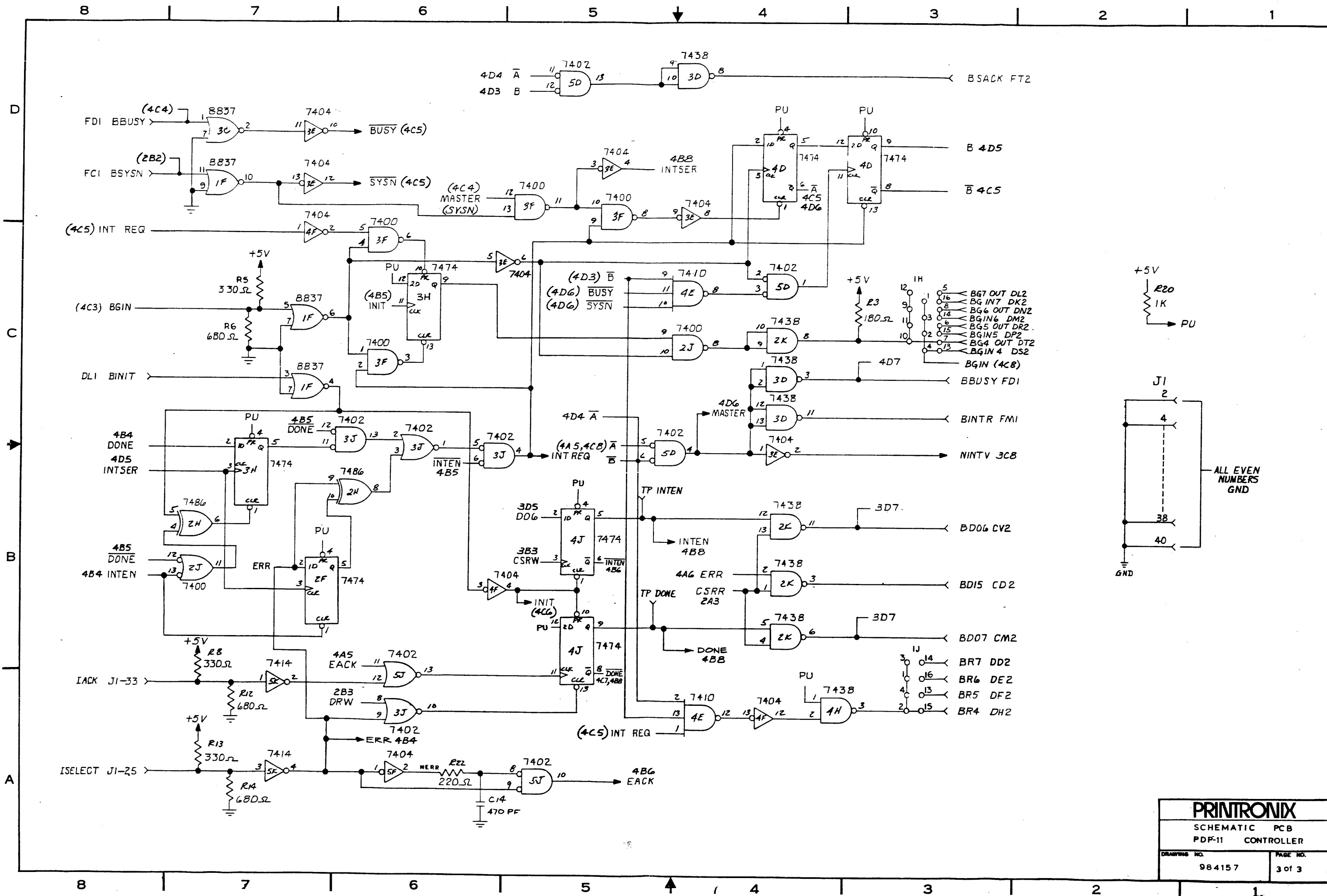


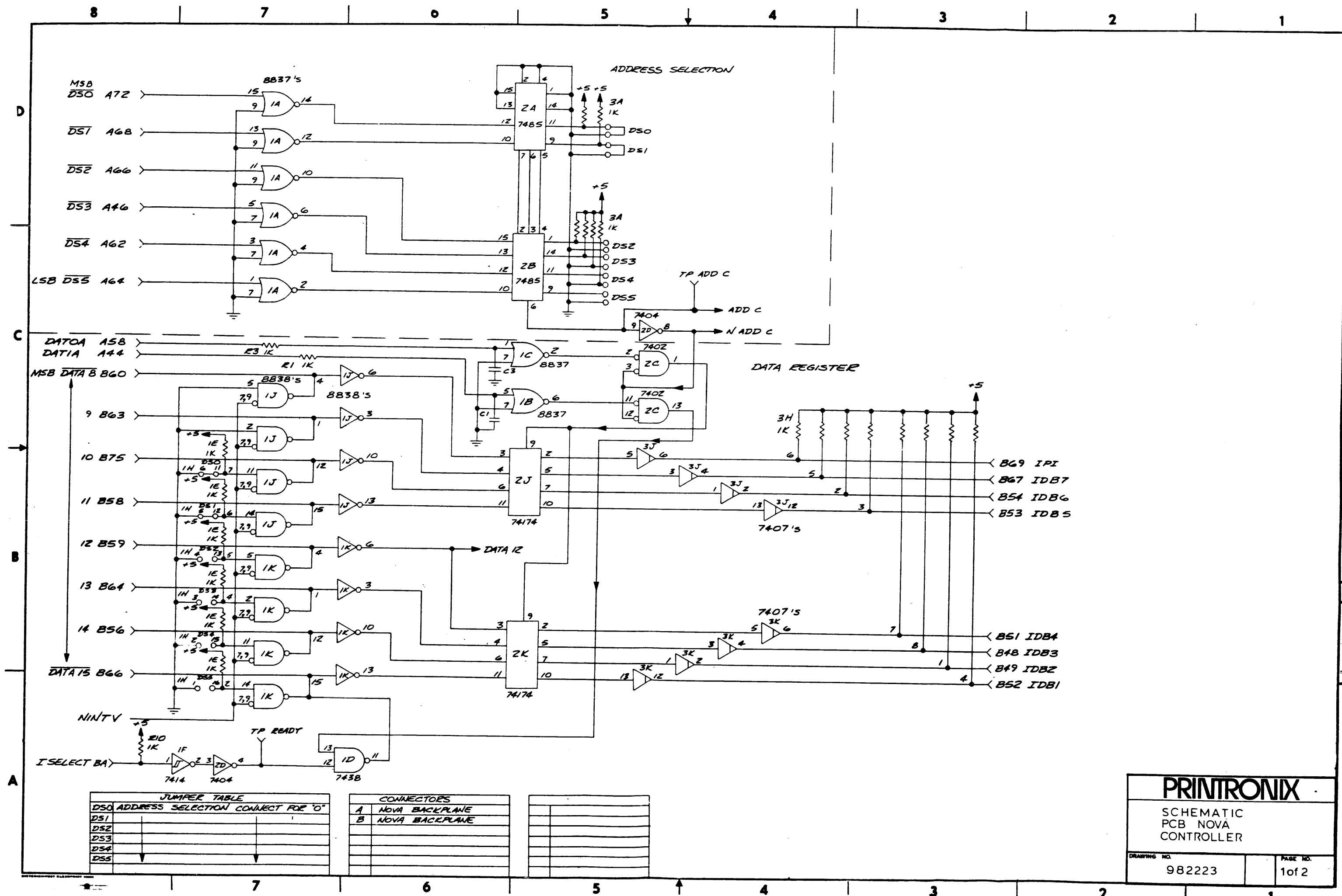
8 7 6 5 4 3 2 1



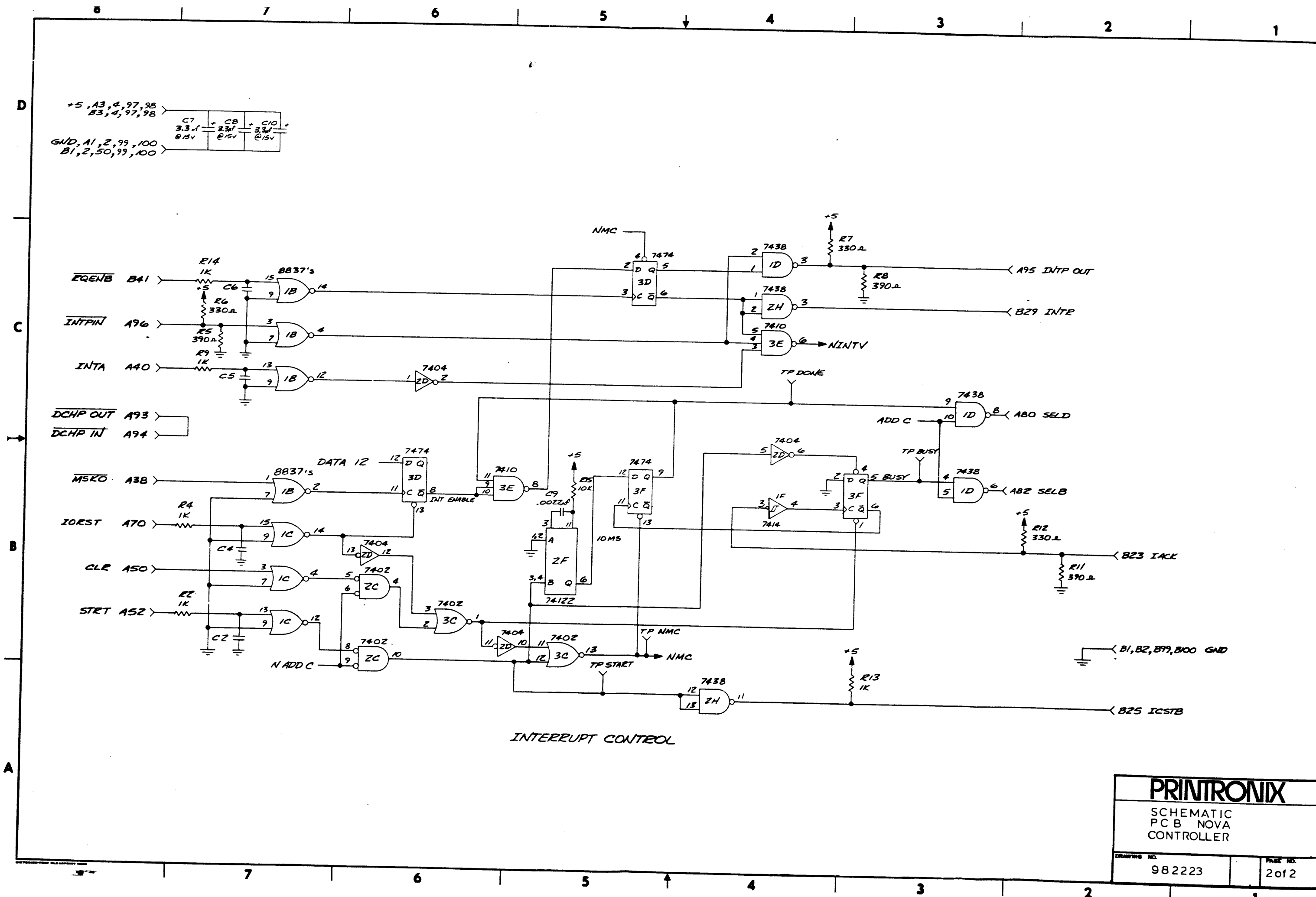
DATA REGISTER





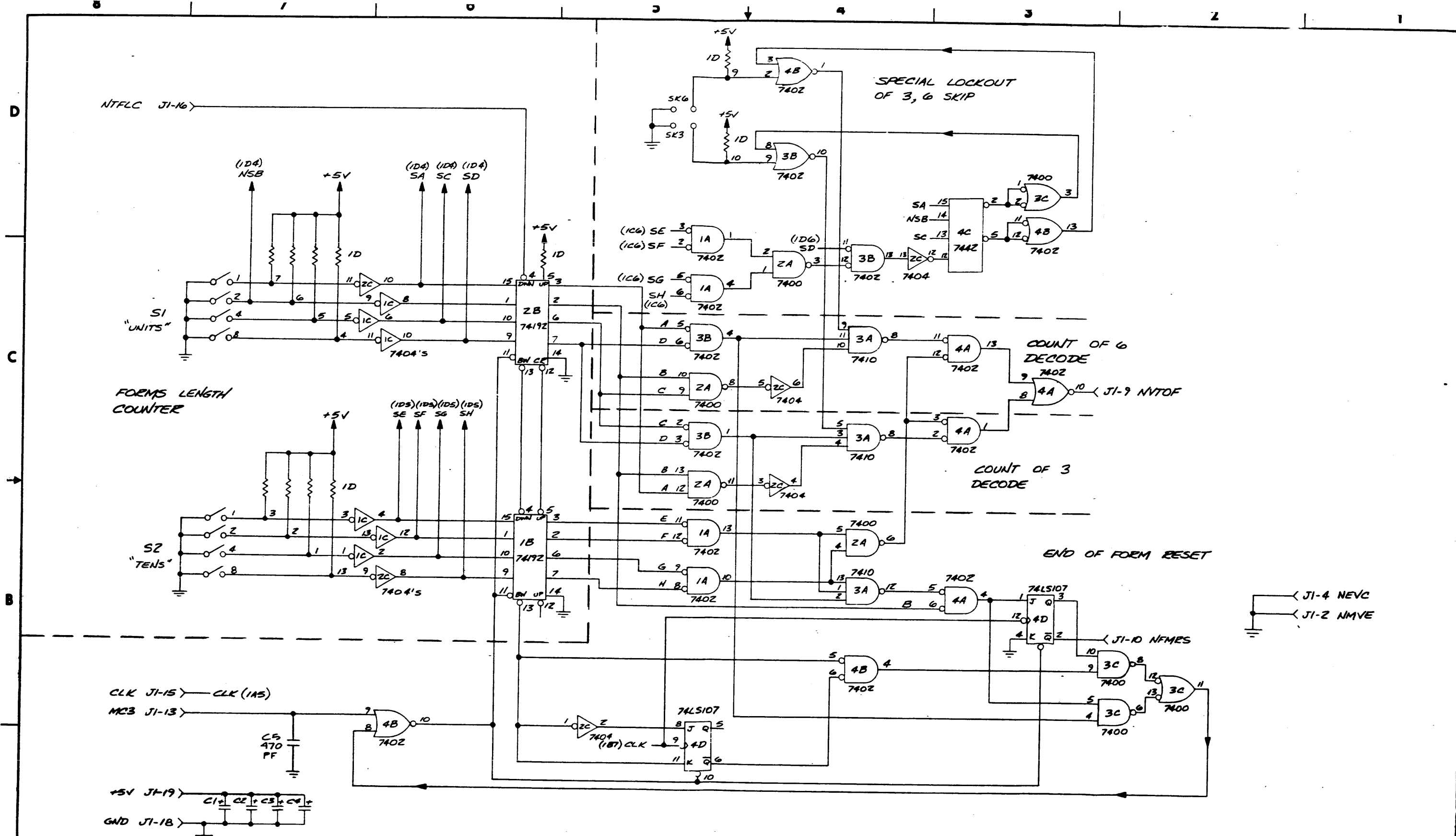






INTERRUPT CONTROL

<b>PRINTRONIX</b>	
SCHEMATIC PCB NOVA CONTROLLER	
DRAWING NO.	PAGE NO.
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JUMPER TABLE	
SK3	SKIP 3 LINES (1/2")
SK6	SKIP 6 LINES (1")

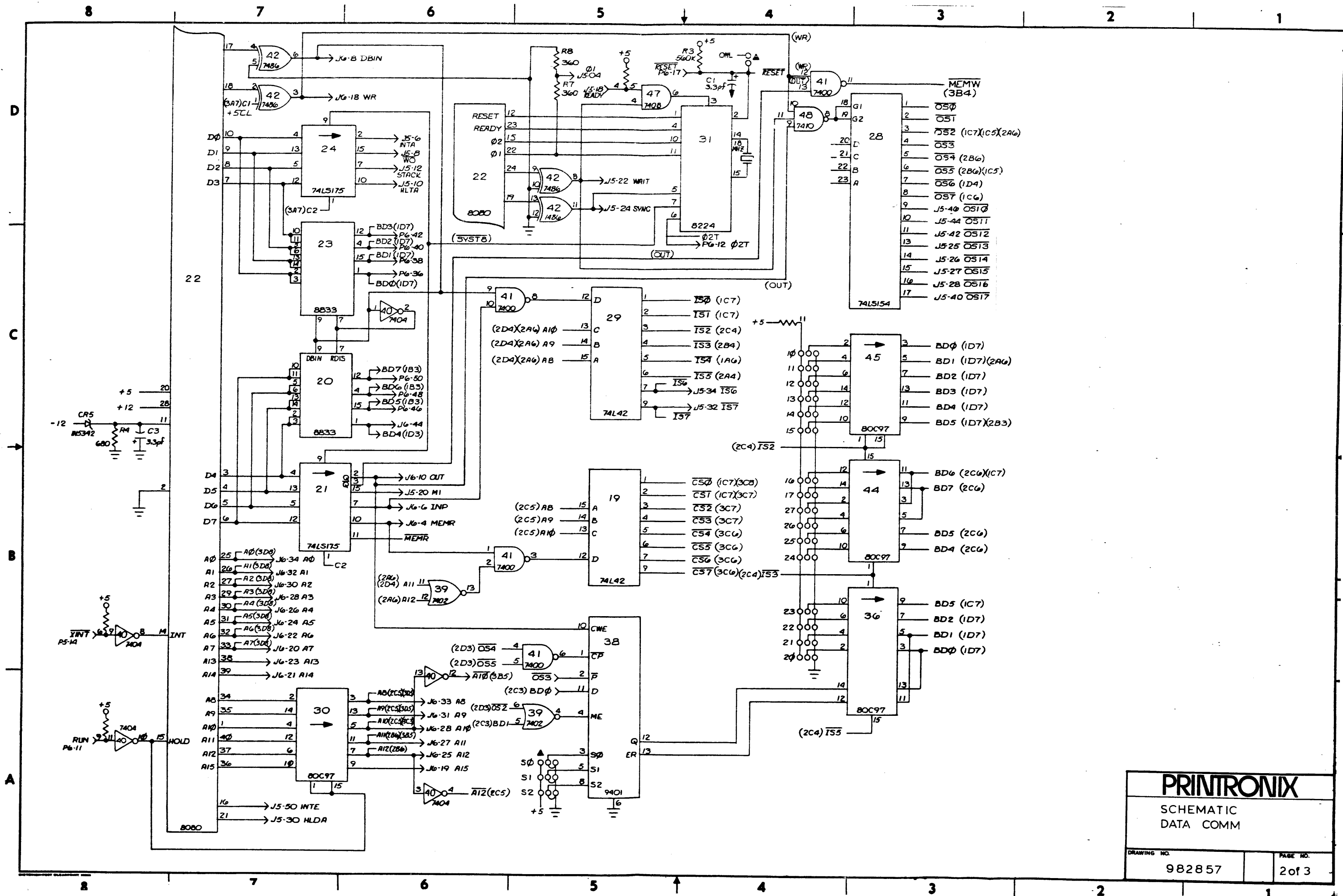
CONNECTOR	
J1	LOGIC BOARD

**PRINTRONIX**

SCHEMATIC  
PCB P600  
FORMS LENGTH SELECTOR

DRAWING NO. 982343	PAGE NO. 1 of 1
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**PRINTRONIX**

SCHEMATIC  
DATA COMM

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