

INSTRUCTION MANUAL

MODEL 143 20 MHz FUNCTION GENERATOR



WAVETEK

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MODEL 143
20 MHz FUNCTION
GENERATOR

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SAFETY

This instrument is wired for earth grounding via the facility power wiring. Do not bypass earth grounding with two wire extension cords, plug adapters, etc.

BEFORE PLUGGING IN the instrument, comply with installation instructions.

MAINTENANCE may require power on with the instrument covers removed. This should be done only by qualified personnel aware of the electrical hazards.

WARNING notes call attention to possible injury or death hazards in subsequent operations.

CAUTION notes call attention to possible equipment damage in subsequent operations.

1

SECTION

GENERAL DESCRIPTION

1.1 THE MODEL 143






The Model 143 20 MHz Function Generator is a precision source of sine, triangle and square waveforms, negative and positive pulses and dc levels. All are front panel and external control variable from 0.0001 Hz to 20 MHz and can be modulated, swept or dc controlled over a 1000:1 range by an external voltage. Output can be continuous or the generator can be triggered or gated by an external voltage or front panel switch.

The waveform amplitude can be varied up to 30 volts peak-to-peak (open circuit) and attenuated up to 80 dB. Pulse amplitude can be varied from ± 15 volts peak (open circuit). DC voltage or dc offset of signal is variable by front panel control and by external control between ± 15 volts (open circuit). Waveform symmetry is variable from 19:1 to 1:19 for control of duty cycle and ramp rise/fall times. Triggered waveform start/stop point is adjustable for creation of special waveforms such as the haversine. A voltage representing generator frequency and a TTL level sync pulse at the frequency of the generator are auxiliary outputs.

1.2 SPECIFICATIONS

1.2.1 Versatility

Waveforms

Selectable sine , square , triangle , positive square , negative square , TTL sync pulse and dc. Symmetry of waveforms may be varied for sawtooth and variable duty cycle pulses.

Operational Modes

Continuous: Generator oscillates continuously at selected frequency.






Triggered: Generator is quiescent until triggered by an external signal or manual trigger, then generates one cycle at selected frequency.

Gated: As triggered mode, except generator oscillates for the duration of the gate signal.

Frequency Range

0.0001 Hz to 20 MHz in 10 overlapping ranges with 1% vernier control.

Function Output

, ,  selectable and variable to 30V p-p (15V p-p into 50 Ω). , , to 15 Vp (7.5 Vp into 50 Ω). All waveforms and dc can supply 150 mA peak current and may be attenuated to 60 dB in 20 dB steps with an additional 20 dB vernier.

DC Output and DC Offset

Selectable thru FUNCTION OUT output. Controlled by front panel control or by applying an external voltage. Adjustable between ± 15 Vdc (± 7.5 Vdc into 50 Ω) with signal peak plus offset limited to ± 15 Vdc (± 7.5 Vdc into 50 Ω). External offset sensitivity approximately -1 V/V with output into open circuit. DC offset and output waveform attenuated proportionately by the 60 dB output attenuator.

Sync Output

A TTL level pulse. Will drive 50 Ω termination.

GCV — Generator Controlled Voltage

At GCV OUT connector, a 0 to +2V signal proportional to generator frequency. 600 Ω source impedance.

VCG — Voltage Controlled Generator

Up to 1000:1 frequency change with external 0 to 2 volt signal to VCG IN connector. Upper and lower frequencies limited to maximum and minimum of selected range.

Slew rate: 2% of range per μ s.

Linearity:

$\pm 0.2\%$ for 10 Hz to 100 kHz.

$\pm 0.75\%$ for 0.001 Hz to 2 MHz.

Input Impedance: 2 k Ω .

Trigger and Gate

Input Range: 1V p-p to ± 10 V.

Impedance: 10 k Ω , 33 pF.

Pulse Width: 25 ns minimum.

Repetition Rate: 10 MHz maximum.

Adjustable Triggered Signal Start/Stop Point (sine and triangle only): Approximately -90° to $+90^\circ$ to 2 MHz.

Symmetry

Symmetry of all waveform outputs is continuously adjustable from 1:19 to 19:1. Varying symmetry provides variable duty cycle pulses, sawtooth ramps and distorted sine waves.

NOTE

When SYMMETRY control is used, indicated frequency is divided by approximately 10.

1.2.2 Frequency Precision

Dial Accuracy

±3% of full range from X .01 Hz to X 1 MHz.

±5% of full range on X 10 MHz.

Time Symmetry

Square wave variation less than:

±1% from 0.001 Hz to 200 kHz

±0.5% from 20 Hz to 20 kHz

1.2.3 Amplitude Precision

Amplitude Change With Frequency

Sine variation less than:

0.1 dB for 0.001 Hz to 200 kHz

0.5 dB for 200 kHz to 2 MHz

3.0 dB for 2 to 20 MHz

Step Attenuator Accuracy

0.3 dB per 20 dB step at 2 kHz.

1.2.4 Waveform Characteristics

Sine Distortion

< 0.5% on X 100 Hz to X 10 kHz.

< 1.0% on X .01 to X 10 Hz and X 100 kHz.

All harmonics 34 dB below fundamental on X 1 MHz.

All harmonics 26 dB below fundamental on X 10 MHz.

Square Wave Rise/Fall Times

At FUNCTION OUT < 20 ns for 15V p-p output into 50Ω load.

1.2.5 General

Stability

Short Term: ±0.05% for 10 minutes.

Long Term: ±0.25% for 24 hours.

Percentages apply to amplitude, frequency and dc offset.

Environmental

Specifications apply at 25°C ±5°C. Instrument will operate from 0°C to 50°C ambient temperatures.

Dimensions

28.6 cm (11 ¼ in.) wide; 13.3 cm (5 ¼ in.) high;

27.3 cm (10 ¾ in.) deep.

Weight

5 kg (11 lb) net; 6.6 kg (14 ½ lb) shipping.

Power

90 to 105V, 108 to 126V, 198 to 231V and 216 to 252V selectable; 48 to 400 Hz; less than 30 watts.

NOTE

All specifications apply from 10 to 100% of a selected frequency range, when FUNCTION OUT is at maximum and 50Ω terminated, with SYMMETRY control at OFF. Symmetry and vernier affect frequency calibration. Maximum possible asymmetry is a function of frequency setting.

SECTION 2 INSTALLATION

2.1 MECHANICAL INSTALLATION

After unpacking the instrument, visually inspect all external parts for possible damage to connectors, surface areas, etc. If damage is discovered, file a claim with the carrier who transported the unit. The shipping container and packing material should be saved in case reshipment is required.

2.2 ELECTRICAL INSTALLATION

2.2.1 Power Connection

WARNING

To preclude injury or death due to shock, the third wire earth ground must be continuous to the facility power outlet. Before connecting to the facility power outlet, examine extension cords, autotransformers, etc., between the instrument and the facility power outlet for a continuous earth ground path. The earth ground path can be identified at the plug on the instrument power cord; of the three terminals, the earth ground terminal is the nonmatching shape, usually cylindrical.

CAUTION

To prevent damage to the instrument, check for proper match of line and instrument voltage and proper fuse type and rating.

NOTE

Unless otherwise specified at the time of purchase, this instrument was shipped from the factory with the power transformer connected for operation on a 108 to 132 Vac line supply and with a 0.5 amp slow blow fuse.

Conversion to other input voltages requires a change in rear panel fuse-holder voltage card position and slow blow fuse according to the following table and procedure.

Card Position	Input Vac	Fuse
100	90 to 105	0.5 amp
120	108 to 126	0.5 amp
220	198 to 231	0.25 amp
240	216 to 252	0.25 amp

1. Open fuse holder cover door and rotate fuse pull to left to remove the fuse.
2. Select operating voltage by orienting the printed circuit board to position the desired voltage on the top left side. Push the board firmly into its module slot.
3. Rotate the fuse-pull back into the normal position and insert the correct fuse into the fuse holder. Close the cover door.
4. Connect the ac line cord to the mating connector at the rear of the unit and the power source.

2.2.2 Signal Connections

Use 3 foot RG58U 50Ω shielded cables equipped with female BNC connectors to distribute all input and output signals.

2.3 ELECTRICAL ACCEPTANCE CHECK

This checkout procedure is a general verification of generator operation. Should a malfunction be found, refer to the warranty in the front of this manual.

A two channel oscilloscope, four 3 foot 50Ω coax cables with female BNC connectors, a coax tee connector and a function generator are required for this procedure.

Preset the generator front panel controls as follows:

Control	Position
Dial	1.0
GENERATOR MODE	CONT
TRIGGER LEVEL	9 o'clock
TRIGGER START/STOP	0° CAL
ATTENUATION	0
ATTENUATION VERNIER	Full cw
FUNCTION	<input type="checkbox"/>
DC OFFSET	OFF
SYMMETRY	OFF
FREQUENCY MULT	1K
VERNIER	Full cw

Set up the oscilloscope, Model 143 and external generator as shown in figure 2-1.

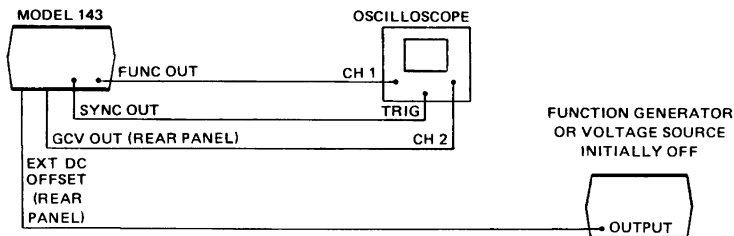


Figure 2-1. Initial Setup

Table 2-1. Acceptance Procedure

Step	Control	Position/Operation	Observe
1	POWER	ON	1 kHz square wave.
2	Dial	Rotate dial. Return to 1.0.	Rotation ccw increases frequency of square on one channel and dc level on other channel; cw decreases frequency and dc level.
3	FREQUENCY MULT	Rotate switch. Return to 1K.	Rotation cw increases frequency; ccw decreases frequency (dc level not affected).
4	VERNIER	Rotate ccw. Return to CAL.	Rotation ccw gives a small decrease in frequency.
5	ATTENUATION	Rotate ccw. Return to 0.	Rotation ccw reduces square wave amplitude.
6	ATTENUATION VERNIER	Rotate ccw.	Square wave amplitude decreases.
7	DC OFFSET	Rotate cw. Return to OFF.	Square wave is immediately offset below previous level; then waveform moves up to a positive level. OFF returns waveform to original position. (Clipping occurs at $\pm 15V$.)
8	Function Generator or Voltage Source	Vary input voltage.	Waveform dc level varies.

Remove EXT DC OFFSET IN cable and connect to VCG IN connector. Remove GCV OUT cable.

9	Function Generator or Voltage Source	Vary input voltage; then disconnect input.	Frequency increases with increased voltage, decreases with decreased voltage.
10	ATTENUATION VERNIER	Rotate cw.	---
11	FUNCTION	Rotate to DC, \sim , \wedge , \sqcap , \sqcup , \sqcup , \sqcap , then \sim .	Note dc level on scope. \sim , \wedge and \sqcap should be centered on dc level. \sqcup should rest on dc level, \sqcup should rise to dc level.
12	SYMMETRY	Rotate cw, then to OFF.	Waveform changes from \sim to \wedge to \sqcap and frequency decreases, then to \wedge at original frequency.

Table 2-1. Acceptance Procedure (Continued)

Step	Control	Position/Operation	Observe
13	GENERATOR MODE	GATE	A dc level.
14	MANUAL TRIG	Press down.	A series of sine waves.
Set up a trigger source as shown in figure 2-2. Trigger on triangle waveform. Set trigger source at 100 Hz ∇ .			
15	TRIGGER LEVEL	Rotate knob. Set for several cycles.	Knob varies number of cycles gated.
16	GENERATOR MODE	TRIG	One cycle per trigger cycle.
17	TRIGGER START/STOP	Rotate knob, then to 0° CAL.	CW starts sine wave at +90°; ccw starts sine wave at -90°. Fully cw gives continuous sine waves.

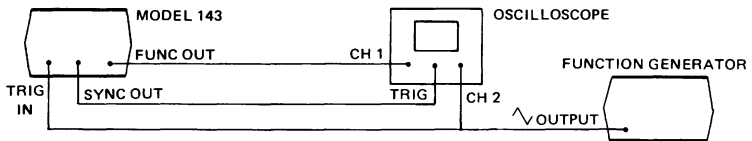


Figure 2-2. Second Setup

as a trigger or gate in the trigger and gate modes. The trigger level can be varied from fully cw, where a positive-going excursion thru approximately $-10V$ is a trigger, to fully ccw, where a positive-going excursion thru approximately $+10V$ level is a trigger.

4 MANUAL TRIGGER Switch

Triggers or gates the output signal when GENERATOR MODE switch **3** is at TRIG or GATE. In trigger mode, one cycle is output when the switch is pressed. In gate mode, cycles are continuously output as long as the switch is held down.

NOTE

Set TRIGGER LEVEL **3** fully ccw.

5 TRIGGER START/STOP Control

Sets the start and stop point of the sine or triangle waveform appearing at **7**. Usually used in the trigger mode and in combination with **10** to create desired waveforms. 0° CAL position ensures conventional waveforms symmetrical about 0 Vdc.

6 ATTENUATION Control

Outer knob reduces output voltage level of all output at FUNCTION OUT with increasing steps of attenuation.

VERNIER Control

Inner knob is a 20 dB vernier which controls the output within the steps of the outer knob. DC and offset voltages are not affected by this control.

7 FUNCTION OUT Connector

The main output of the generator. The output of the function selected.

8 SYNC OUT Connector

Furnishes a TTL pulse for each cycle or period of the generator. To be used for scope or similar synchronization.



9 FUNCTION Switch

Selects one of six output signals: dc, waveforms or pulses.

10 DC OFFSET Control

Offsets the waveform or dc level at **7** from $-15V$ to $+15V$ (open circuit; $\pm 7.5V$ into 50Ω). An OFF position ensures no offset.

11 SYMMETRY Control

Normal symmetrical output results when SYMMETRY is set to OFF; an asymmetrical, or unbalanced, waveform results when SYMMETRY is set between  and . (Asymmetrical operation reduces generator frequency to approximately 1/10th the normal output.) Figure 3-2 shows the effect of SYMMETRY control on the waveforms.

NOTE

When SYMMETRY control is used, the output frequency is different from the dial indicated frequency. The maximum symmetry ratio obtainable also depends on the frequency dial setting.

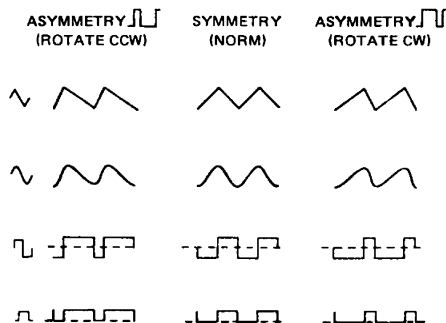


Figure 3-2. Effect of SYMMETRY Control

12 TRIG IN Connector

Accepts a 1V p-p to $\pm 10V$ external signal to trigger the generator. (Up to $\pm 50V$ will not damage circuitry.) Triggers on rising edge of input which crosses TRIGGER LEVEL **3** setting from negative to positive.

13 VCG IN Connector

Accepts 0 to $+2V$ ac or dc voltages to vary up to 1000:1 the frequency and period of the outputs.

The upper and lower limits are defined by the maximum and minimum dial (2) settings multiplied by (14). VCG input will not drive the generator beyond the normal dial limits of a range.

(14) FREQUENCY MULT Switch

The outer knob selects one of ten frequency multipliers for the dial (2) setting.

VERNIER Control

A fine adjustment of the frequency dial (2) setting.

Not Shown GCV OUT Connector (Rear Panel)

This connector gives a 0 to +2V signal proportional to the frequency of the generator within any given range. The signal can be used as the X drive for X-Y recorders.

Not Shown EXT DC OFFSET IN Connector (Rear Panel)

Applied voltage offsets the selected waveform linearly. Offset is 1V for each -1V applied with output connected into an open circuit. Maximum input is $\pm 15V$. Offset is affected by the attenuator (6)

3.2 OPERATION

Perform the initial checkout in Section 2 for the feel of the instrument. Any questions concerning individual controls and connectors may be answered in paragraph 3.1.

3.2.1 Signal Termination

Proper signal termination, or loading, of the generator connectors is necessary for its specified operation. For example, the proper termination of the main output is shown in figure 3-3. Placing the 50Ω terminator, or 50Ω resistance,

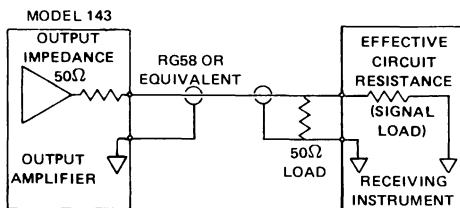


Figure 3-3. Signal Termination

in parallel with a higher impedance matches the receiving instrument input impedance to the generator output impedance, thereby minimizing signal reflection or power loss on the line due to impedance mismatch.

3.2.2 Voltage Controlled Function Generator Operation

Operation as a voltage controlled function generator (VCG) is as for a manually controlled function generator, only the frequency within particular ranges is additionally controlled with dc levels ($\pm 2V$ excursions) injected at the VCG IN connector. Set the frequency dial to a reference from which the frequency is to be voltage controlled.

1. For frequency control with positive dc inputs at VCG IN, set the dial for a lower frequency limit.
2. For frequency control with negative dc inputs at VCG IN, set the dial for an upper frequency limit.
3. For modulation with an ac input at VCG IN, set the dial at the desired center frequency. Do not exceed the maximum dial range of the selected frequency range.

Figure 3-4 is a nomograph with examples of dial and voltage effects. Example 1 shows that with 0V VCG input, frequency is as determined by the main dial setting, 1.0 in this

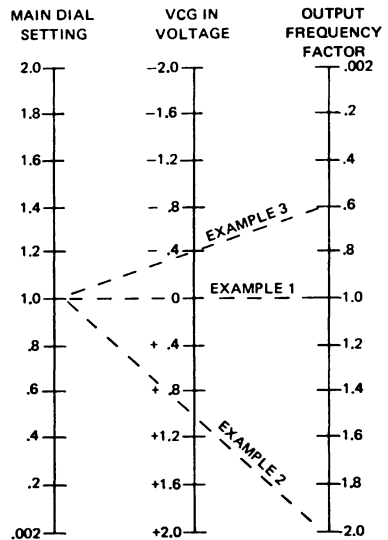


Figure 3-4. VCG Voltage-to-Frequency Nomograph

example. Example 2 shows that with a positive VCG input, output frequency is increased. Example 3 shows that with a negative VCG input, output frequency is decreased. (Note that the Output Frequency Factor column value must be multiplied by a frequency range multiplier to give the actual output frequency.)

NOTE

The frequency vernier must be rotated fully ccw for 1000:1 range.

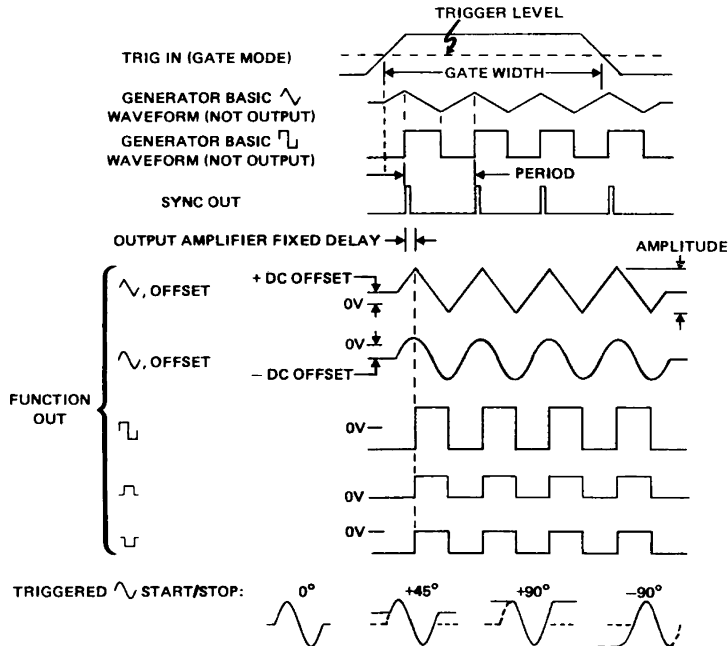
Nonlinear operation results when the VCG input voltage is excessive; that is, when the attempted generator frequency exceeds the range setting

(2 times the multiplier setting) or in the other direction, 1/1000th of the range setting.

The up to 1000:1 VCG sweep of the generator frequencies available in each range results from a 2V excursion at the VCG IN connector. With the frequency dial set to 2.0, excursions between -2V and 0V at VCG IN provide the up to 1000:1 frequency sweep. With the dial set to .002, excursions between 0V and +2V at VCG IN provide the up to 1000:1 sweep within the set frequency range.

3.2.3 Waveforms

See figure 3-5 for definition of controllable waveform characteristics.



NOTES

1. *Period is controlled by the generator frequency setting.*
2. *In trigger mode, just one period is generated for each trigger pulse.*
3. *DC offset plus peak waveform voltage > | 7.5V | causes waveform clipping.*

Figure 3-5. Waveform Characteristics

SECTION 3

OPERATION

3.1 CONTROLS AND CONNECTIONS

The generator front panel controls and connectors are shown in figure 3-1 and keyed to the following descriptions.

- ① **POWER Switch**
Turns generator on and off.
- ② **Frequency Dial**
Settings under the dial index mark multiplied by ⑭ determine the output signal frequency. The frequency can be varied by the vernier ⑭ and the VCG signal ⑬.
- ③ **GENERATOR MODE Switch**
Selects one of the following three modes.

CONT – Continuous output at **FUNCTION OUT**, and **SYNC OUT** connectors.

TRIG – DC level output at both output connectors until the generator is triggered by **MANUAL TRIGGER** switch or with a signal at the **TRIG IN** connector. When triggered, the generator output is one cycle of waveform or one pulse period followed by a dc level.

GATE – As for **TRIG** except the output is continuous for the duration of the trigger signal at **TRIG IN**. The last cycle or period started is completed.

TRIGGER LEVEL Control

Determines the level at which the input trigger signal at the **TRIG IN** connector ⑫ is accepted

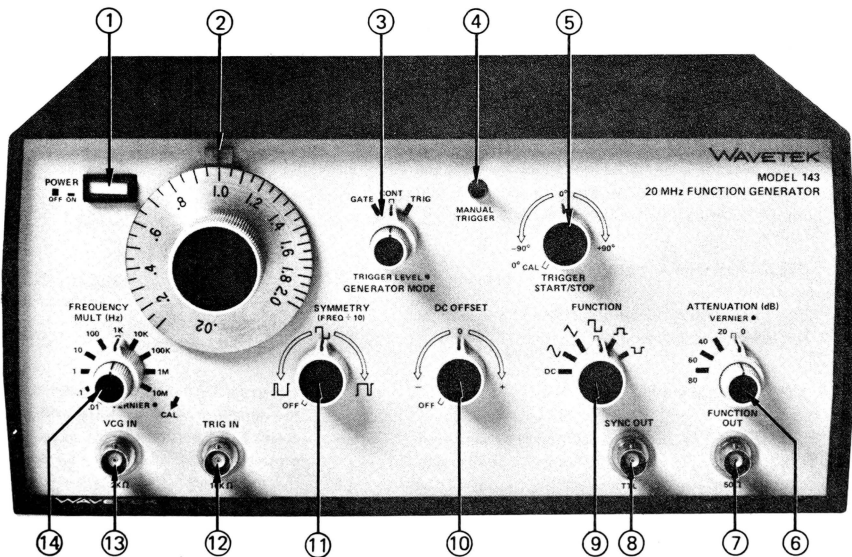


Figure 3-1. Controls and Connectors

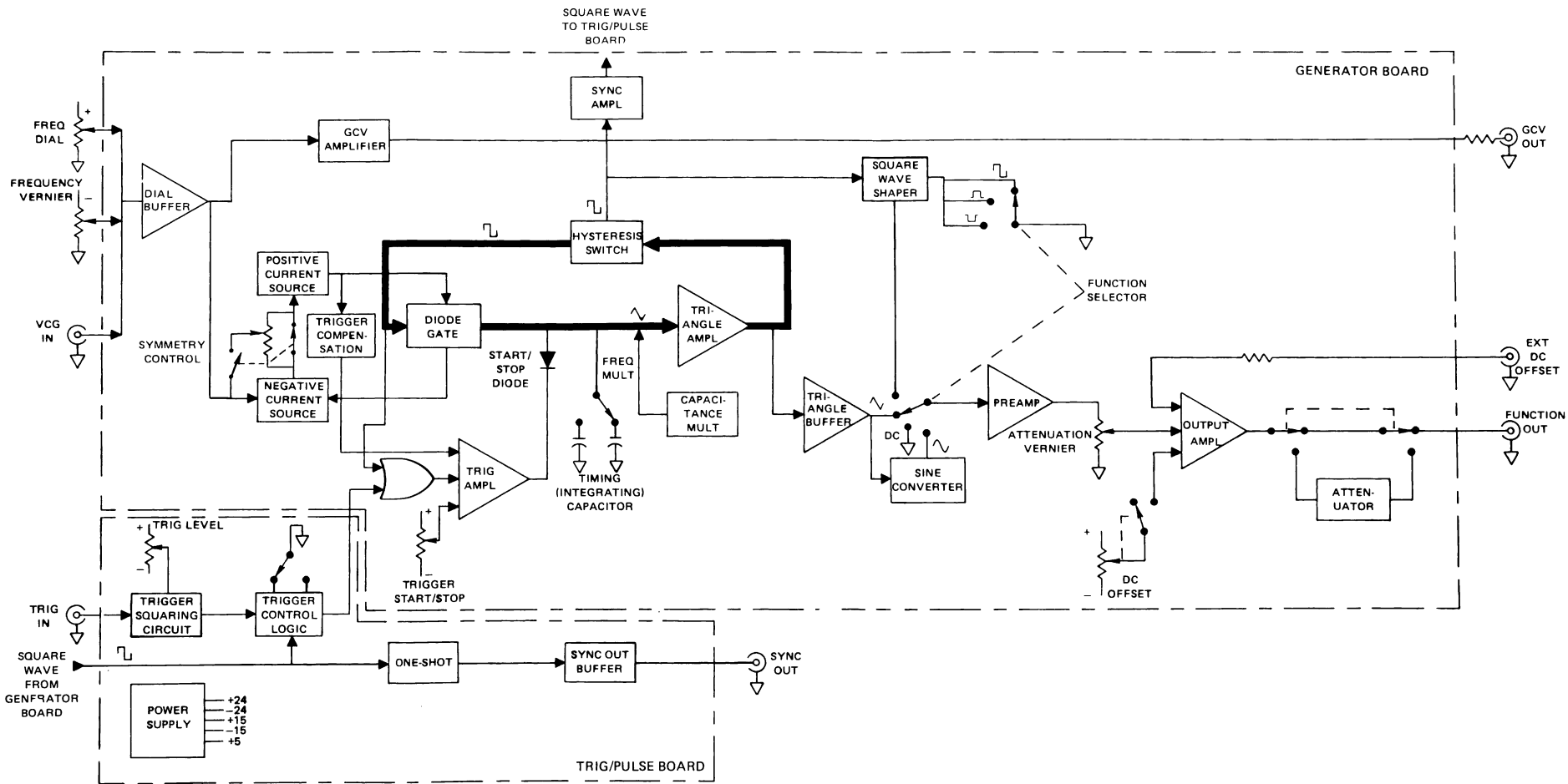


Figure 4-1. Overall Block Diagram

SECTION 4

CIRCUIT DESCRIPTION

4.1 BASIC WAVEFORM DEVELOPMENT

The heart of the generator (the bold path in figure 4-1) is a triangle and square wave generator. The triangle waves are developed by capacitor charging ramps that are alternately reversed in polarity. The polarity reversal is caused by a flip-flop circuit, or hysteresis switch, that in turn produces the square waves. The flip-flop changes states upon detecting amplitude limits of the charging ramps through the triangle amplifier.

As shown in figure 4-1, the VCG dial buffer sums the currents from the frequency dial, frequency vernier and VCG in connector. The VCG dial buffer is an inverting amplifier whose output voltage is used to control a positive current source and a negative current source. For symmetrical output waveforms, the currents from the two current sources are equal and directly proportional to the voltage of the VCG dial buffer output. The diode gate, which is controlled by the hysteresis switch, is used to switch the positive or the negative current to the integrating capacitor selected by the frequency multiplier. If the positive current is switched into the integrating capacitor, the voltage across the capacitor will rise linearly to generate the triangle rise transition. If the current is negative, the voltage across the integrating capacitor will fall linearly to produce the fall transition.

The triangle amplifier is a unity gain amplifier whose output is fed to the hysteresis switch. The hysteresis switch has two voltage limit points (+1.25V and -1.25V) at its input.

During the time the output voltage of the triangle amplifier is rising, the output voltage of the hysteresis switch is positive, but when the output voltage of the triangle reaches +1.25V, it triggers the hysteresis switch causing the output to switch negative. Once the control voltage into the diode gate becomes negative, it will switch the positive current out and switch the negative current in to the integrating capacitor, so that the voltage across the capacitor will reverse, starting a linear decrease of the waveform. When the decreasing voltage reaches -1.25V, the output of the hysteresis switch will switch back to positive, reversing the process. This action generates the triangle waveform as shown in figure 4-2. Since the output of the hysteresis switch is a square wave, the result is simultaneous generation of a square wave and a triangle wave at the same frequency.

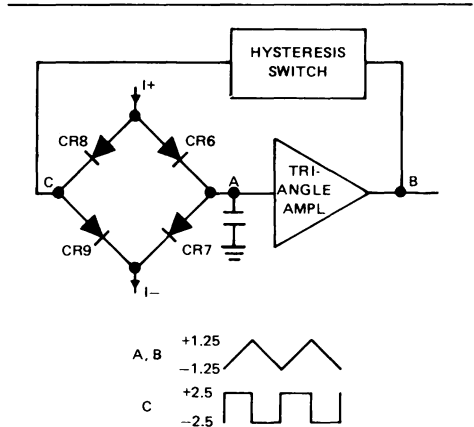


Figure 4-2. Basic Generator and Timing Diagram

The output frequency is determined by the magnitude of the capacitor selected by the frequency multiplier and the magnitude of the positive and negative current sources. Since the current sources are linearly proportional to the control voltage of the VCG circuit, the output frequency will also be linearly proportional to the control voltage.

When the symmetry control is turned on, the current of the negative current source is decreased by 19 times, and the fall time of the triangle is 19 times longer than the rise time of the triangle, resulting in an unsymmetrical waveform and a division of the frequency by a factor of 10. Gradually increasing the current from the negative current source and decreasing the current from the positive current source causes the time for the triangle to complete one cycle to remain constant, while the symmetry of the output waveform is continuously varied.

The output of the hysteresis switch is fed to the sync amplifier and also the square wave shaper. The square wave shaper consists of a shaping circuit which limits the square wave output swing to $\pm 1.25V$. For positive pulse outputs, it limits the output voltage swing from -1.25 to 0V; and for negative pulse outputs, it limits the output swing from 0 to +1.25V.

The triangle wave from the triangle amplifier is coupled through a buffer amplifier and made available to the function selector switch. The buffer amplifier provides a low impedance to drive the sine converter circuit. The sine converter, using the nonlinear characteristics of its diodes, converts the triangle wave into a sine wave.

The square wave from the sync amplifier, processed through a one-shot and the sync out buffer, is externally available at the sync out connector. The sync pulse, then, is a TTL level pulse output of the generator frequency.

4.2 AMPLITUDE OFFSET AND ATTENUATION

The selected waveform is inverted and amplified in the pre-amplifier. The preamplified waveform is sent to the output amplifier.

The output amplifier is an inverting amplifier with a current limiting output stage for short circuit protection. The dc offset control provides the offset to the selected waveforms center reference. The dc offset can be set by voltage at the external dc offset connector. The output amplifier establishes the generator 0 dB attenuation reference. An output attenuator decreases this reference amplitude in operator selected 20 dB steps. The attenuator consists of three voltage dividers. Attenuation between the steps is provided by the attenuation vernier.

4.3 TRIGGER AND GATE CONTROL

Generator operation is controlled by allowing or preventing the timing capacitor to charge. For figure 4-3 shows in detail this portion of the circuit. For continuous operation, the trigger amplifier maintains a positive level above the positive peak developed by the charging capacitors. This reverse biases (turns off) the start/stop diode, and the trigger amplifier does not interfere with continuous operation.

When the trigger amplifier outputs some level below the positive peak charging level, the diode is forward biased

(turned on) to sink the integrating current from the current source, preventing the capacitors from charging to the positive peak. This stops waveform generation and holds the triangle output at some dc level called the trigger baseline. The trigger baseline is the level where a triangle waveform cycle starts and where it stops. This baseline is directly applicable to the triangle waveform and thus affects the sine wave. The square wave levels, output via the hysteresis switch, are not affected by the triangle baseline levels.

The normal trigger baseline is zero volts, analogous to 0° phase of a sine or triangle waveform. The trigger start/stop control offsets the trigger amplifier output and can change the baseline for starting and stopping a sine or triangle waveform from its negative peak (-90°) to its positive peak ($+90^\circ$) range. At the extreme positive peak level setting, though, the diode is again reverse biased and generator operation goes continuous.

When charging level is being held, the positive current generator still varies its output with corresponding frequency control inputs. These varying currents must be sunk through the diode to keep the timing capacitors from varying their charge, and thus varying the trigger baseline. The baseline compensation circuit monitors the output from the positive current generator to control the trigger amplifier and thus control the necessary compensating current through the diode.

The trigger control logic determines that after a waveform starts, it always stops at a complete cycle and at the same phase at which it started. The trigger control logic latches the trigger amplifier for an enabling output from the time the cycle starts to when the negative peak of the last cycle is reached (just one cycle in the trigger mode). Upon reaching the negative peak, the timing capacitor continues charging positive again, but stops upon reaching the trigger baseline. A square wave from the hysteresis switch synchronizes the last negative peak time for unlatching the trigger amplifier for its trigger baseline output.

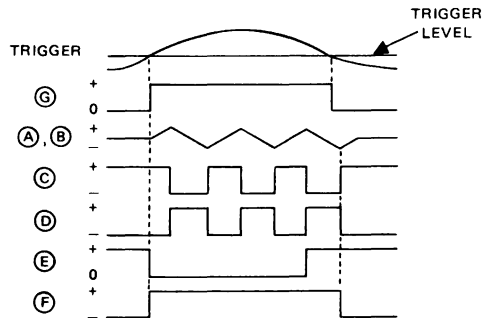
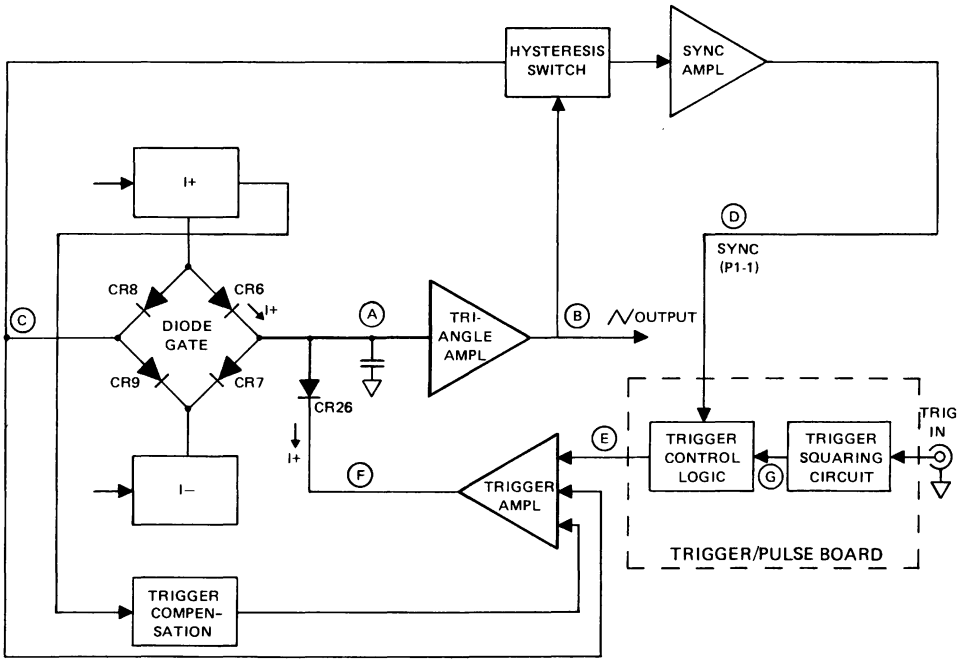


Figure 4-3. Trigger Circuit and Timing

SECTION 5

CALIBRATION

5.1 FACTORY REPAIR

Wavetek maintains a factory repair department for those customers not possessing the necessary personnel or test equipment to maintain the instrument. If an instrument is returned to the factory for calibration or repair, a detailed description of the specific problem should be attached to minimize turnaround time.

5.2 REQUIRED TEST EQUIPMENT

Voltmeter 0.1 mVdc resolution (0.1% accuracy)
 Oscilloscope, Dual Channel \geq 150 MHz bandwidth
 Distortion Analyzer To 600 kHz
 Counter To 20 MHz (0.1% accuracy)
 50 Ω Load \pm 0.1% accuracy, 2W
 Generator 200 kHz signal, 2 to 20V p-p

5.3 REMOVING GENERATOR COVERS

1. Invert the instrument, remove the four screws in the cover.
2. Turn the instrument upright, remove the top cover, and remove the four screws securing the bottom cover.
3. Replace the top cover and turn the instrument upside down.

NOTE

Remove the bottom cover only when it is necessary to make adjustments or measurements.

5.4 CALIBRATION

After referring to the following preliminary data, perform calibration, as necessary, per table 5-1. If performing partial

calibration, check previous settings and adjustments for applicability. See figures 5-1 and 5-2 for calibration point location.

1. Unless otherwise noted, all measurements made at the 50 Ω OUT connector should be terminated into a 50 Ω (\pm 0.1%) load.
2. Verify operation in TRIG and GATE modes by connecting an external generator to the TRIG IN BNC and observing proper operation of TRIGGER LEVEL and TRIGGER START/STOP controls (paragraph 3.1).
3. Verify SYNC OUT is an approximate 30 ns positive pulse into 50 Ω and that GCV OUT is a voltage proportional to dial position with a 2V max (open circuit).
4. Start the calibration by connecting the unit to an ac source and setting the front panel switches as follows:

Dial02
FREQ MULT	100K
FREQ VERNIER	Full cw
GENERATOR MODE	CONT
TRIGGER LEVEL	Full ccw
TRIGGER START/STOP	0° CAL
SYMMETRY	OFF
DC OFFSET	OFF
FUNCTION	DC
ATTENUATION	20 I 0
ATTENUATION VERNIER	Full ccw
POWER	ON

5. Allow the unit to warm up at least 30 minutes for final calibration. Keep the instrument covers on to maintain heat. Remove covers only to make adjustments or measurements.

Table 5-1. Calibration Chart

Step	Check	Tester	Cal Points	Control Settings	Adjust	Desired Results	Remarks
1	Power Supply	DVM	C111			+15 ±0.05 Vdc	If voltage is incorrect, proceed to step 3.
2			C112			-15 ±0.05 Vdc	If voltage is correct, proceed to step 9.

Steps 3 - 7 are on the trig/pulse board. Place the cover on the generator and turn it upright. Remove the top cover for access to the trig/pulse board.

3	Power Supply	DVM	TP1 (COM) TP2 (±15 Vdc)		R27	+15 ±0.02 Vdc	
4			TP3			-15 ±0.05 Vdc	
5			TP4			+24 ±1 Vdc	
6			TP5			-24 ±1 Vdc	
7			TP6			+5 ±0.2 Vdc	

If steps 3 - 7 were performed, place the cover on, invert the generator and warm up the generator for ½ hour. Remove the uppermost cover for generator board access when required.

8	Cap Mult Balance	DVM (DCV)	TP5 (COM) TP1		R55	< 5 mV		
9	Power Ampl Balance		FUNCTION OUT		R181	0 ±0.01 Vdc		Terminate with 50Ω load.
10	Preamp Balance				ATTENUATION VERNIER: full cw	R252		
11	VCG Null	Scope	FUNCTION: □	R12	Minimum frequency shift	Observe one cycle at 50μs/div. Alternately short and open VCG IN BNC while adjusting R12.		
12	1000:1 Freq		FREQ VERNIER: full ccw	R13 BOD Freq Adj	< 1 cycle (< 200 Hz)	Scope on .5 ms/div.		

Table 5-1. Calibration Chart (Continued)

Step	Check	Tester	Cal Points	Control Settings	Adjust	Desired Results	Remarks
13	1000:1 Symmetry	Scope	FUNCTION OUT		R16 BOD Sym	Symmetrical waveform	<i>NOTE: Steps 13 and 14 are interactive.</i>
14	Main Symmetry			FREQ VERNIER: full cw Dial: 2.0 FREQ MULT: 1K	R35 TOD Sym	Symmetrical waveform	
15	Sine Distortion	Distortion Analyzer, Scope		FUNCTION: \wedge	R120 Triangle Balance	Symmetrical residue	Connect FUNCTION OUT to distortion analyzer and distortion analyzer output to scope. Set scope to .1V/div. Sync scope to SYNC OUT BNC loaded into 50 Ω .
16				R93, R107 Triangle Peaks	Minimum sine distortion	If either adjustment is going near a stop, re-center both pots and return to step 15.	
17	Main Freq	Frequency Counter/ Timer		FUNCTION: \sqcap	R4 TOD Freq Adj	2000 \pm 10 Hz	Remove SYNC OUT cable.
18	Cap Mult Freq			FREQ MULT: 10	R48	20 \pm 0.1 Hz	
19	X 10M Freq			FREQ MULT: 10M Dial: Vary	C40	Best frequency tracking over X 10M range	
20	X 1M Freq			FREQ MULT: 1M Dial: Vary	C34	Best frequency tracking over X 1M range	This adjustment must be made each time step 20 is done.
21	Trigger Baseline	Scope	FUNCTION: \wedge GENERATOR MODE: TRIG Dial: Vary	R162	Minimum shift of baseline around 0 Vdc		

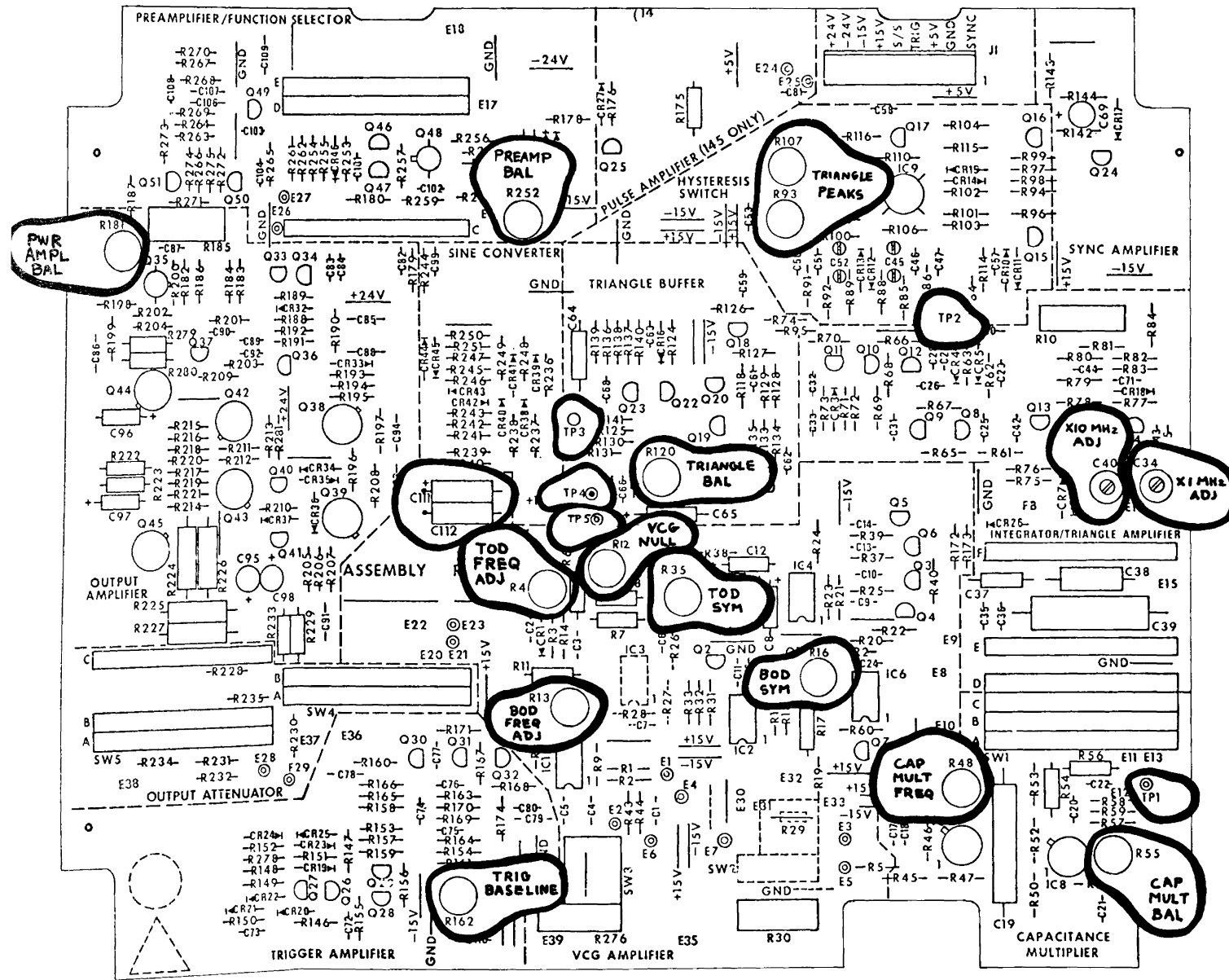


Figure 5-1. Generator Board

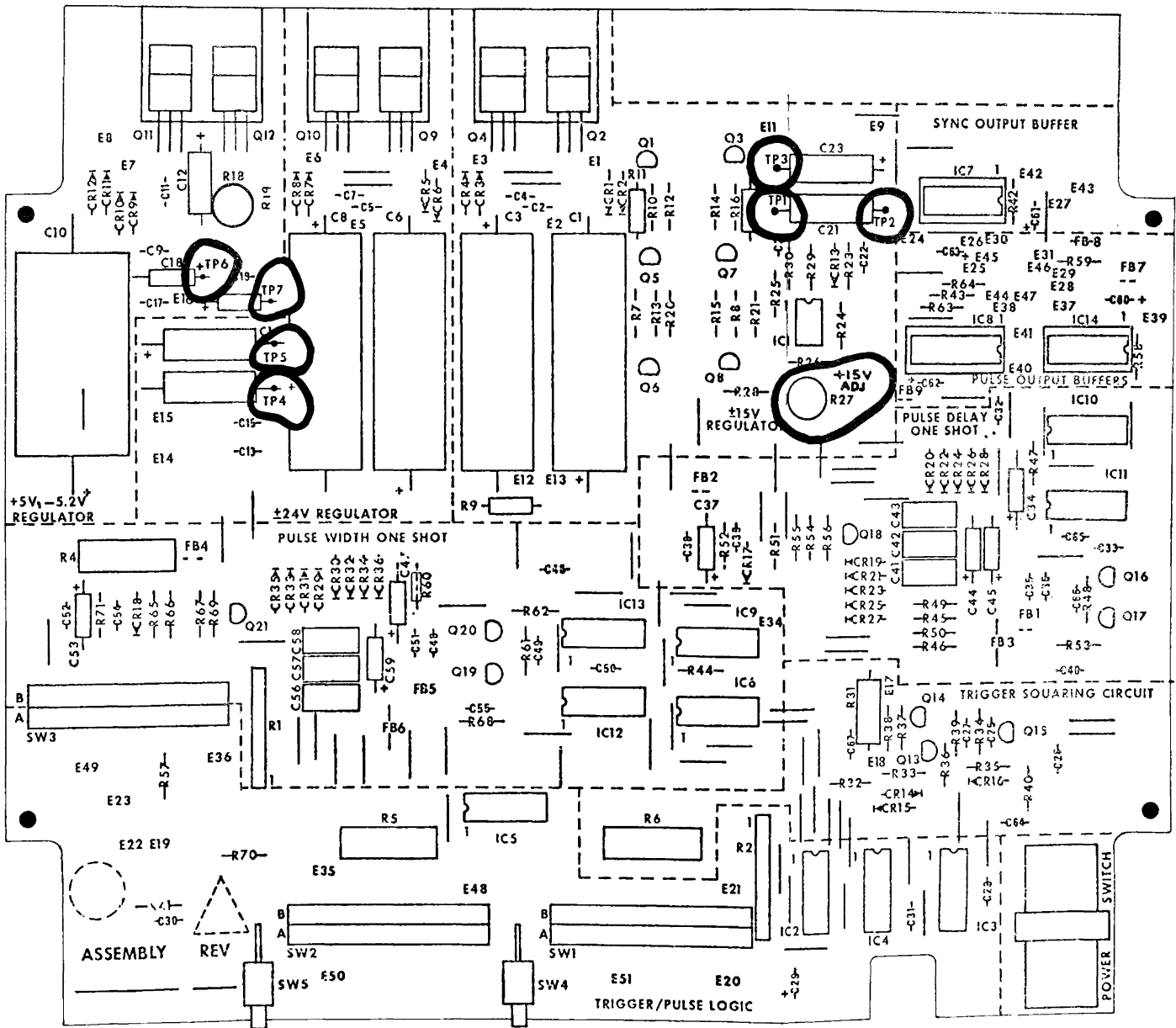


Figure 5-2. Trig/Pulse Board

6

SECTION

6

TROUBLESHOOTING

6.1 FACTORY REPAIR

Wavetek maintains a factory repair department for those customers not possessing the necessary personnel or test equipment to maintain the instrument. If an instrument is returned to the factory for calibration or repair, a detailed description of the specific problem should be attached to minimize turnaround time.

6.2 TROUBLESHOOTING CHARTS

Troubleshooting charts are given in figures 6-1 thru 6-9. The charts do not cover every possible trouble, but will be an aid in systematically isolating faulty components.

Figure 6-1. Initial Checks, Generator Board

Figure 6-2. Generator Loop Checks, Generator Board

Figure 6-3. VCG Checks, Generator Board

Figure 6-4. Generator Output Checks

Figure 6-5. Trigger and Gate Mode Checks, Trig/Pulse Board

Figure 6-6. Power Supply Checks, Trig/Pulse Board

Figure 6-7. Generator Input and Output Checks

6.3 TROUBLESHOOTING INDIVIDUAL COMPONENTS

6.3.1 Transistor

1. A transistor is defective if more than one volt is measured across its base emitter junction in the forward direction.
2. A transistor when used as a switch may have a few volts reverse bias voltage across base-emitter junction.
3. If the collector and emitter voltages are the same, but the base emitter voltage is less than 500 mV forward voltage (or reversed bias), the transistor is defective.
4. A transistor is defective if its base current is larger than 10% of its emitter current (calculate currents from voltage across the base and emitter series resistors).
5. In a transistor differential pair (common emitter stages), either their base voltages are the same in normal operating condition, or the one with less forward voltage across its base emitter junction should be off (no collector current); otherwise, one of the transistors is defective.

6.3.2 Diode

1. A diode is defective if there is greater than one volt (typically 0.7 volt) forward voltage across it.

6.3.3 Operational Amplifier (e.g., 741, 1458)

1. The “+” and “-” inputs of an operational amplifier will have less than 15 mV voltage difference when operating under normal conditions.
2. When the output of the amplifier is connected to the “-” input (voltage follower connection), the output should be the same voltage as the “+” input voltage; otherwise, the operational amplifier is defective.

6.3.4 Capacitor

1. Shorted capacitors have zero volts across their terminals.
2. Opened capacitor can be located (but not always) by using a good capacitor connected in parallel with the capacitor under test and observing the resulting effect.

6.3.5 Digital TTL IC's (e.g. 7400 Series)

1. The device is operating correctly if the output high state is $> +2.4V$ and low state is $< +0.5V$.
2. The input must show the same two levels as in step 1. If the levels are between $+0.8V$ and $+2.0V$, the connection to the driving circuit output is open.

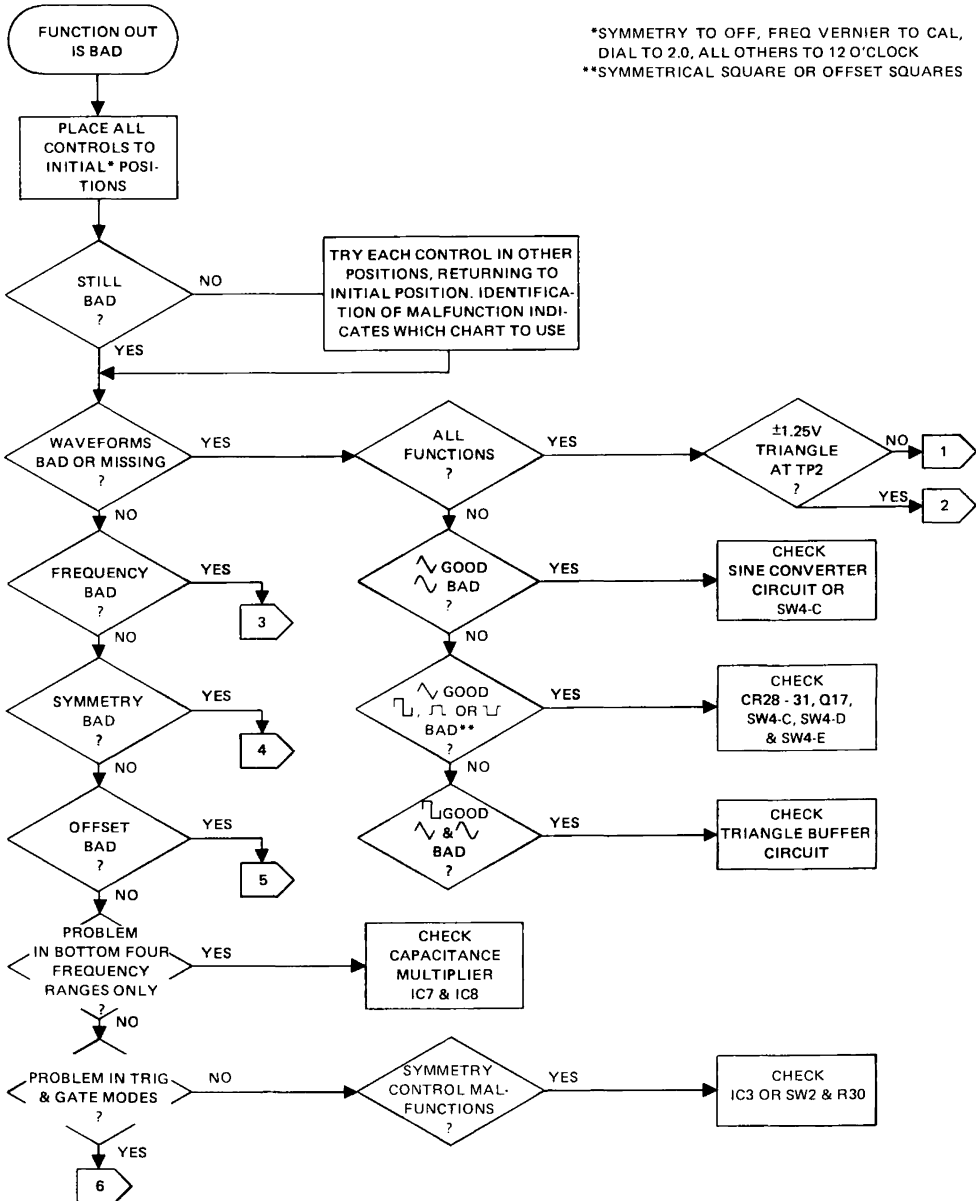
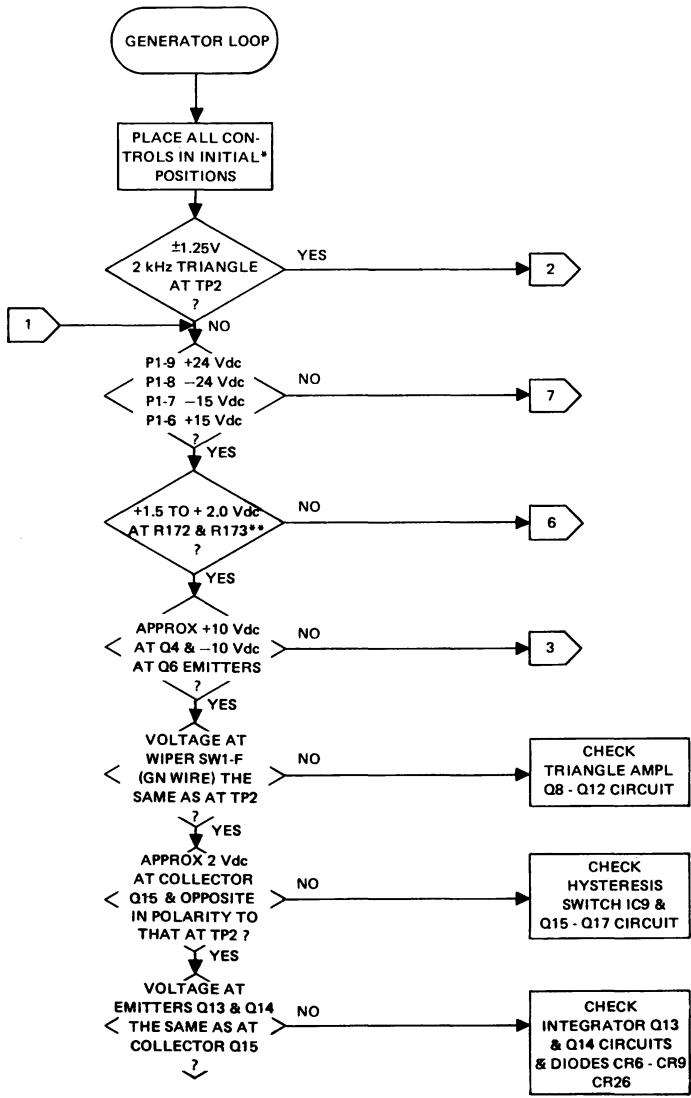
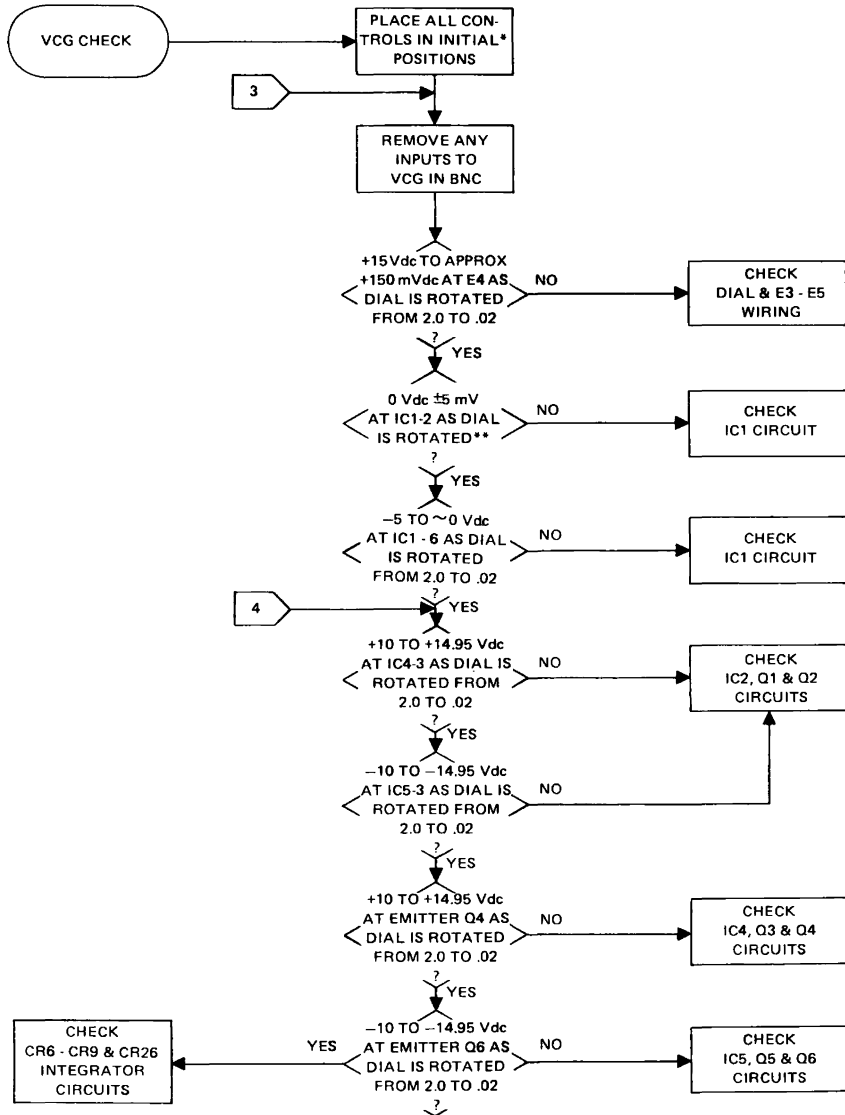


Figure 6-1. Initial Checks, Generator Board



*SYMMETRY TO OFF, FREQ VERNIER TO CAL, DIAL TO 2.0, ALL OTHERS TO 12 O'CLOCK
 **A NEGATIVE VOLTAGE HERE STOPS GENERATOR FOR TRIGGERED OPERATION
 ***USE SCOPE AND HIGH IMPEDANCE PROBE

Figure 6-2. Generator Loop Checks, Generator Board



*SYMMETRY TO OFF, FREQ VERNIER TO CAL, DIAL TO 2.0, ALL OTHERS TO 12 O'CLOCK
 **USE SCOPE AND HIGH IMPEDANCE PROBE FOR THIS AND SUBSEQUENT VCG MEASUREMENTS

Figure 6-3. VCG Checks, Generator Board

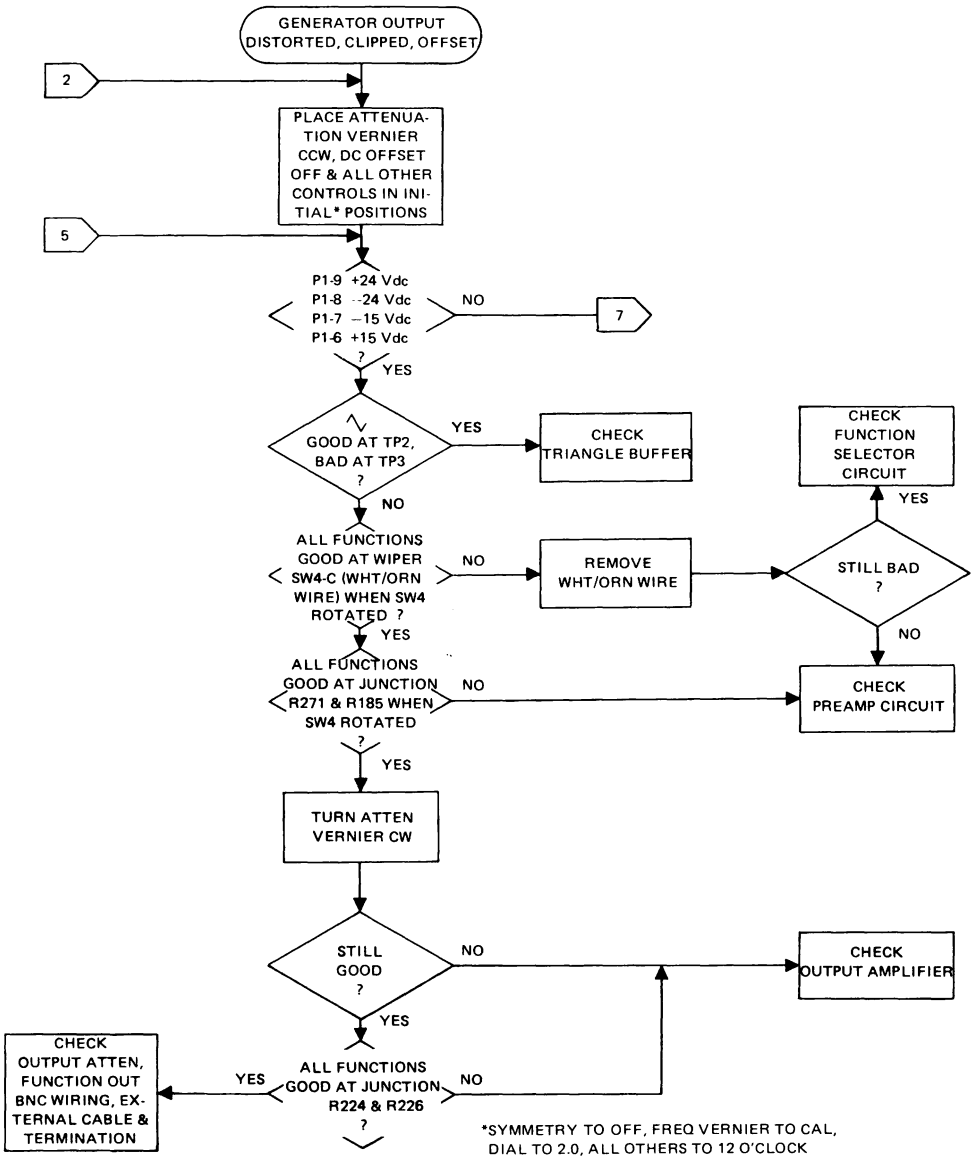
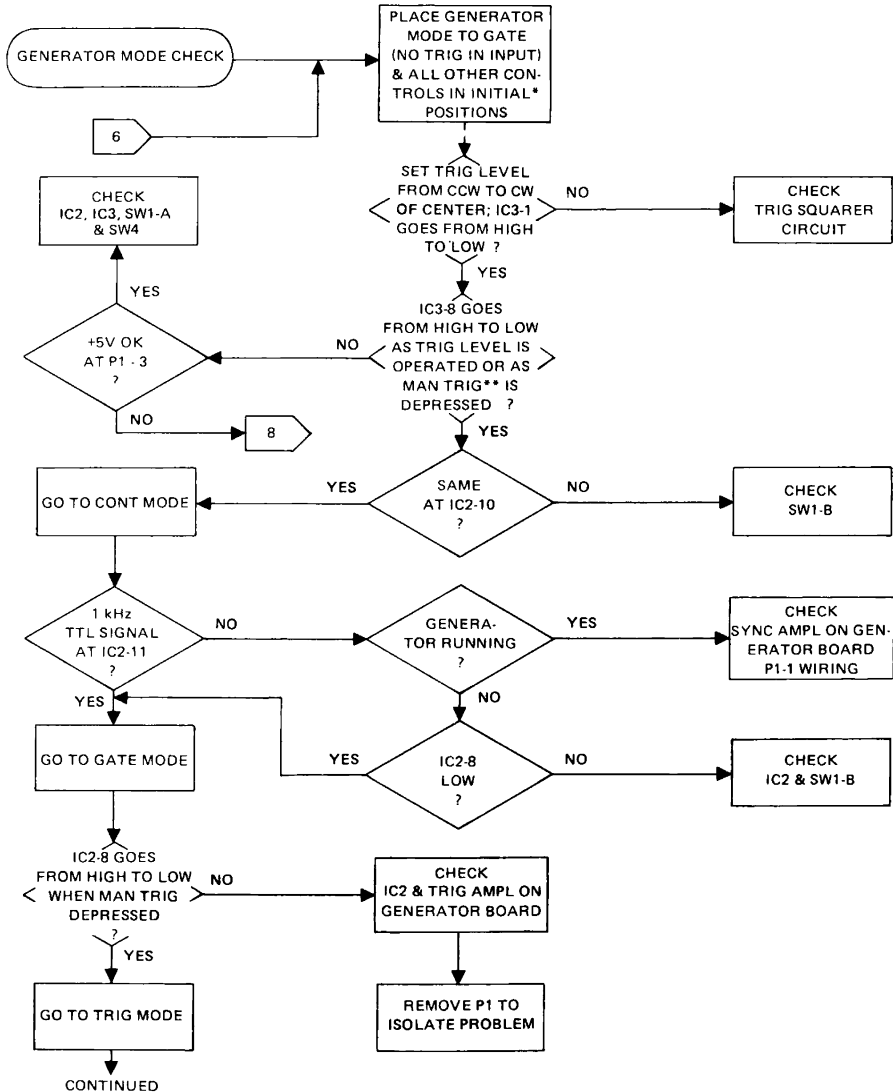


Figure 6-4. Generator Output Checks, Generator Board



*SYMMETRY TO OFF, FREQ VERNIER TO CAL, DIAL TO 2.0, ALL OTHERS TO 12 O'CLOCK
 **RETURN TRIG LEVEL CCW TO OPERATE MANUAL TRIGGER

Figure 6-5. Trigger and Gate Mode Checks, Trig/Pulse Board (Page 1 of 2)

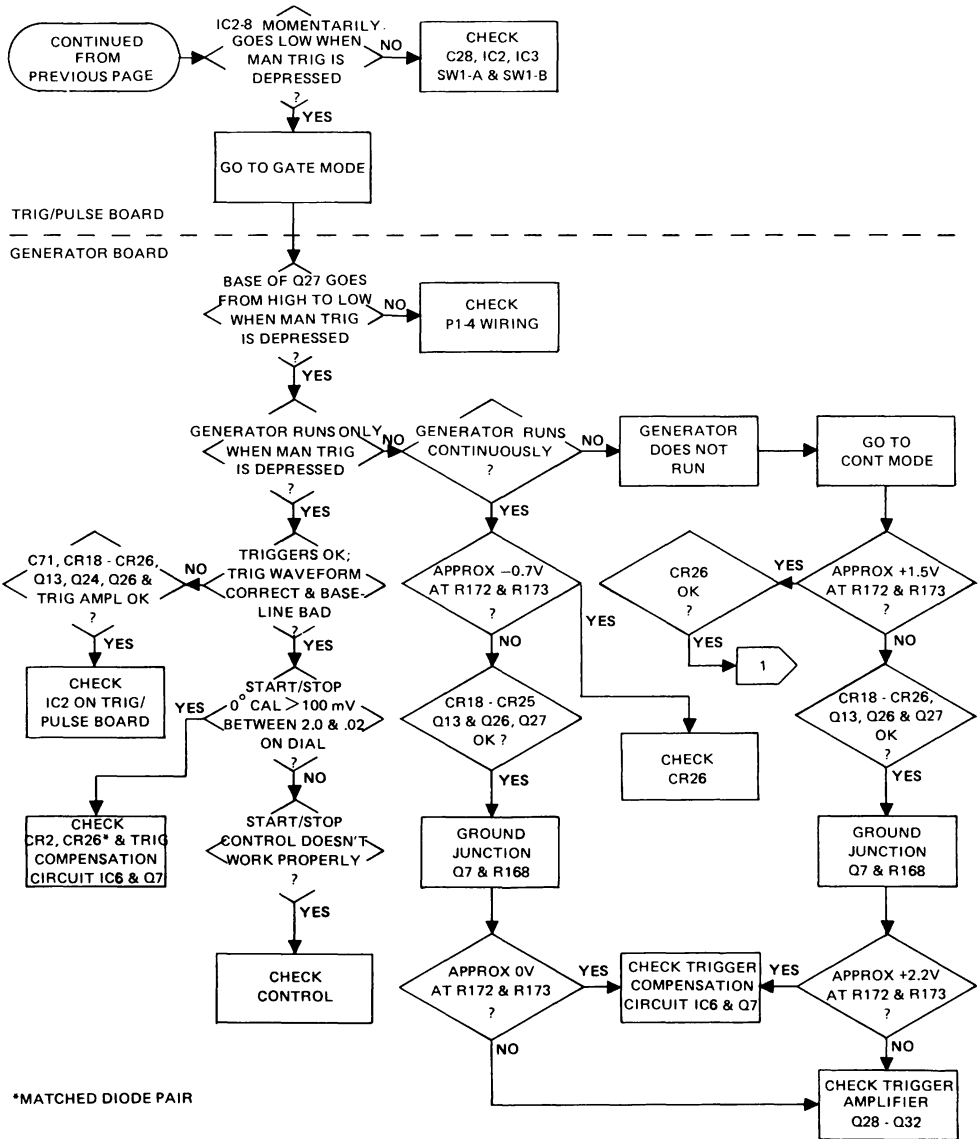


Figure 6-5. Triger and Gate Mode Checks, Trig/Pulse Board (Page 2 of 2)

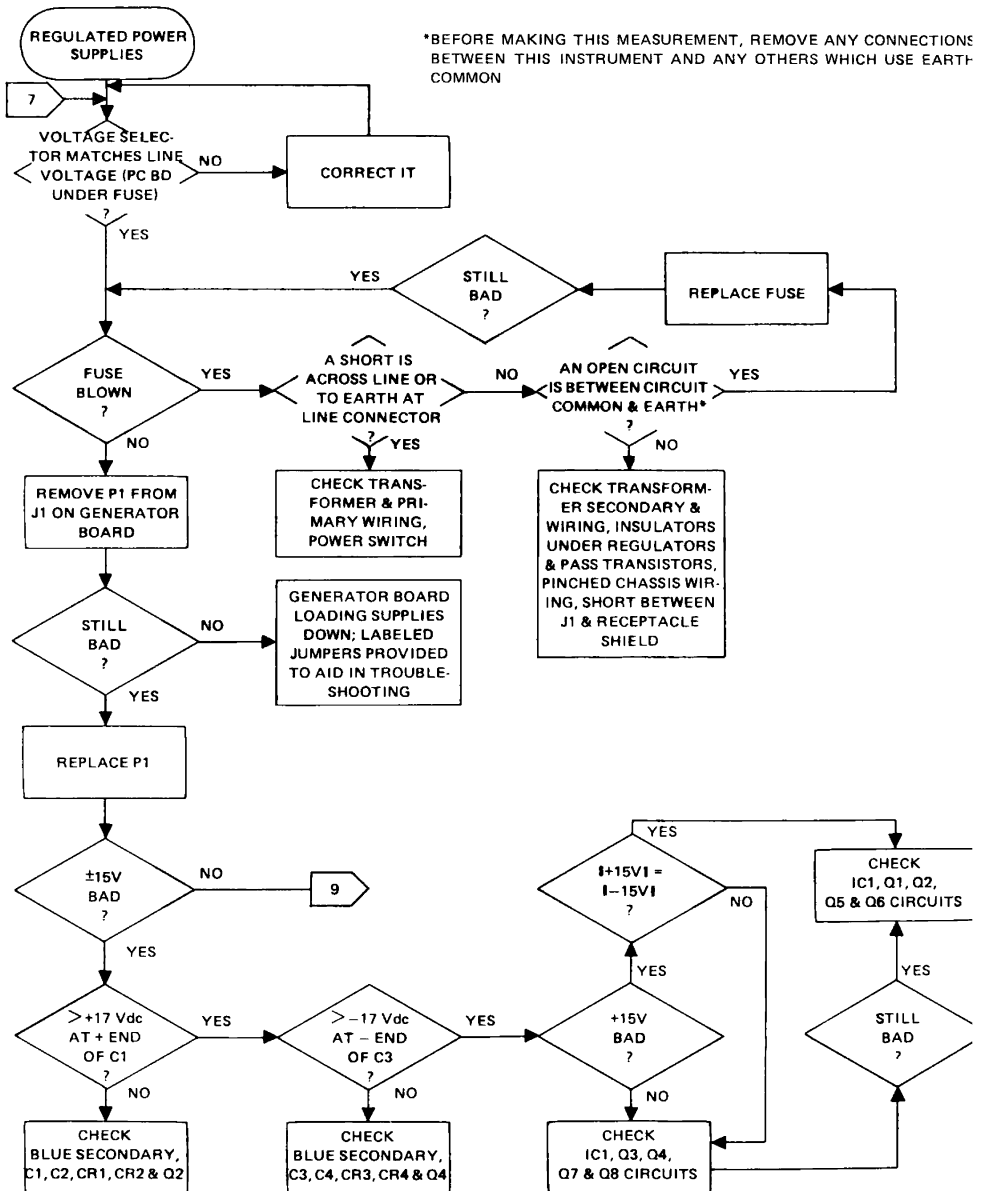


Figure 6-6. Power Supply Checks, Trig/Pulse Board (Page 1 of 2)

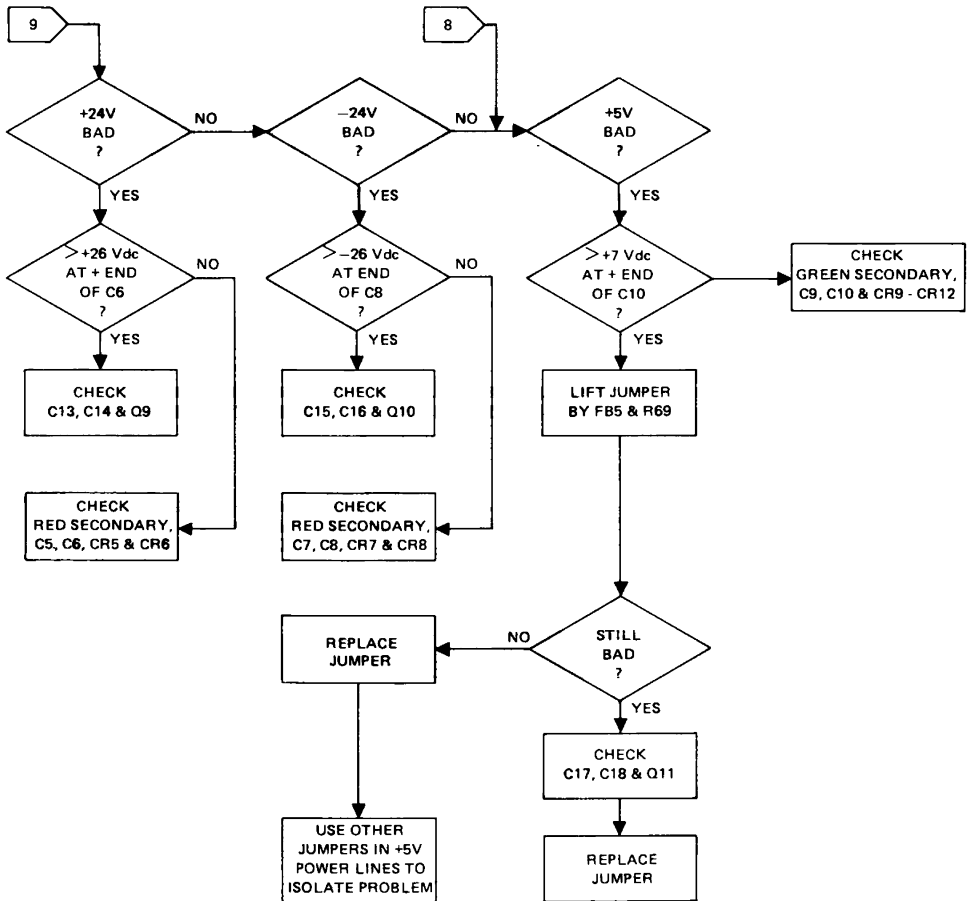


Figure 6-6. Power Supply Checks, Trig/Pulse Board (Page 2 of 2)

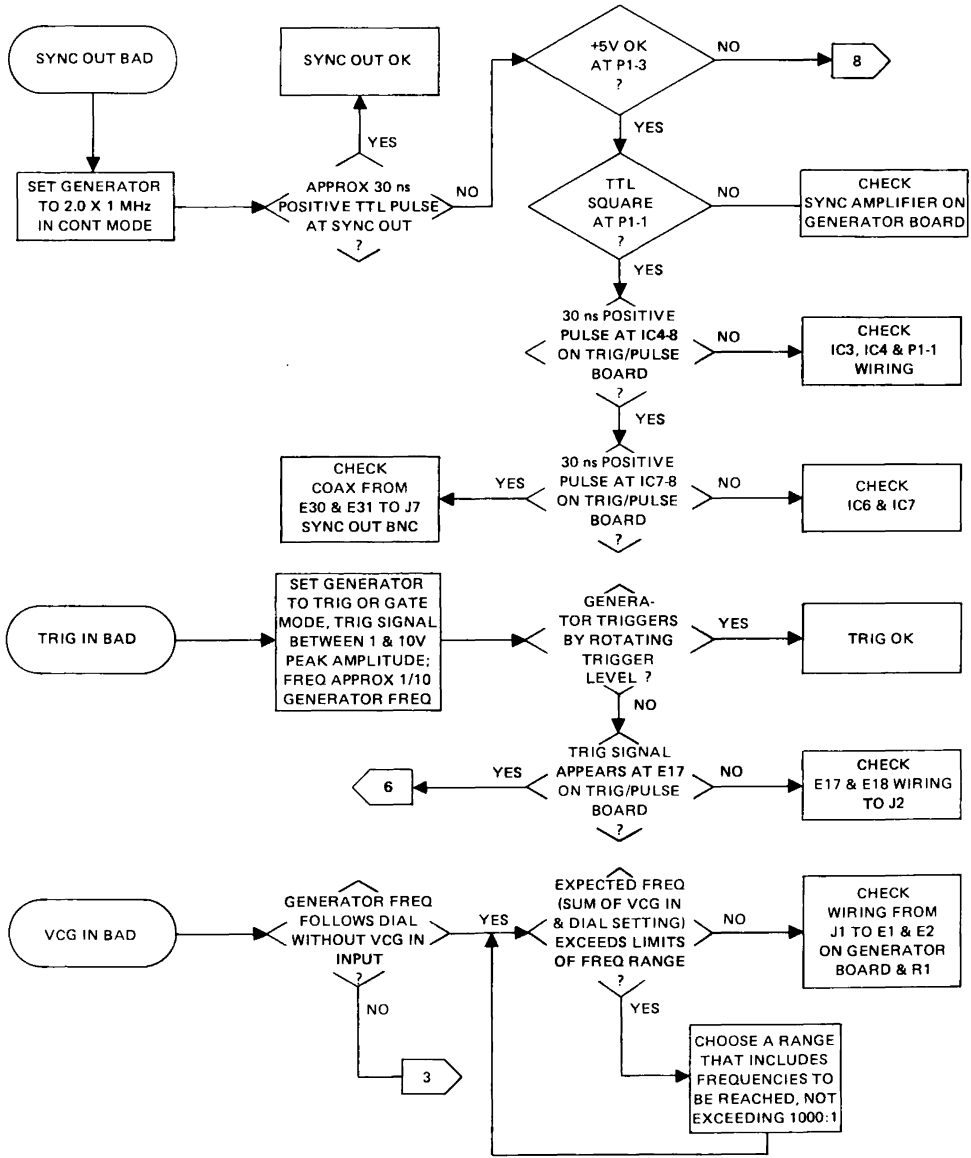


Figure 6-7. Generator Input and Output Checks (Page 1 of 2)

SECTION 7

PARTS AND SCHEMATICS

7.1 DRAWINGS

The following assembly drawings (with parts lists) and schematics are in the arrangement shown below.

7.2 ORDERING PARTS

When ordering spare parts, please specify part number, circuit reference, board, serial number of unit and, if applicable, the function performed.

7.3 ADDENDA

Under Wavetek's product improvement program, the latest electronic designs and circuits are incorporated into each Wavetek instrument as quickly as development and testing permit. Because of the time needed to compose and print instruction manuals, it is not always possible to include the most recent changes in the initial printing. Whenever this occurs, addendum pages are prepared to summarize the changes made and are inserted immediately inside the rear cover. If no such pages exist, the manual is correct as printed.

Drawings	Drawing No.
CHASSIS	
Assembly Drawing	0102-00-0589
Schematic	0004-00-0101
Parts List	1101-00-0589
GENERATOR BOARD	
Parts Locator Drawing	0100-00-0556
Assembly Drawing (Sheet 2 of 2 only)	0101-00-0584
Schematic	0103-00-0556
Parts List	1100-00-0584
TRIG/PULSE BOARD	
Parts Locator Drawing	0100-00-0565
Assembly Drawing (Sheet 2 of 2 only)	0101-00-0585
Schematic	0103-00-0565
Parts List	1100-00-0585

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REV	ECN	BY	DATE	APP
B	ECN 1800	JRM	9-5-8	✓
C	ECN 1801	JRM	9-6-8	✓
D	# 383	U	11-1-87	✓

D

D

C

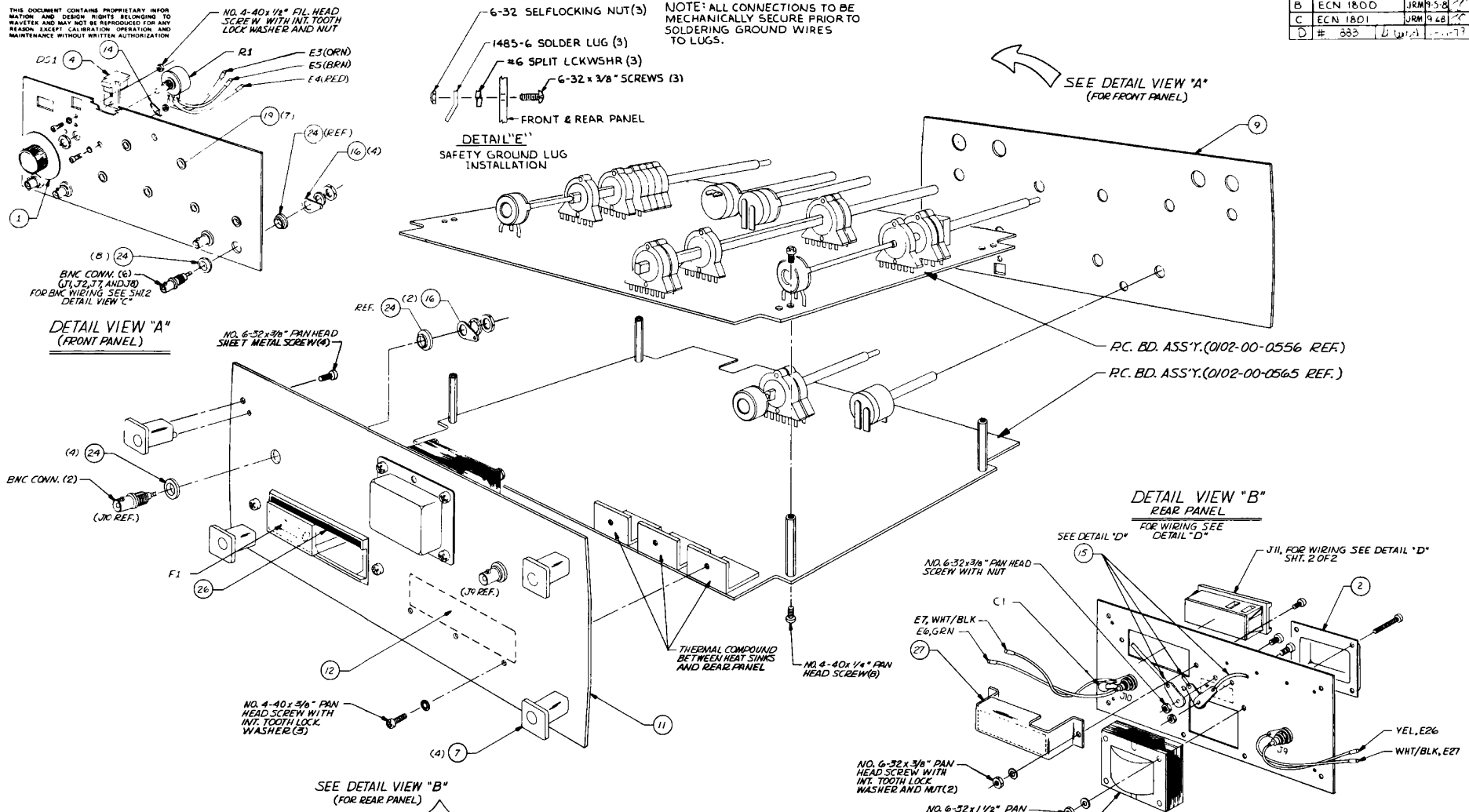
C

B

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A

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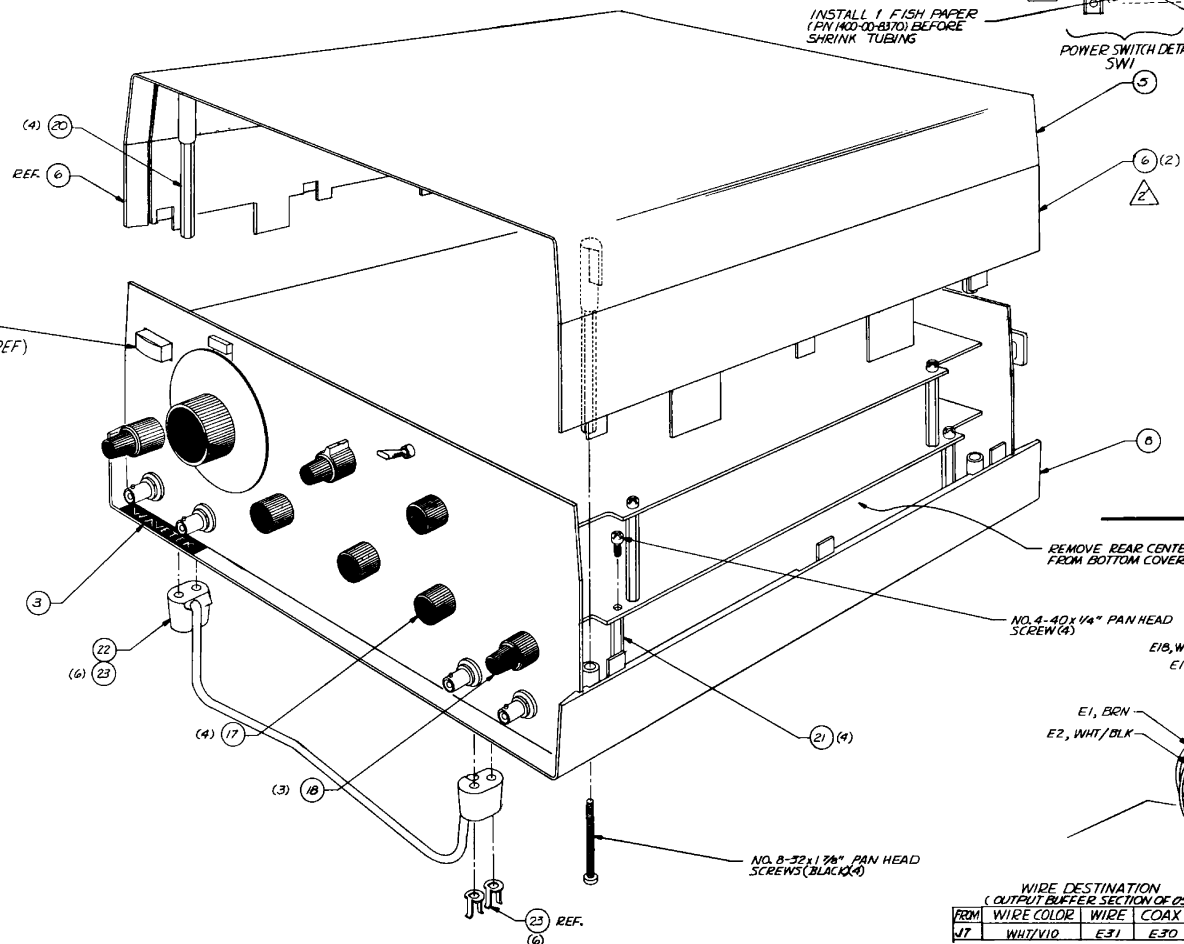


NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES		DATE	3-22-77	WAVETEK SAN DIEGO • CALIFORNIA	
MATERIAL		PROJ	143		
FINISH		TOLERANCES UNLESS OTHERWISE SPECIFIED		TITLE ASSEMBLY STANDARD CHASSIS	
WAVETEK PROCESS		XXX ± .010 ANGLES 1:1			
DO NOT SCALE DWG		SCALE			
MODEL NO	143	DWG NO	002-00-0589	REV	D
SCD	23338	SHEET		1 OF 2	

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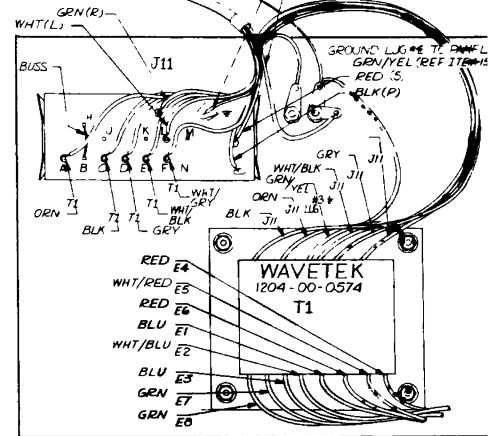
REV	ECN	BY	DATE
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INSTALL 1 FISH PAPER (PN 400-00-8370) BEFORE SHRINK TUBING

POWER SWITCH DETAIL SWI

GROUND LUG FRONT PANEL (GRN/YEL)



DETAIL VIEW "C" BMC CONN. WIRING, FRONT PANEL

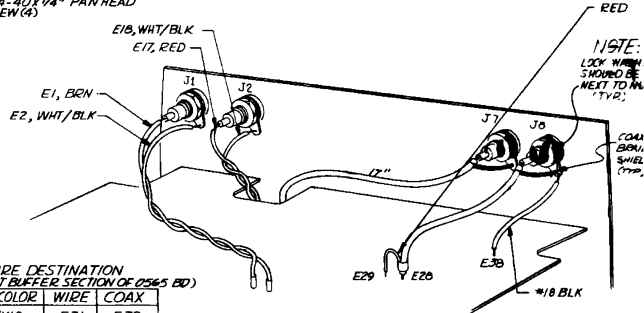
REMOVE REAR CENTER NUB FROM BOTTOM COVER

NO. 4-40 x 1/4" PAN HEAD SCREW (4)

NO. 8-32 x 1 3/8" PAN HEAD SCREWS (BLACK) (4)

WIDE DESTINATION (OUTPUT BUFFER SECTION OF 0565 BD)

FROM	WIRE COLOR	WIRE	COAX
J1	WHT/YEL	E31	E30



2 CEMENT ITEM 6(2) TO ITEM 5 (TYR BOTH SIDES)

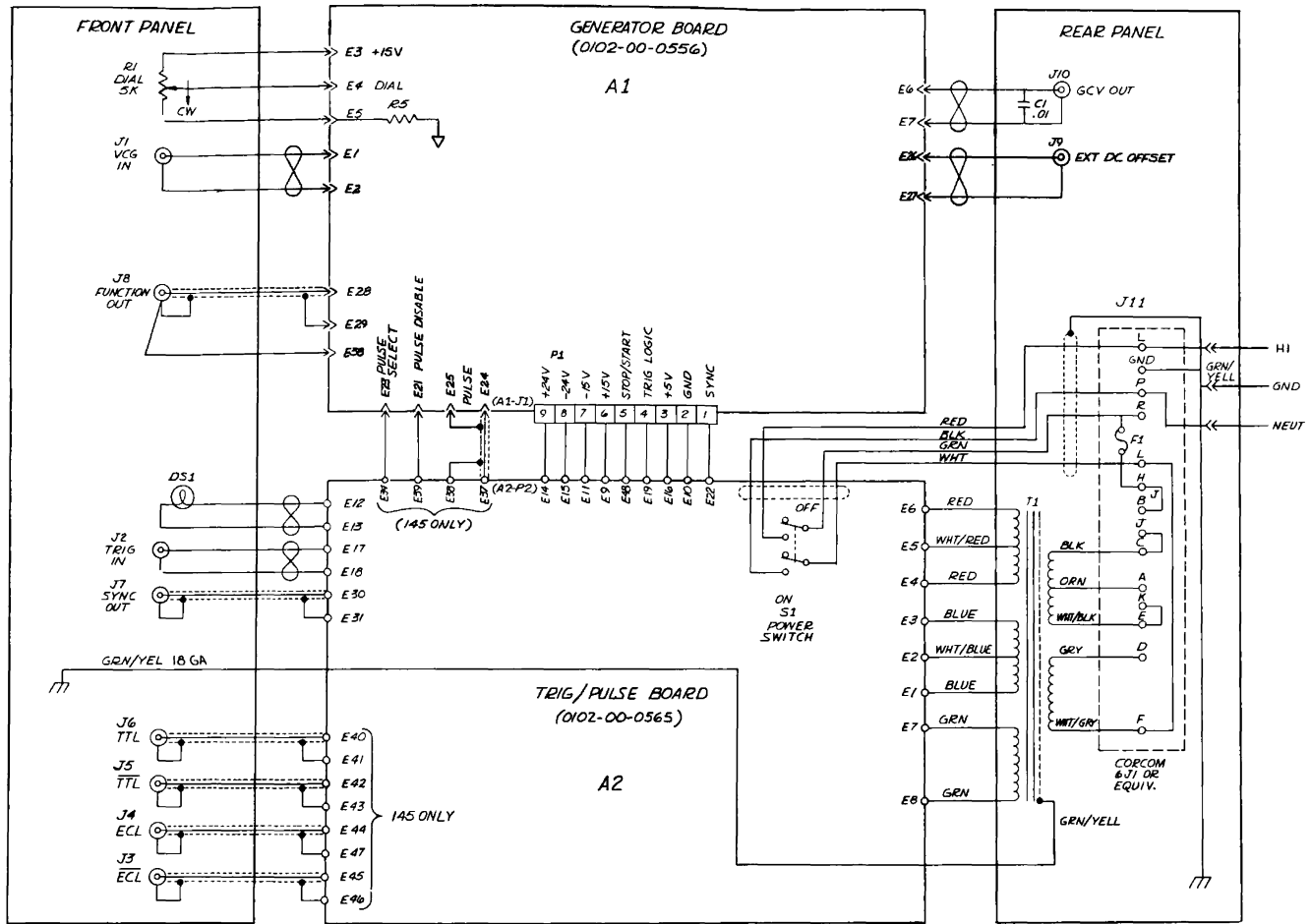
1 MOUNT SWITCH TO P.C. BOARD PRIOR TO INSTALLING FRONT PANEL. USE NO. 2-56 x 1/4" PAN HEAD SCREW WITH NUT (2)

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DESIGNER	DATE	
MATERIAL	D. COOPER	WBT	
RELEASE NUMBER	3-28-77	TITLE	ASSEMBLY STANDARD CHASSIS
TOLERANCES UNLESS OTHERWISE SPECIFIED	20X 1 810	ANGLES 1"	
FINISH	AS B	DO NOT SCALE DWG	
WAVETEK PROCESS	SCALE	DWG NO.	143
		FORM NO.	0102-00-0589
		ISS	2333B
		SHEET	2 OF 4

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REV	ECN	BY	DATE	APP
B	ECN 1600	JRM	9-5-87	
C	ECN 1601	JRM	9-6-87	

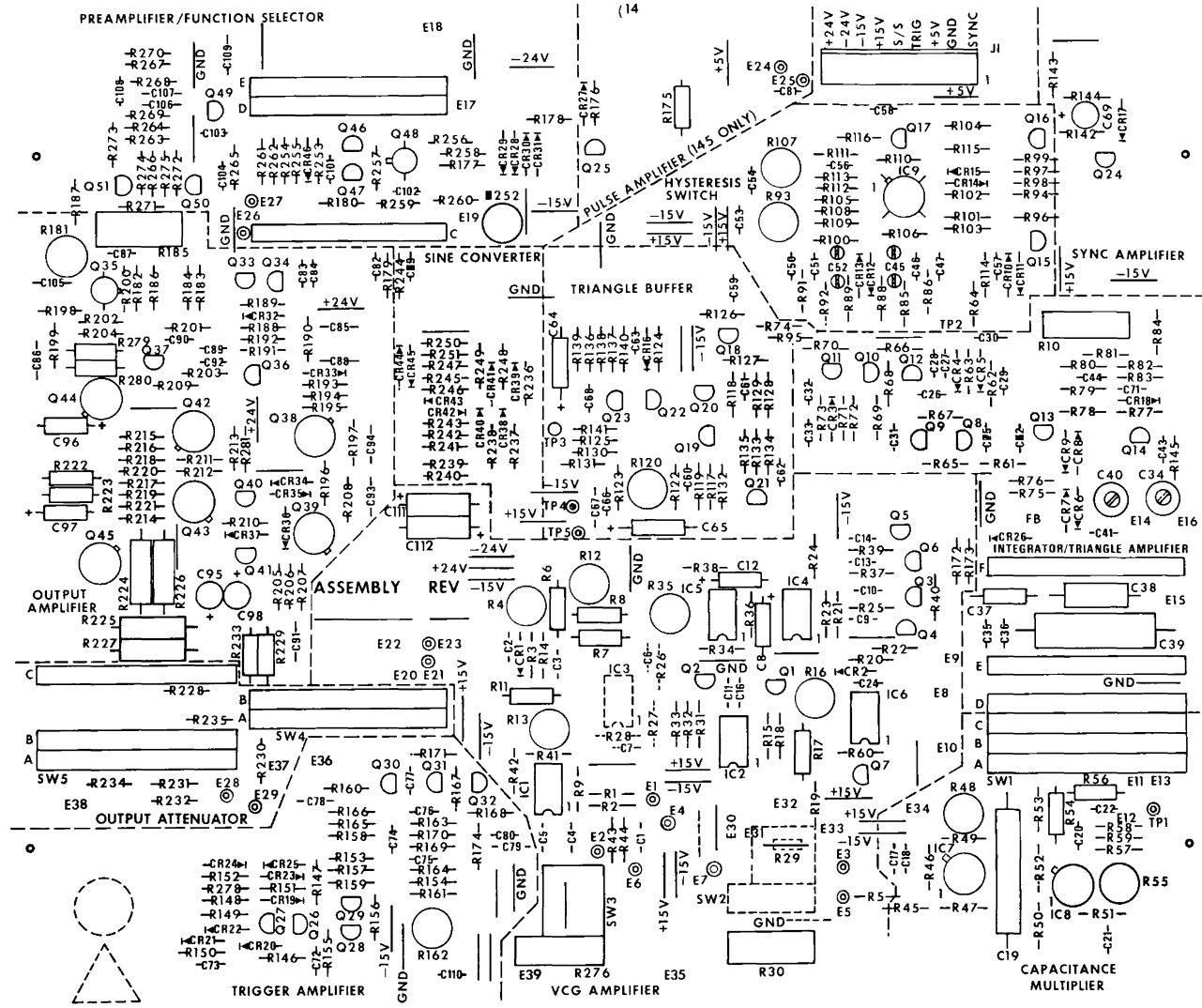


3/16A 220-240VAC
1. F1 - 3/8A 100-120VAC

NOTE: UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN D. COOPER	DATE 4-27-87	
MATERIAL	DESIGNED BY D. COOPER	DATE 3-23-87	
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XX - 010 XX - 020	SCALE DO NOT SCALE DWG	INSTRUMENT SCHEMATIC
MODEL NO 143/145	DWG NO 0004-00-0101	REV C	
3000 23338	SHEET 1 OF 1		

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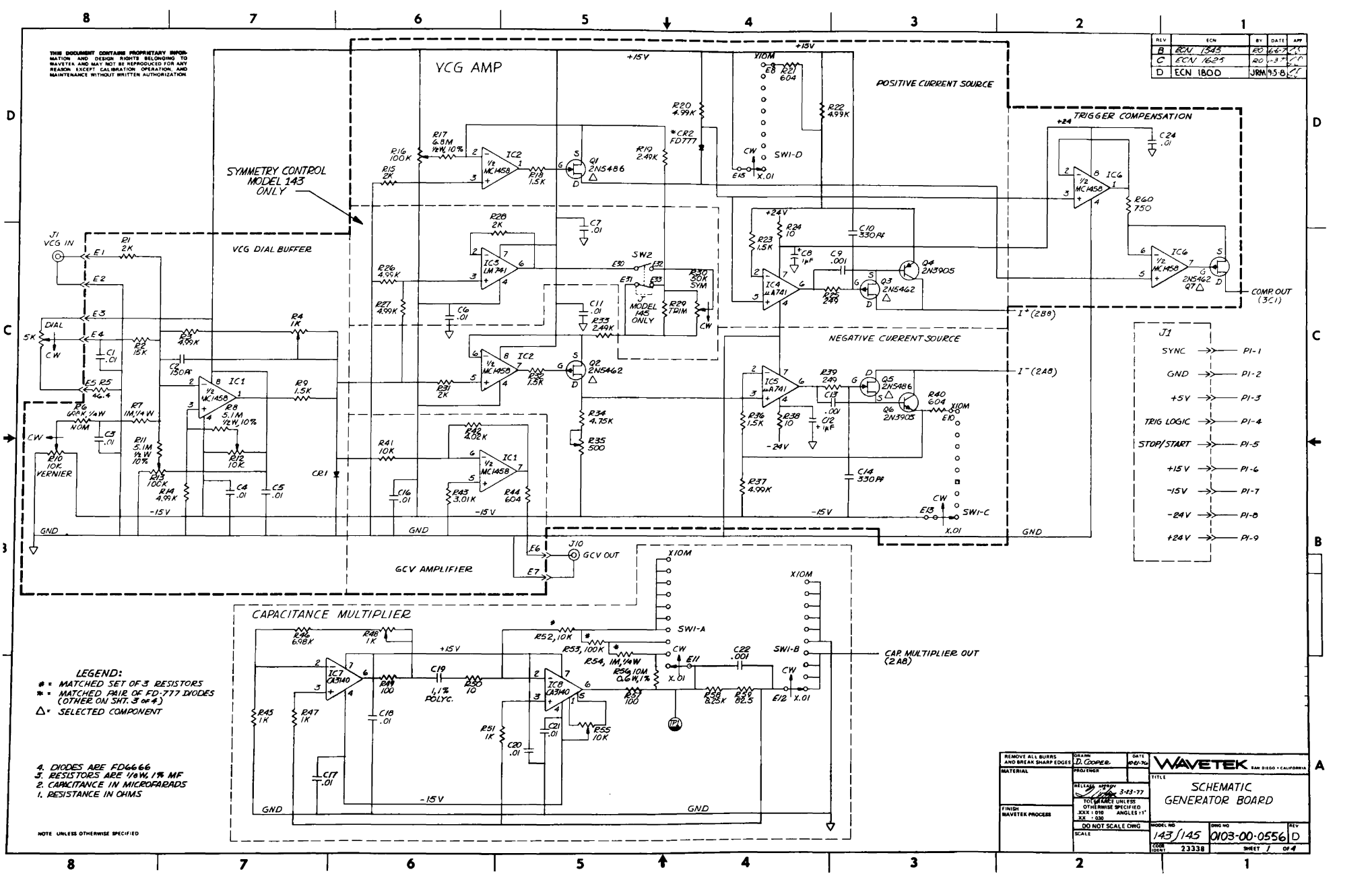


NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DATE	WAVETEK SAN DIEGO - CALIFORNIA	
	PROF. ENGR.	TITLE	
	REL. AUTH. APPROV.	GENERATOR BOARD PARTS LOCATER	
	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX - 010 ANGLES 11° ± .030 DO NOT SCALE DWGS	MODEL NO.	REV
FINISH WAVETEK PROCESS	SCALE	143/145	0100-00-0566 C
		23338	SHEET OF

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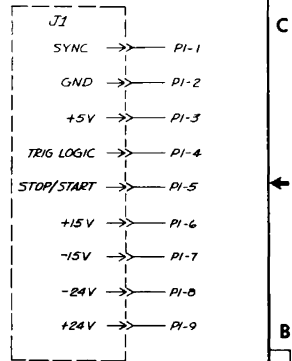
REV	ECN	BY	DATE	APP
B	ECN 1543	EO	6-6-77	1
C	ECN 1625	RO	1-3-78	1
D	ECN 1800	JRM	9-5-80	1



LEGEND:
 ** MACHED SET OF 3 RESISTORS
 * MACHED PAIR OF FD-777 DIODES (OTHER ON SHT. 3 OF 4)
 Δ SELECTED COMPONENT

4. DIODES ARE FD6666
 3. RESISTORS ARE 1/8W, 1% MF
 2. CAPACITANCE IN MICROFARADS
 1. RESISTANCE IN OHMS

NOTE UNLESS OTHERWISE SPECIFIED

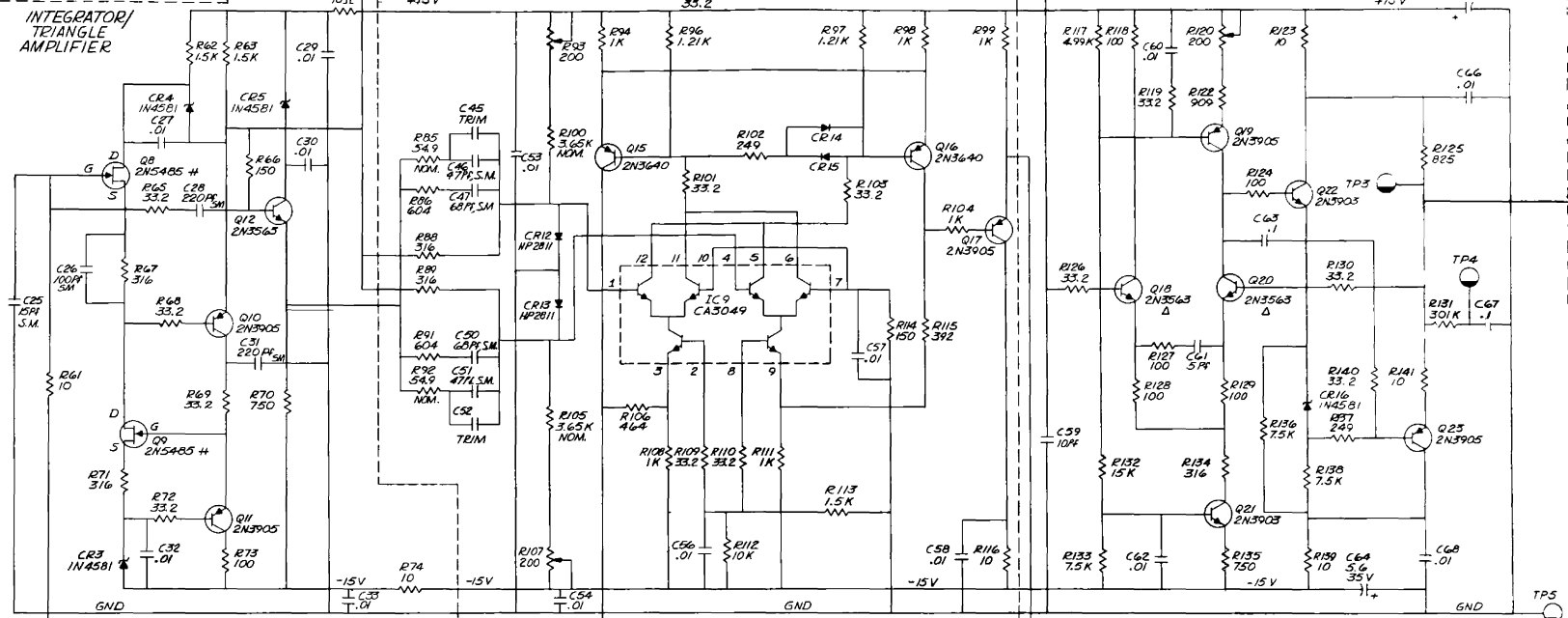


REMOVE ALL BURRS AND BREAK SHARP EDGES	DATE 10-6-76	WAVETEK SAN DIEGO • CALIFORNIA
MATERIAL	DESIGNER D. COOPER	
FINISH WAVETEK PROCESS	RETAIN 3-43-77 TO SCALE UNLESS OTHERWISE SPECIFIED 3/8" = 010" ANGLES 11° ± .030 DO NOT SCALE DIMS	TITLE SCHEMATIC GENERATOR BOARD
SCALE	MODEL NO 143/145 SCALE 23338	PART NO Q103-00-0556 D REV 1 SHEET 7 OF 4

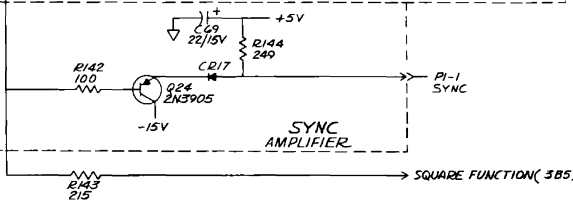
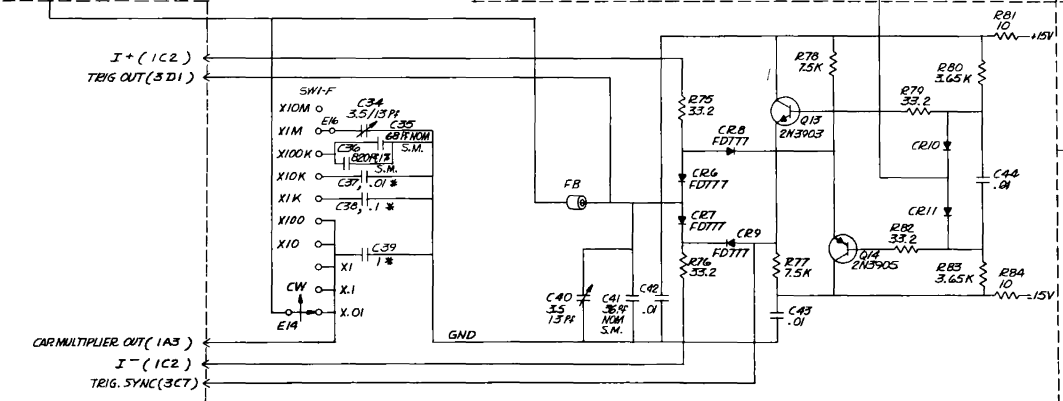
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HYSTERESIS SWITCH

TRIANGLE BUFFER



TRI-FUNCTION (4B8, 3B1)

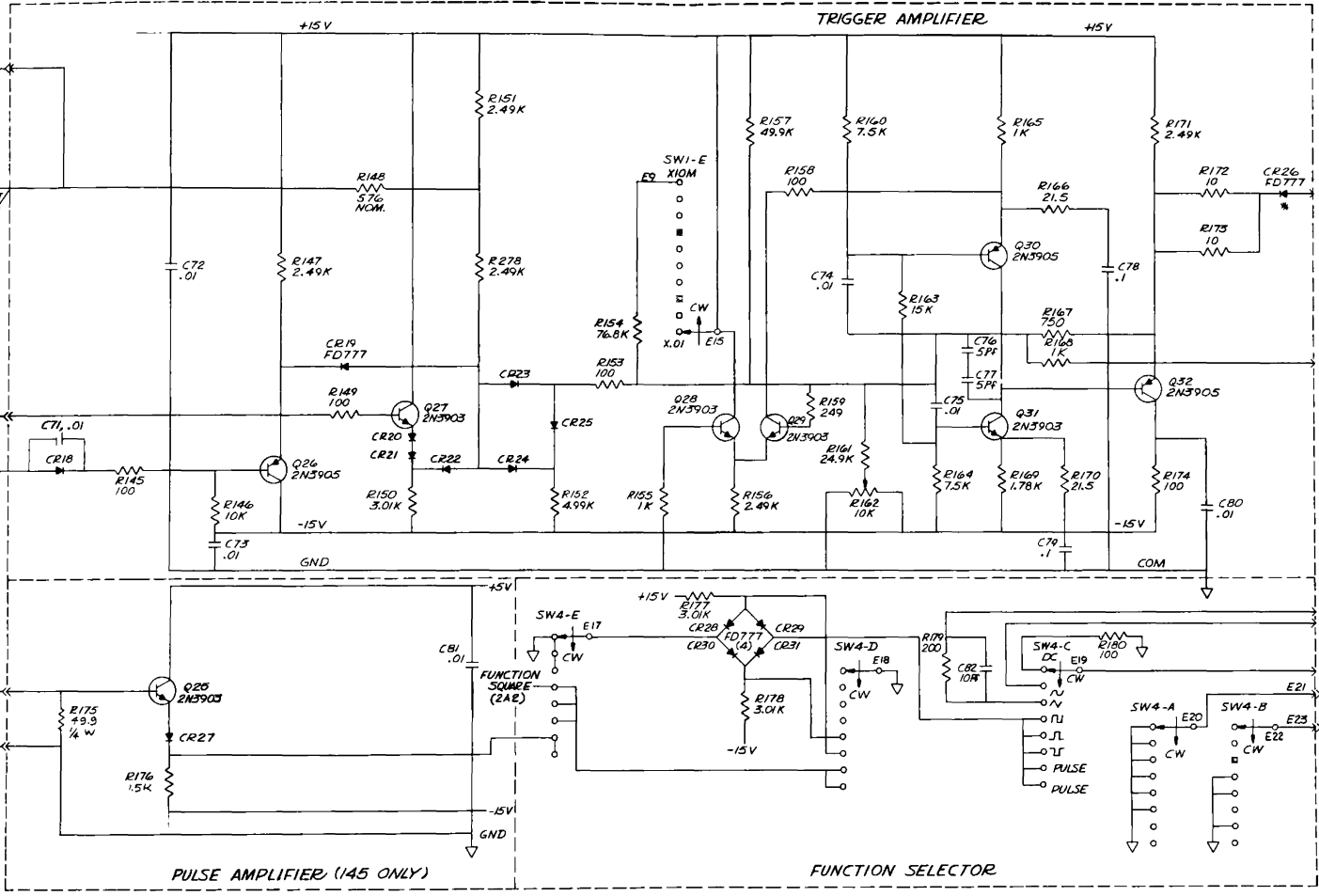
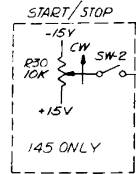


- LEGEND**
- Δ MATCHED SET (2) 2N3563
 - ≡ MATCHED SET (2) 2N5485
 - * MATCHED SET (3) CAPACITORS

REMOVE ALL BURRS AND BREAK SHARP EDGES FINISH WAVETEK PROCESS		DATE: 3-25-77 RECEIVED APPROV: [Signature] TOLERANCES UNLESS OTHERWISE SPECIFIED ANGLES: 1° DIM. > .005 DO NOT SCALE DIMS SCALE:	TITLE: SCHEMATIC GENERATOR BOARD
MODEL NO: 143/145 REV: 318	SHEET NO: 0103-00-0556 SHEET 2	WAVETEK SAN DIMM 11	

NOTE UNLESS OTHERWISE SPECIFIED
 2. ALL CAPACITORS IN MICROFARADS
 1. ALL RESISTORS IN OHMS: 1/BW, 1%

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D

C

B

A

- 5. DIODES ARE FD6666
- 4. RESISTORS ARE 1/4W, 1%, MF
- 3. CAPACITANCE IN MICROFARADS
- 2. RESISTANCE IN OHMS
- 1. * MATCHED PAIR OF FD777 (OTHER ON SWT. 1)

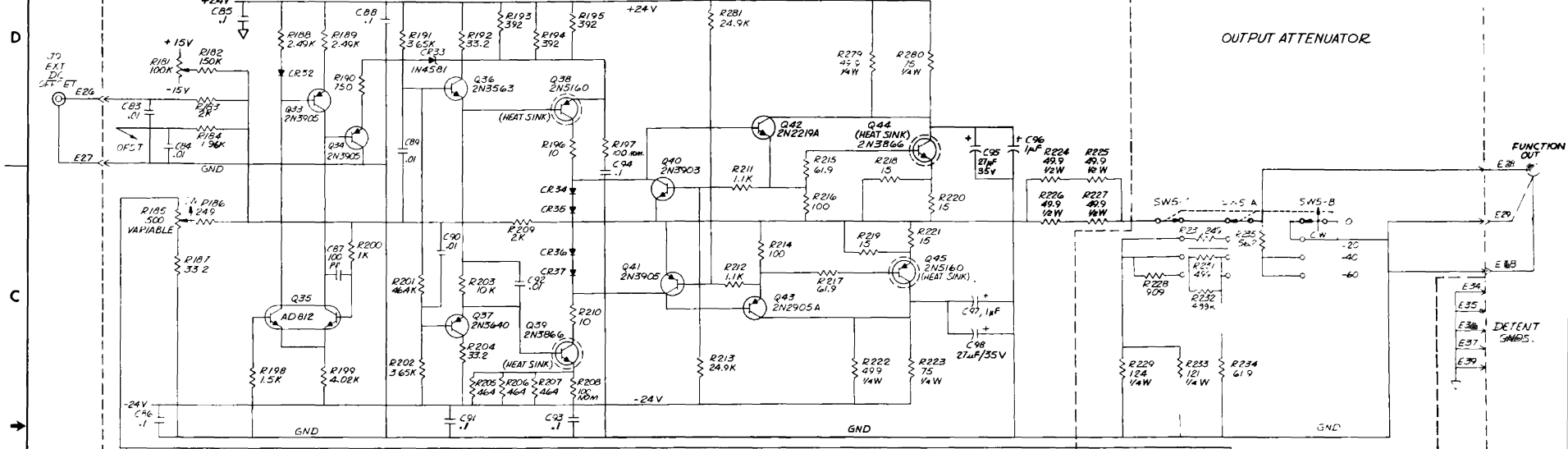
NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN J. COOPER	DATE 10/27/76	WAVETEK SAN DIEGO • CALIFORNIA
MATERIAL	DESIGNED BY J. COOPER	DATE 3-28-77	TITLE SCHEMATIC GENERATOR BOARD
FINISH	TO TOLERANCE UNLESS OTHERWISE SPECIFIED XXX - 010 ANGLES 1:1 ALL - SIZE	DO NOT SCALE DIMS	MODEL NO 145/145
SCALE	SCALE	DWG NO 0103-00-0556	REV D
		CODE 23338	SHEET 3 OF 4

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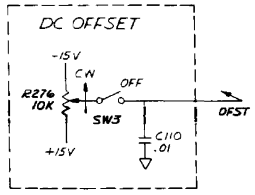
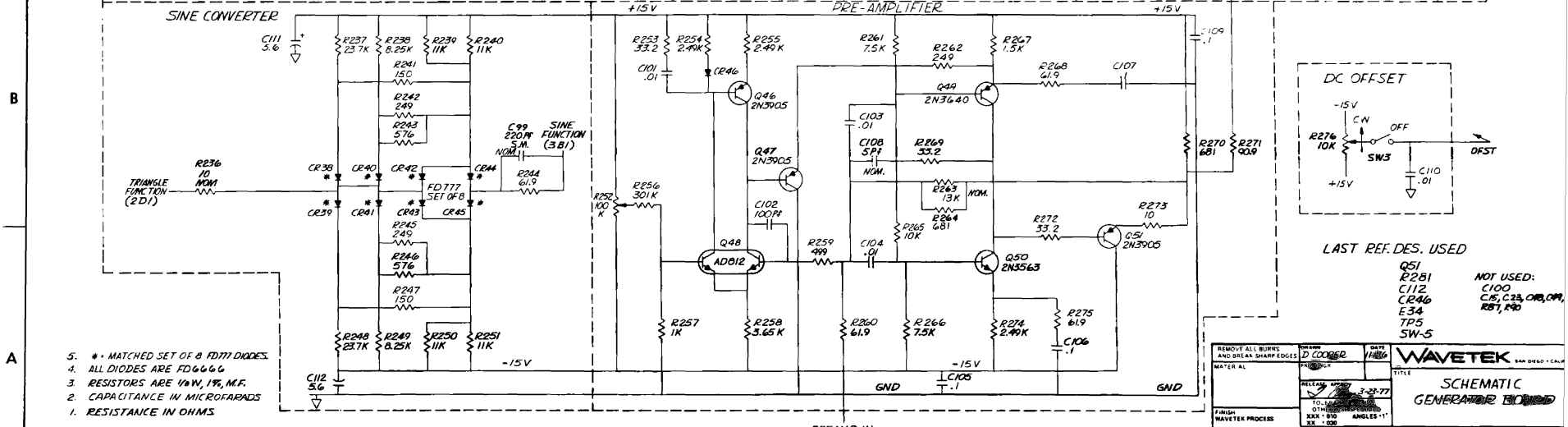
OUTPUT AMPLIFIER

OUTPUT ATTENUATOR



SINE CONVERTER

PRE-AMPLIFIER



LAST REF. DES. USED

- Q51
 - R281
 - C112
 - CR46
 - E34
 - E35
 - E36
 - E37
 - E39
 - TP5
 - SW-5
- NOT USED:
C100
C15, C23, C26, C27, C28
R27, R30

5. * - MATCHED SET OF 8 FD771 DIODES.
4. ALL DIODES ARE FD6666
3. RESISTORS ARE 1/4W, 1%, M.F.
2. CAPACITANCE IN MICROFARADS
1. RESISTANCE IN OHMS

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURNS AND BREAK SHARP EDGES	DATE	DATE	DATE
MATER. AC.	D COOPER	7/80	7/80
FINISH WAVE/TEK PROCESS	REVISION	2-25-77	
SCALE	NO. OF SHEETS	1	1
	OTHER COMMENTS		
	XXX - 100 ANGLES - 1		
	XX - 100		
	NO NOT SCALE DWG		
	SCALE		
	MODEL NO	143/145	0103-00-0556
	SHEET	23338	1

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REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
NONE	ASSY DRNG GENERATOR	0101-00-0504	WVTK	0101-00-0504	1
NONE	SCHEMATIC GENERATOR	0103-00-0550	WVTK	0103-00-0550	1
C10B7 C61 C76 C77	CAP,CEM,50PF,1KV	DD-050	CHL	1500-00-5011	4
C59 C82	CAP,CER,100PF,1KV	DD-100	CHL	1500-01-0111	2
C102 C87	CAP,CER,100PF,1KV	DD-101	CHL	1500-01-0111	4
C13 C22 C9	CAP,CER,.001MF,1KV	DD-102	CHL	1500-01-0211	3
C1 C101 C103 C104 C11 C110 C16 C17 C18 C24 C21 C24 C27 C29 C3 C30 C32 C33 C4 C42 C43 C44 C5 C53 C54 C56 C57 C58 C6 C60 C62 C66 C68 C7 C71 C72 C73 C74 C76 C80 C83 C84 C89 C90 C92	CAP,CER,.01MF,50V	CK-103	CHL	1500-01-0310	45
C105 C106 C107 C109 C63 C67 C78 C79 C85 C86 C88 C91 C93 C94	CAP,CER,.1MF,50V	CK-104	CHL	1500-01-0410	14
C2	CAP,CER,150PF,1KV	DD-151	CHL	1500-01-5111	1
C10 C14	CAP,CER,330PF,1KV	DD-331	CHL	1500-03-3111	2
C26	CAP,MICA,100PF,500V	DM15-101J	AMCO	1500-11-0100	1
C25	CAP,MICA,15PF,500V	DM15-150J	AMCO	1500-11-5000	1
C20 C31 C901	CAP,MICA,220PF,500V	DM15-221J	AMCO	1500-12-2100	3
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV J	PAGE 1

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
C39	CAP,SET,POLYIC COND,1111500-00-0005 TMU10007			1509-00-0008	
	CAP,POLYIC,1MF,100V PART OF 1509-00-0008 R21R111500-01-0504				
	GENERATOR	1700-00-0556	WVTK	1700-00-0556	1
	CONN,SPIN	09-60-1091	MOLEX	2100-02-0052	1
	BOLDED LUG	1497	BWITM	2100-04-0012	5
	TERM	200091	UNSECO	2100-05-0009	4
	PIN,MALE	61102-2	AMP	2100-05-0020	10
	HEAT BLNK	MF-207	WAKE	2000-11-0001	2
	THANBIPAD	10140	MELTRIS	2000-11-0004	4
	HEATSLINK	MF-209	WAKE	2000-11-0000	2
	FERRITE BEAD	56-590-65/3B	FEHRI	3100-00-0001	1
	POT,TRIM,1K	91AKIK	BECK	4000-01-0209	2
	POT,TRIM,10K	91AK10K	BECK	4000-01-0315	3
	POT,TRIM,100K	91AK100K	BECK	4000-01-0402	4
	POT,TRIM,200	91AR200	BECK	4000-02-0101	3
	POT,TRIM,500	91AR500	BECK	4000-05-0100	1
	R4 R88				
	R12 R162 H55				
	R13 R16 R181 H252				
	R107 R120 R93				
	R35				
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV D	PAGE 3

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
R239 R240 R250 R251	RES,MF,1/8W,12,11K	RN550-1102F	TNN	4701-03-1102	4
R66 R97	RES,MF,1/8W,12,1.21K	RN550-1211F	TNN	4701-03-1211	2
R267	RES,MF,1/8W,12,1.15K	RN550-1302F	TNN	4701-03-1302	1
R114 R241 R247 R46	RES,MF,1/8W,12,150	RN550-1500F	TNN	4701-03-1500	4
R113 R18 R198 R23 R247 R32 R36 R42 R43 R4	RES,MF,1/8W,12,1.3K	RN550-1501F	TNN	4701-03-1501	10
R132 R163 R2	RES,MF,1/8W,12,1.15K	RN550-1502F	TNN	4701-03-1502	3
R182	RES,MF,1/8W,12,1.150K	RN550-1503F	TNN	4701-03-1503	1
R118 R219 R220 R221	RES,MF,1/8W,12,1.15	RN550-1505F	TNN	4701-03-1509	4
R149	RES,MF,1/8W,12,1.78K	RN550-1781F	TNN	4701-03-1781	1
R184	RES,MF,1/8W,12,1.146K	RN550-1961F	TNN	4701-03-1961	1
R179	RES,MF,1/8W,12,1.20K	RN550-2000F	TNN	4701-03-2000	1
R1 R15 R183 R249 R28 R31	RES,MF,1/8W,12,200	RN550-2001F	TNN	4701-03-2001	6
R143	RES,MF,1/8W,12,215	RN550-2150F	TNN	4701-03-2150	1
R146 R170	RES,MF,1/8W,12,21.5	RN550-2155F	TNN	4701-03-2159	2
R100 R214	RES,MF,1/8W,12,23.7K	RN550-2372F	TNN	4701-03-2372	2
R102 R137 R144 R159	RES,MF,1/8W,12,249	RN550-2490F	TNN	4701-03-2490	11
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV D	PAGE 5

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
C41T	CAP,MICA,30PF,500V	DM15-300J	AMCO	1500-13-0000	1
C46 C51	CAP,MICA,47PF,500V	DM15-470J	AMCO	1500-14-7000	2
C35T C47 C50	CAP,MICA,68PF,500V	DM15-680J	AMCO	1500-16-8000	3
C36	CAP,MICA,820PF,300V	DM15-821F	AMCO	1500-18-2101	1
C19	CAP,POLYIC,1MF,100V	210-81C-105F	IMN	1500-41-0304	1
C34 C40	VARI,3.5-13PF,250V	75-TR1N0-02 3.5/13PF	TRIKO	1500-51-3000	2
C12 C8 C94 C97	CAP,TANT,1MF,35V	150010594035A2	SPHAG	1500-71-0502	4
C69	CAP,TANT,22MF,15V	194022694015K1	SPHAG	1500-72-2601	1
C95 C98	CAP,TANT,27MF,15V	194027400035F8	SPHAG	1500-72-7402	4
C111 C112 C44 C45	CAP,TANT,5.6MF,35V	15005694035B2	SPHAG	1500-75-0502	4
	CAP,SET,POLYIC COND,111500-00-0005 TMU10007	100-501-101	WVTK	1509-00-0008	1
C37	CAP,POLYIC,.01MF,100V PART OF 1509-00-0008 UT10111500-01-0304				
C56	CAP,POLYIC,1MF,100V PART OF 1509-00-0008 UT10111500-01-0404				
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV D	PAGE 2

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
R276	POT,SWITCH,10K	6W-1879	C18	4002-01-0300	1
R30	POT,SWITCH,50K	LM-4909	C18	4002-05-0301	1
R10	POT,CONT,10K FROM14000-01-0312	4009-71-0301	WVTK	4009-71-0301	1
R105	POT,CONT,500 FROM14000-05-0105	4009-75-0101	WVTK	4009-75-0101	1
R11 R8	RES,C,1/2W,10K,5,1M	RC200F-515	SIPL	4700-25-5104	2
R17	RES,C,1/2W,10K,0.8M	RC200F-685	SIPL	4700-25-6804	1
R110 R124 R127 R128 R129 R142 R145 R149 R153 R158 R174 R180 R197 R2007 R214 R216 R44 R57 R73	RES,MF,1/8W,12,100	RN550-1000F	TNN	4701-03-1000	19
R112 R146 R203 R265 R41	RES,MF,1/8W,12,10K	RN550-1002F	TNN	4701-03-1002	5
R116 R123 R139 R164 R172 R173 R194 R210 R216 R24 R273 R38 R50 R61 R64 R74 R81 R84	RES,MF,1/8W,12,10	RN550-1000F	TNN	4701-03-1009	10
R211 R212	RES,MF,1/8W,12,1,1K	RN550-1101F	TNN	4701-03-1101	2
R104 R108 R111 R159 R145 R148 R200 R207 R45 R47 R51 R94 R96 R99	RES,MF,1/8W,12,1K	RN550-1001F	TNN	4701-03-1001	14
R112 R146 R203 R265 R41	RES,MF,1/8W,12,10K	RN550-1002F	TNN	4701-03-1002	5
R116 R123 R139 R164 R172 R173 R194 R210 R216 R24 R273 R38 R50 R61 R64 R74 R81 R84	RES,MF,1/8W,12,10	RN550-1000F	TNN	4701-03-1009	10
R211 R212	RES,MF,1/8W,12,1,1K	RN550-1101F	TNN	4701-03-1101	2
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV D	PAGE 4

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFG#-PART-NO	MFG#	WAVETEK NO.	QTY/P1
R146 R230 R242 R245 R25 R262 H39	RES,MF,1/8W,12,2.49K	RN550-2491F	TNN	4701-03-2491	12
R147 R151 R156 R171 R18 R181 R19 R254 R255 R274 R278 R33	RES,MF,1/8W,12,24.9K	RN550-2492F	TNN	4701-03-2492	3
R161 R213 R281	RES,MF,1/8W,12,30.1K	RN550-3011F	TNN	4701-03-3011	4
R150 R177 R178 R43	RES,MF,1/8W,12,30.1K	RN550-3013F	TNN	4701-03-3013	4
R131 R256	RES,MF,1/8W,12,310	RN550-3101F	TNN	4701-03-3100	5
R134 R67 R71 R88 R89	RES,MF,1/8W,12,33.2	RN550-3322F	TNN	4701-03-3329	23
R101 R103 R109 R110 R119 R128 R130 R140 R167 R192 R204 R254 R249 R272 R65 R68 R69 R72 R75 R76 R79 R84 R95	RES,MF,1/8W,12,3.45K	RN550-3451F	TNN	4701-03-3451	7
R107 R105 R191 R202 R258 R80 R83	RES,MF,1/8W,12,392	RN550-3920F	TNN	4701-03-3920	4
R115 R193 R194 R195	RES,MF,1/8W,12,4.02K	RN550-4021F	TNN	4701-03-4021	2
R149 R42	RES,MF,1/8W,12,400	RN550-4040F	TNN	4701-03-4040	4
R106 R205 R206 R207	RES,MF,1/8W,12,40.4K	RN550-4042F	TNN	4701-03-4042	1
R201	RES,MF,1/8W,12,40.4	RN550-4048F	TNN	4701-03-4049	1
WAVETEK PARTS LIST		TITLE PCA,GENERATOR	ASSEMBLY NO. 1100-00-0504	REV D	PAGE 6

REMOVE ALL BURRS AND BREAK SHARP EDGES

MATERIAL: _____

PROFINDER: _____

RELEASE APPROV: _____

FINISH: _____

WAVETEK PROCESS: _____

TOLERANCES UNLESS OTHERWISE SPECIFIED:
X.XX - 010 ANGLES - 1/8
X.XX - 000

DO NOT SCALE DWG

SCALE: _____

MODEL NO: 143

DWG NO: 1100-00-0594

REV: D

DATE: 23338

SHEET 1 OF 2

WAVETEK SAN DIEGO - CALIFORNIA

GENERATOR

NOTE UNLESS OTHERWISE SPECIFIED

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D

Table with 4 main sections (A, B, C, D) containing parts lists. Each section has columns for REFERENCE DESIGNATORS, PART DESCRIPTION, ORIG-MFGN-PART-NU, MFR, WAVETEK NO., QTY/P1, and REV. Includes sub-sections for WAVE-TEK PARTS LIST and TITLE PCA:GENERATOR.

C

B

A

Table with 4 main sections (A, B, C, D) containing parts lists. Each section has columns for REFERENCE DESIGNATORS, PART DESCRIPTION, ORIG-MFGN-PART-NU, MFR, WAVETEK NO., QTY/P1, and REV. Includes sub-sections for WAVE-TEK PARTS LIST and TITLE PCA:GENERATOR.

Technical drawing header with fields: REMOVE ALL BURRS AND BREAK SHARP EDGES, DRAWN, DATE, MATERIAL, FINISH, WAVE-TEK PROCESS, TOLERANCE UNLESS OTHERWISE SPECIFIED, DO NOT SCALE DIMS, and WAVE-TEK GENERATOR.

NOTE UNLESS OTHERWISE SPECIFIED

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REV	ECN	BY	DATE	APP

D

D

C

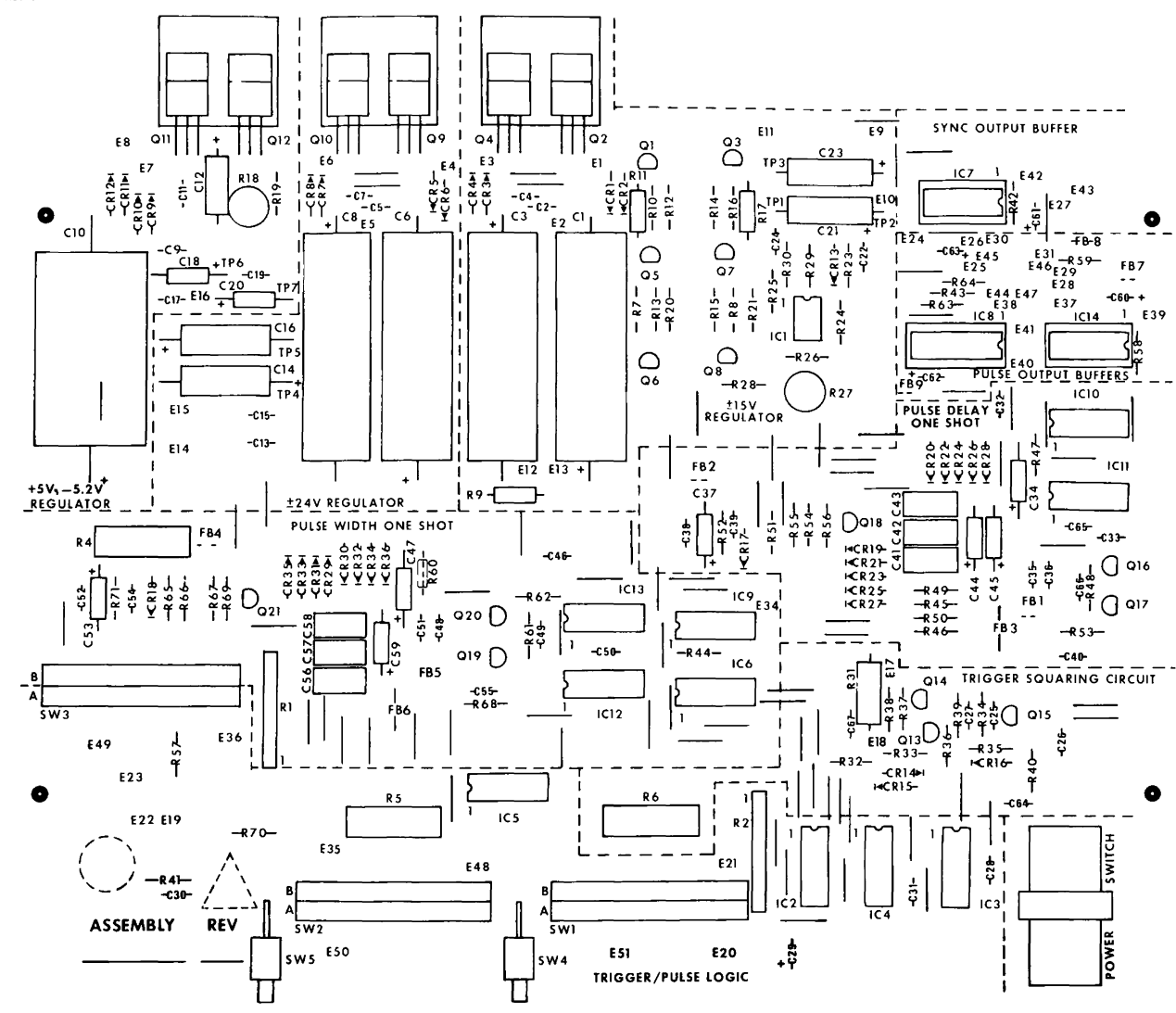
C

B

B

A

A



NOTE UNLESS OTHERWISE SPECIFIED

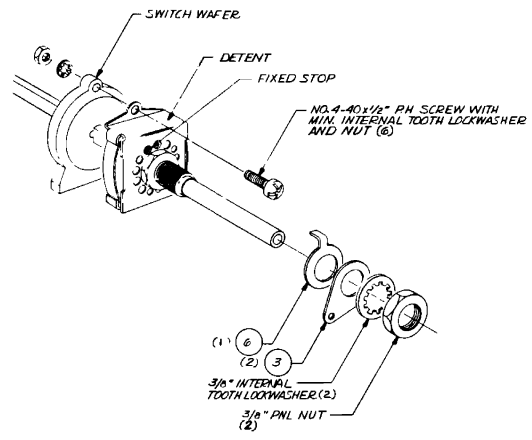
REMOVE ALL BURRS AND BREAK SHARP EDGES	DATE	DATE
MATERIAL	PROJ ENGR	
	RELEASE APPROV	
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED XXX - 010 ANGLER 1:1 XX - 020 ANGLER 1:1	MODEL NO 143/145
	DO NOT SCALE DWG	ISS NO 0100-00-0565
	SCALE	REV C
		CODE 23338
		SHEET OF C

WAVETEK SAN DIEGO - CALIF. INC.

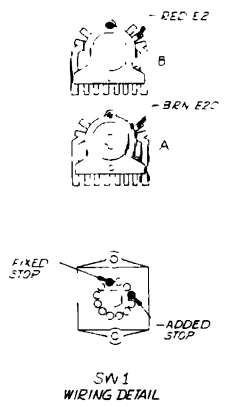
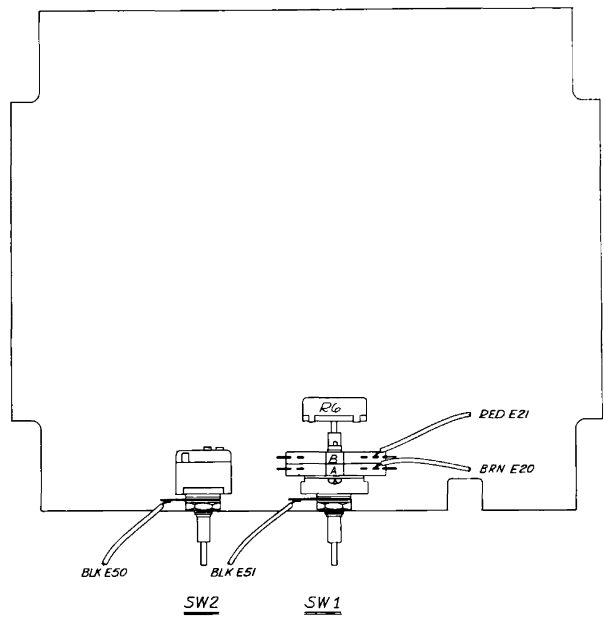
TITLE
TRIG/PULSE BOARD PARTS LOCATOR

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D
C
B
A



TYPICAL HARDWARE STACK-UP



DETENT SHOWN FROM FRONT VIEW IN FULL COUNTER CLOCKWISE POSITION

NOTE UNLESS OTHERWISE SPECIFIED

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAMA A COOPER	DATE 12/77	WAVETEK 800 8000 • CA
MATERIAL	RELEASE APPROV	3-25-77	TITLE ASSEMBLY TRIG/PULSE BD.
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED DIM (IN) ANGLES (°) XS - .00	DO NOT SCALE DWG	DRG. NO. 143
	SCALE		ORG. NO. 001-00-0585
			REV. 23338 SHEET 4

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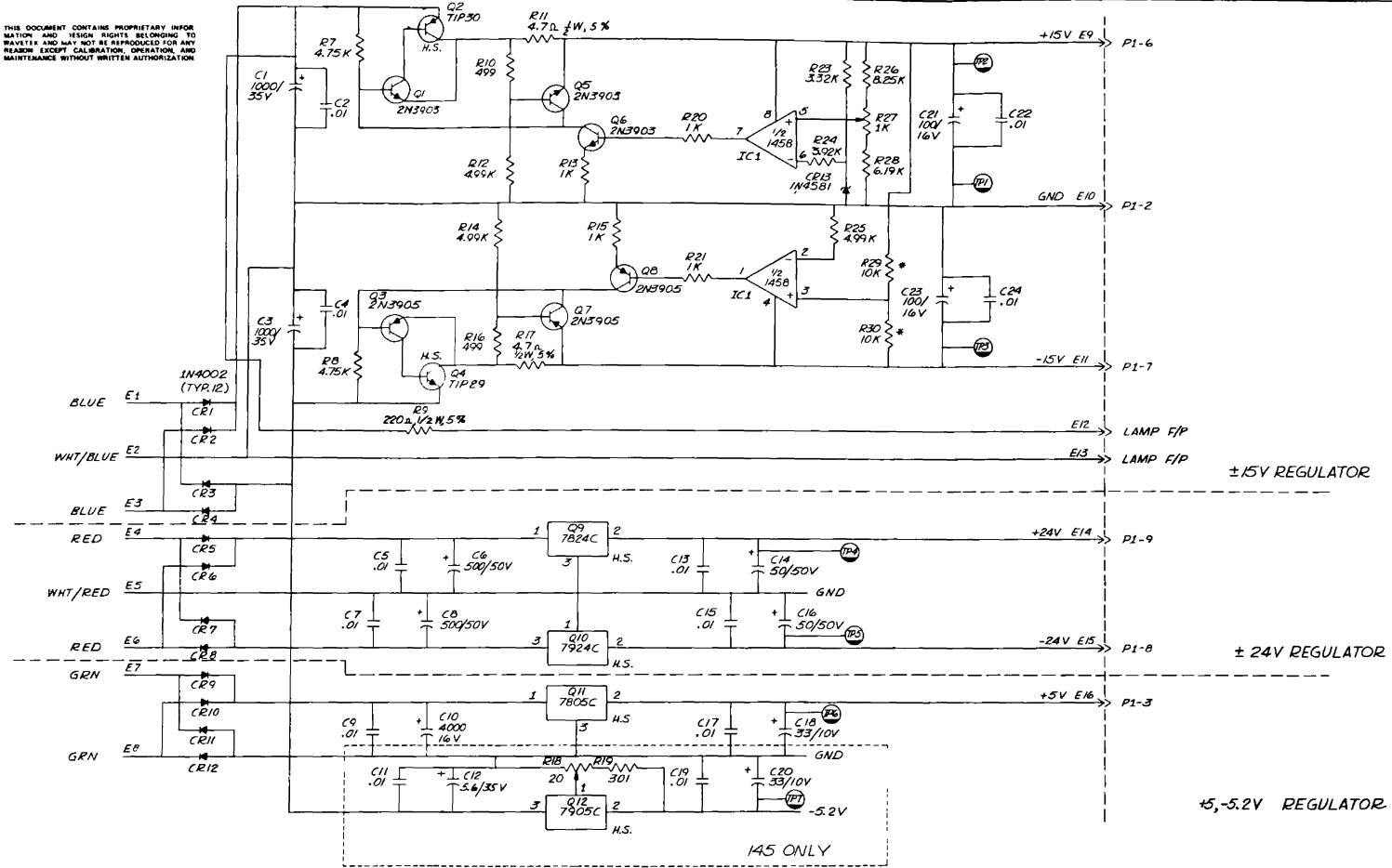
REV	ECN	BY	DATE	APP
B	ECN 1674	RO	2/28/77	
C	ECN 1800	JRM	9-58	

D

C

B

A



±15V REGULATOR

±24V REGULATOR

+5,-5.2V REGULATOR

4. RESISTORS ARE 1/8W, 1% MF
3. RESISTANCE IN OHMS
2. CAPACITANCE IN MICROFARADS
1. * - M.R. 10K RESISTORS

NOTE UNLESS OTHERWISE SPECIFIED

LAST REF DES USED
 C67
 CR36
 FB 9
 IC14
 R71
 SW4
 Q21

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN D. COOPER	DATE 1/10/77	WAVETEK SAN DIEGO • CALIFORNIA		
MATERIAL	PROFESSOR	TITLE	SCHEMATIC TRIG/PULSE BOARD		
FINISH WAVETEK PROCESS	RELEASED 3-23-77	TOLERANCE UNLESS OTHERWISE SPECIFIED DIM - .010 ANGLES .1 XX - .020	MODEL NO. 143/145	DATE NO. 0103-00-0565	REV. C
SCALE	DO NOT SCALE DIMS	FORM NO. 2333B	SHEET 1 OF 2		

REV	ECN	BY	DATE
B	ECN 1674	RD	8/28/77
C	ECN 1800	JRM	9/5/80

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TRIGGER SQUARING CIRCUIT

TRIGGER/PULSE LOGIC

PULSE DELAY ONE-SHOT (MODEL 145 ONLY)

PULSE WIDTH ONE-SHOT (MODEL 145 ONLY)

PULSE OUTPUT BUFFERS (MODEL 145 ONLY)

SYNC OUT BUFFER

STOP/START (MODEL 145 ONLY)

D

C

B

A

- ALL DIODES ARE FD6666
- ALL RESISTORS ARE 1/8W, 1%, MF
- CAPACITANCE IN MICROFARADS
- RESISTANCE IN OHMS

NOTE: UNLESS OTHERWISE SPECIFIED

PULSE DELAY SW2-B
50ms E25
100ms E30
1ms E32

PULSE WIDTH SW3-B
OFF E36
25ms E37
100ms E38
1ms E39

E49 DETENT SW3
E50 DETENT SW2
E51 DETENT SW1

REMOVE ALL BURRS AND BREAK SHARP EDGES	DATE 7-20-77		SCHEMATIC TRIG/PULSE BOARD
MATERIAL	PROFESSOR		
FINISH: WAVETEK PROCESS	SCALE: 1:1	143/145 Q103-00-056-1 23338	143/145 Q103-00-056-1 23338

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REV ECN BY DATE APP

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGN-PART-NO	MFGR	NAVETEK NO.	QTY/PT
NONE	ASST DMG TRIG/PULSE	0101-00-0585	NVTA	0101-00-0585	1
NONE	SCHEMATIC TRIG/PULSE	0103-00-0585	NVTA	0103-00-0585	1
NONE	BRKT,HEAT SHNK	102-300	NVTA	1400-00-5143	3
C27	CAP,CEM,10PF,1KV	DD-108	CHL	1500-01-0011	1
C13 C15 C17 C2 C22 C24 C25 C26 C28 C29 C5 C8 C7 C9	CAP,CEM,01MF,50V	CK-103	CHL	1500-01-0310	14
C30	CAP,CEM,22PF,1KV	DD-220	CHL	1500-02-2011	1
C47	CAP,CEM,33PF,1KV	DD-330	CHL	1500-03-3011	1
C20 C31	CAP,MICA,500PF,300V	DM15-561J	ANCO	1500-15-0100	2
C21 C23	CAP,ELECT,100MF,10V	50001070010DC7	SPHAL	1500-31-0101	2
C1 C3	CAP,ELECT,1000MF,35V	3901080356L0	SPRAG	1500-33-0212	2
C10	CAP,ELECT,4000MF,15V	TCC020015N1L	MAL	1500-34-0211	1
C14 C16	CAP,ELECT,500MF,50V	50050060500D7	SPHAG	1500-35-0003	2
C6 C8	CAP,ELECT,500MF,50V	3905070500L4	SPHAG	1500-35-0103	2
C29 C48	CAP,TANT,22MF,15V	19022249015NA1	SPHAG	1500-72-2001	2
C18	CAP,TANT,33MF,10V	15003349010B2	SPHAG	1500-73-3001	1
1	TRIGGER/PULSE	1700-00-0565	NVTA	1700-00-0565	1
ARP1	CURN,9PIN	04-50-7001	MOLEX	2100-02-0051	1

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGN-PART-NO	MFGR	NAVETEK NO.	QTY/PT
R37	RES,RF,1/8W,1K,24,9K	RN550-2422F	TNR	4701-03-2492	1
R23	RES,RF,1/8W,1K,3,32K	RN550-3321F	TNR	4701-03-3321	1
R38T	RES,RF,1/8W,1K,3,92K	RN550-3920F	TNR	4701-03-3920	1
R24	RES,RF,1/8W,1K,3,92K	RN550-3921F	TNR	4701-03-3921	1
R7 R8	RES,RF,1/8W,1K,4,75K	RN550-4751F	TNR	4701-03-4751	2
R10 R16	RES,RF,1/8,1K,499	RN550-4990F	TNR	4701-03-4990	2
R12 R14 R25 R34 R40	RES,RF,1/8W,1K,4,99K	RN550-4991F	TNR	4701-03-4991	5
R28	RES,RF,1/8W,1K,6,19K	RN550-6191F	TNR	4701-03-6191	1
R36	RES,RF,1/8W,1K,6,98K	RN550-6981F	TNR	4701-03-6981	1
R26	RES,RF,1/8W,1K,8,25K	RN550-8251F	TNR	4701-03-8251	1
R1 R2	RES MODULE	4310R-101-103	BOURN	4770-00-0000	2
R2W R30	RES,SECT,2-10K,1/8W	142-501-60A	WVTK	4789-00-0010	1
CR13	DIODE	1N4561	MIKRO	4801-01-4561	1
CR1 CR10 CR11 CR12 CR3 CR4 CR5 CR6 CR7 CR8 CR9	DIODE	1N4148	BEHTC	4801-02-0001	12
CR14 CR15 CR16	DIODE	FD-0006	FAIR	4807-02-0006	3
Q1 Q11 Q14 Q15 Q5 W4	TRANS	2N3903	FAIR	4901-03-9330	6

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGN-PART-NO	MFGR	NAVETEK NO.	QTY/PT
NONE	WIRE,MU,22GA	15U-WHITE/BROWN	GAVTT	6000-32-2691	2
NONE	WIRE,MU,22GA	15U-WHITE/RED	GAVTT	6000-32-2692	2
NONE	WIRE,MU,22GA	15U-WHITE/ORANGE	GAVTT	6000-32-2903	1
NONE	WIRE,MU,22GA	15U-WHITE/YELLOW	GAVTT	6000-32-2904	1
NONE	WIRE,MU,22GA	15U-WHITE/GREEN	GAVTT	6000-32-2905	1
NONE	WIRE,MU,22GA	15U-WHITE/BLUE	GAVTT	6000-32-2906	1
NONE	WIRE,MU,22GA	15U-WHITE/VIOLET	GAVTT	6000-32-2907	1
NONE	CABLE,COAX,28AWG	RG 174/U	ITT	6001-40-0001	1
IC1	IC	MC1458P	MU1	7000-14-5800	1
Q9	IC	7824	FAIR	7000-74-2400	1
Q10	IC	7924	FAIR	7000-74-2400	1
IC3 IC6	IC	74L000	TI	8000-74-0010	2
IC4	IC	74L300	TI	8000-74-0010	1
IC11	IC	74L874	TI	8000-74-7410	1
Q12	VOLTAGE REGULATOR	7805393	FAIR	8000-78-0500	1
IC7	IC	748140	TI	8007-41-4001	1

WAVETEK PARTS LIST TITLE PCA,TRIGGER/PULSE ASSEMBLY NO. 1100-00-0585 REV C PAGE 1

WAVETEK PARTS LIST TITLE PCA,TRIGGER/PULSE ASSEMBLY NO. 1100-00-0585 REV C PAGE 3


WAVETEK PARTS LIST TITLE PCA,TRIGGER/PULSE ASSEMBLY NO. 1100-00-0585 REV C PAGE 5

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGN-PART-NO	MFGR	NAVETEK NO.	QTY/PT
2	SKT,IC,14PIN	14-DIP	CINCH	2100-03-0011	1
3	SOLDER LUG	1497	SMITH	2100-14-0012	2
4	PLN	08-50-0105	MOLEX	2100-05-0025	9
5	STANDOFF	P-404-M03-F05-040	NVTA	2800-02-0010	6
P80	FERRITE BEAD	50-590-05/30	FELHX	3100-00-0001	1
R27	POT,TRIM,1K	914M1K	SEKA	4600-01-9209	1
RW2	POT,SWITCH,10K	SW-1079	CTS	4802-01-0300	1
R6	POT,CONT,10K	4809-71-0303	NVTA	4809-71-0303	1
R11 R17	RES,C,1/2W,5K,4,7	RC206F-4R7	STNPL	4700-25-0479	2
R9	RES,C,1/2W,5K,220	RC206F-221	STNPL	4700-25-2200	1
R31	RES,C,1W,10K,10K	RC320F-103	STNPL	4700-35-1002	1
R33 R41	RES,RF,1/8W,1K,100	RN550-1000F	TNR	4701-03-1000	2
R13 R15 R20 R21	RES,RF,1/8W,1K,1K	RN550-1001F	TNR	4701-03-1001	4
R39	RES,RF,1/8W,1K,1,5K	RN550-1501F	TNR	4701-03-1501	1
R52	RES,RF,1/8W,1K,1,5K	RN550-1502F	TNR	4701-03-1502	1
R55	RES,RF,1/8W,1K,1,70K	RN550-1701F	TNR	4701-03-1701	1
R42	RES,RF,1/8W,1K,2,21K	RN550-2211F	TNR	4701-03-2211	1

REFERENCE DESIGNATORS	PART DESCRIPTION	ORIG-MFGN-PART-NO	MFGR	NAVETEK NO.	QTY/PT
Q3 Q7 Q8	TRANS	2N3905	FAIR	4901-03-9050	3
Q4	TRANS	71P-24	TI	4902-00-0290	1
Q2	TRANS	71P-30	TI	4902-00-0300	1
R1A R1B-D	PAFER	147-480	NVTA	5104-02-0015	2
6	SWITCH STOP	215-33-001-03-2d	CTO	5104-07-0003	1
RW1	DETENT MTD	740M15104-01-0010	WVTK	5104-09-0031	1
RW4	SWITCH,TOGGLE	710P3YAVG	CAK	5104-00-0020	1
NONE	WIRE,MU,22GA	15U-BROWN	GAVTT	6000-32-2001	1
NONE	WIRE,MU,22GA	15U-RED	GAVTT	6000-32-2002	2
NONE	WIRE,MU,22GA	15U-ORANGE	GAVTT	6000-32-2003	2
NONE	WIRE,MU,22GA	15U-YELLOW	GAVTT	6000-32-2004	2
NONE	WIRE,MU,22GA	15U-GREEN	GAVTT	6000-32-2005	2
NONE	WIRE,MU,22GA	15U-BLUE	GAVTT	6000-32-2006	2
NONE	WIRE,MU,22GA	15U-VIOLET	GAVTT	6000-32-2007	2
NONE	WIRE,MU,22GA	15U-GRAY	GAVTT	6000-32-2008	2
NONE	WIRE,MU,22GA	15U-WHITE	GAVTT	6000-32-2009	2
NONE	WIRE,MU,22GA	15U-WHITE/BLACK	GAVTT	6000-32-2010	2

WAVETEK PARTS LIST TITLE PCA,TRIGGER/PULSE ASSEMBLY NO. 1100-00-0585 REV C PAGE 2

WAVETEK PARTS LIST TITLE PCA,TRIGGER/PULSE ASSEMBLY NO. 1100-00-0585 REV C PAGE 4

REMOVE ALL BURRS AND BREAK SHARP EDGES	DRAWN	DATE	
MATERIAL	PROJECT	TITLE	
	RELEASE APPROV	TRIGGER / PULSE	
FINISH WAVETEK PROCESS	TOLERANCE UNLESS OTHERWISE SPECIFIED 300 ± 0.18 ANGLES 1° XX ± .030	DO NOT SCALE DWG	
SCALE	MODEL NO.	DWG NO.	REV
	143	1100-00-0585	C
DATE	23338	SHEET	1 OF 1

NOTE: UNLESS OTHERWISE SPECIFIED