

MB82VT032-9/-10/-12

CMOS 32K x 32 SYNCHRONOUS SRAM

CMOS 32768 WORDS BY 32-BIT SYNCHRONOUS STATIC RANDOM ACCESS MEMORY

The Fujitsu MB82VT032 is a 1M bit CMOS Synchronous Static Random Access Memory (SSRAM) containing 1,048,576 memory cells accessible in a 32-bit format. The MB82VT032 features a fully synchronous operation referenced to a positive edge clock whereby all operations are synchronized at a clock input which enables high performance and simple user interface coexistence.

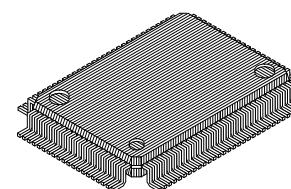
The MB82VT032 SRAM integrates 32K x 32 SRAM core, input registers, burst counters, write data buffers, and output circuits. In addition, MB82VT032 integrates flow-through output so the system may be more performable.

The MB82VT032 is housed in a 100-pin plastic TQFP package. The MB82VT032 features 3.3V low voltage power supply and I/O level is confirmed JEDEC LVTTTL standards.

The MB82VT032 is fabricated with Fujitsu high performance CMOS technology.

FEATURES

- **Non-Pipelined Operation (FLOW-THROUGH OUTPUT)**
- Clock controlled, registered, address, data input and flow-through output control
- Clock 9ns access/15ns cycle, 10ns access/15ns cycle and 12ns access/20ns cycle
- Burst access function (Interleaved or Linear burst)
- Single +3.3V – 0.2V, + 0.3V
- Three Chip Enables for simple memory depth expansion
- Individual BYTE Write and GLOBAL Write control
- Internally self-timed Write cycle
- Common data inputs and outputs
- 100 pin JEDEC standard TQFP package
- 5V safe inputs only



**PLASTIC PACKAGE
FPT-100P-M16**

ABSOLUTE MAXIMUM RATINGS (see NOTE)

Parameter	Symbol	Value	Unit
Supply Voltage	VCC	–0.5 to +4.6	V
Input Voltage on any pin referenced to GND	VIN	–0.5 to +6.0	V
I/O Voltage on any pin referenced to GND	VI/O	–0.5 to VCC+0.5	V
Power Dissipation	PD	TBD	W
Output Current	IOUT	±50	mA
Temperature under Bias	TBIAS	–10 to +85	°C
Storage Temperature	TSTG	–40 to +125	°C

NOTE: Permanent device damage may occur if the above **Absolute Maximum Ratings** are exceeded. Functional operation should be restricted to the conditions as detailed in the operational sections of this data sheet. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Package and Ordering Information

– 100–pin plastic QFP,
order as MB82VT032–xxPFQ

This device contains circuitry to protect the inputs against damage due to high static voltages or electric fields. However, it is advised that normal precautions be taken to avoid application of any voltage higher than maximum rated voltages to this high impedance circuit.